CANNES-L

CPU : Intel Penryn
Chip Set : Intel Cantiga & ICH9M
Remarks : Montevina Platform

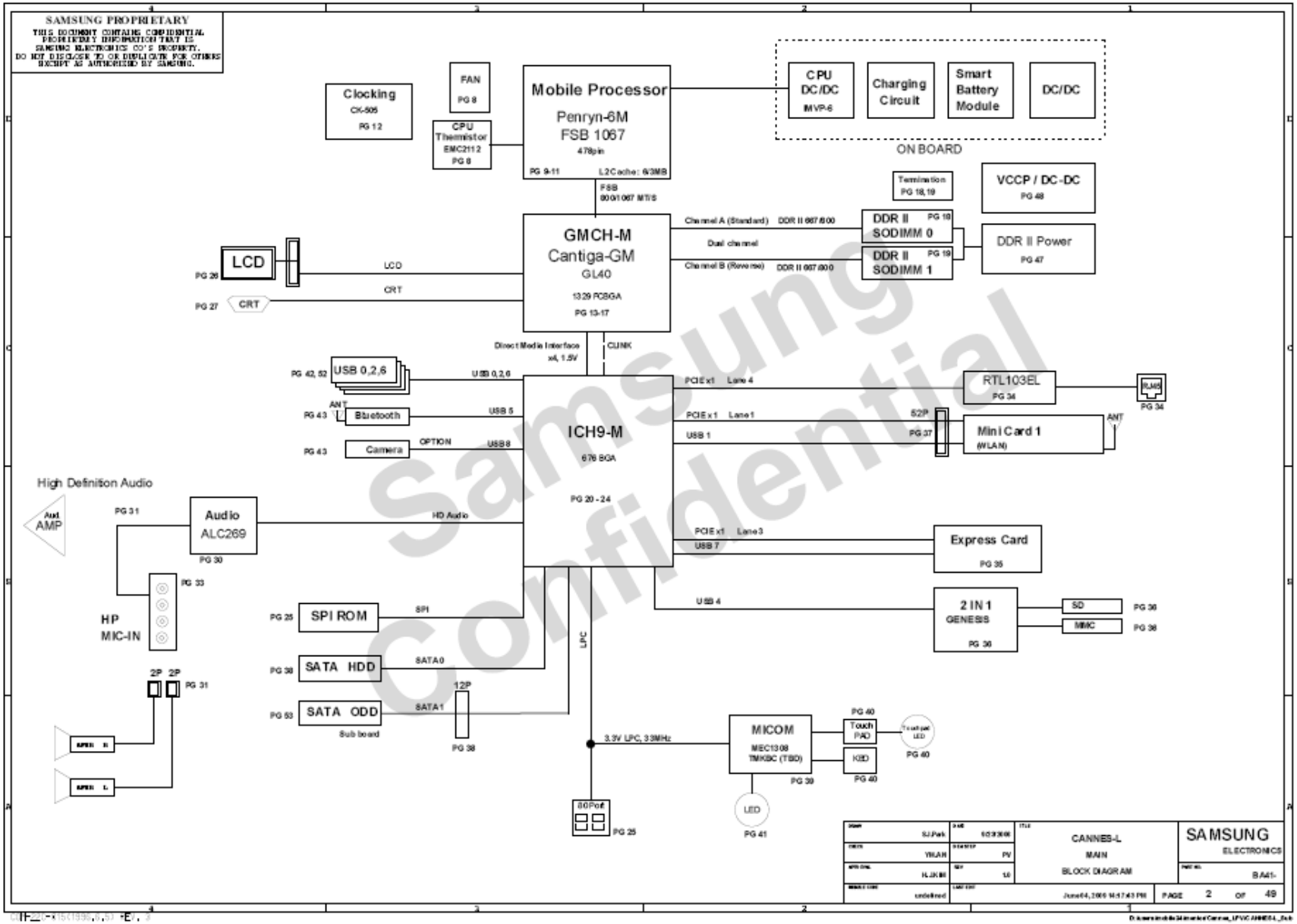
Model Name : R719
PBA Name : MAIN
PCB Code : GCE :
NAN :
HAN :
Dev. Step : PV
Revision : 1.0
T.R. Date : 2009.06.04

Design	CHECK	APPROVAL

■ *Owner : SEC Mobile R & D* *Signature :* *X*

CANNES-L
MAIN
COVER
SAMSUNG ELECTRONICS
PAGE 1 OF 49

8. Block diagram and Schemetic



BOARD INFORMATION

SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

PCI Devices

Devices	IDSEL#	REQ/GNT#	Interrupts
Cardbus	AD25	3	A,B,C
USB	AD29(Internal)	-	USB2.0 #0 (USB0) : A USB2.0 #1 (USB1) : D USB2.0 #2 (USB4) : C USB2.0 #3 (USB5) : E USB2.0 #4 (EHCI) : H
Hub to PCI LPC bridge/IDE/AC97/SMBUS	AD30(Internal) AD31(Internal)	- -	- B
Internal MAC AC Link GLAN	AD24(Internal) - -	- - -	E B F

Crystal / Oscillator

TYPE	FREQUENCY	DEVICE	USAGE
Crystal	32.768KHz	ICH9-M	Real Time Clock
Crystal	10MHz	MICOM	HD64F2169FZ16D
Crystal	14.318MHz	CLOCK-Generator	CK-505
Crystal	25MHz	LAN	RealTek 8E8057

LCD Pannel Detect (TBD)

Devices	Resolution	PANNEL_DETECT_0

Voltage Rails

VDC	Primary DC system power supply (9 to 20V)
VCC_CORE	Core Voltage for CPU
P1.05V (VCCP)	VTT for CPU, Crestline & ICH9-M
P3.3V_MICOM	3.3V always power rail (for Micom)
P1.5V	1.5V switched power rail (off in S3-S5)
P1.8V	1.8V switched power rail (off in S3-S5)
P1.8V_AUX	1.8V power rail for DDR (off in S4-S5)
P0.9V	0.9V power rail for DDR (off in S3-S5)
P3.3V	3.3V switched power rail (off in S3-S5)
P3.3V_AUX	3.3V switched on power rail (off in S4-S5)
P5.0V	5.0V switched power rail (off in S3-S5)
P5.0V_AUX	5.0V switched on power rail (off in S4-S5)
P5.0V_ALW	5.0V always power rail

I²C / SMB Address

Devices	Address	Hex	Bus
ICH9-M	Master	-	SMBUS Master
CPU Thermal Sensor	0111 101x	7Ah	Thermal Sensor
SODIMM0	1010 000x	A0h	-
SODIMM1	1010 010x	A4h	-
Thermal Sensor on SODIMM0	0011 00Dx	30h	-
Thermal Sensor on SODIMM1	0011 010x	34h	-
CK-505M (Clock Generator)	1101 001x	D2h	Clock, Unused Clock Output Disable

USB PORT Assign

PORT #	ASSIGNED TO
0	SYSTEM PORT 0
1	SYSTEM PORT 1
2	SYSTEM PORT 2
3	NC
4	NC
5	Bluetooth
6	Mini PCI Express 2
7	Camera
8	NC
9	NC

PCI Express Assign

PORT #	ASSIGNED TO
0	NC
1	Mini Card 1 (WLAN)
2	NC
3	LOM
4	Mini Card 2 (ROBSON or DVB-T)
5	NC

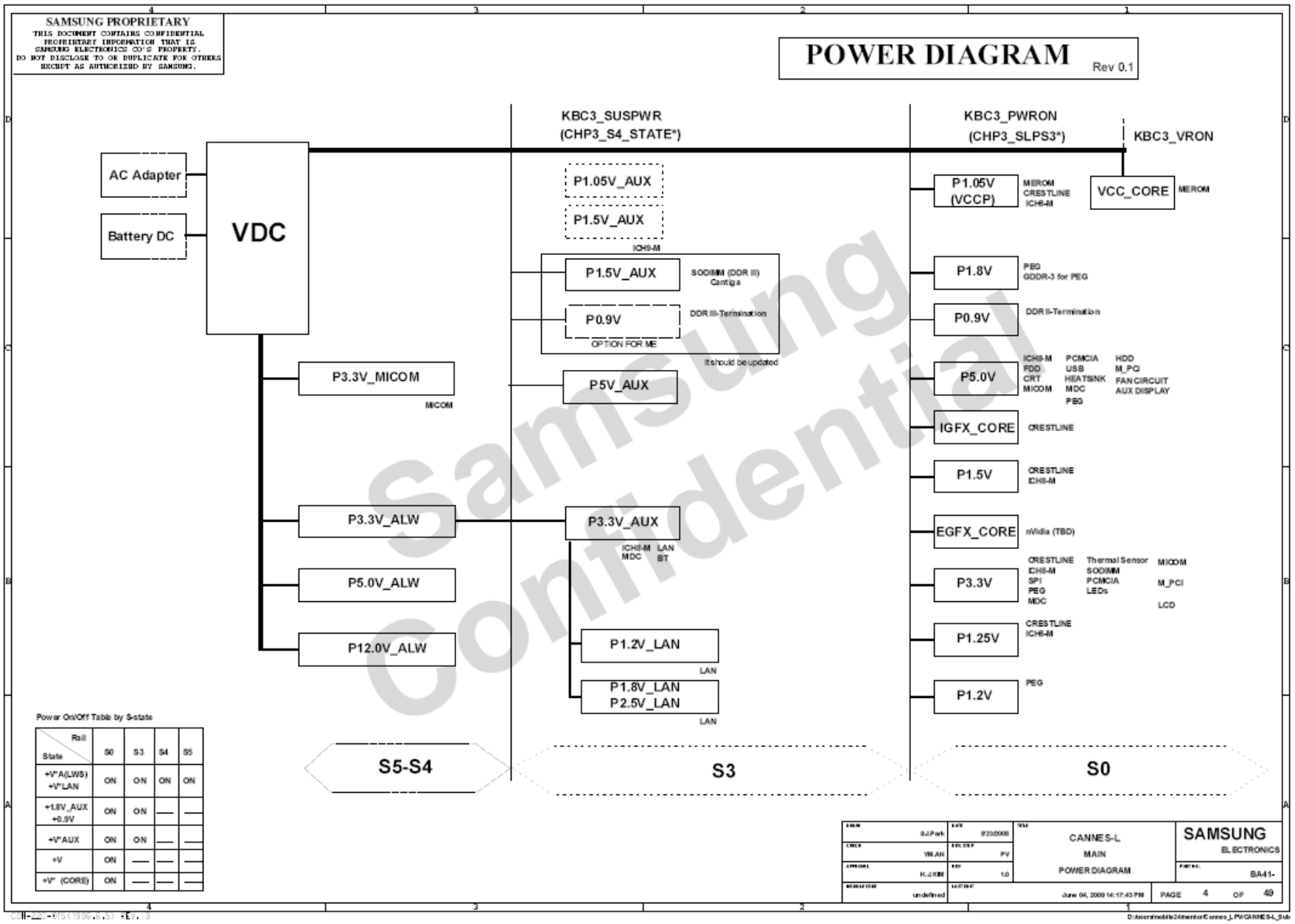
(This table will be update.)

REVISION HISTORY

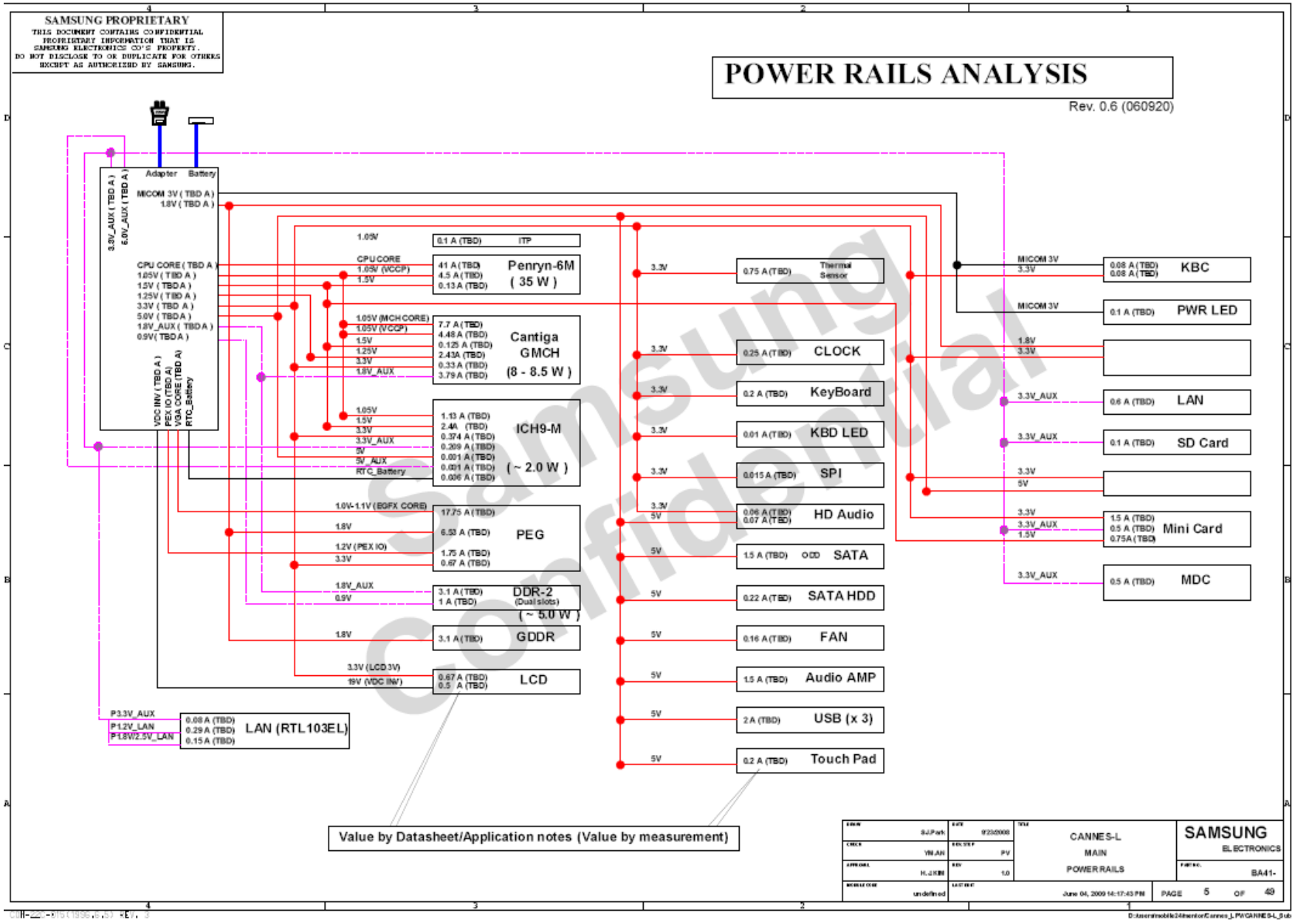
See rev notes for more information.

DRAW: S.J.Park	DATE: 9/23/2008	TITLE: CANNES-L MAIN BOARD INFO	SAMSUNG ELECTRONICS
CHECK: Y.N.JUN	ENG STEP: PV		
APPROVAL: H.J.KIM	REV: 1.0		PART NO.: BA41-
MODULE CODE: undefined	LAST DATE: June 04, 2009 14:17:43 PM	PAGE 3 OF 49	

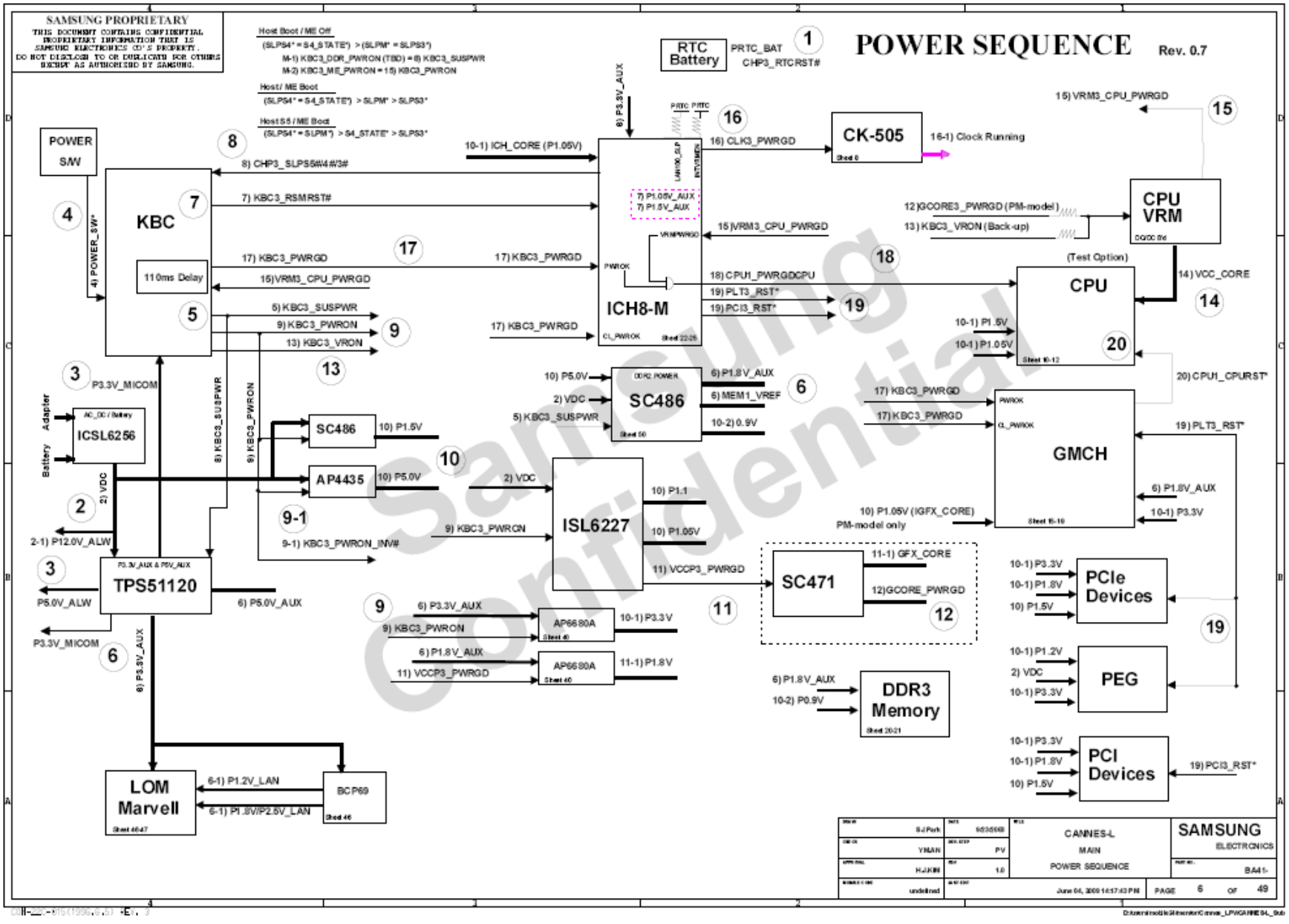
8. Block diagram and Schemetic



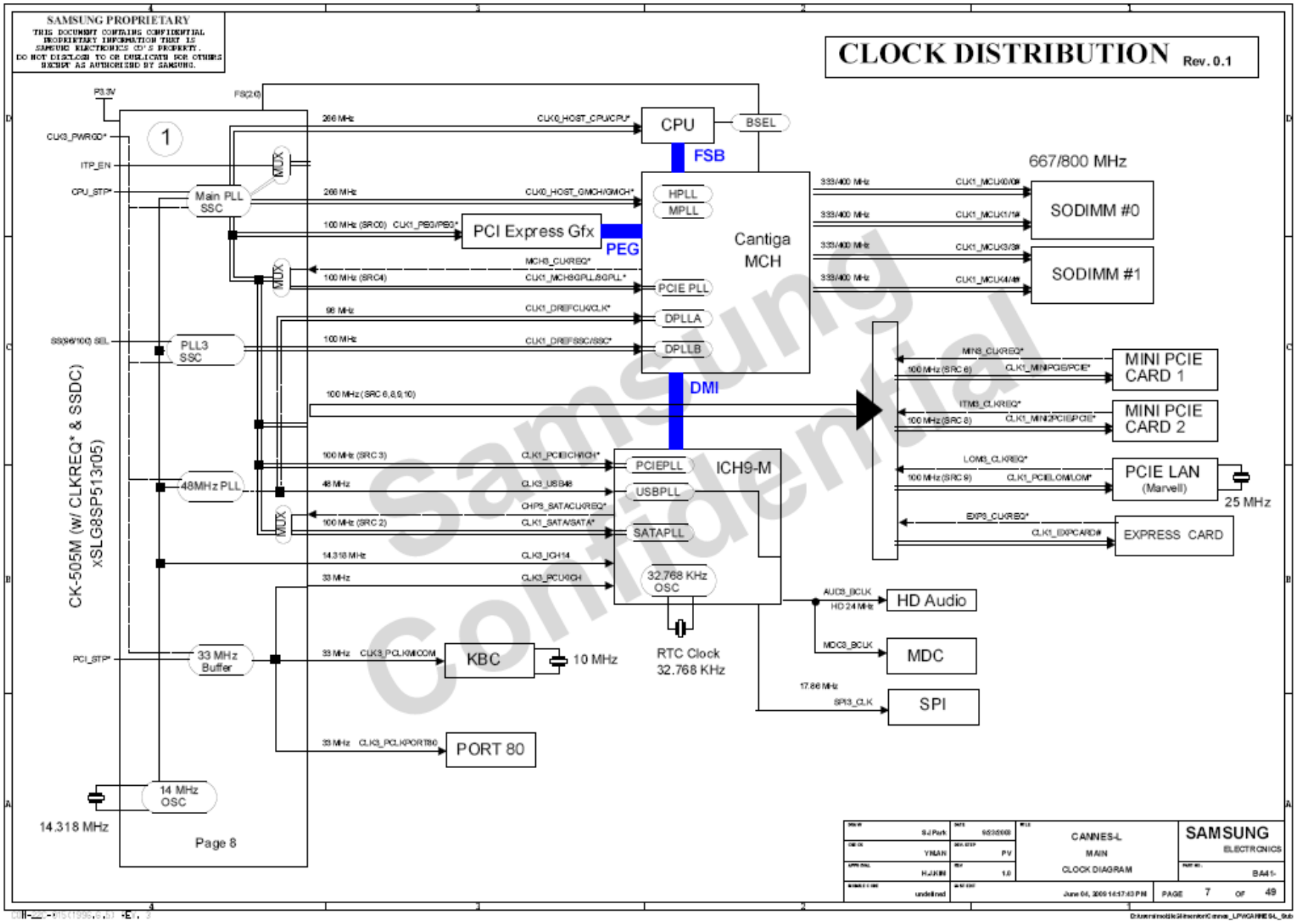
8. Block diagram and Schemetic



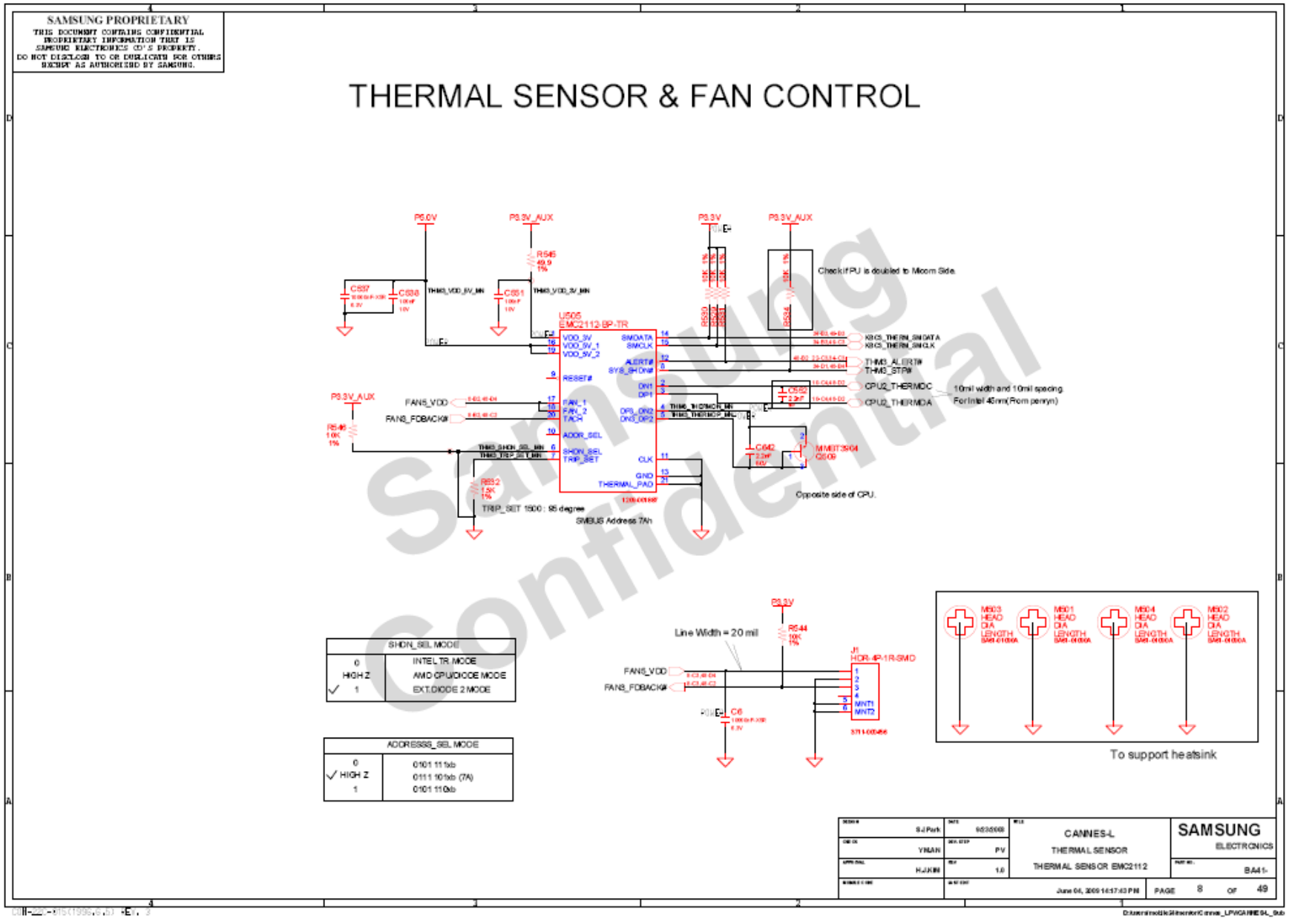
8. Block diagram and Schemetic



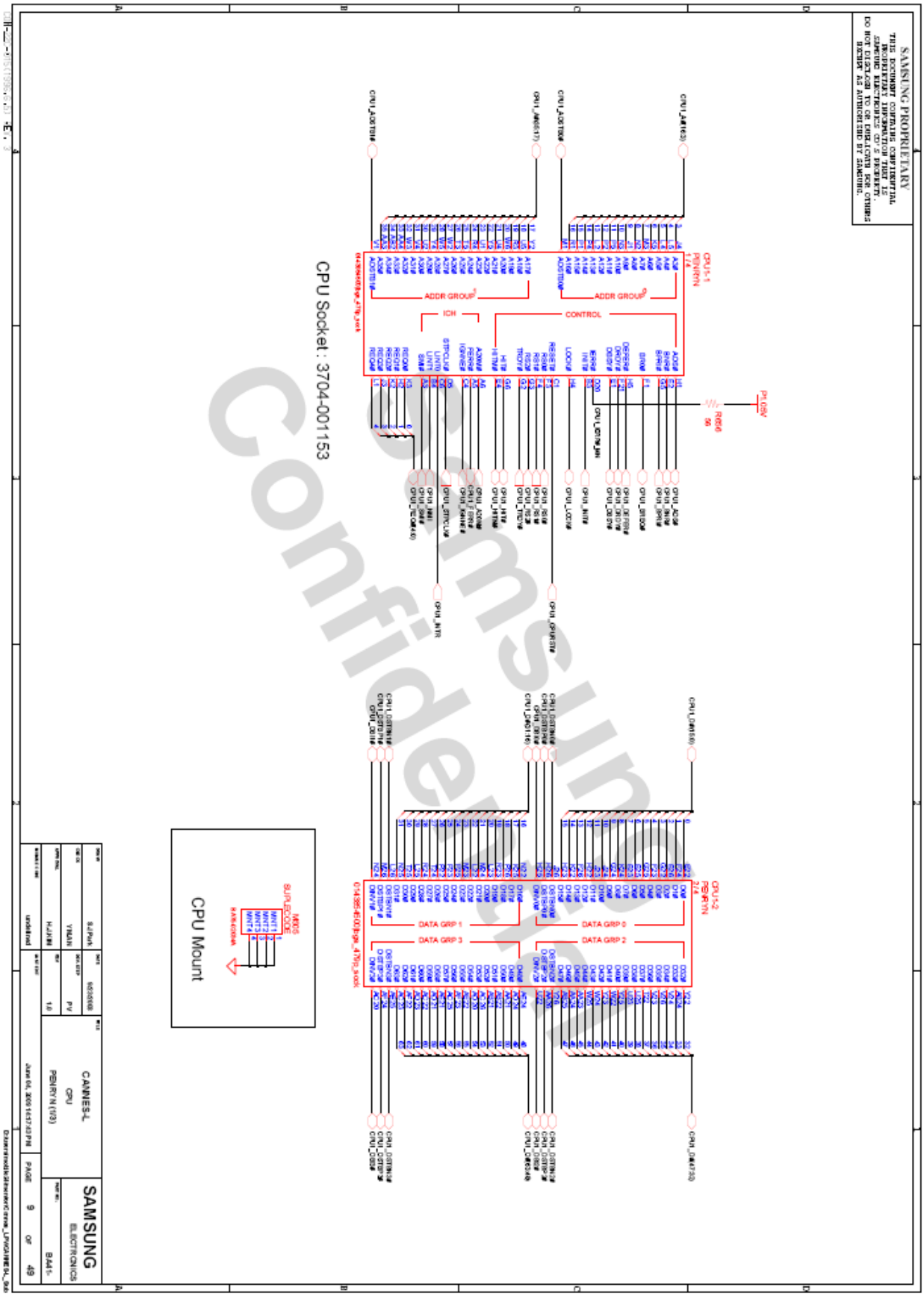
8. Block diagram and Schemetic



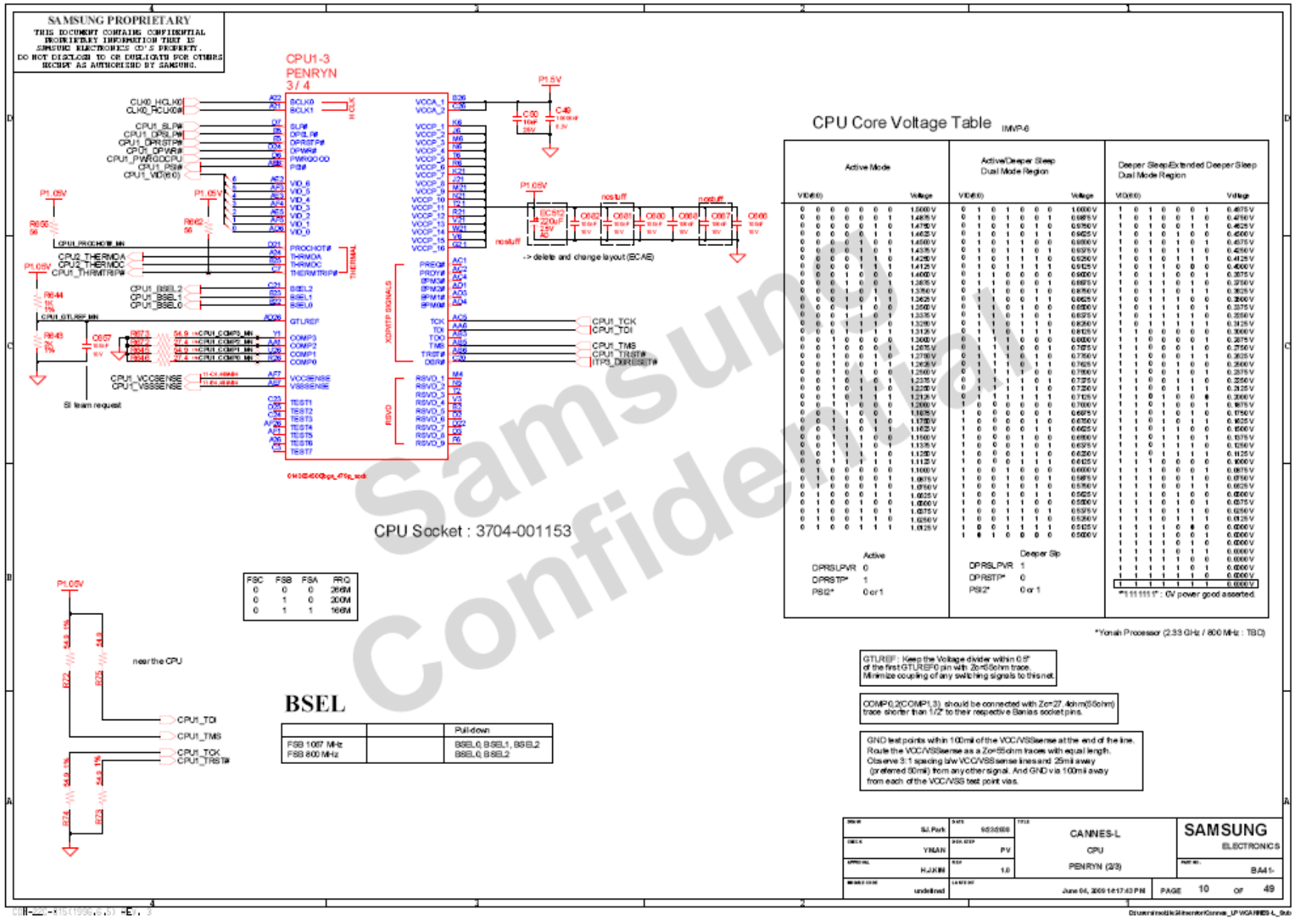
8. Block diagram and Schemetic



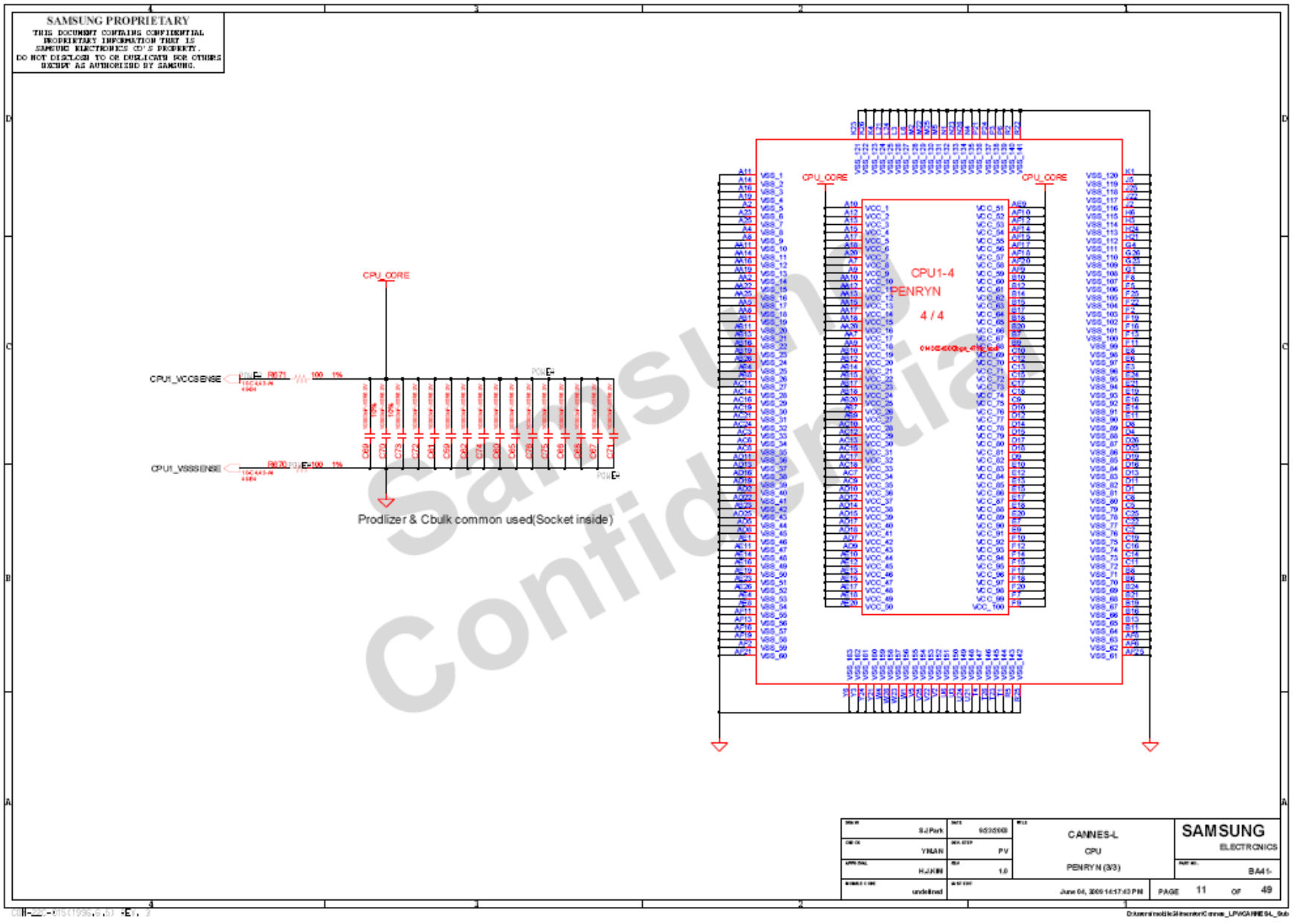
8. Block diagram and Schemetic



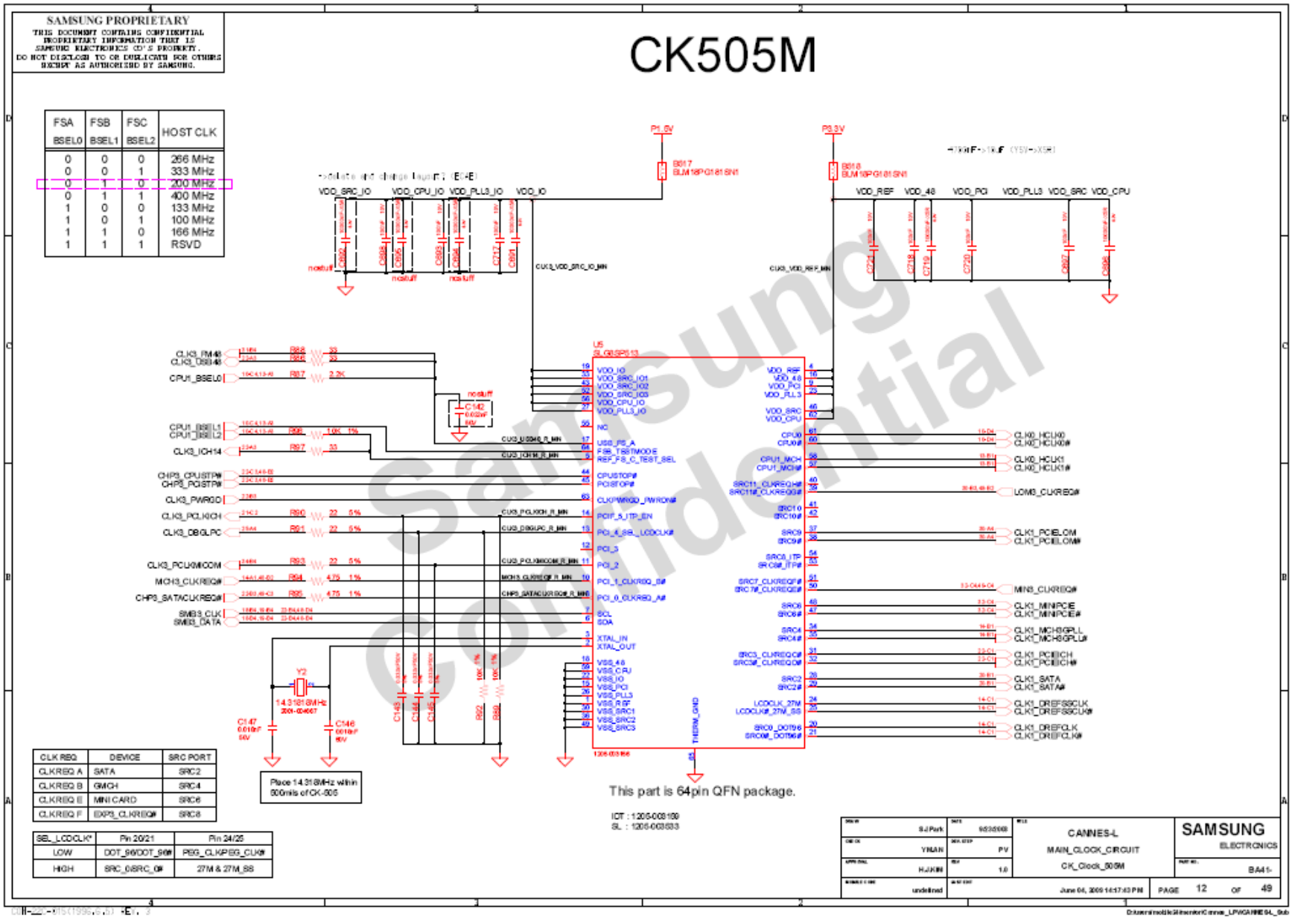
8. Block diagram and Schemetic



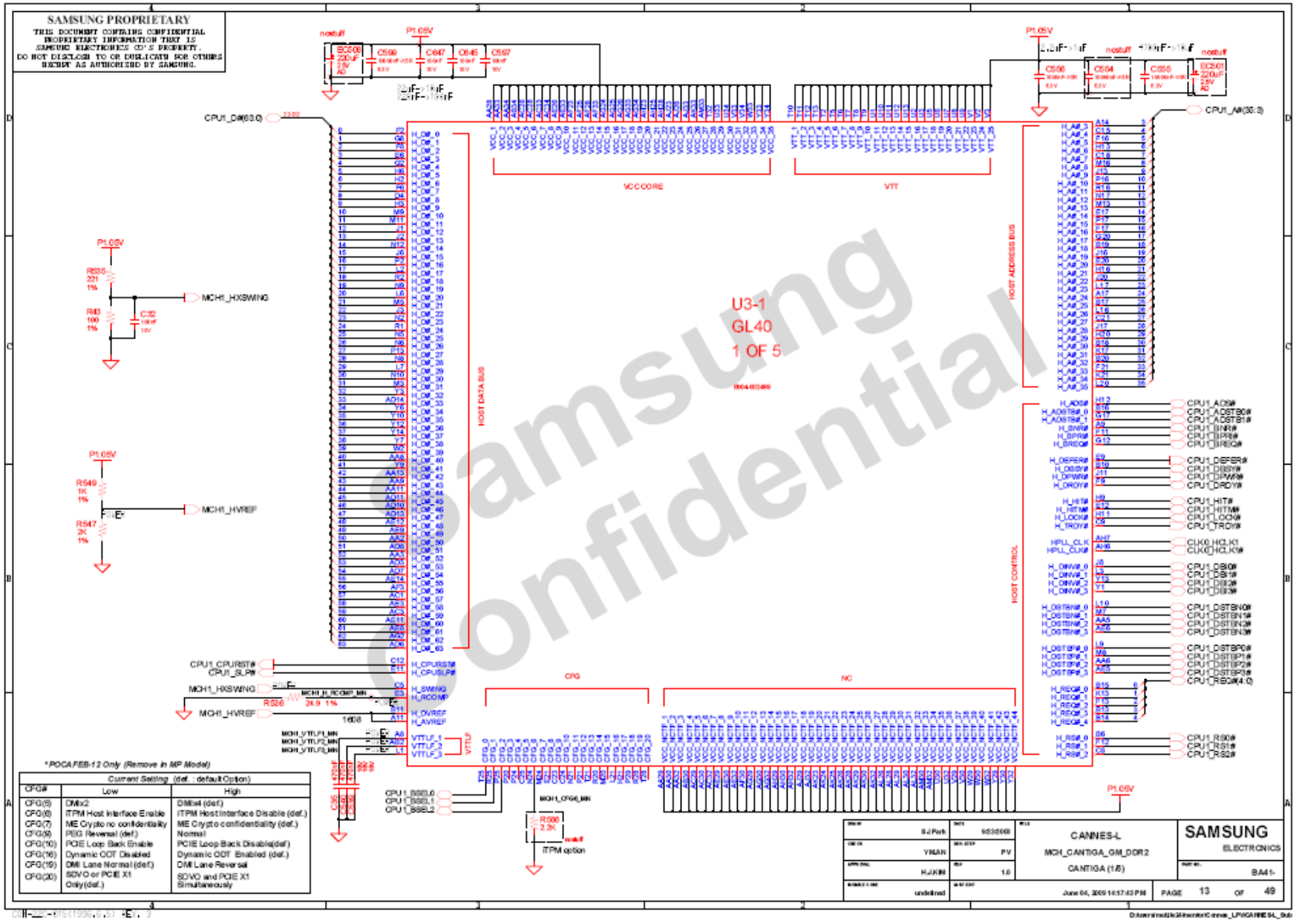
8. Block diagram and Schemetic



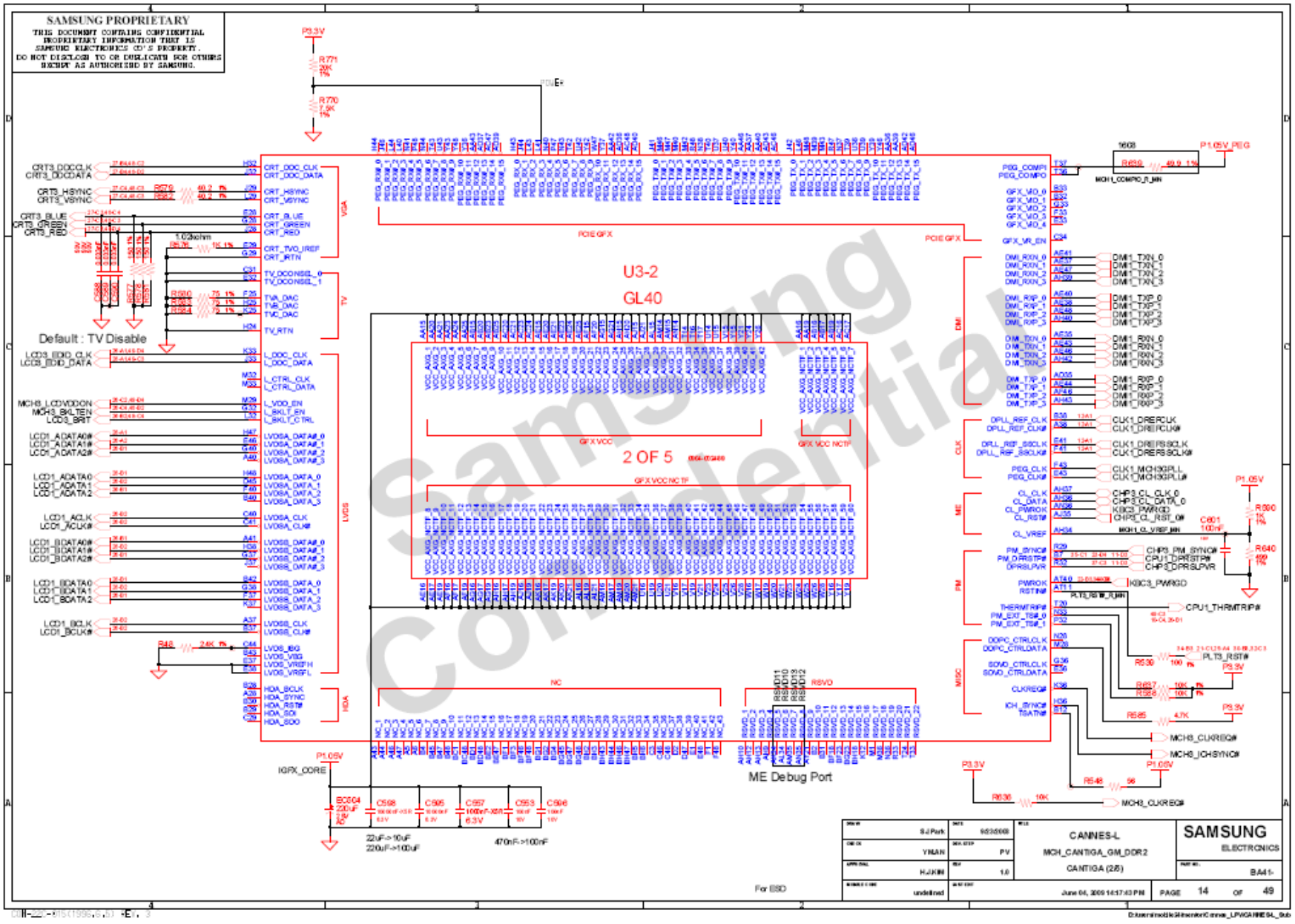
8. Block diagram and Schemetic



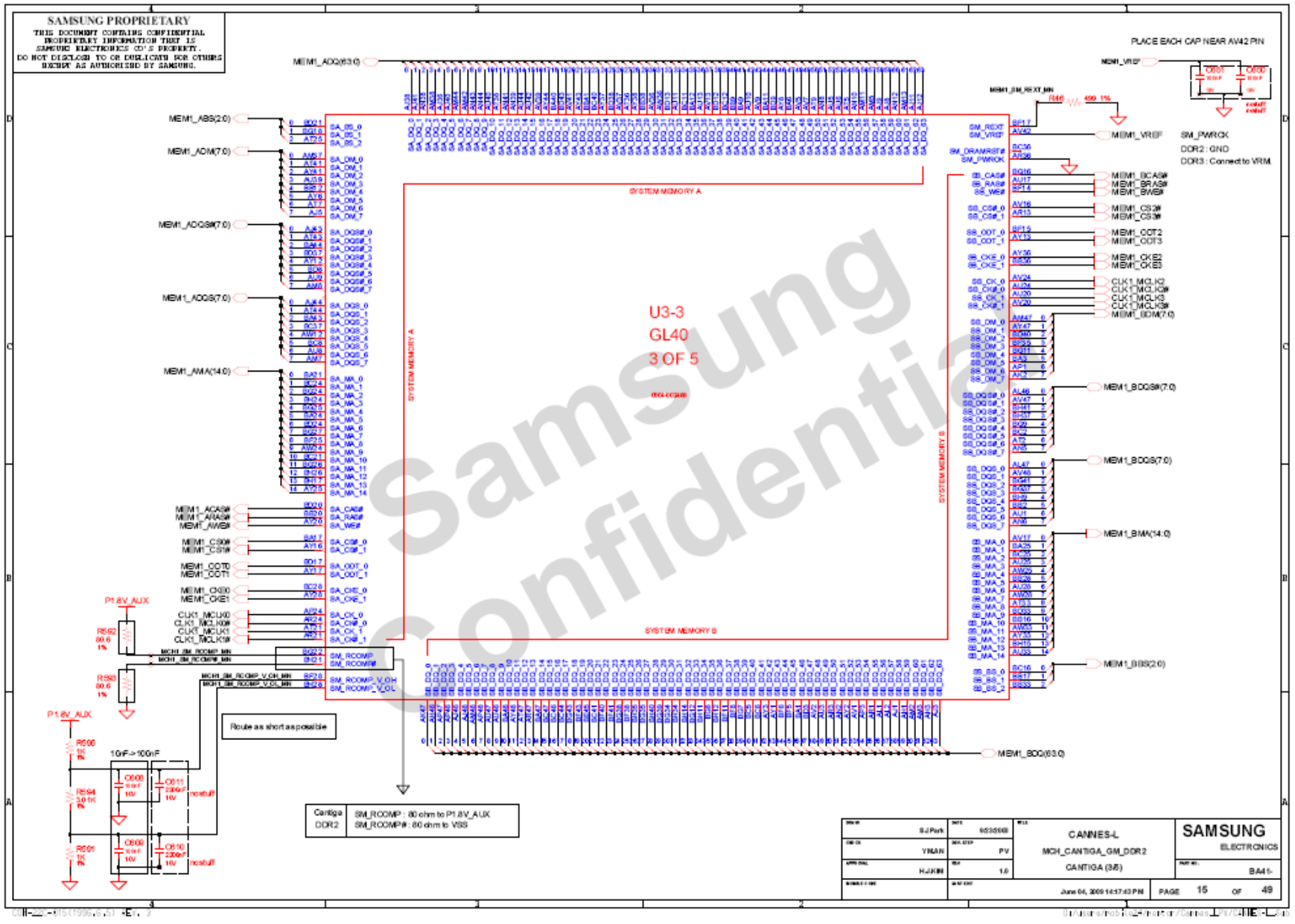
8. Block diagram and Schemetic



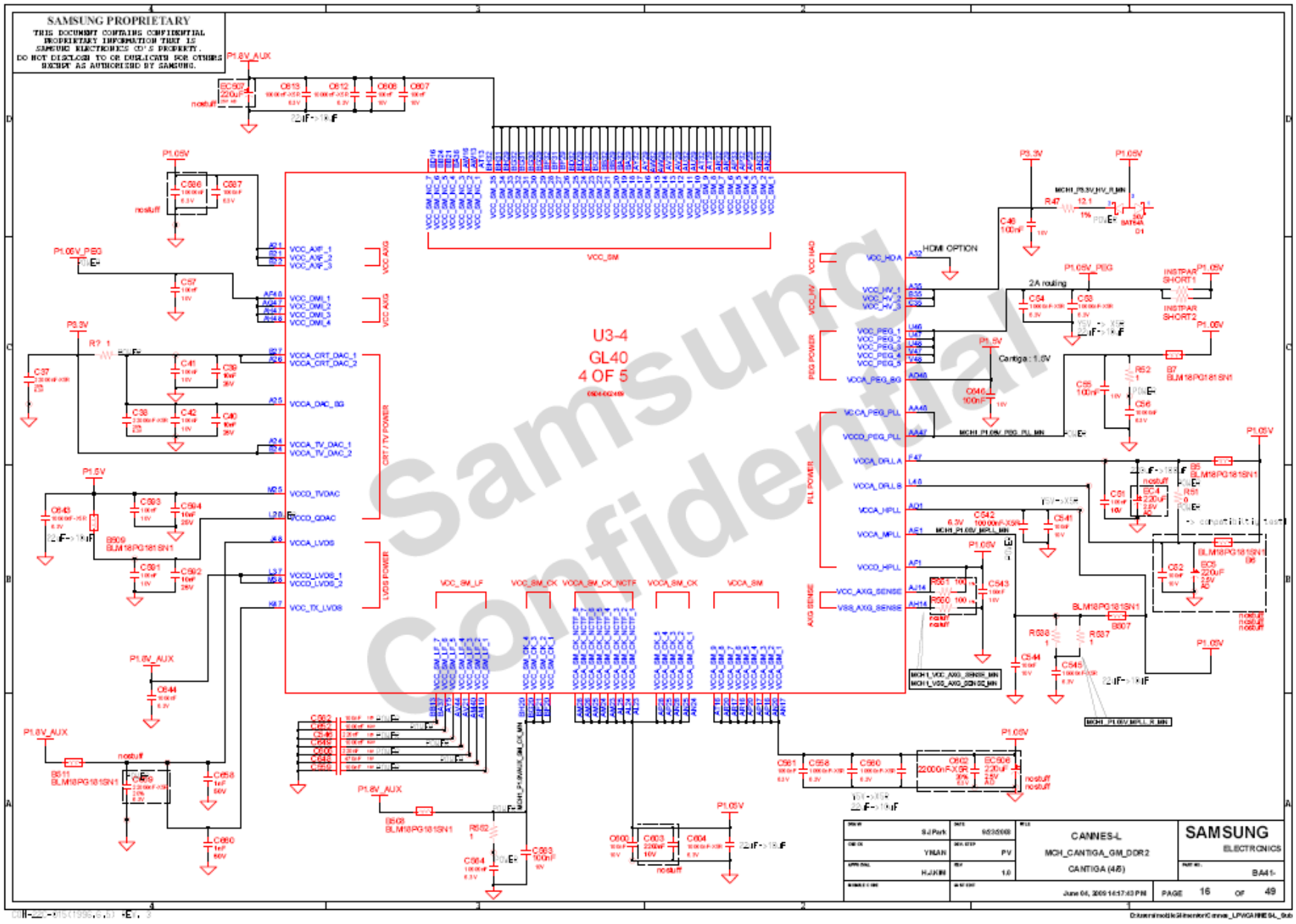
8. Block diagram and Schemetic



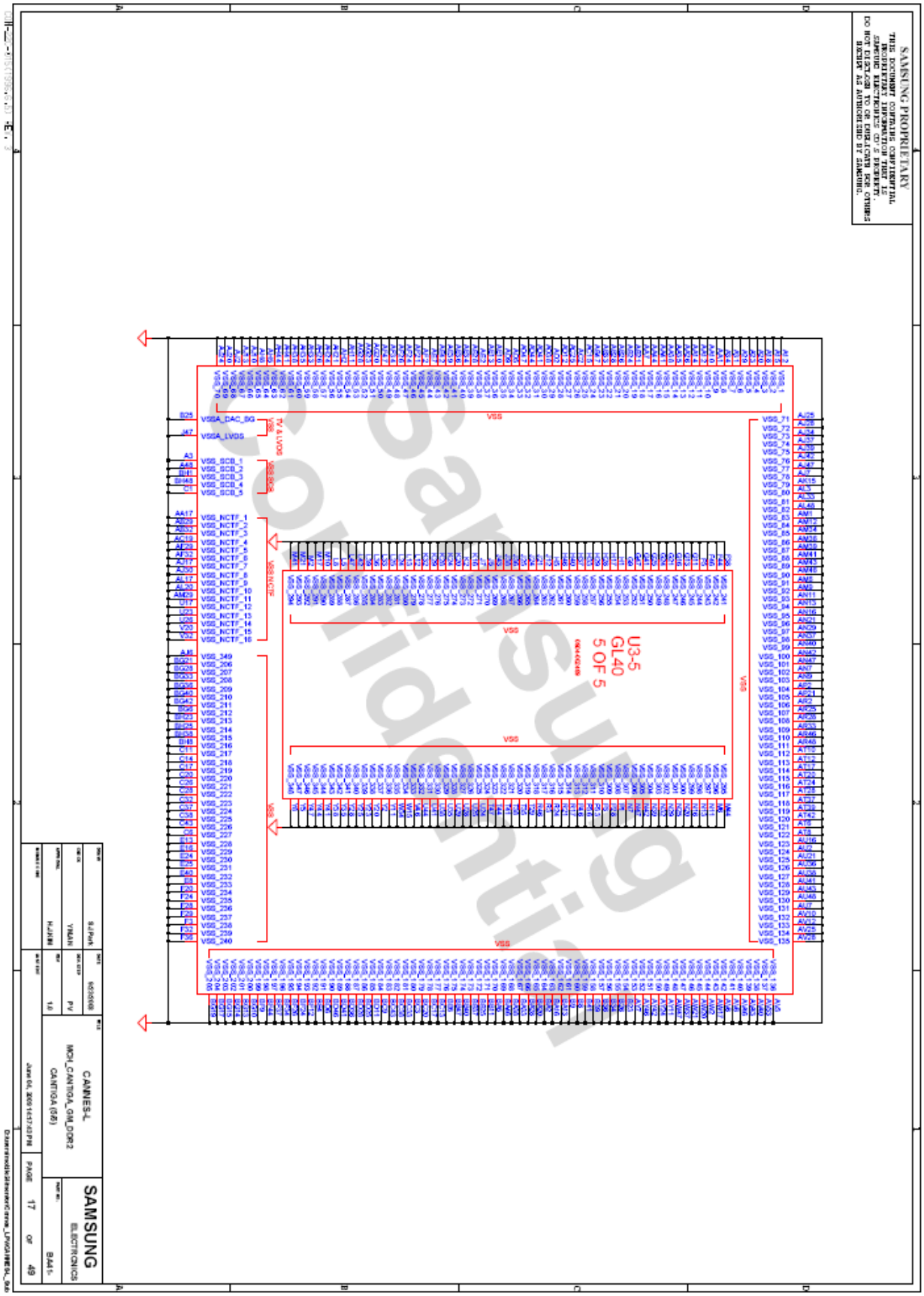
8. Block diagram and Schemetic



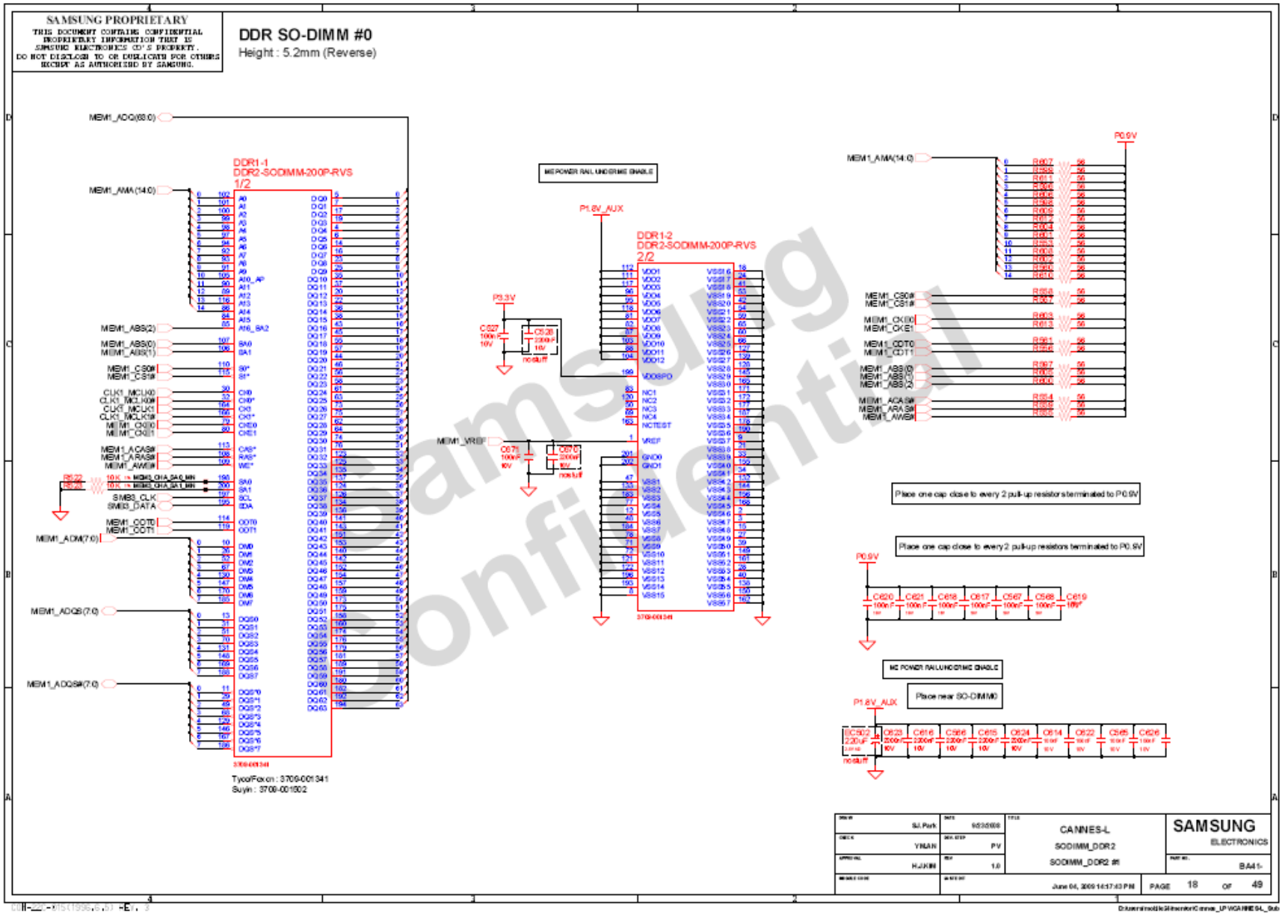
8. Block diagram and Schemetic



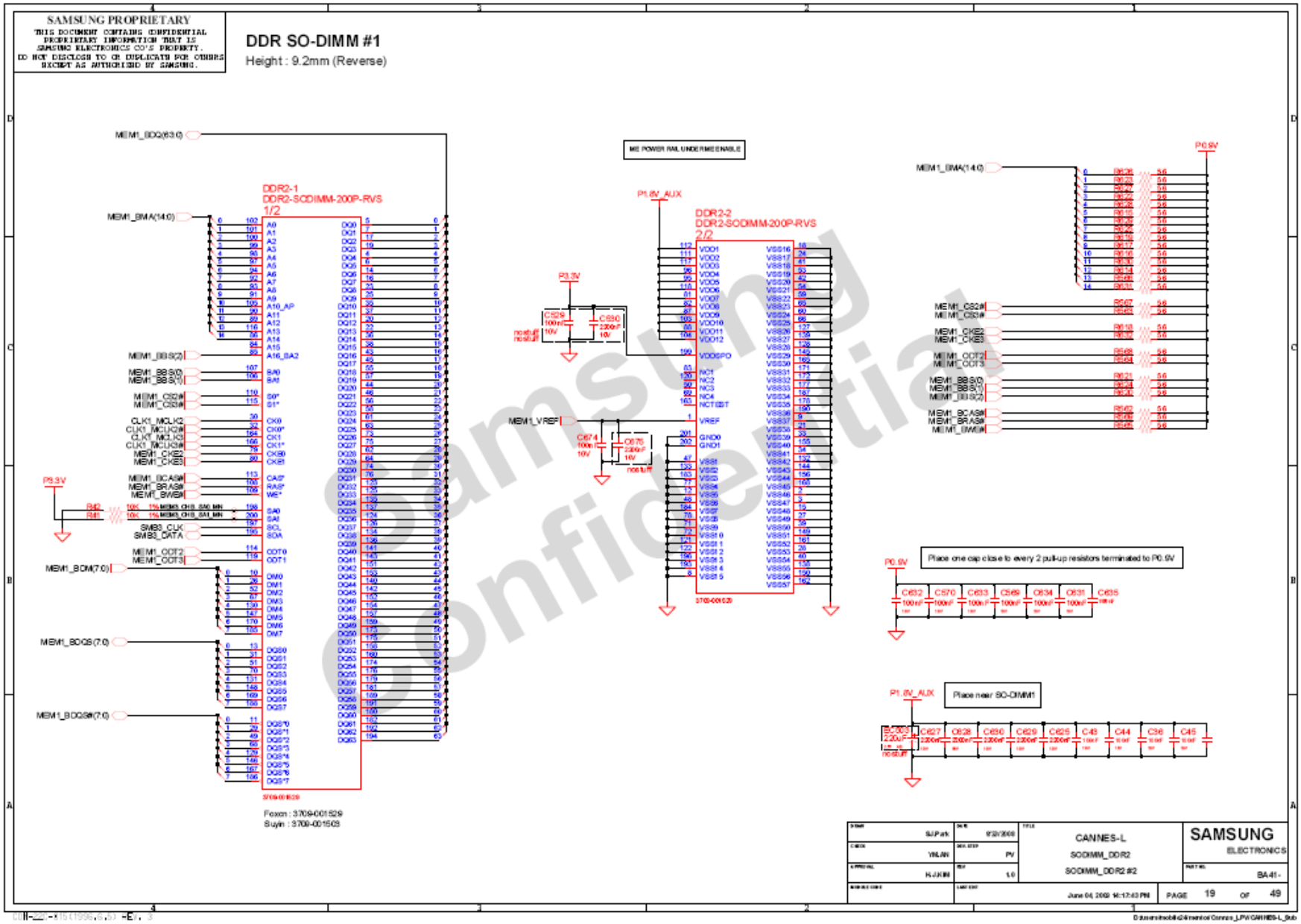
8. Block diagram and Schemetic



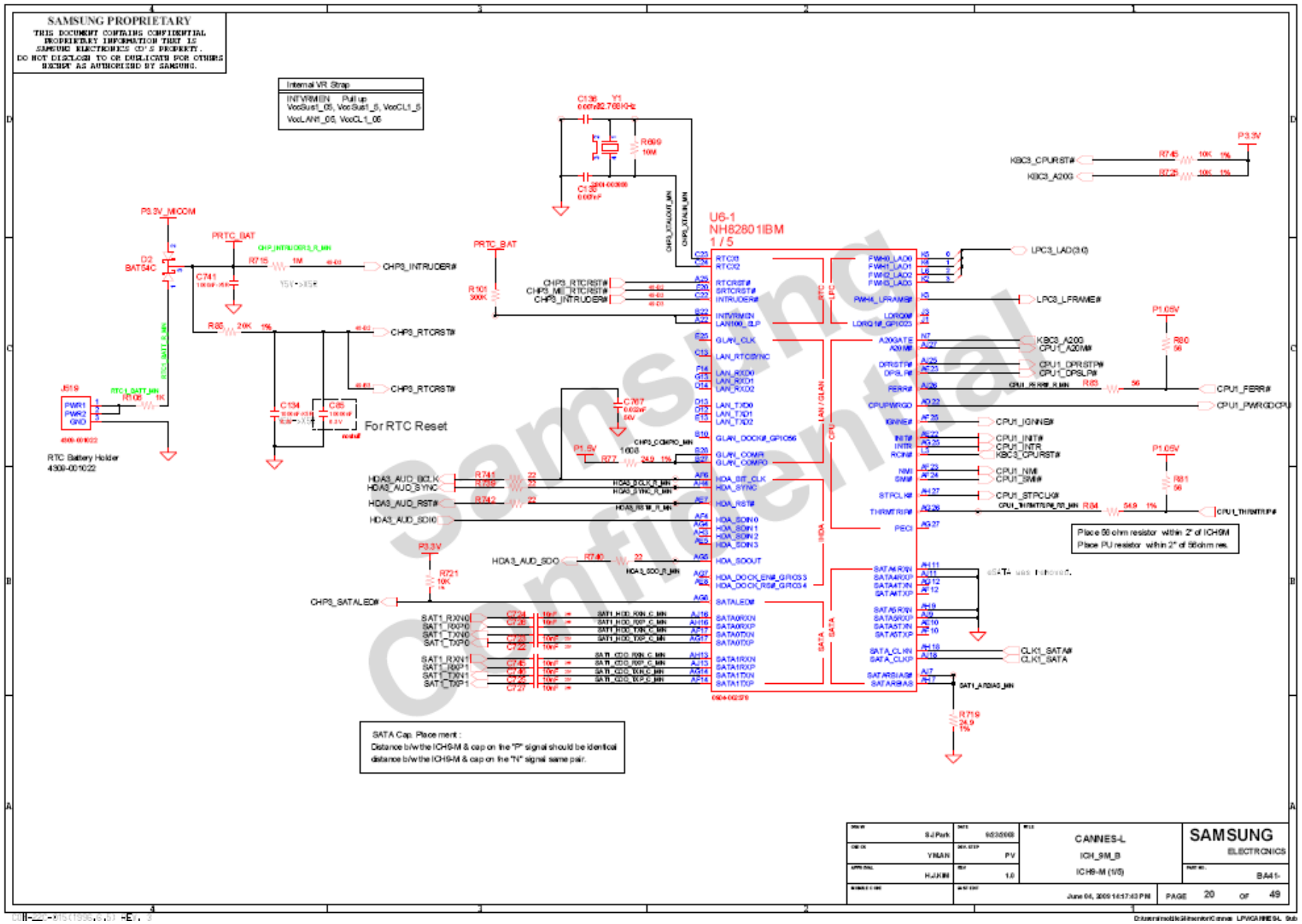
8. Block diagram and Schemetic



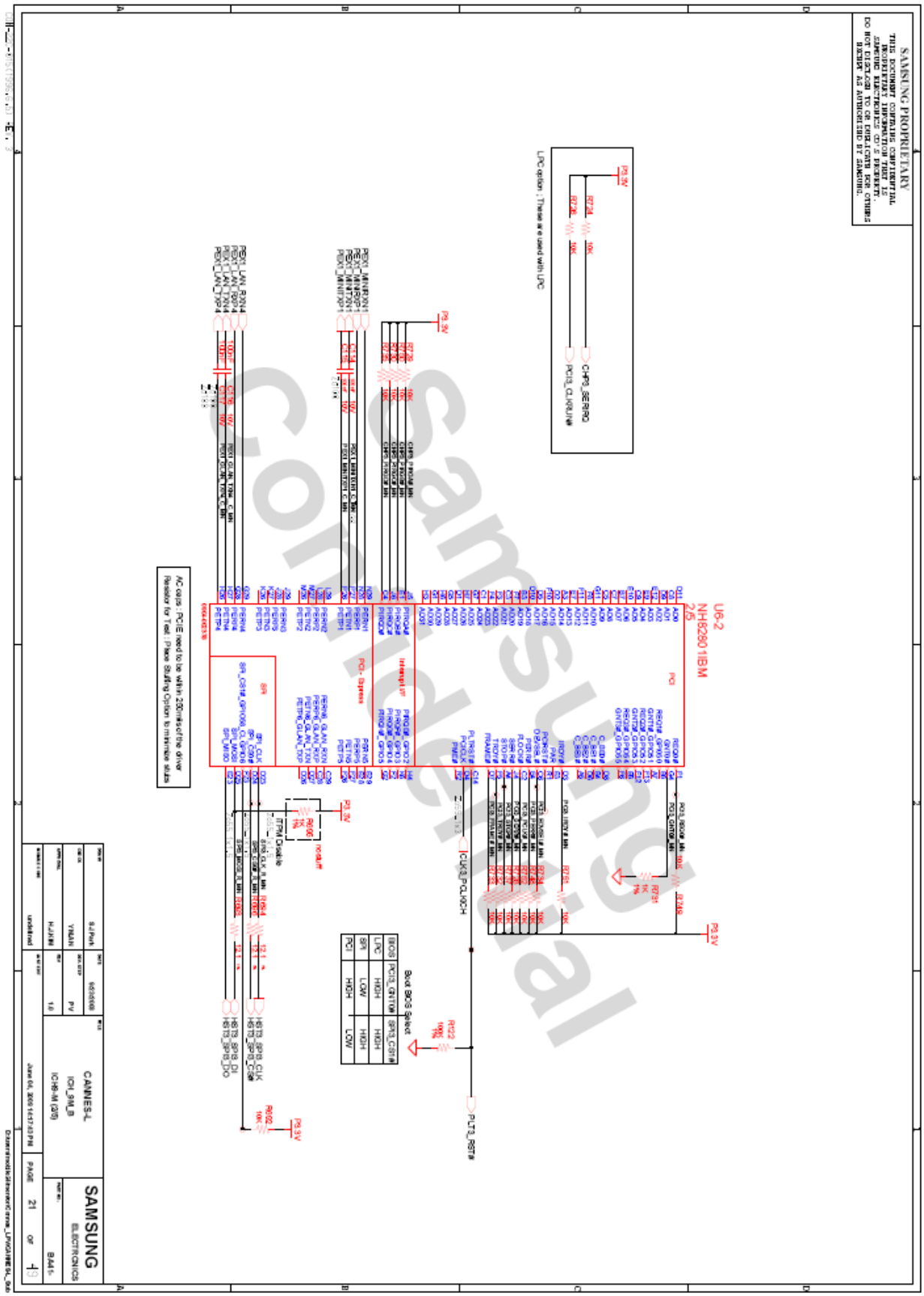
8. Block diagram and Schemetic



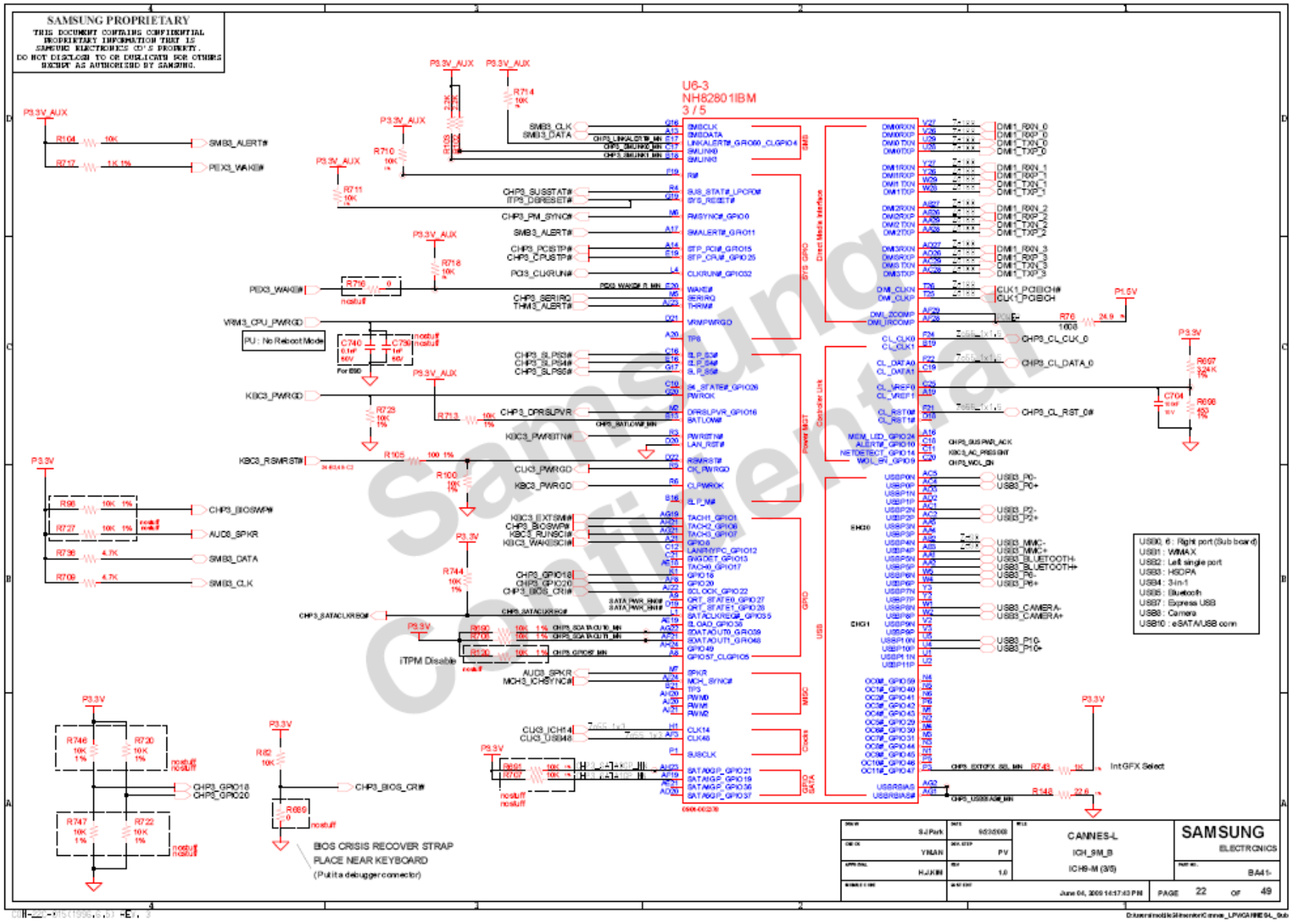
8. Block diagram and Schemetic



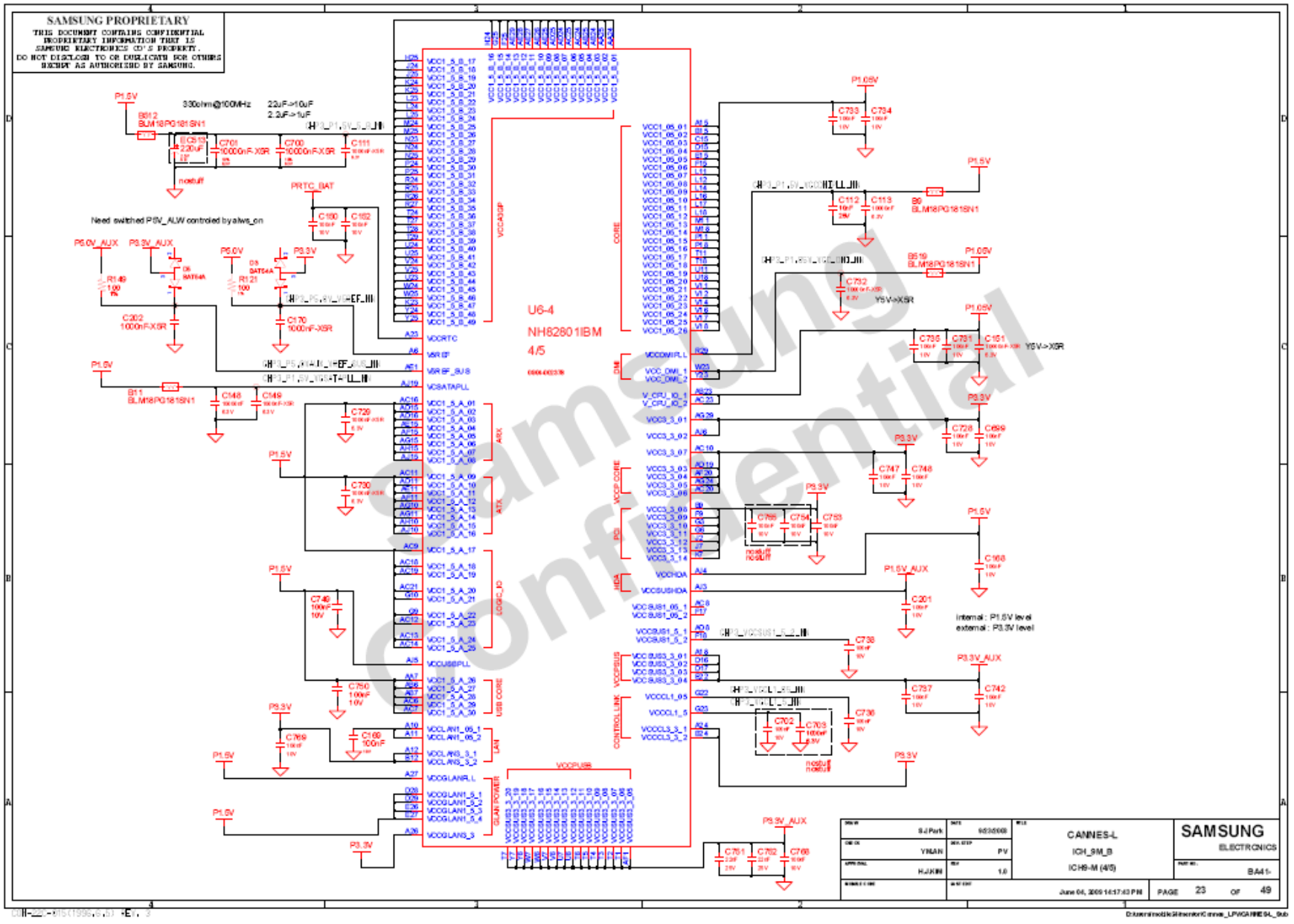
8. Block diagram and Schemetic



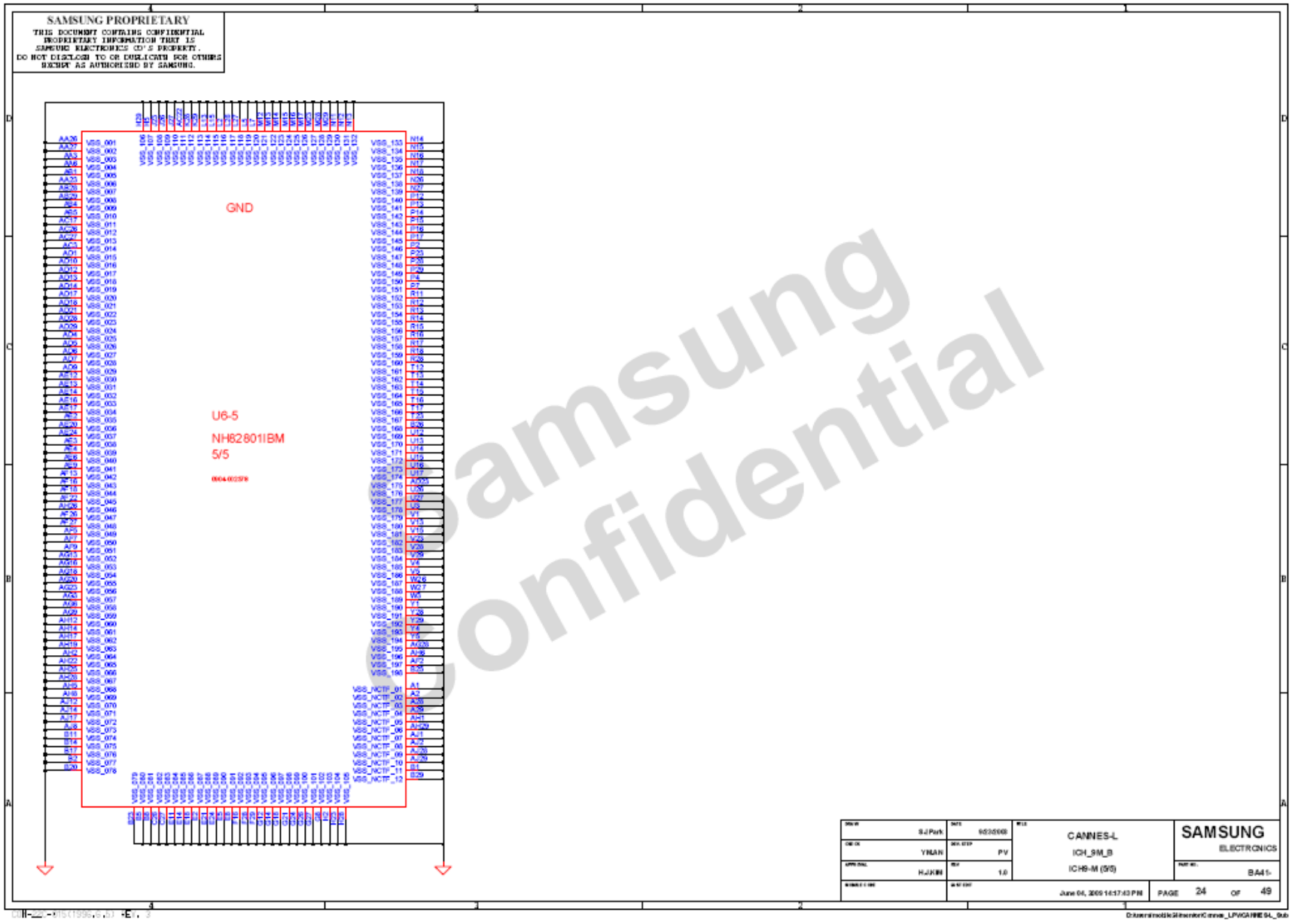
8. Block diagram and Schemetic

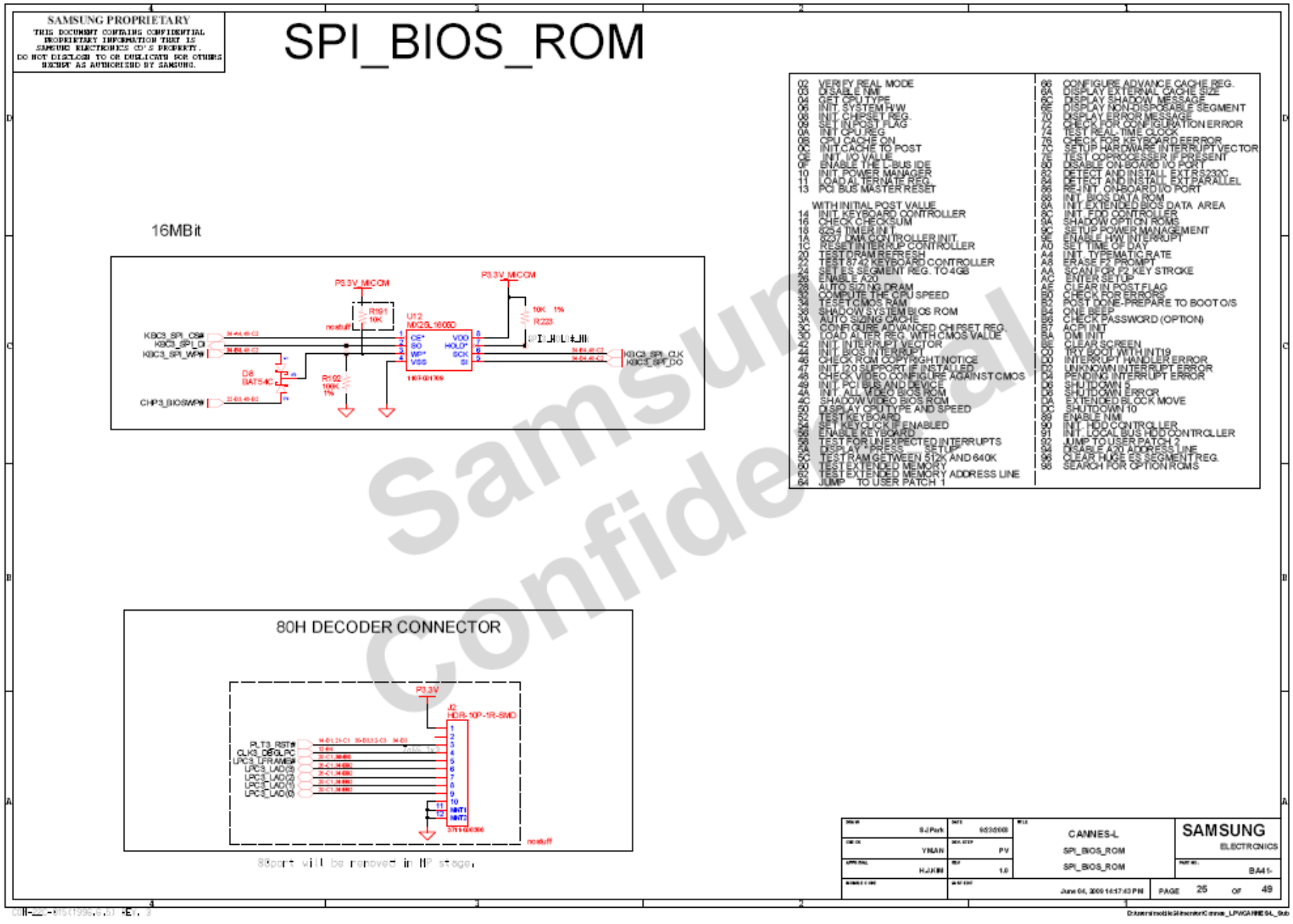


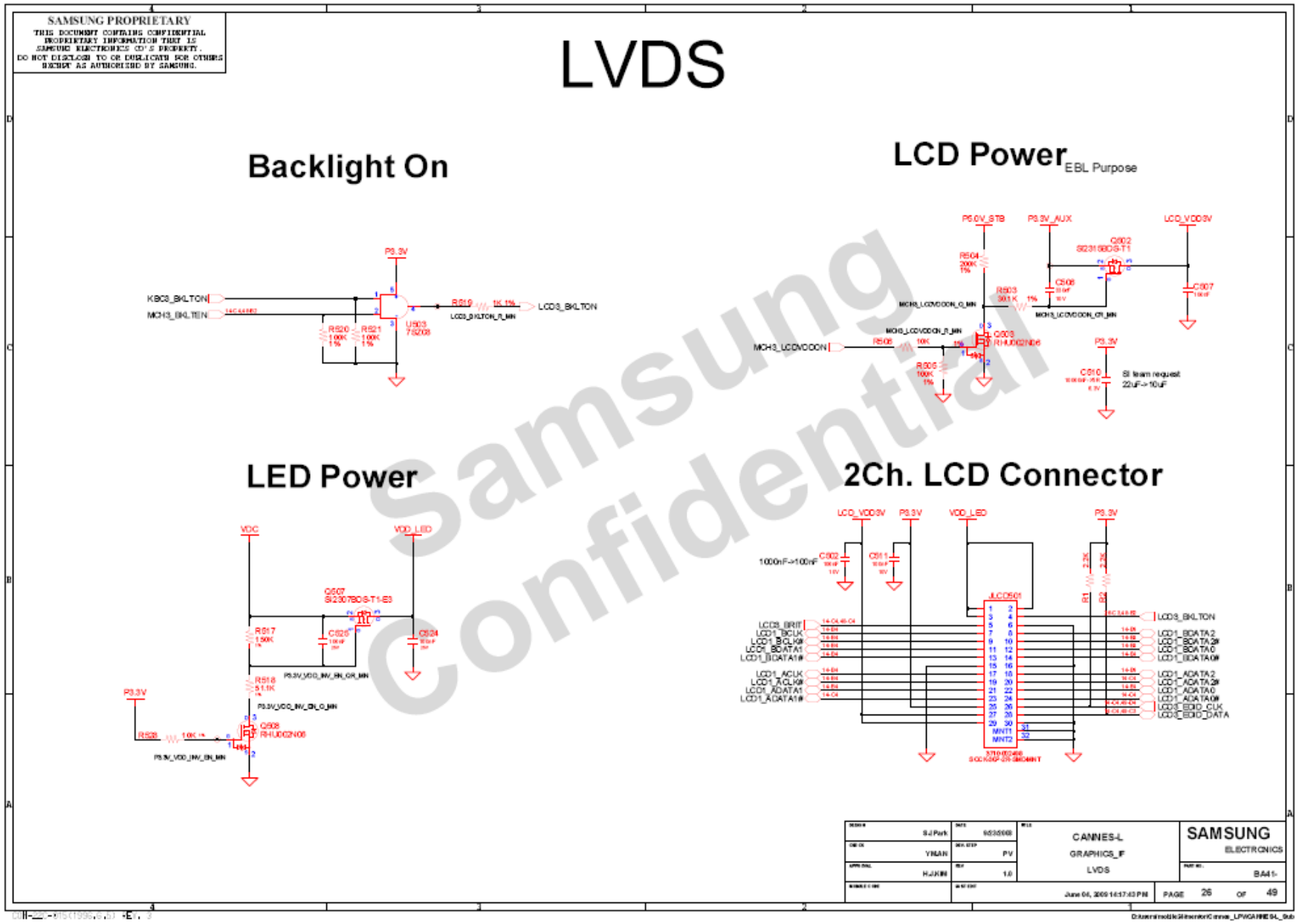
8. Block diagram and Schemetic



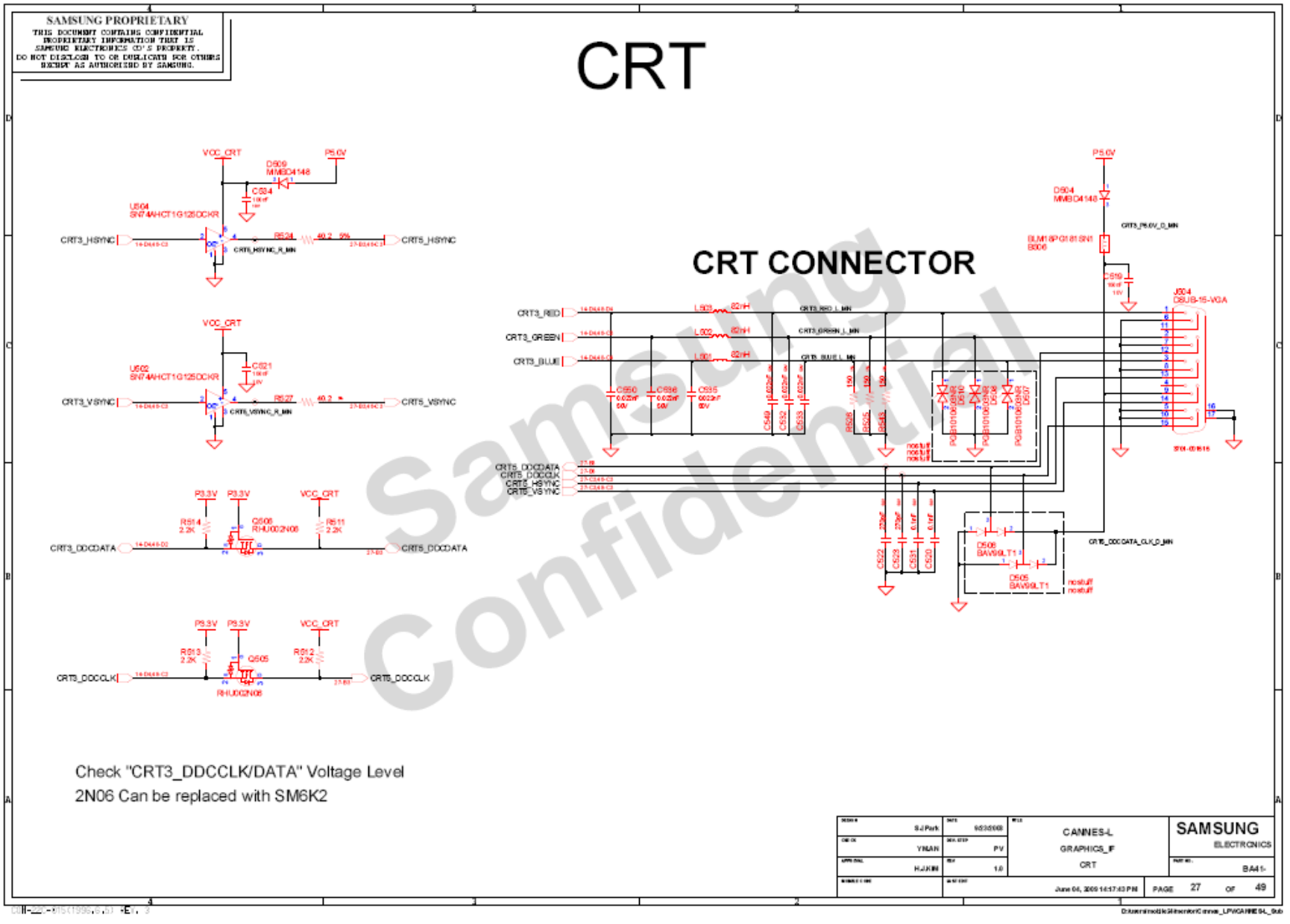
8. Block diagram and Schemetic



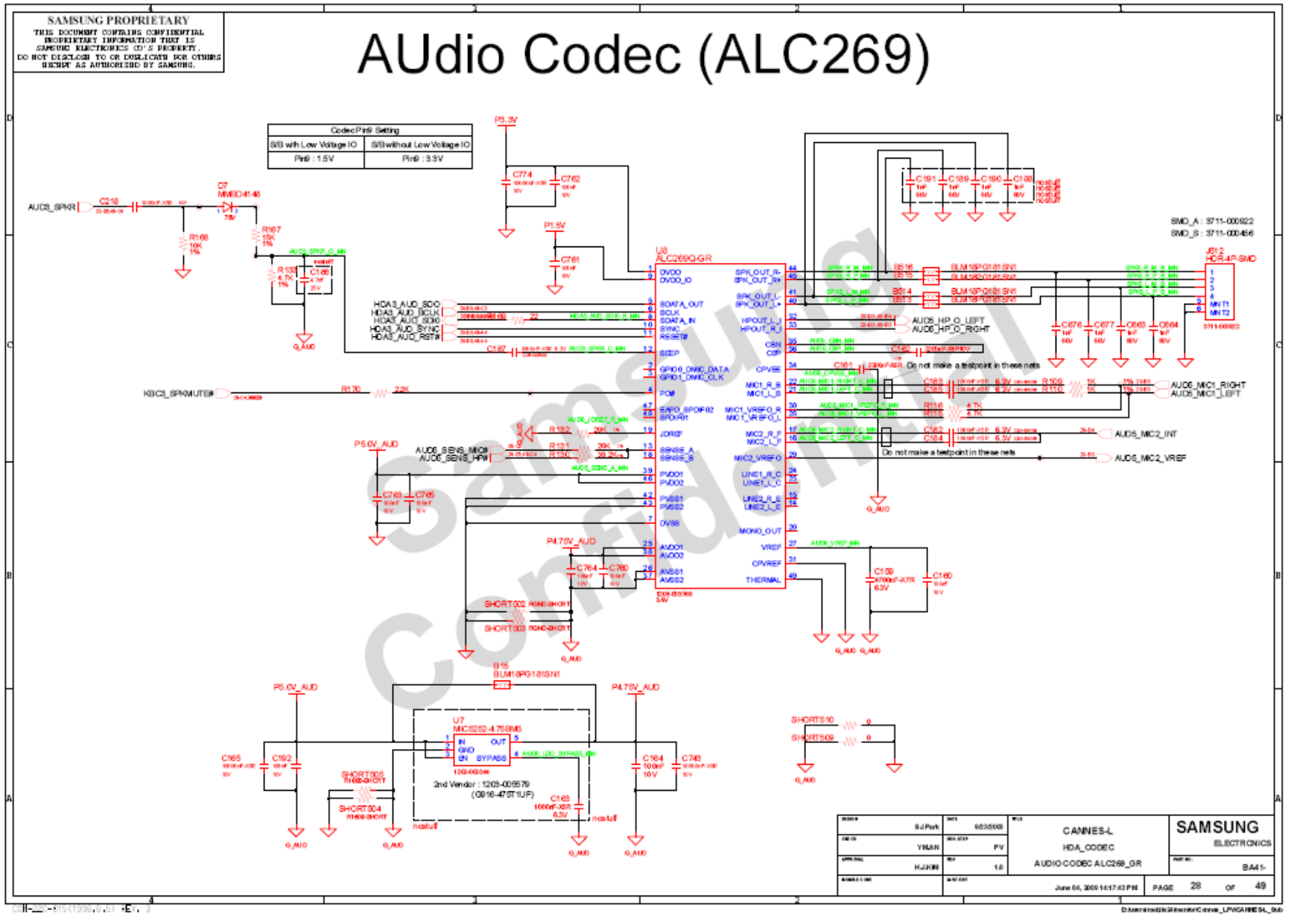




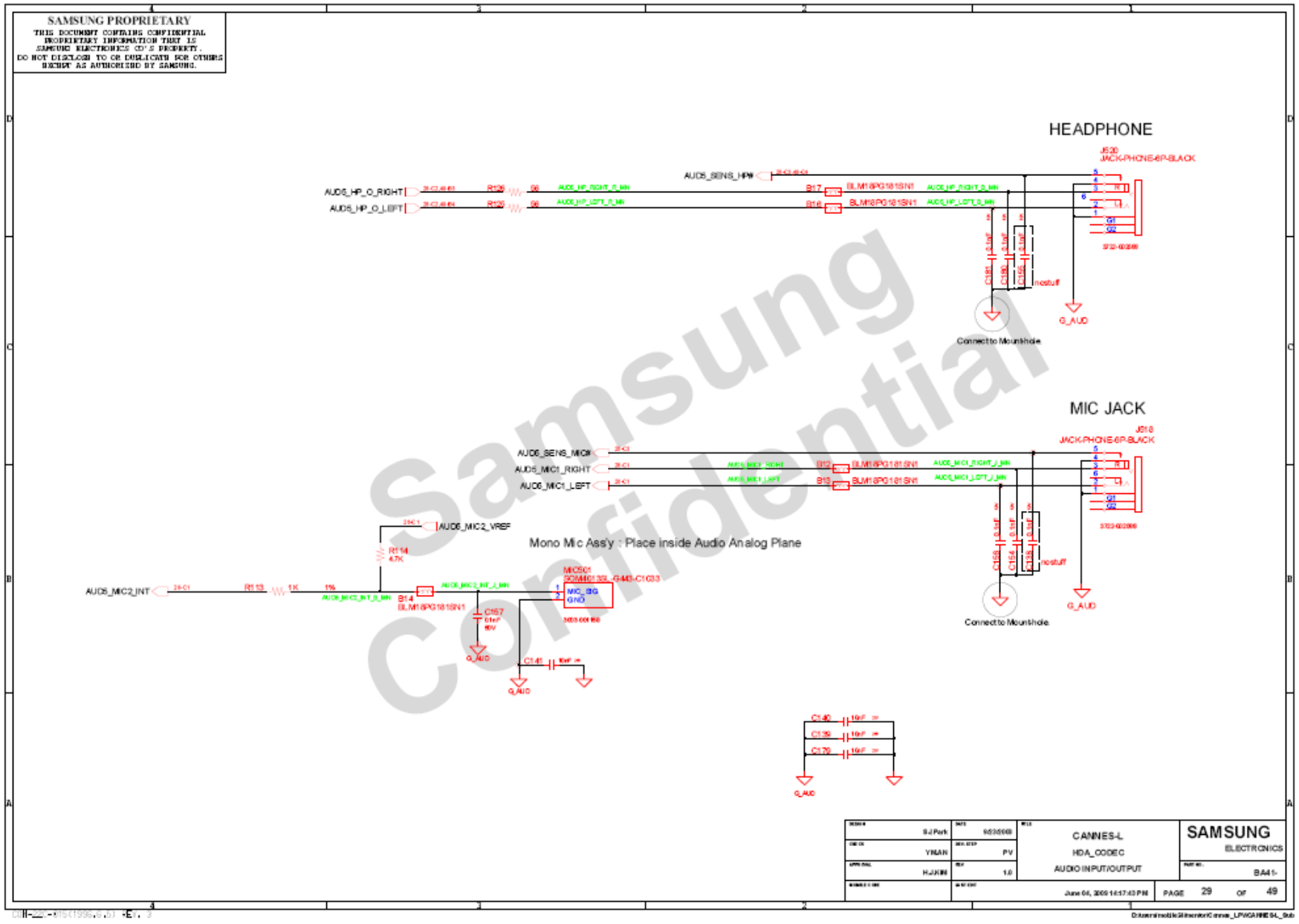
8. Block diagram and Schemetic



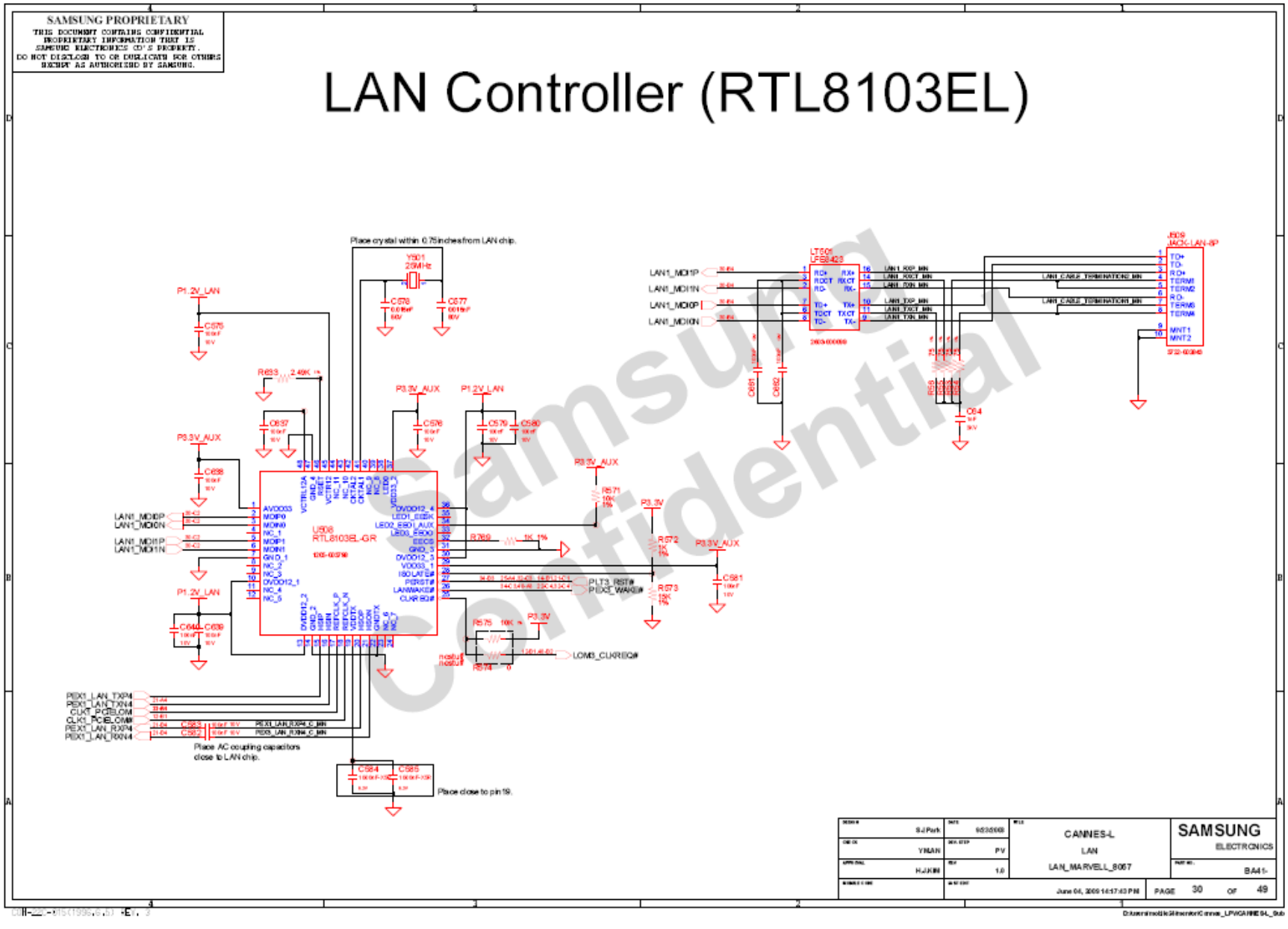
8. Block diagram and Schemetic



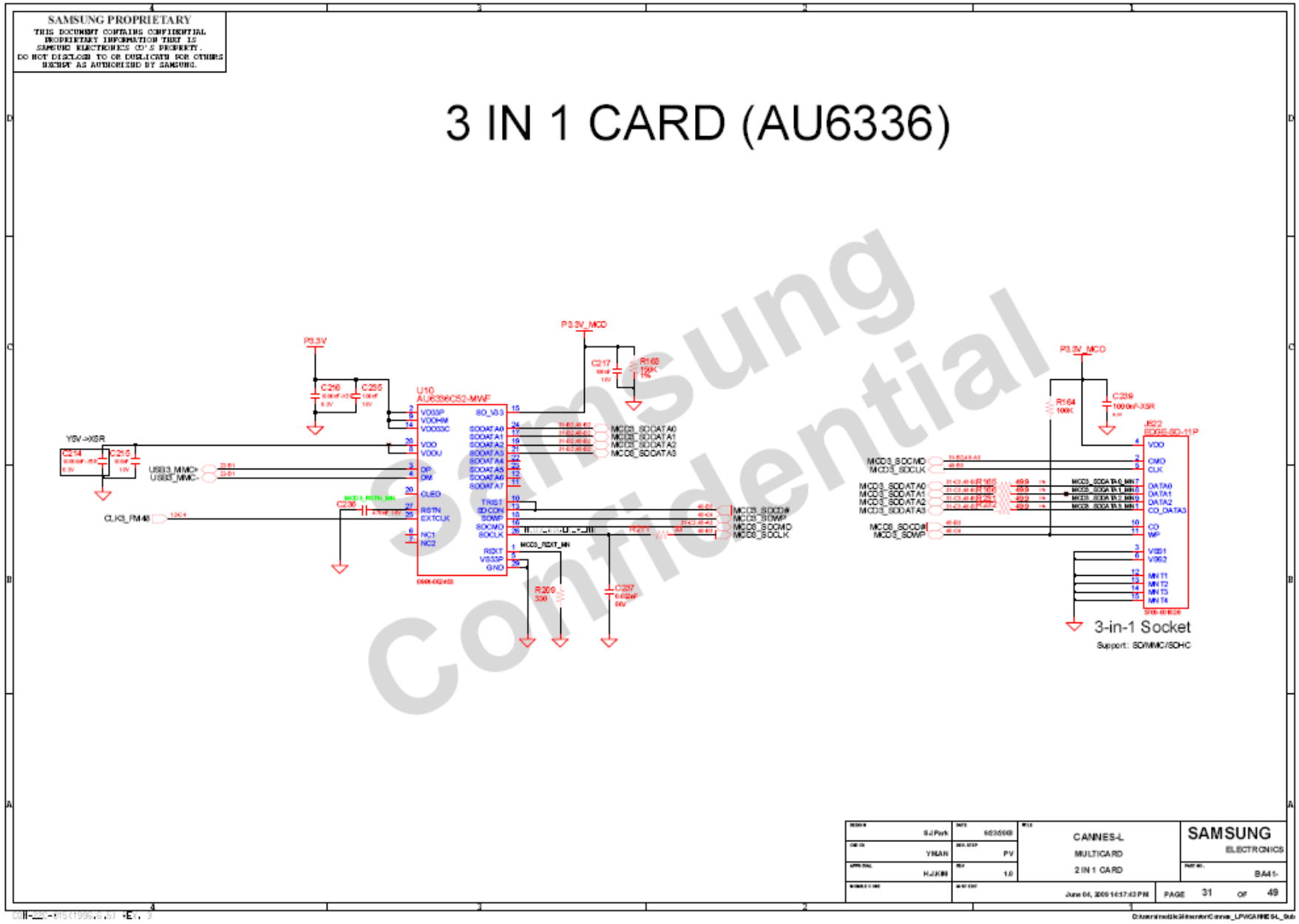
8. Block diagram and Schemetic



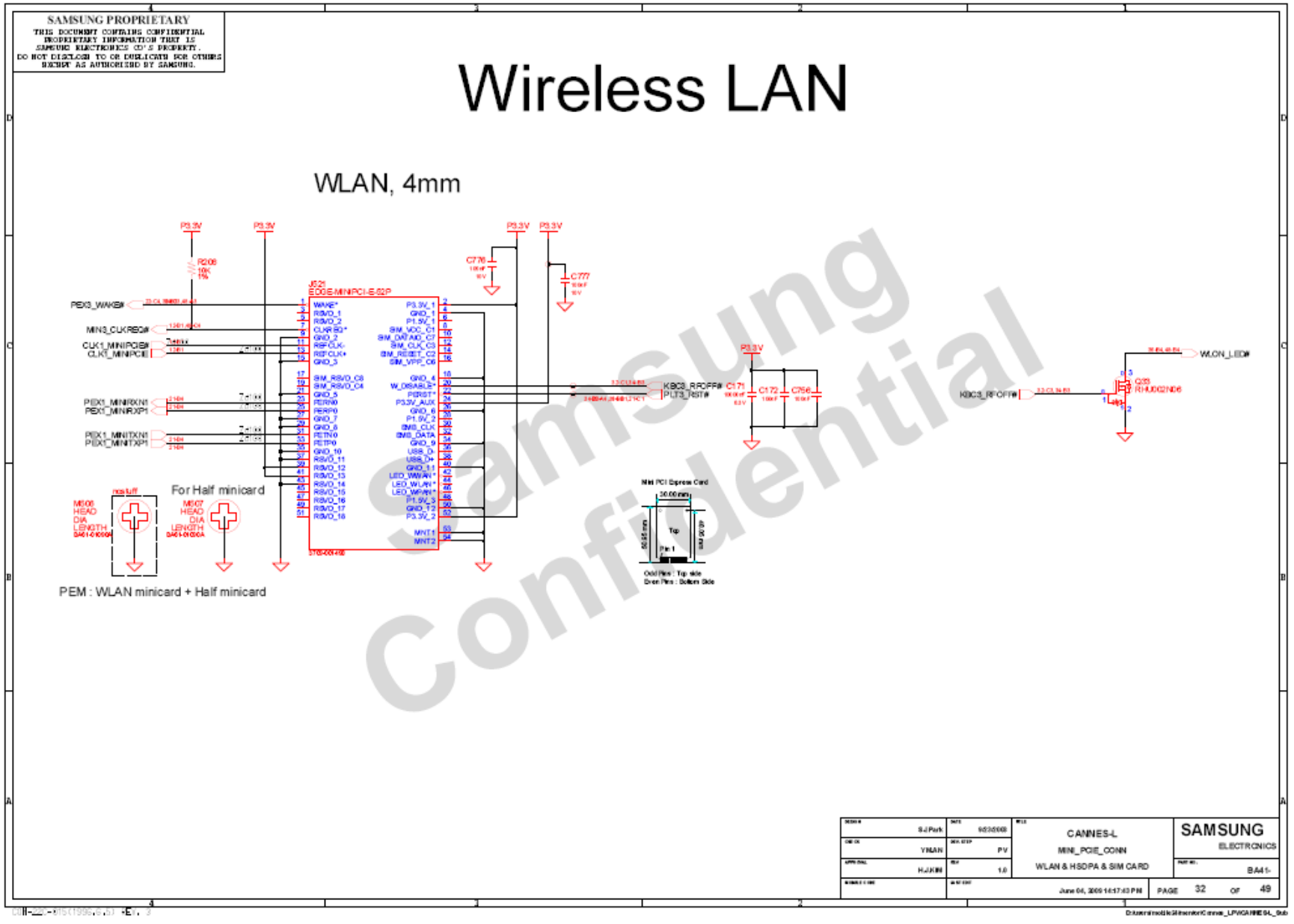
8. Block diagram and Schemetic

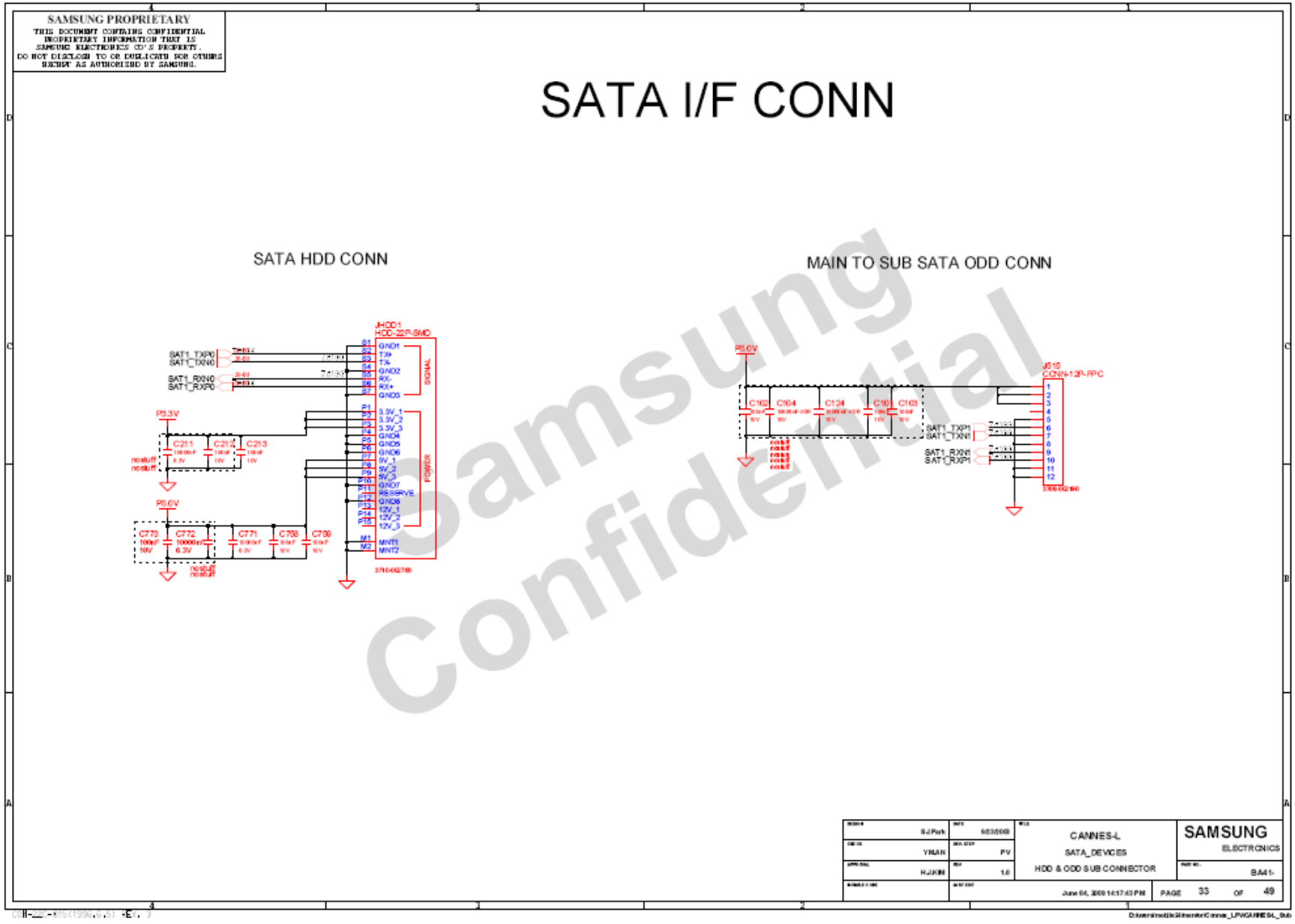


8. Block diagram and Schemetic

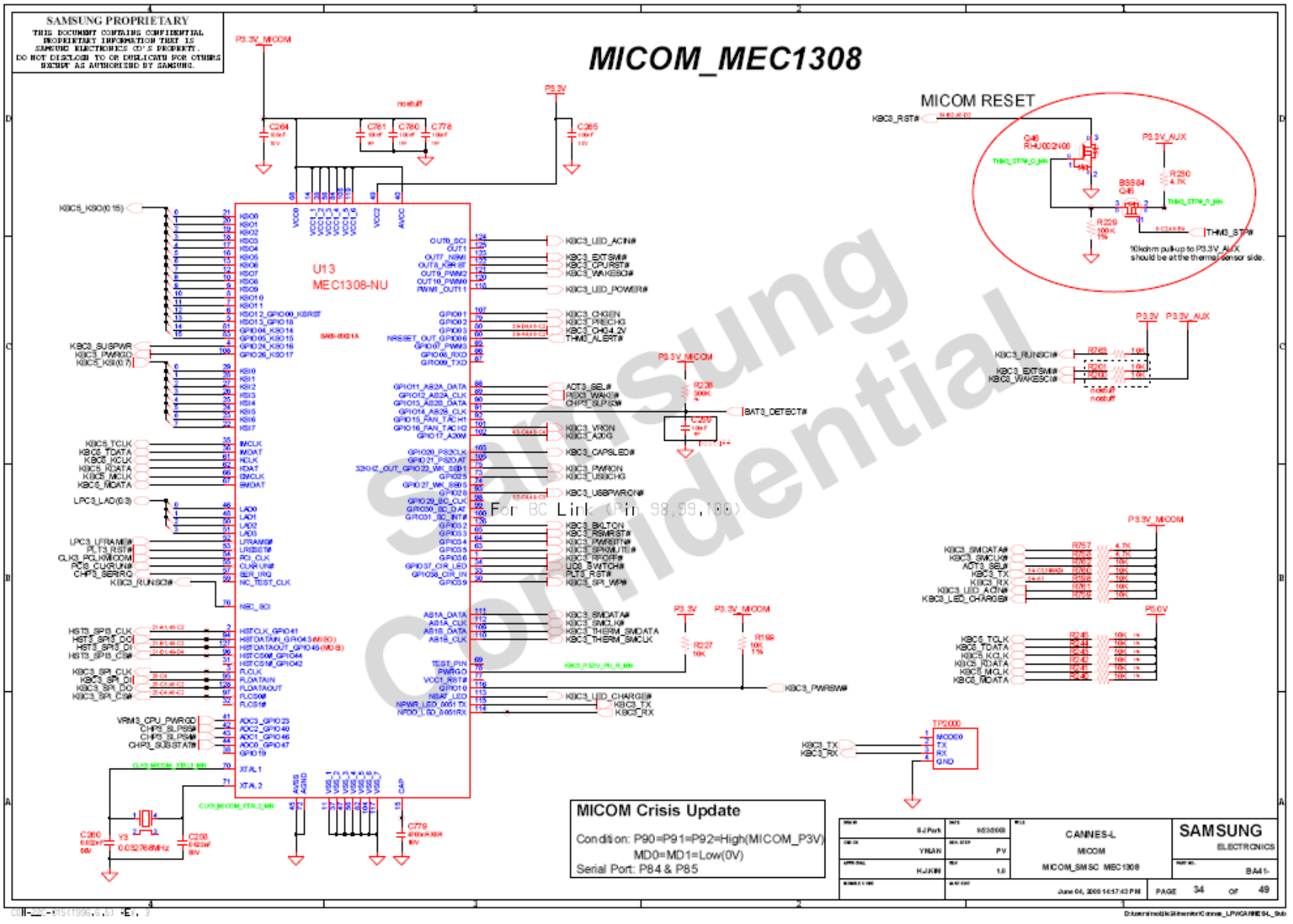


8. Block diagram and Schemetic

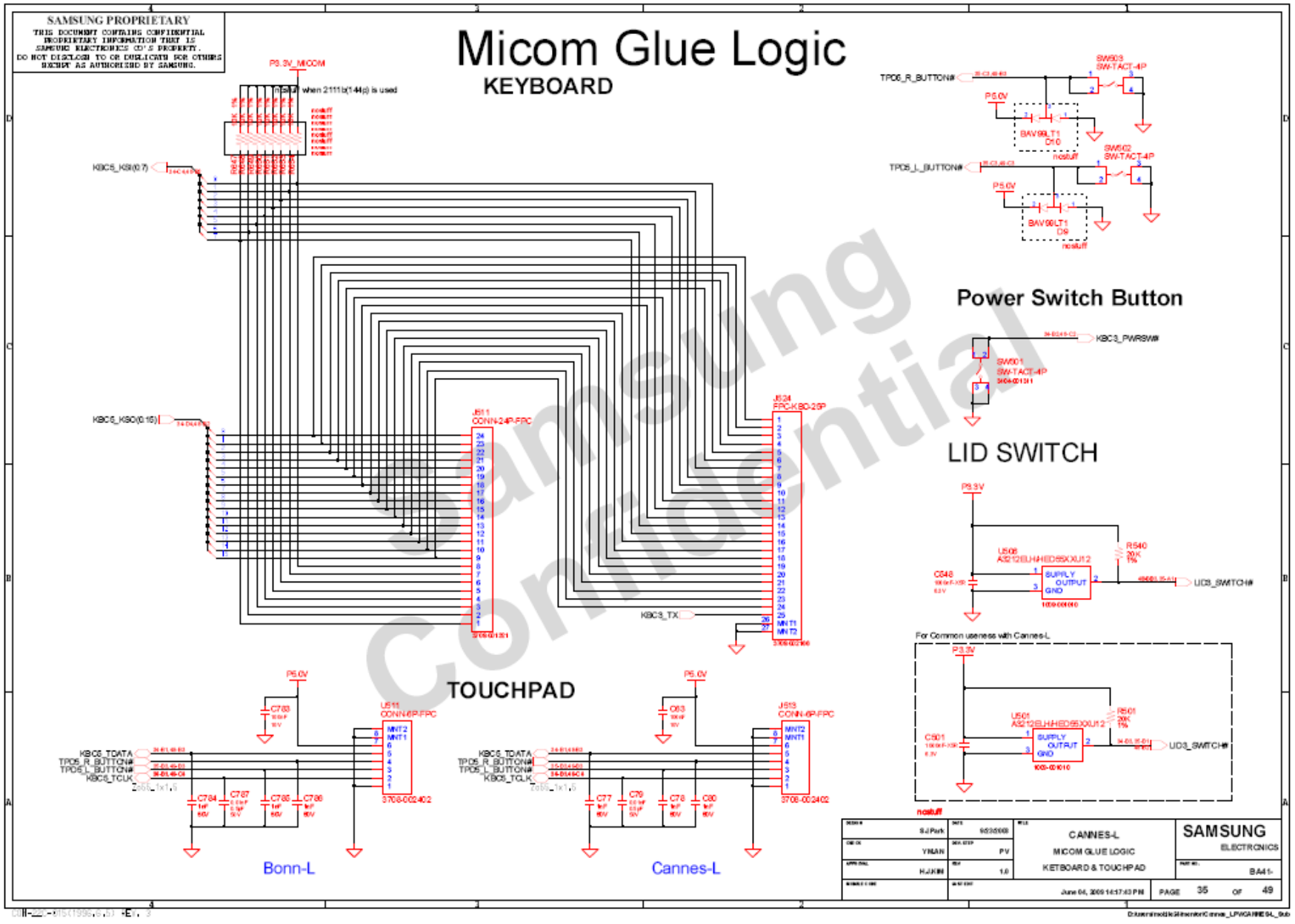


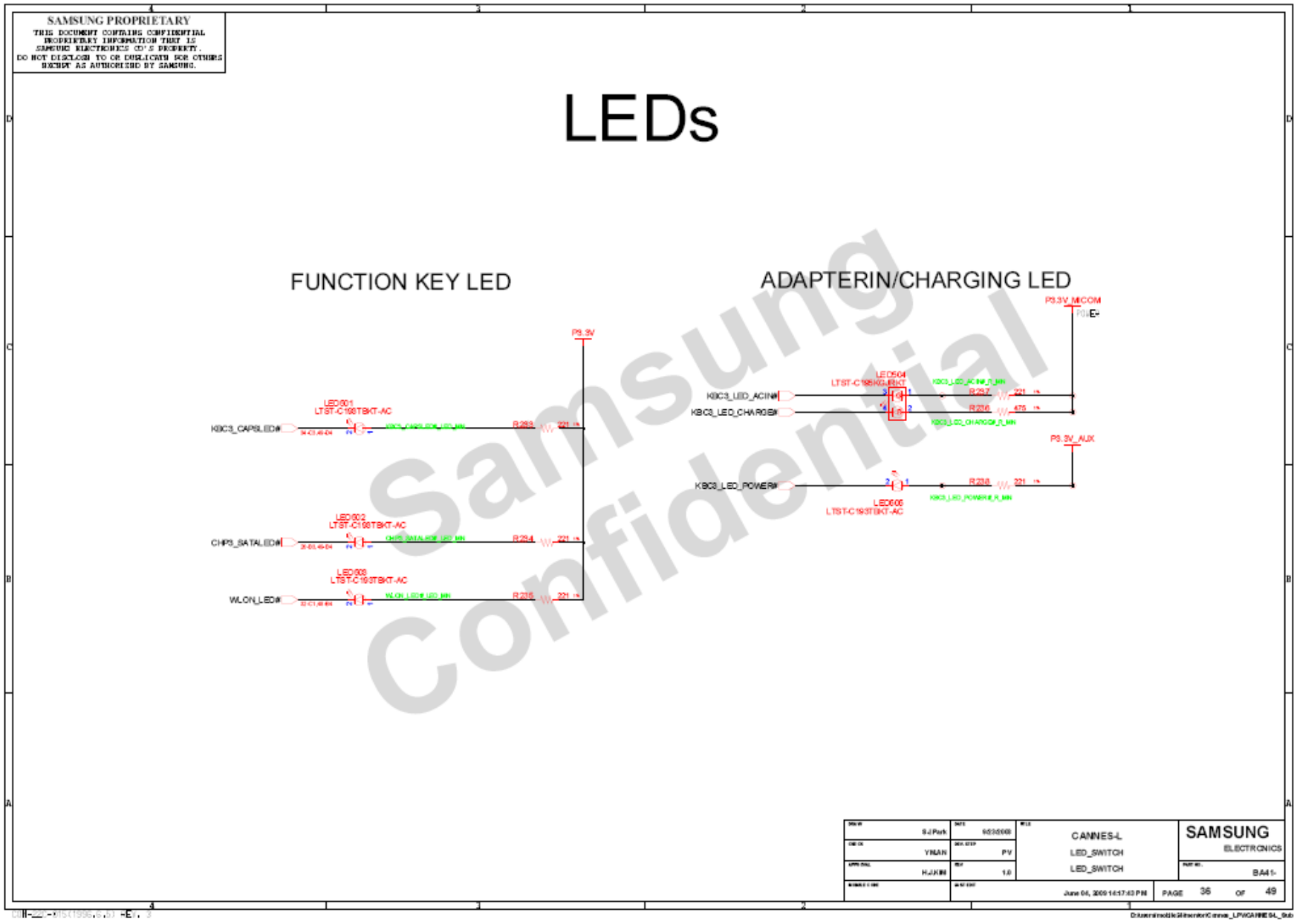


8. Block diagram and Schemetic

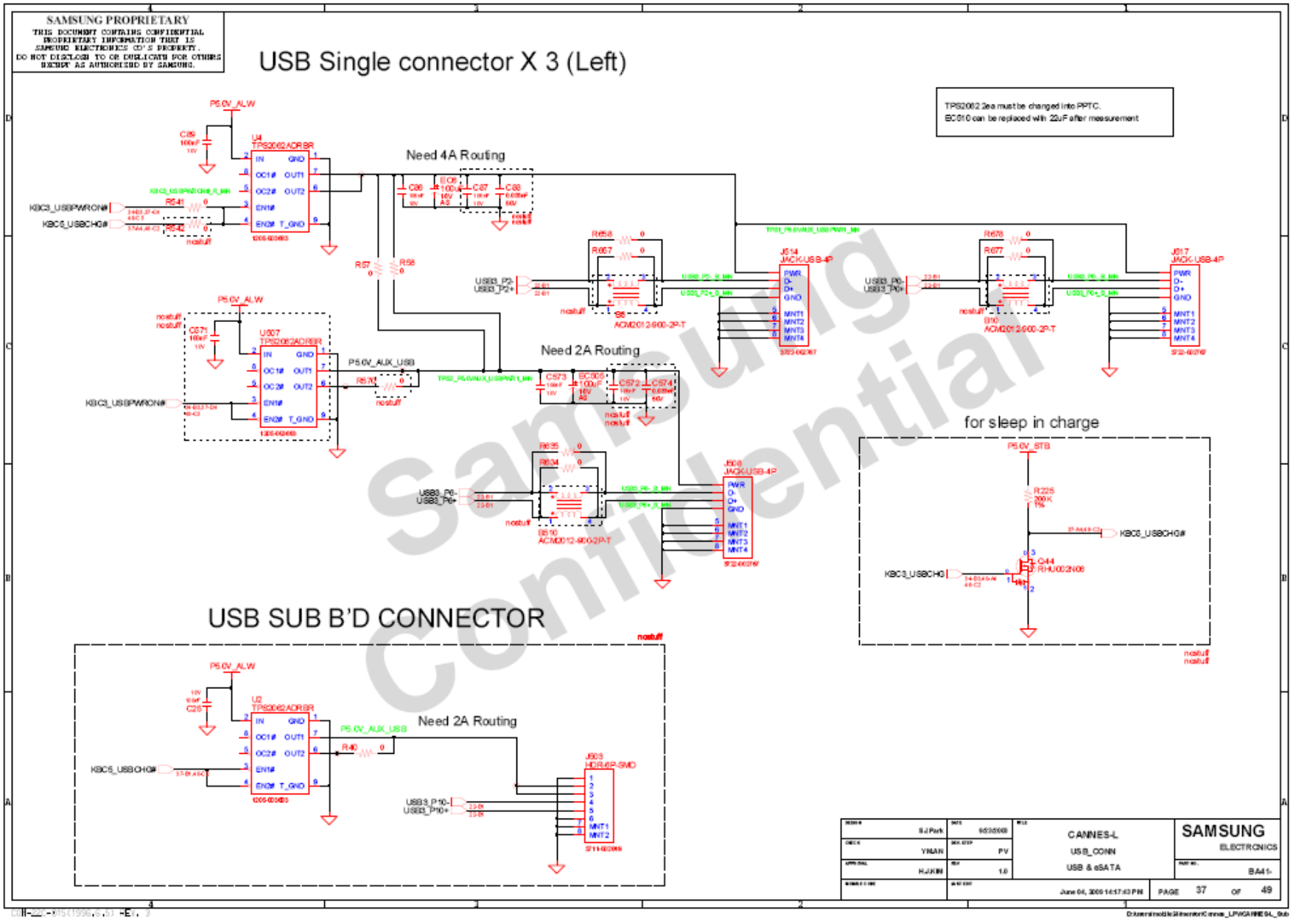


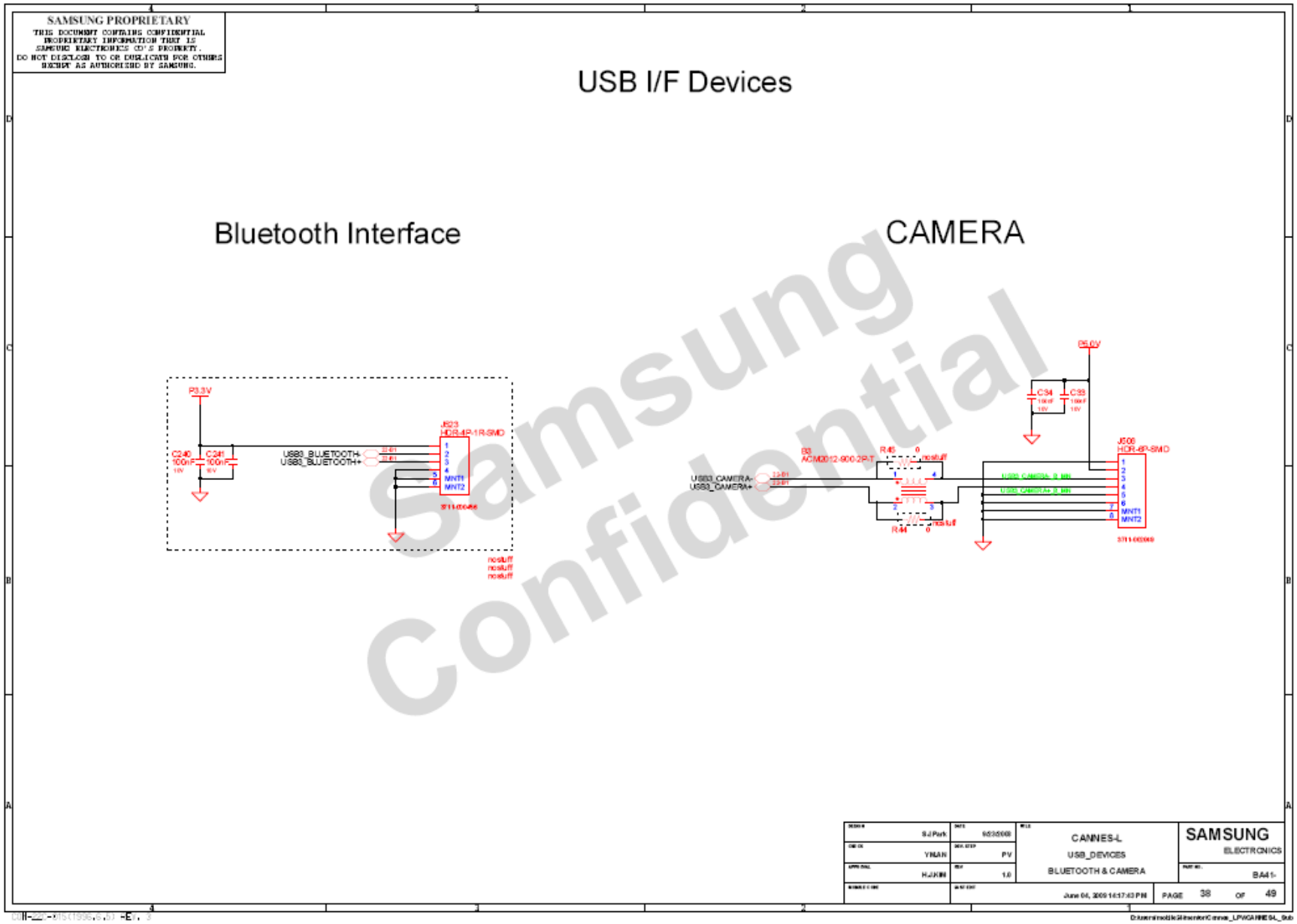
8. Block diagram and Schemetic



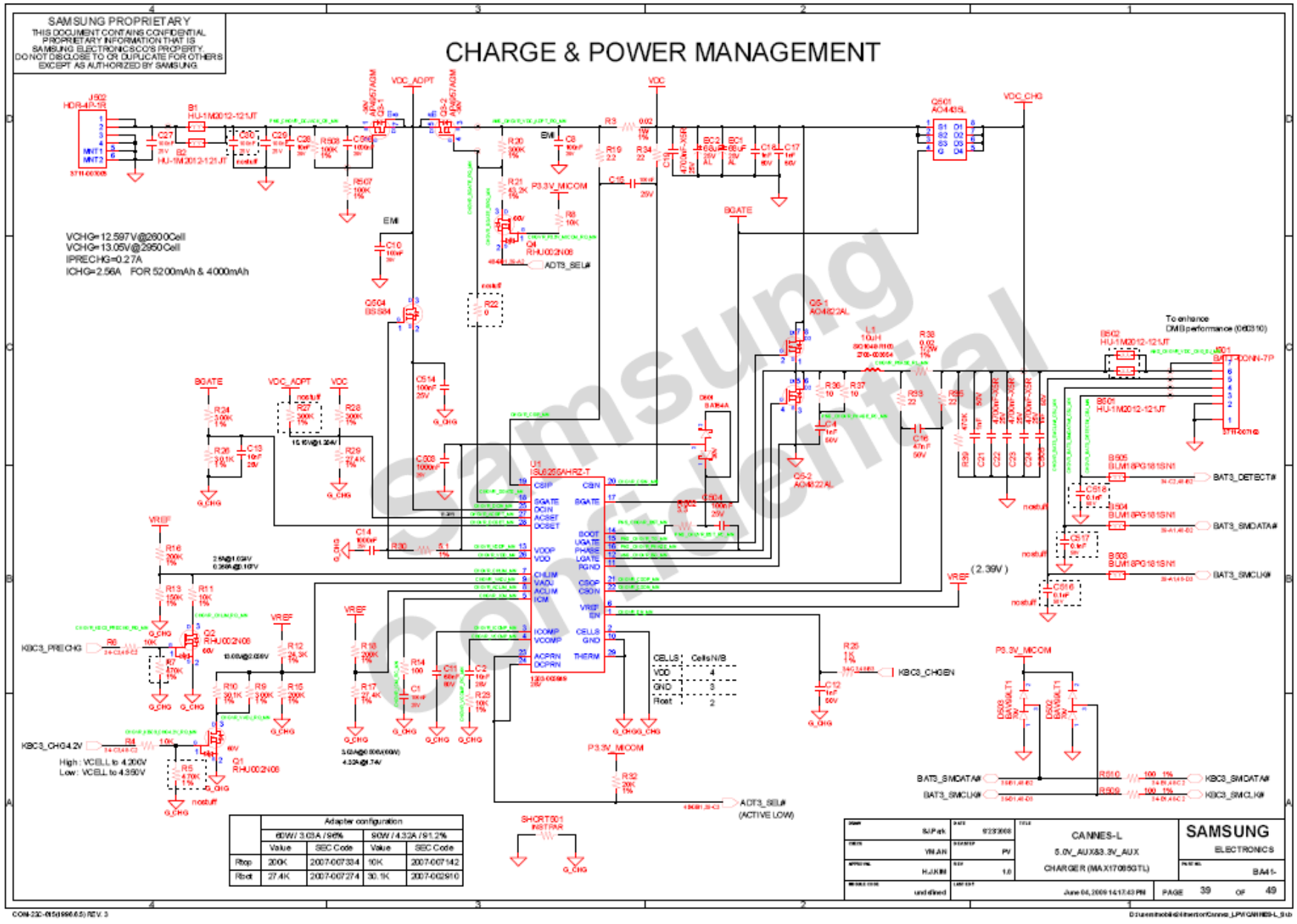


8. Block diagram and Schemetic

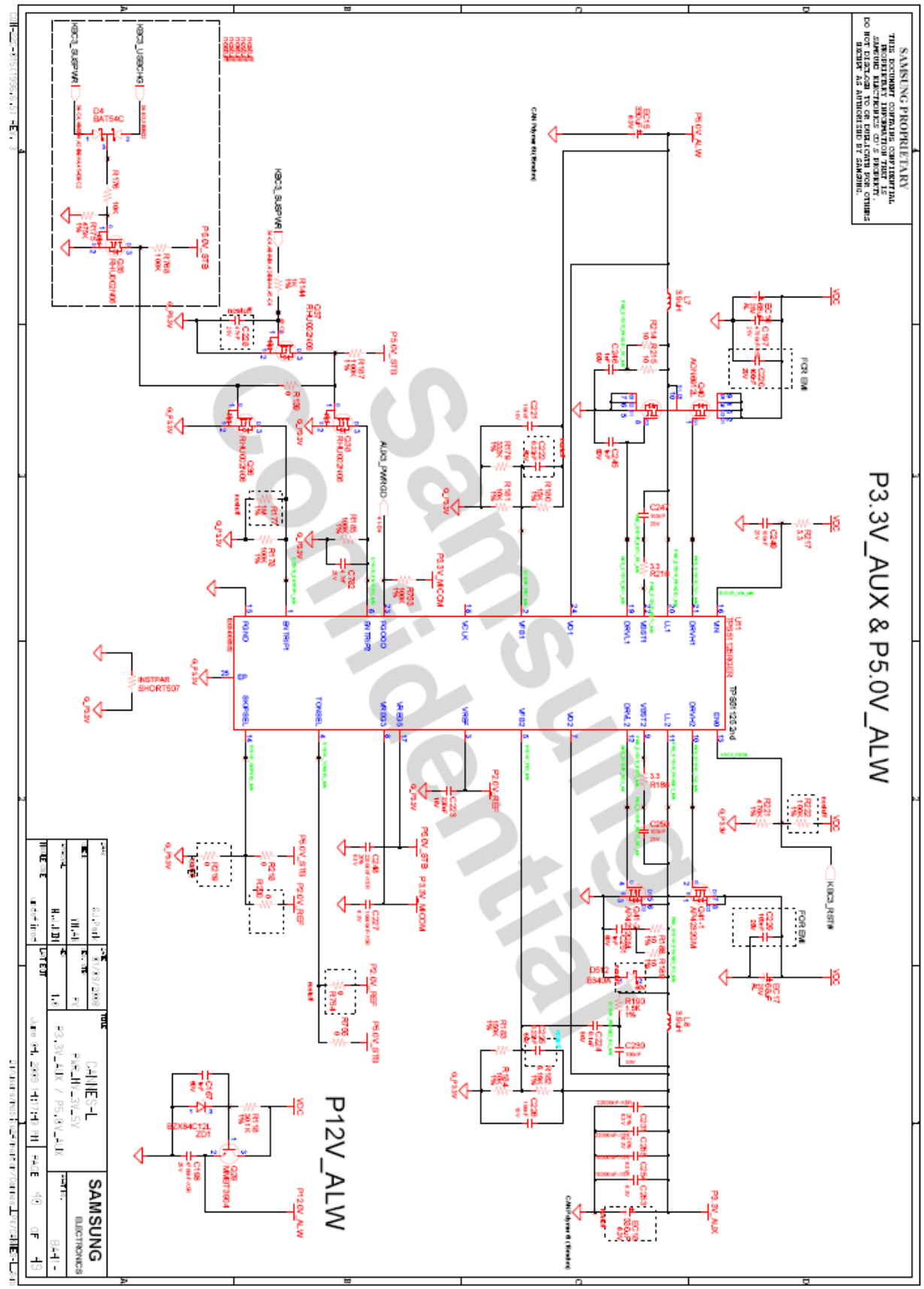




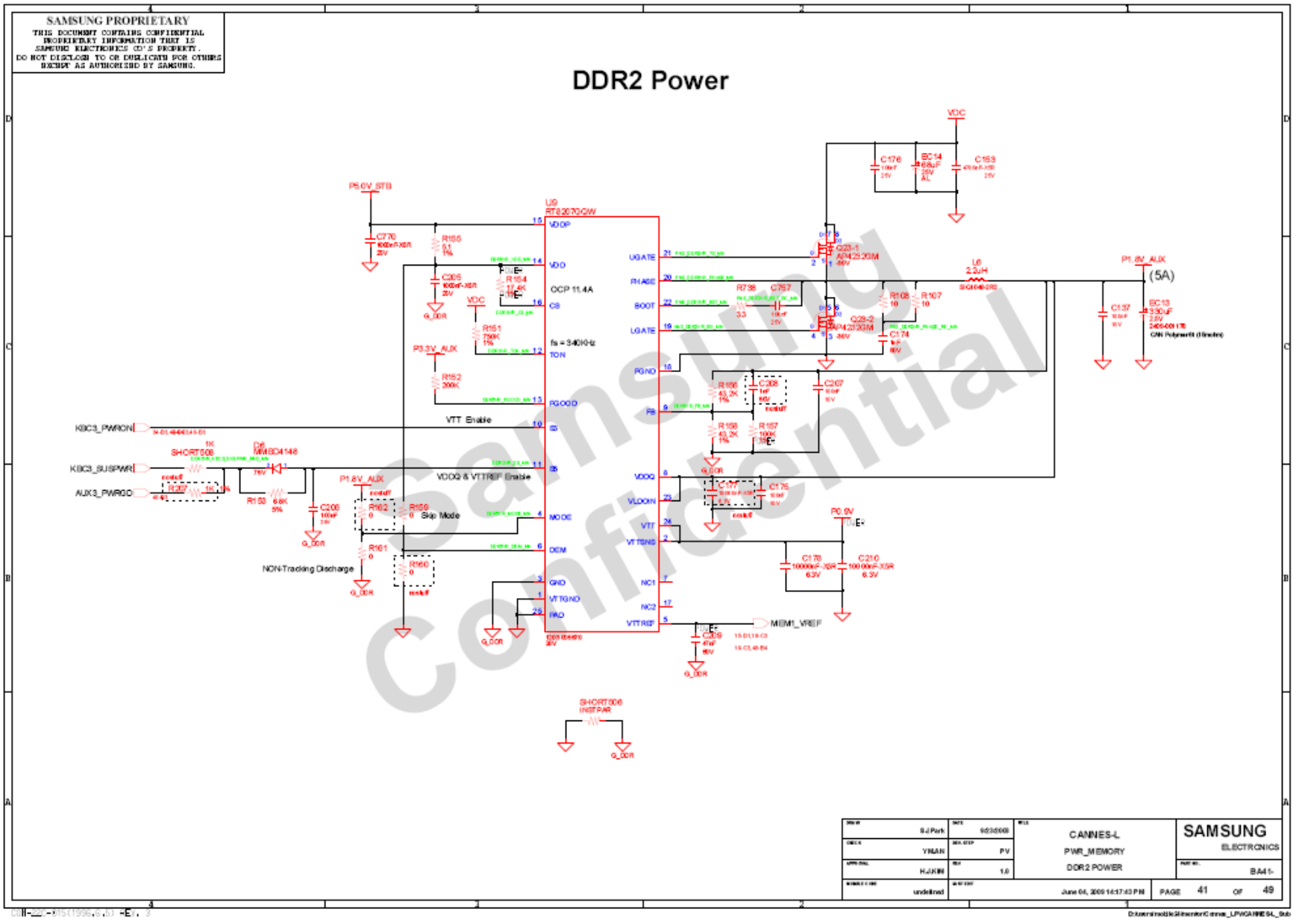
8. Block diagram and Schemetic



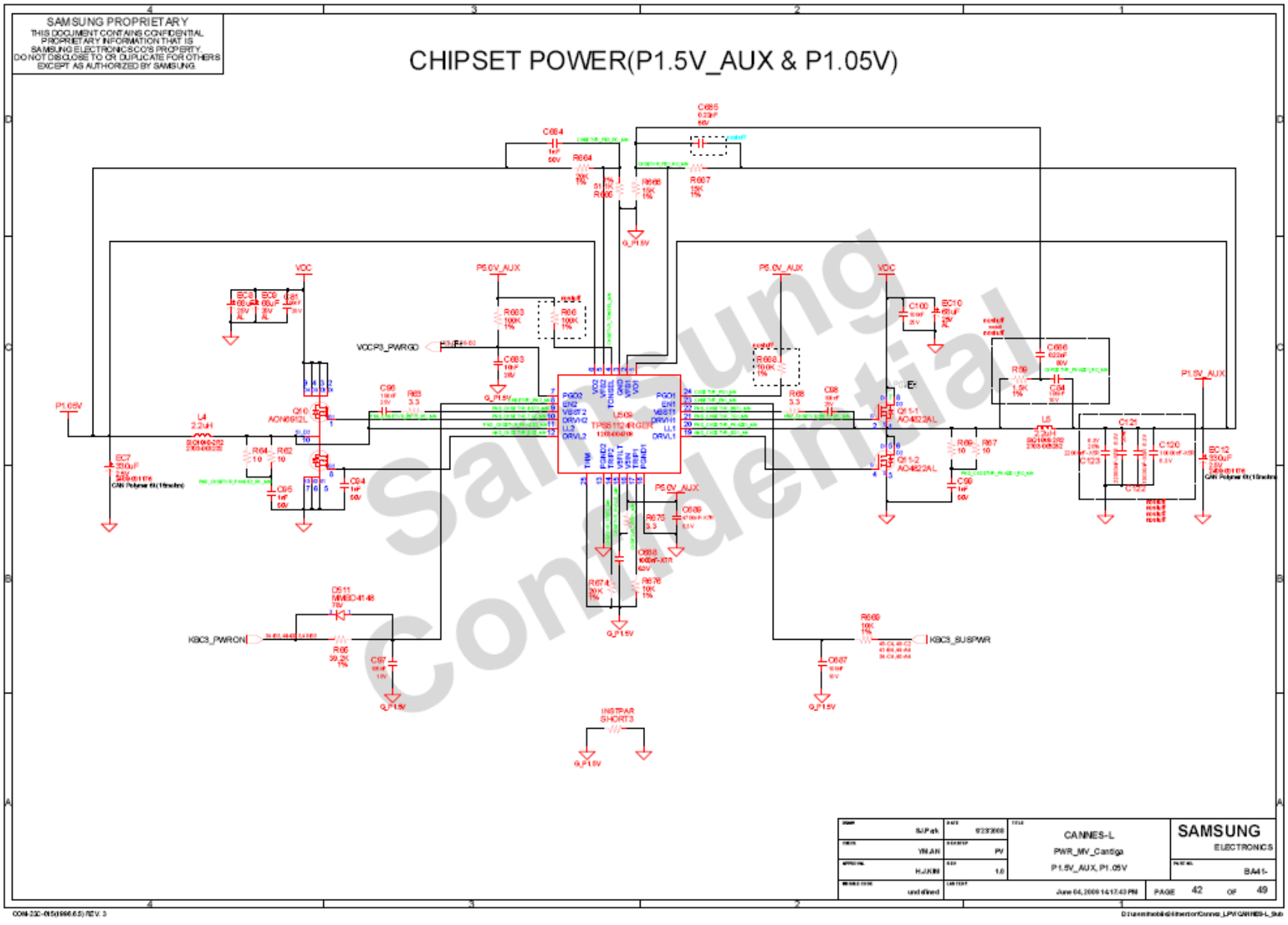
8. Block diagram and Schemetic



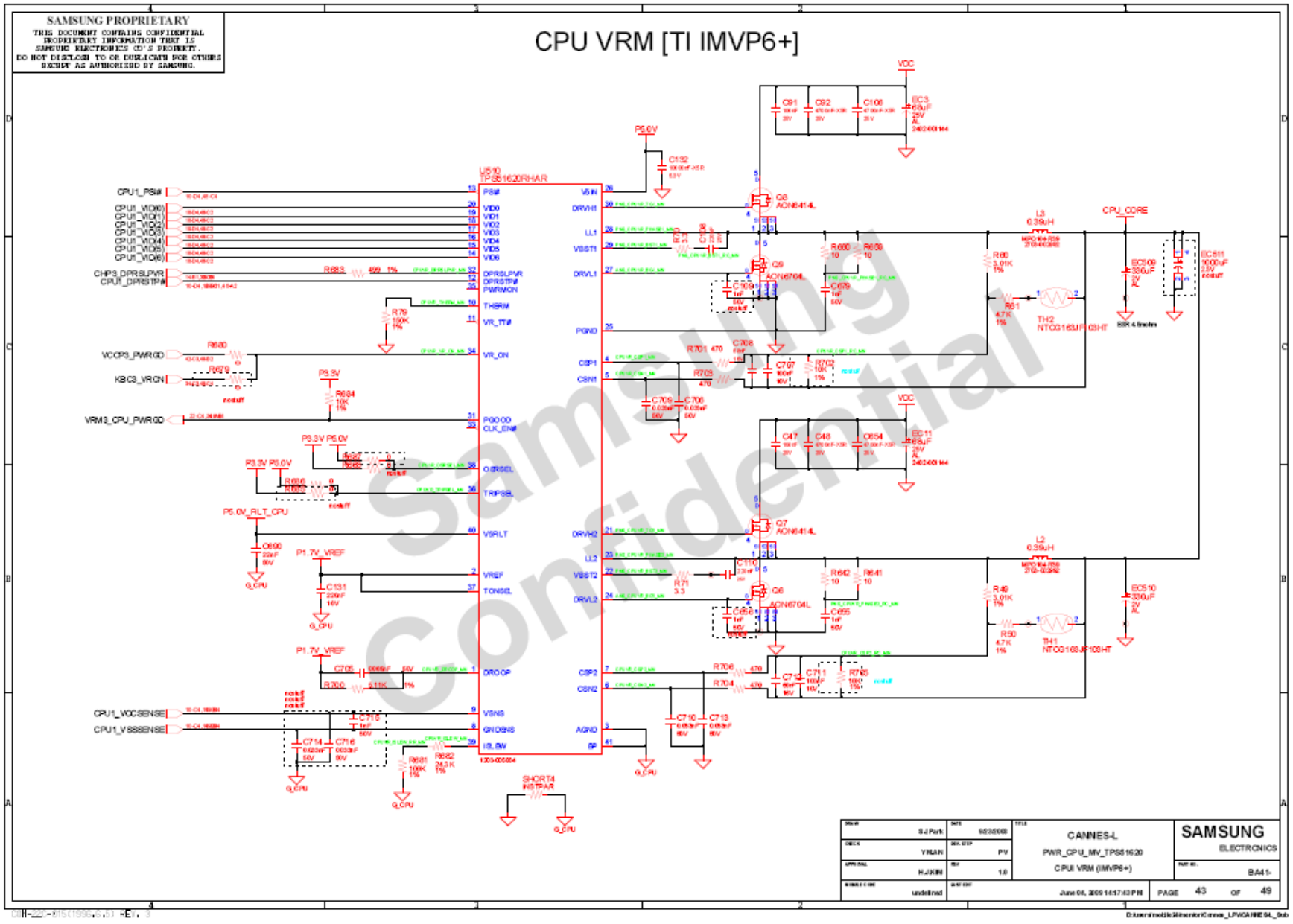
8. Block diagram and Schemetic



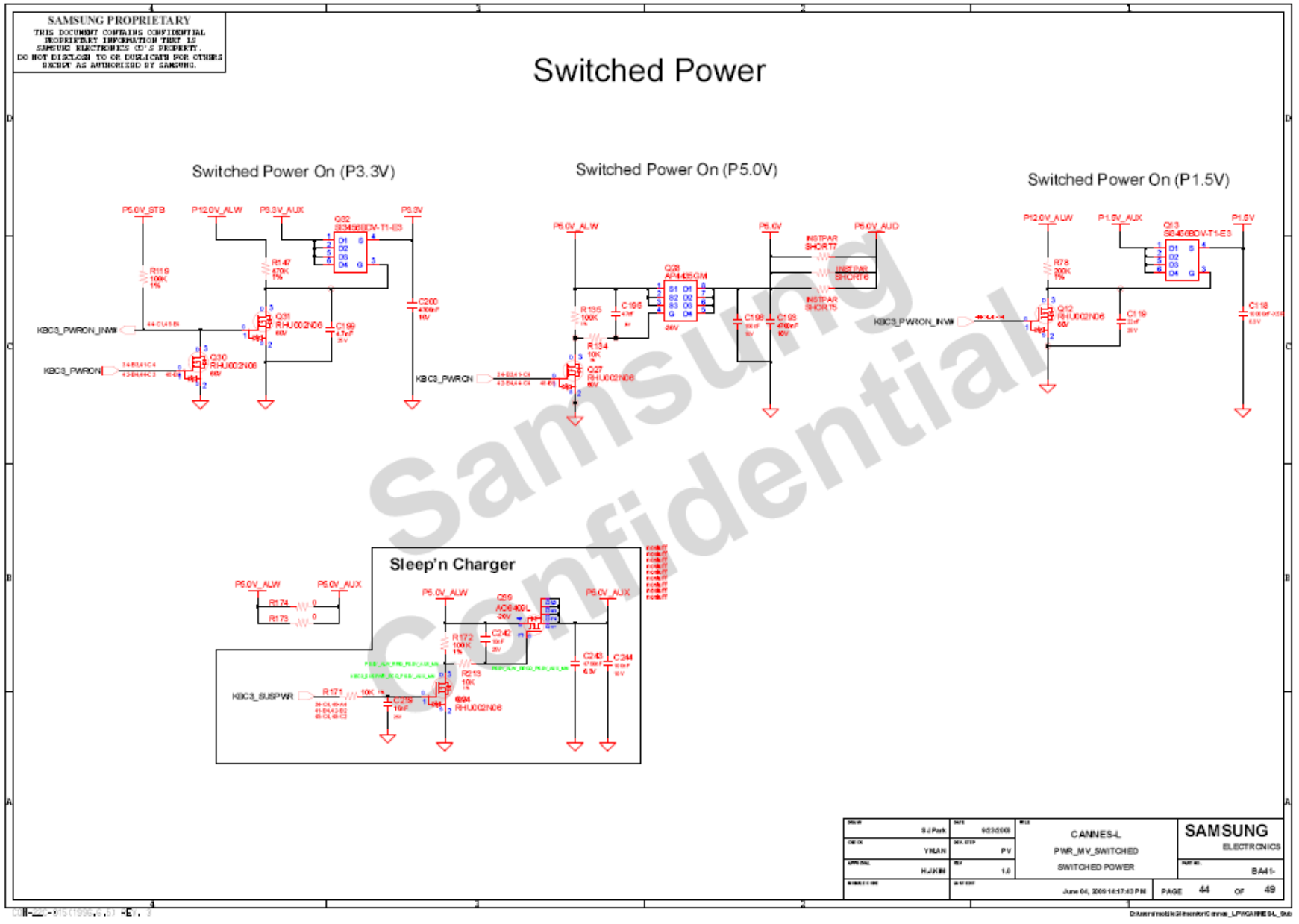
8. Block diagram and Schemetic



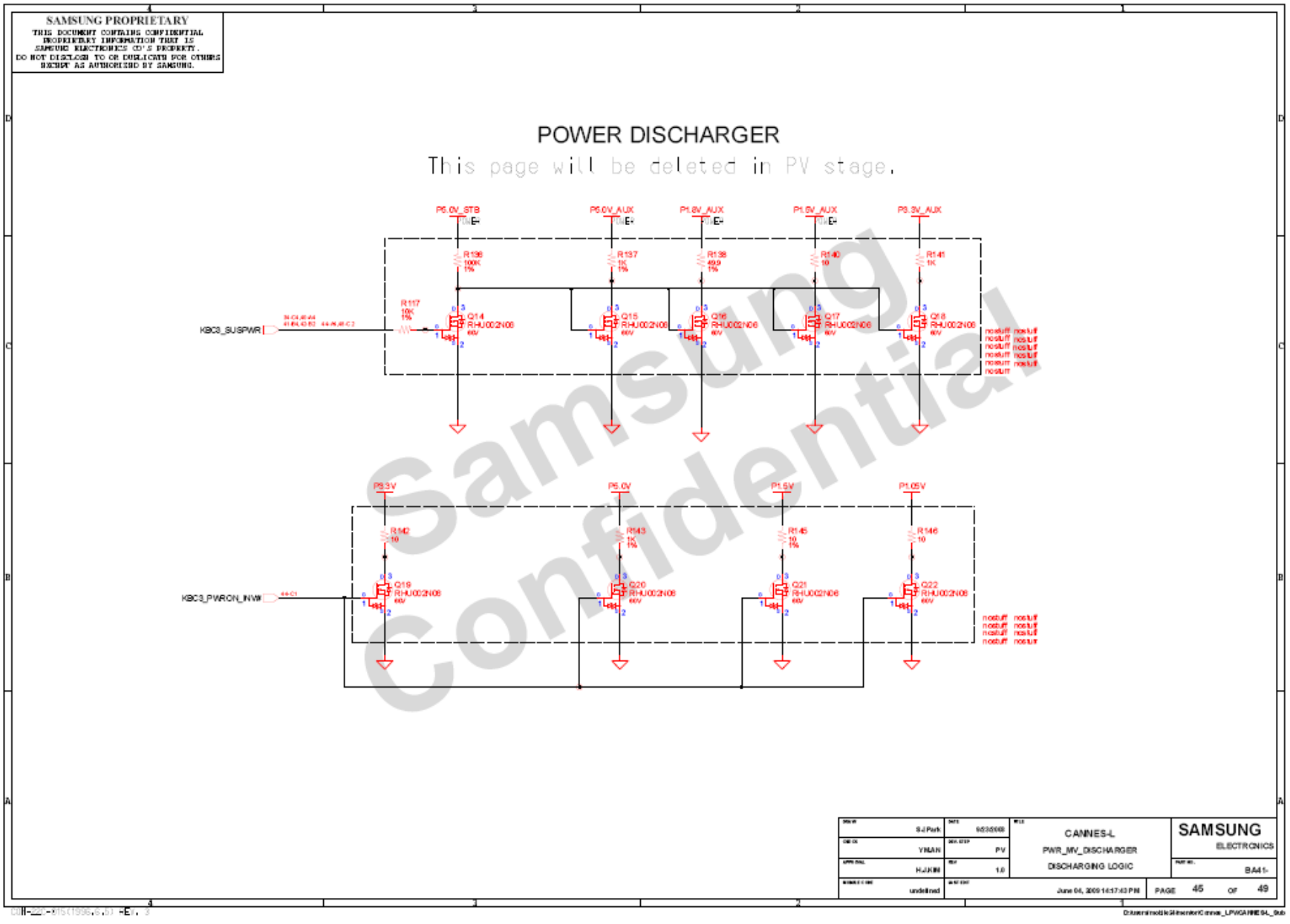
8. Block diagram and Schemetic



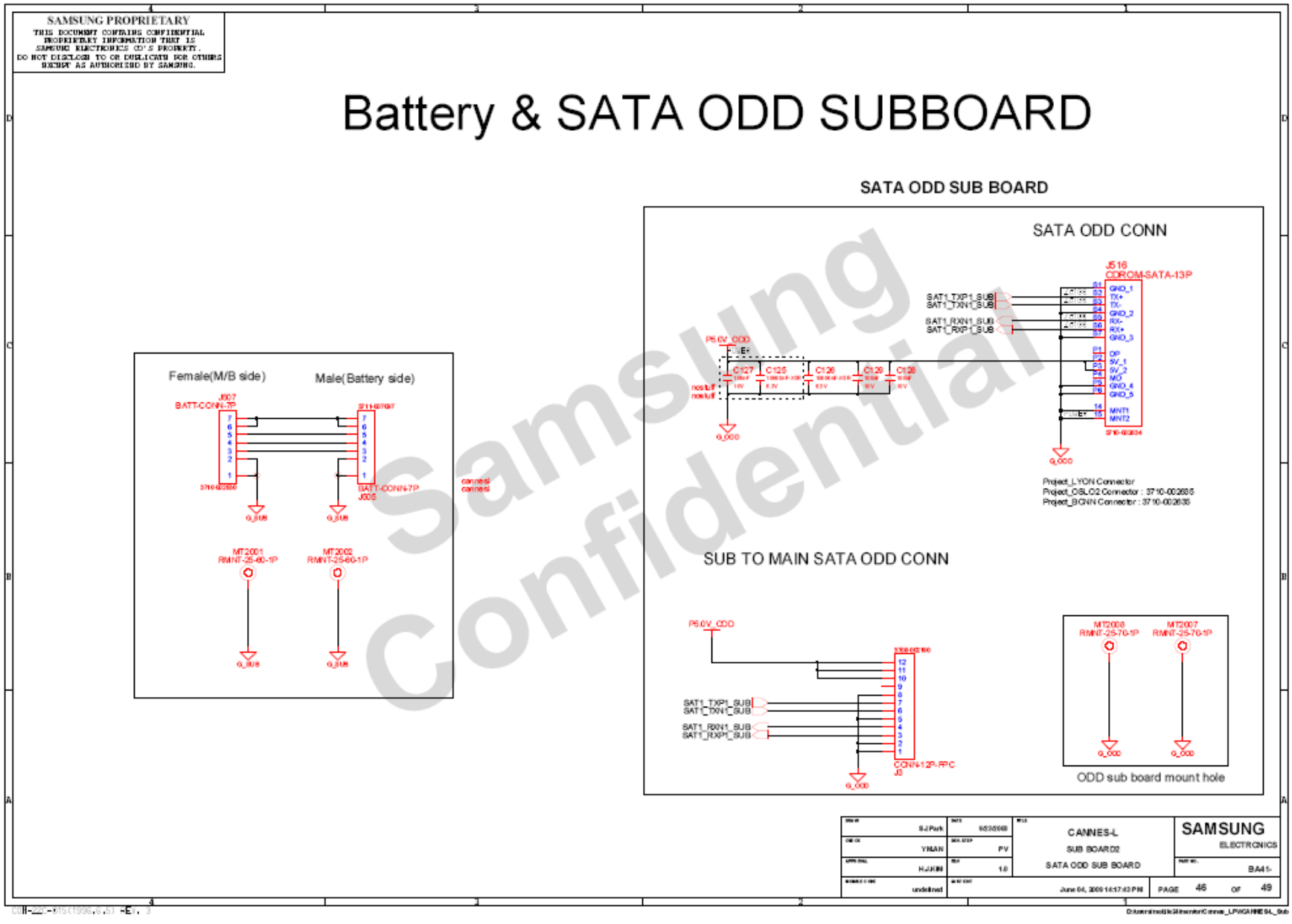
8. Block diagram and Schemetic



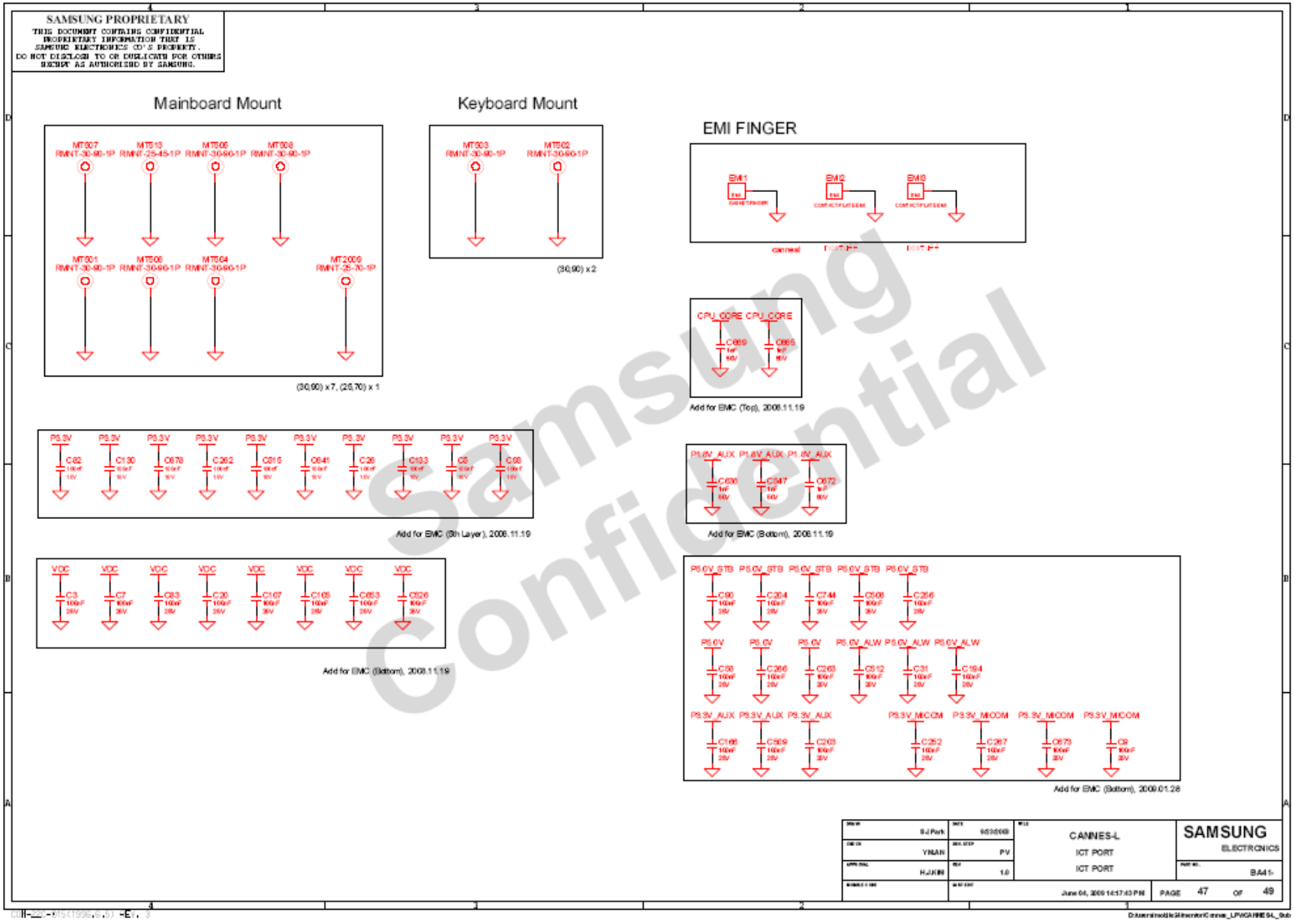
8. Block diagram and Schemetic



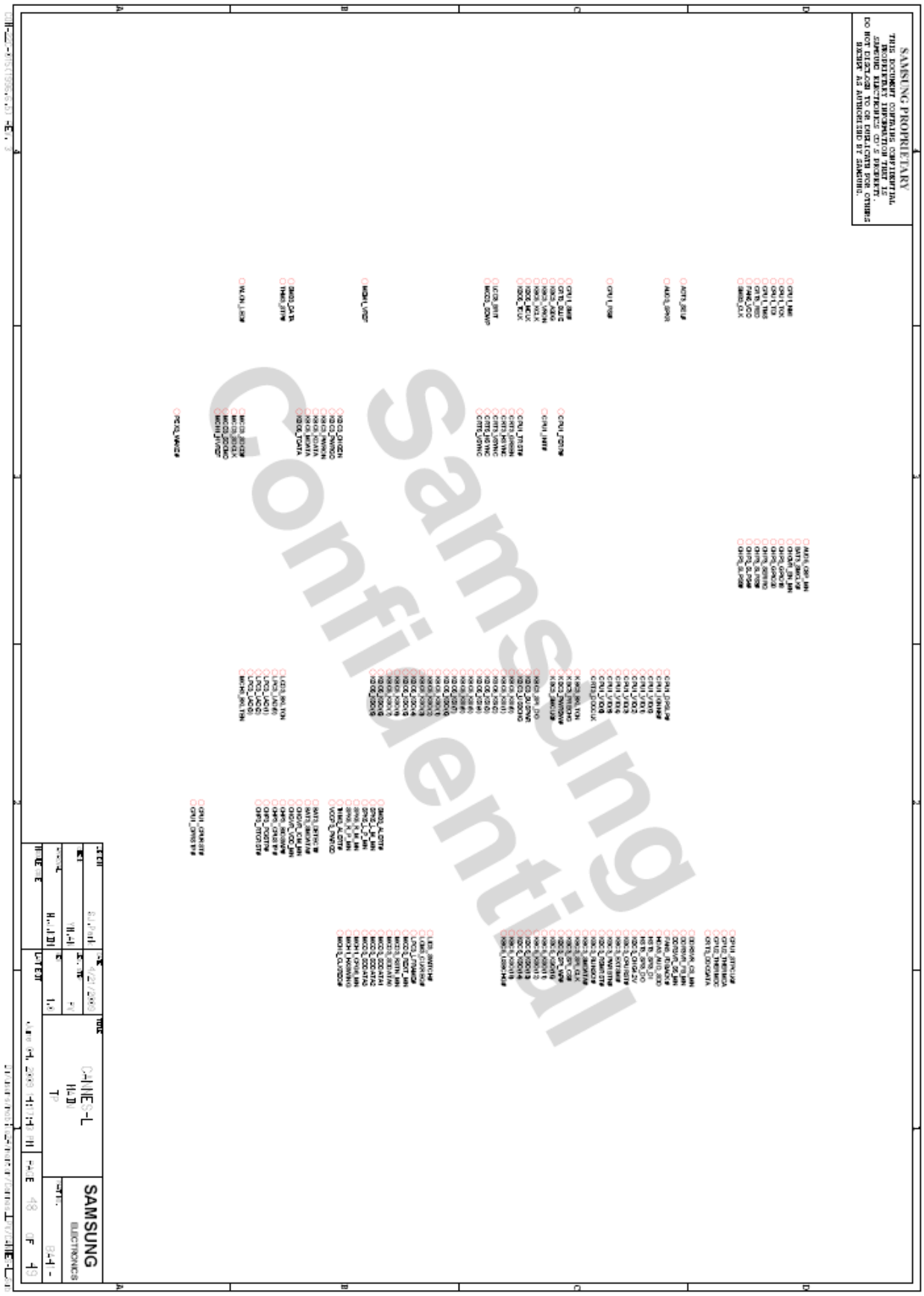
8. Block diagram and Schemetic



8. Block diagram and Schemetic



8. Block diagram and Schemetic



8. Block diagram and Schemetic