

DDD Discrete GDDR2

PV Build

2007.06.15

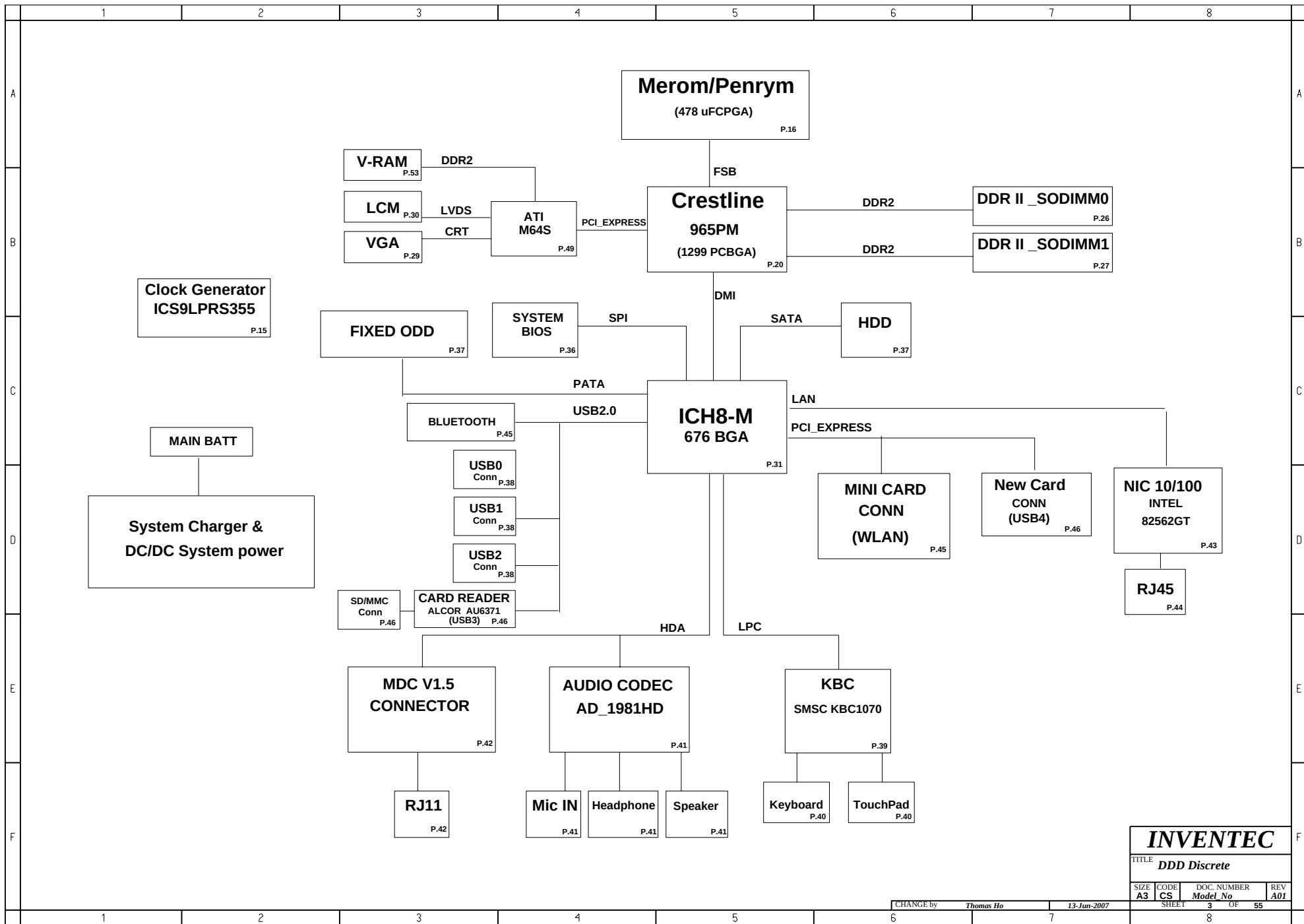
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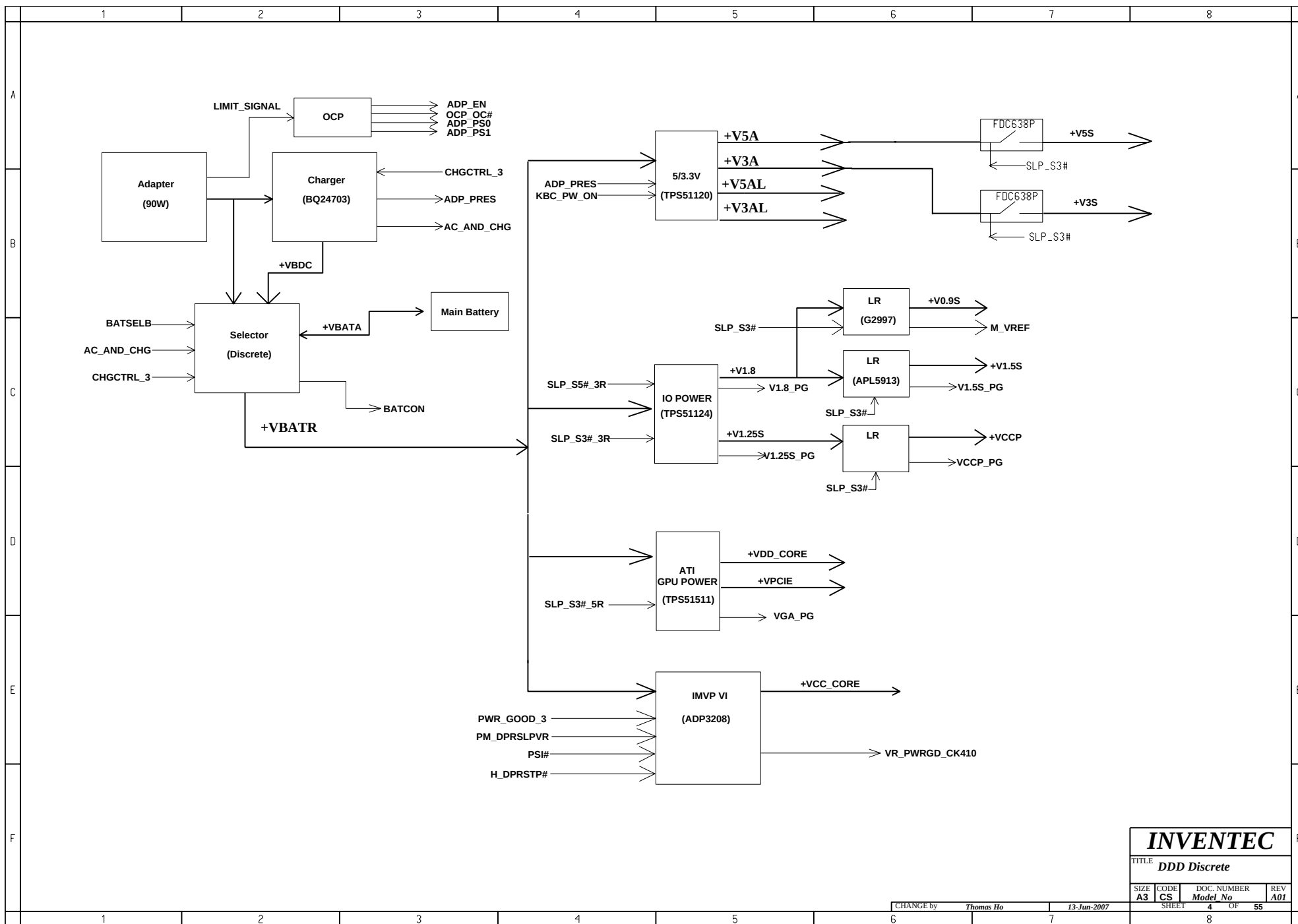
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DESIGN					TITLE		
CHECK					DDD Discrete		
RESPONSIBLE							
SIZE = 3					SIZE	CODE	DOC. NUMBER
FILE NAME : XXXX-XXXXXX-XX					A3	CS	Model No
P/N	XXXXXXXXXXXX				SHEET 1 OF 55		

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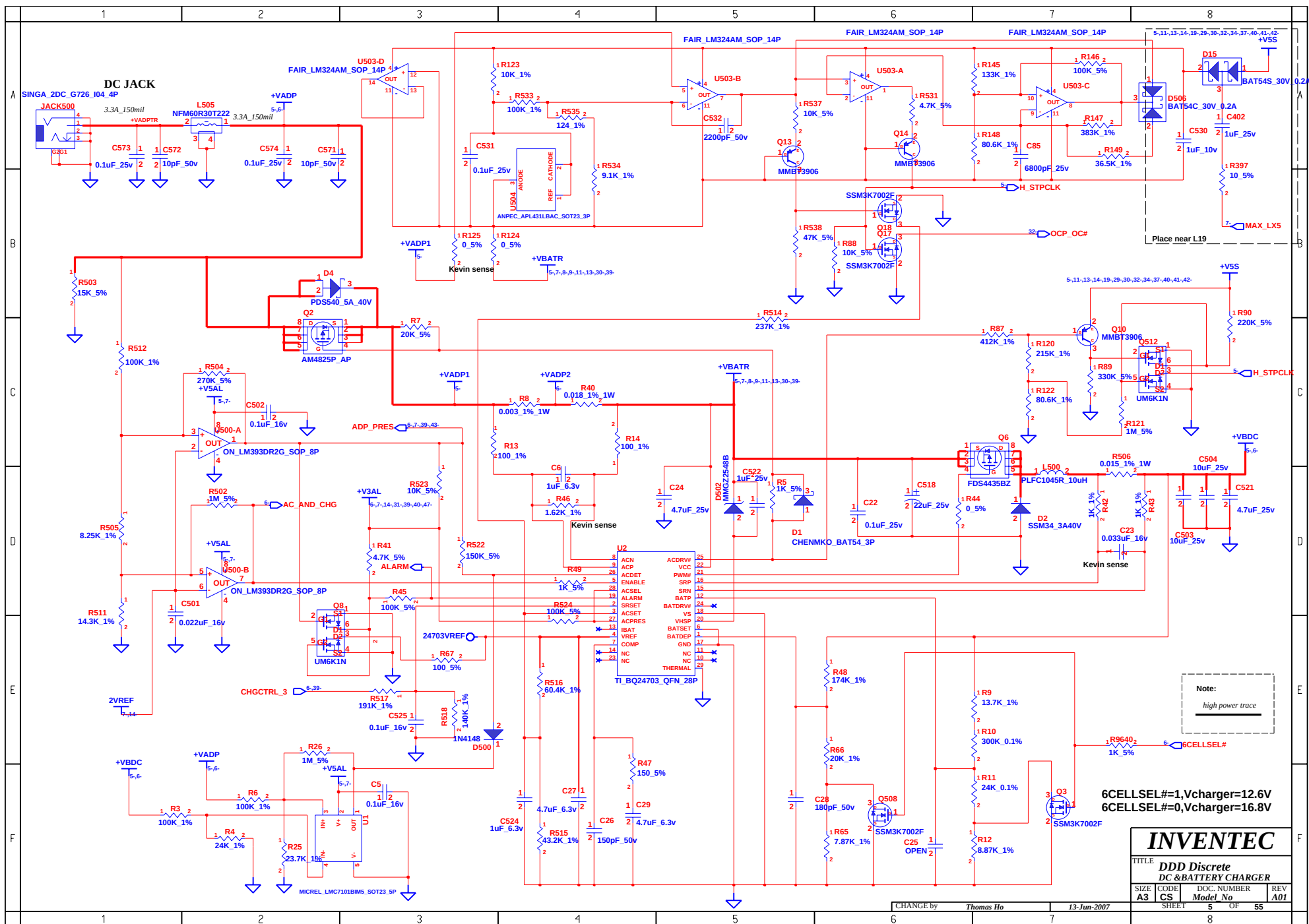


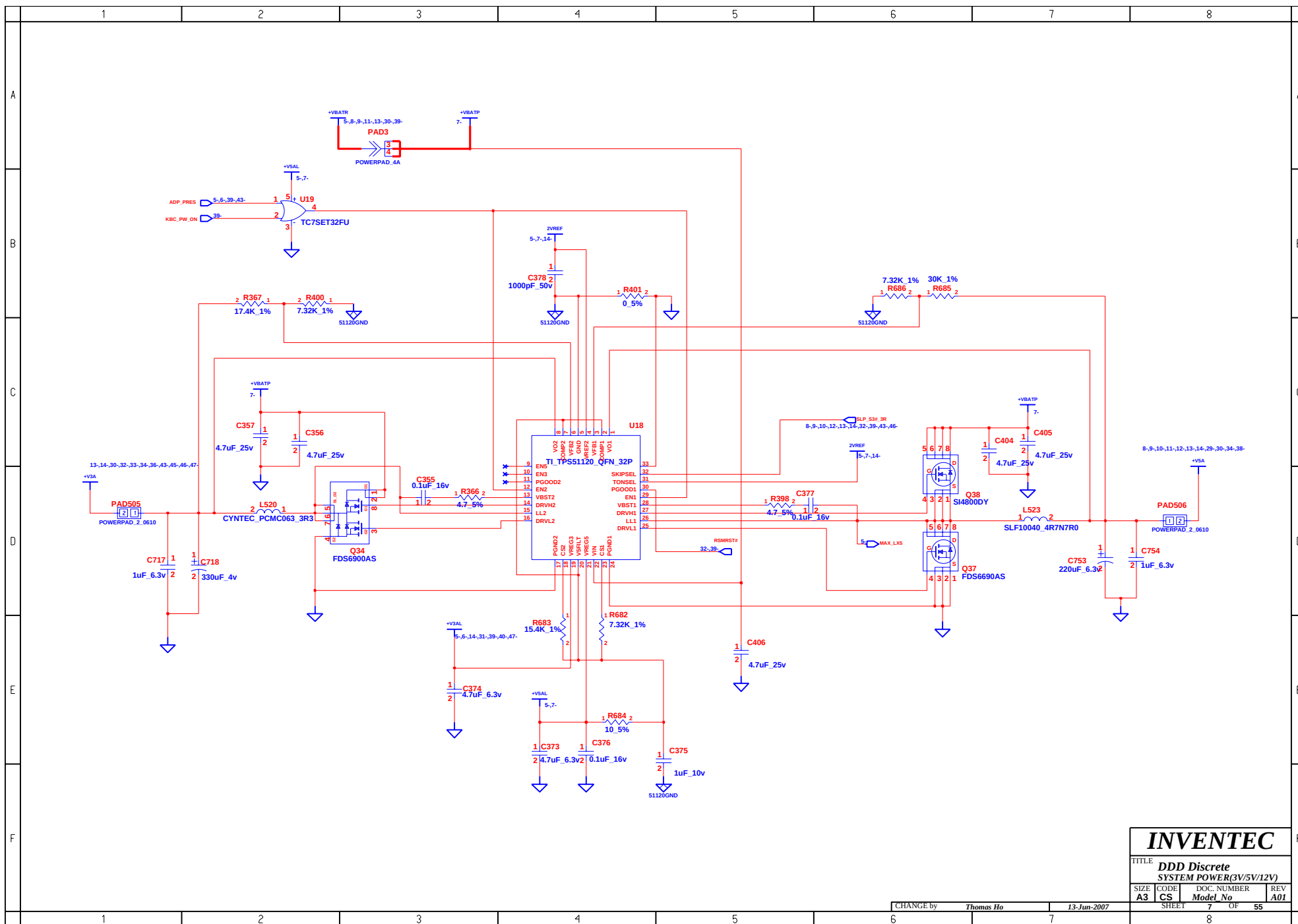


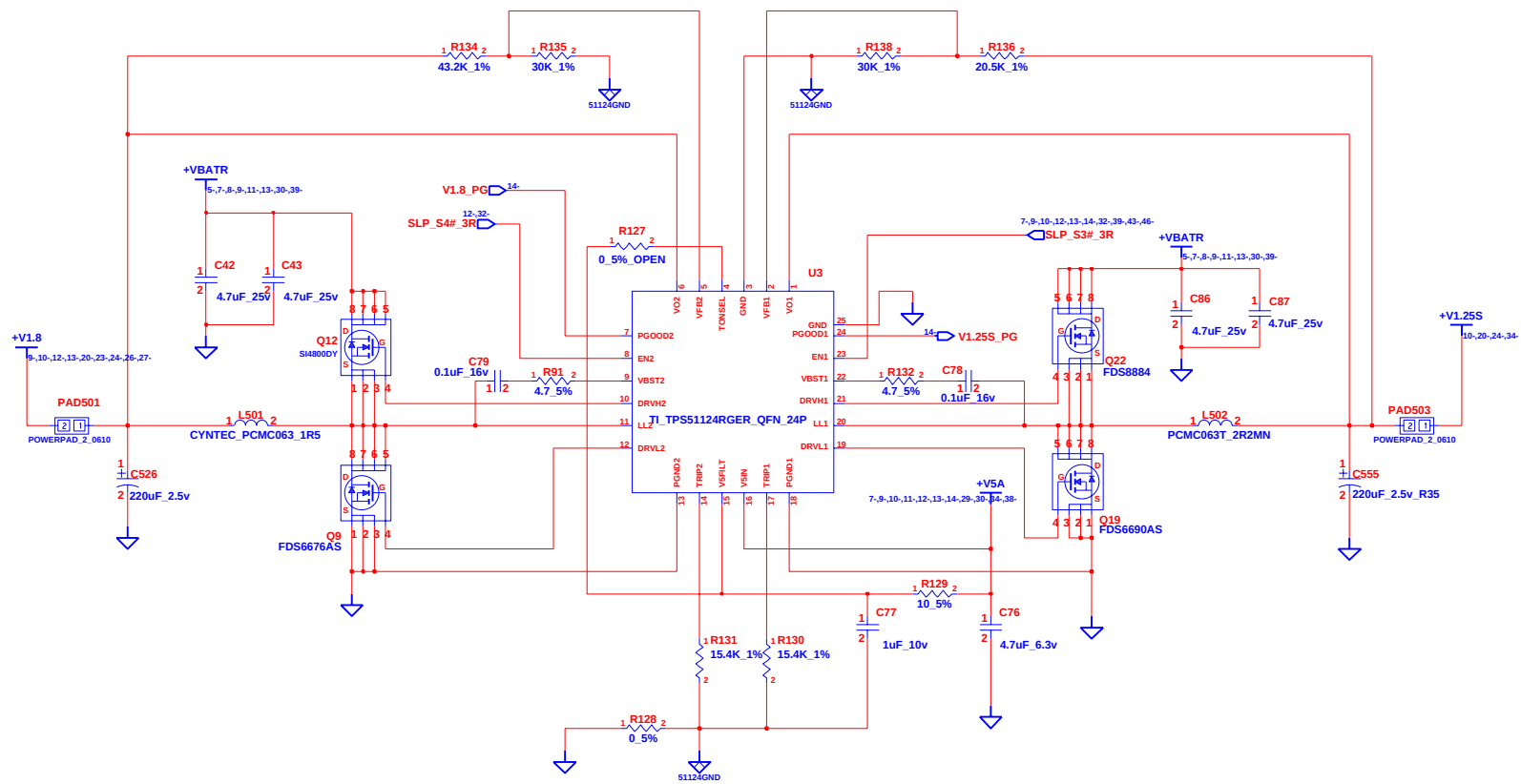
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DDD Discrete			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01

CHANGE by Thomas Ho 13-Jun-2007

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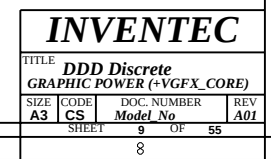


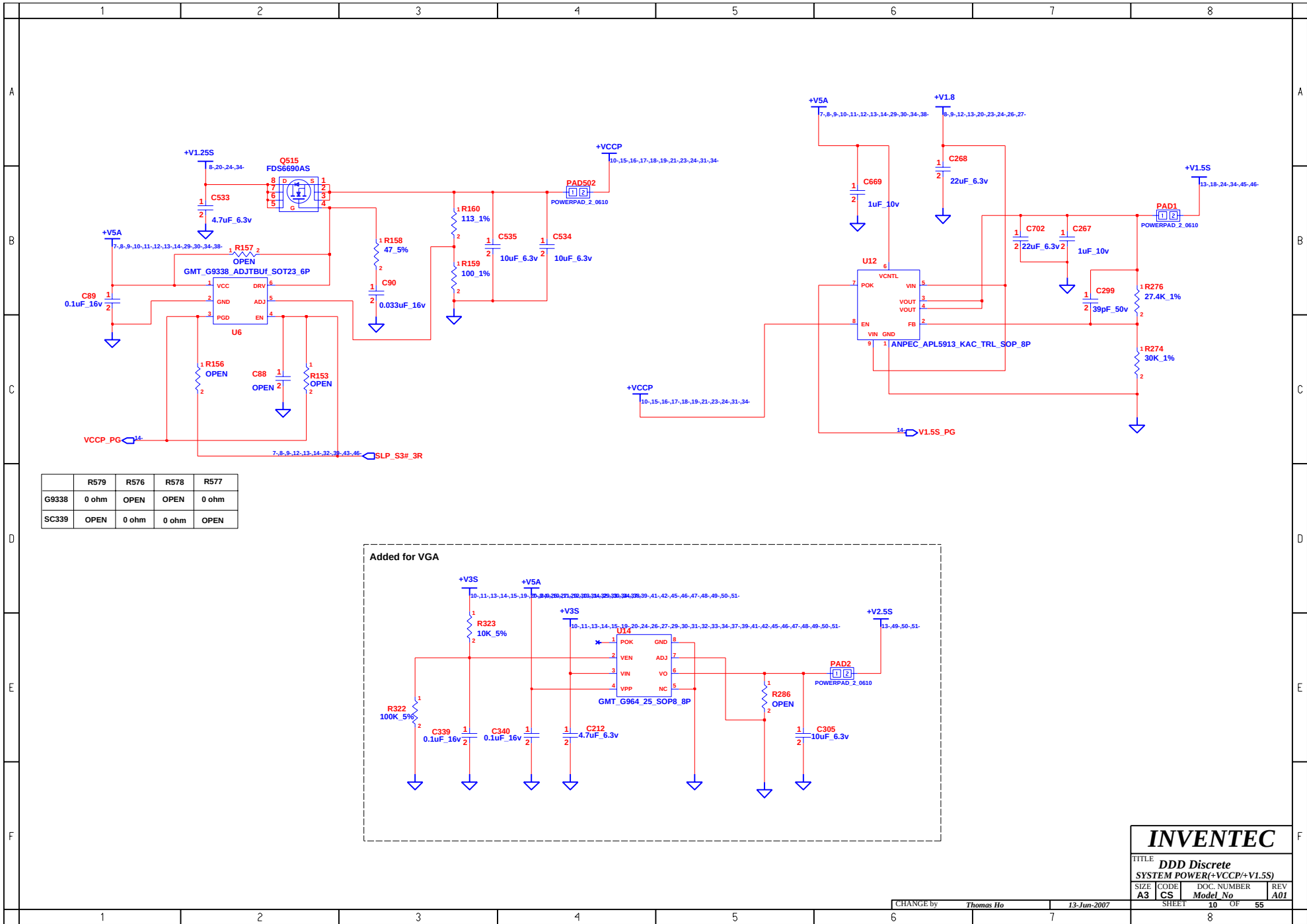
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TITLE			
DDD Discrete			
SYSTEM POWER(+V1.8/+V1.25S)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01

CHANGE by Thomas Ho 13-Jun-2007

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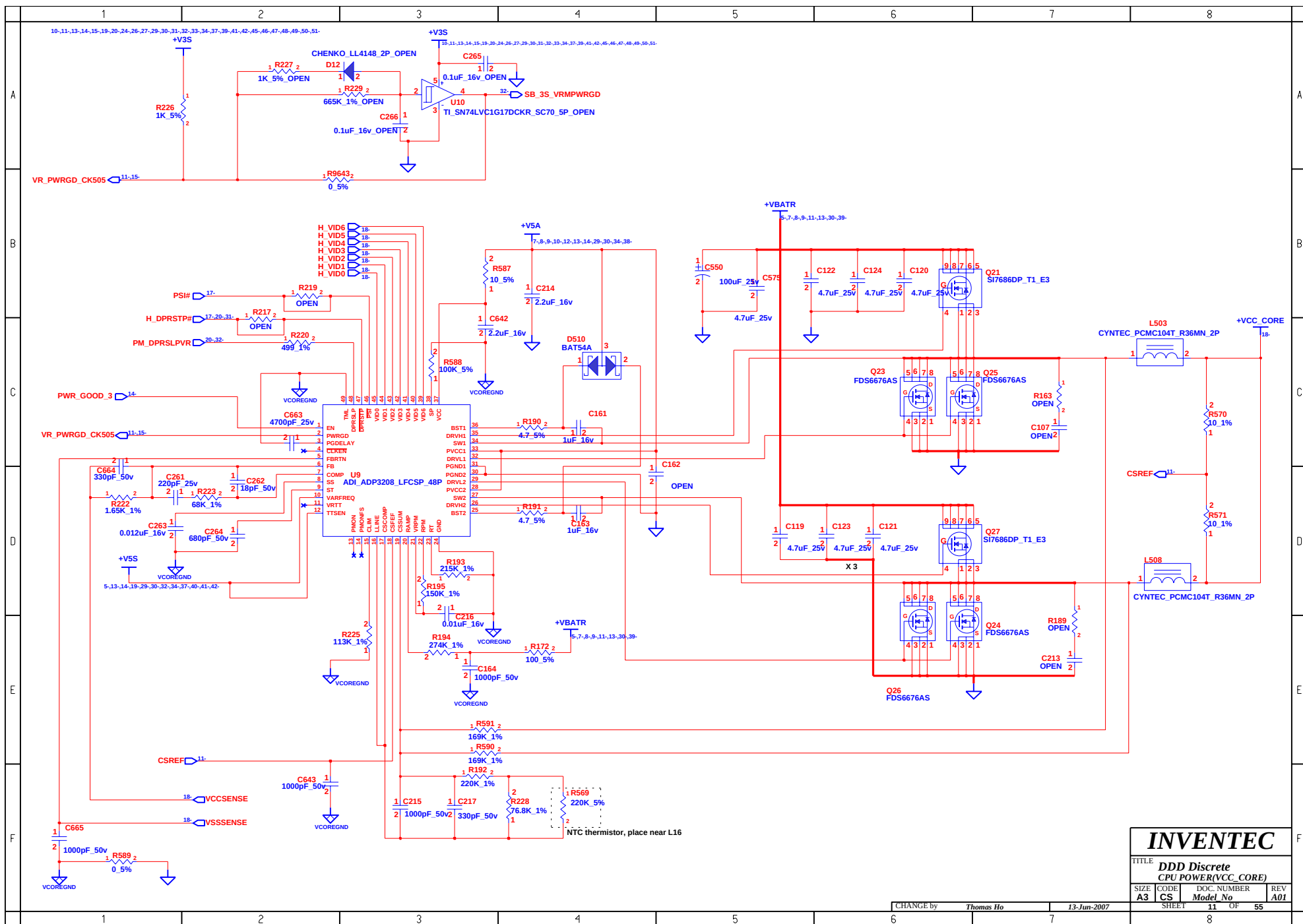
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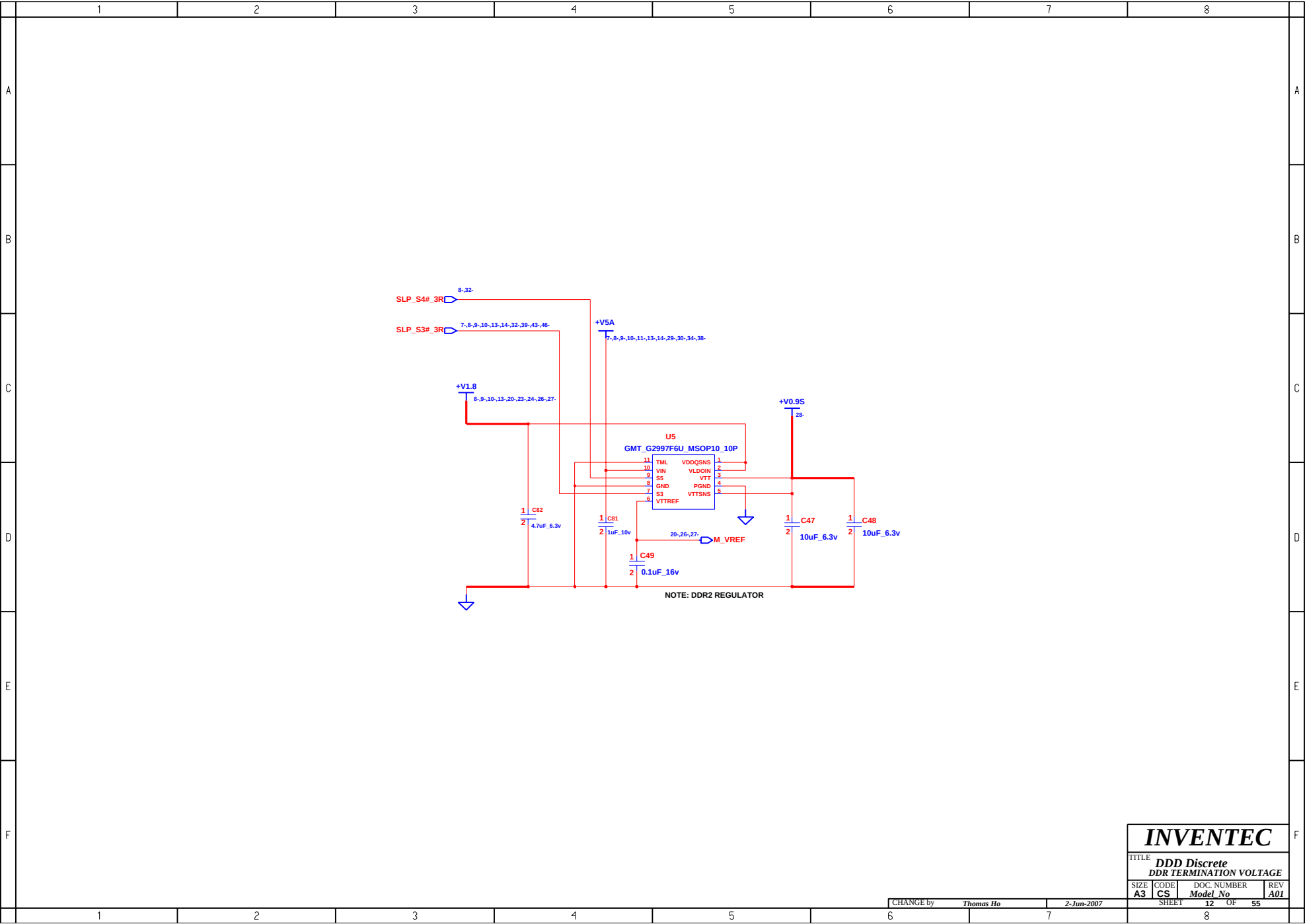
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SYSTEM POWER(+VCCP/+V1.5S)

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01

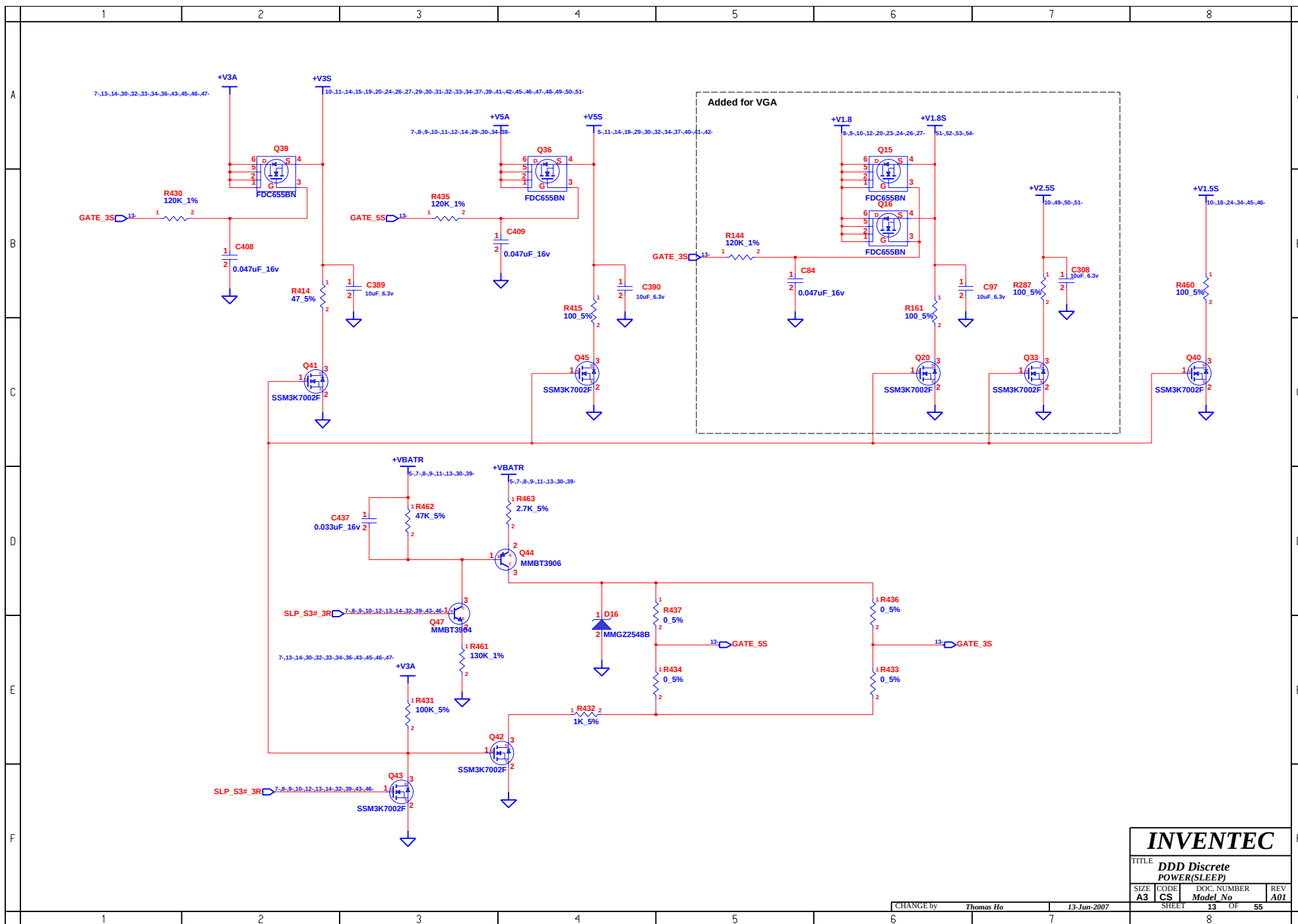
CHANGE by **Thomas Ho** 13-Jun-2007

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INVENTEC			
TITLE DDD Discrete DDR TERMINATION VOLTAGE			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A01
CHANGE by Thomas Ho		2-Jun-2007	
SHEET 12		OF 55	
1		8	

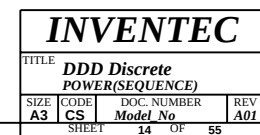


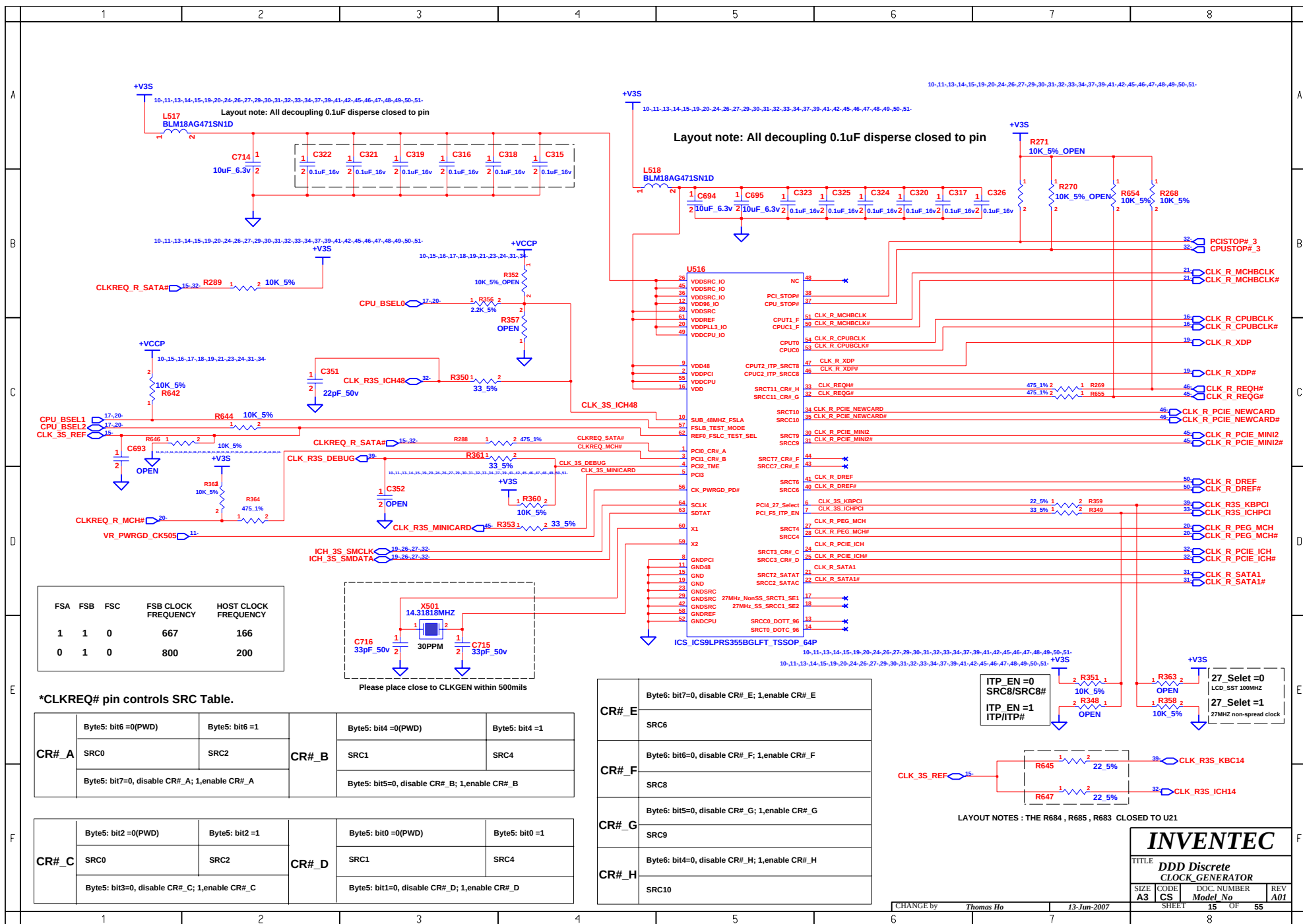
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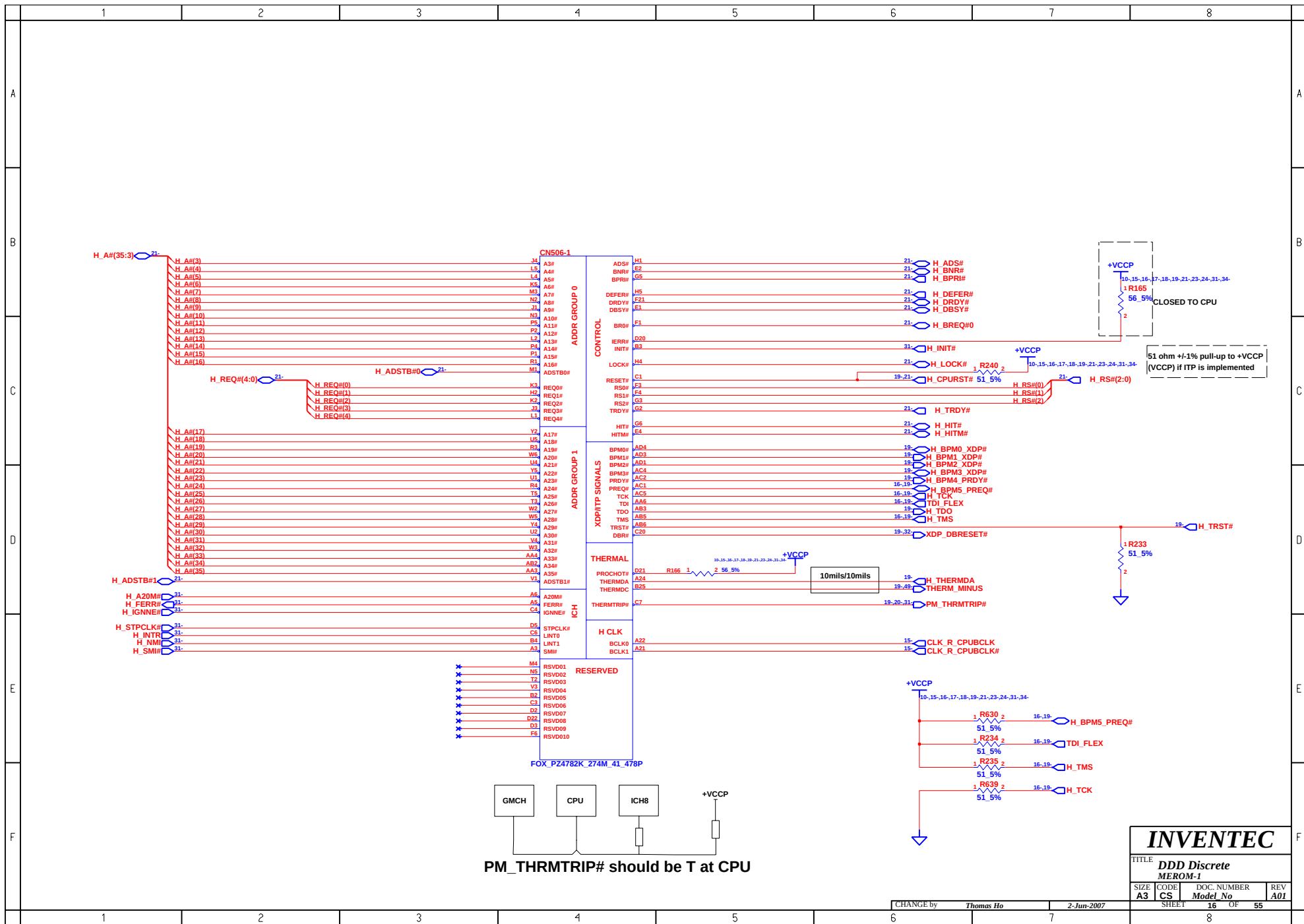
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SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01

CHANGE by Thomas Ho 13-Jun-2007

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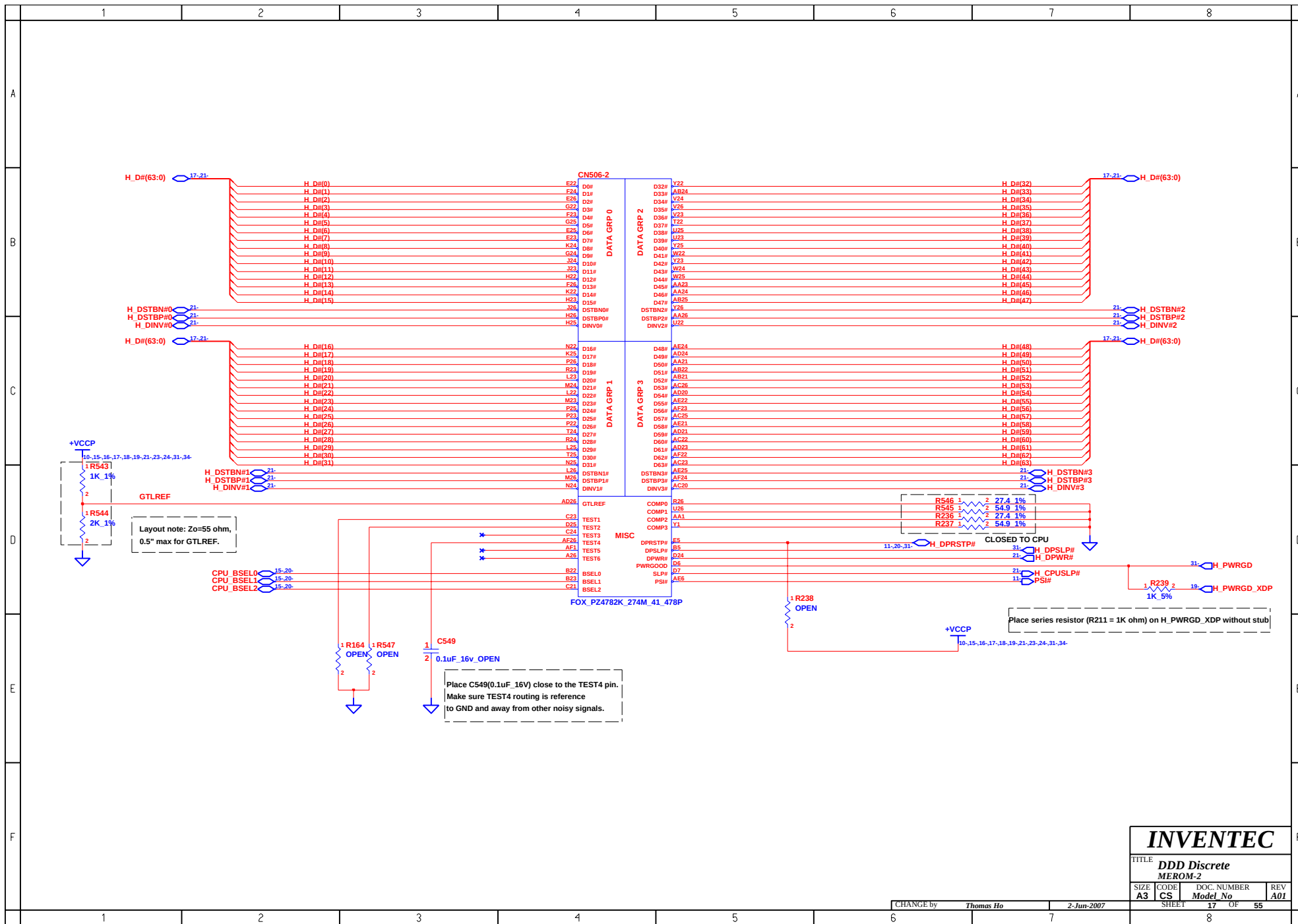




INVENTEC

TITLE DDD Discrete MEROM-1			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A01

CHANGE by	Thomas Ho	2-Jun-2007	SHEET 16 OF 55
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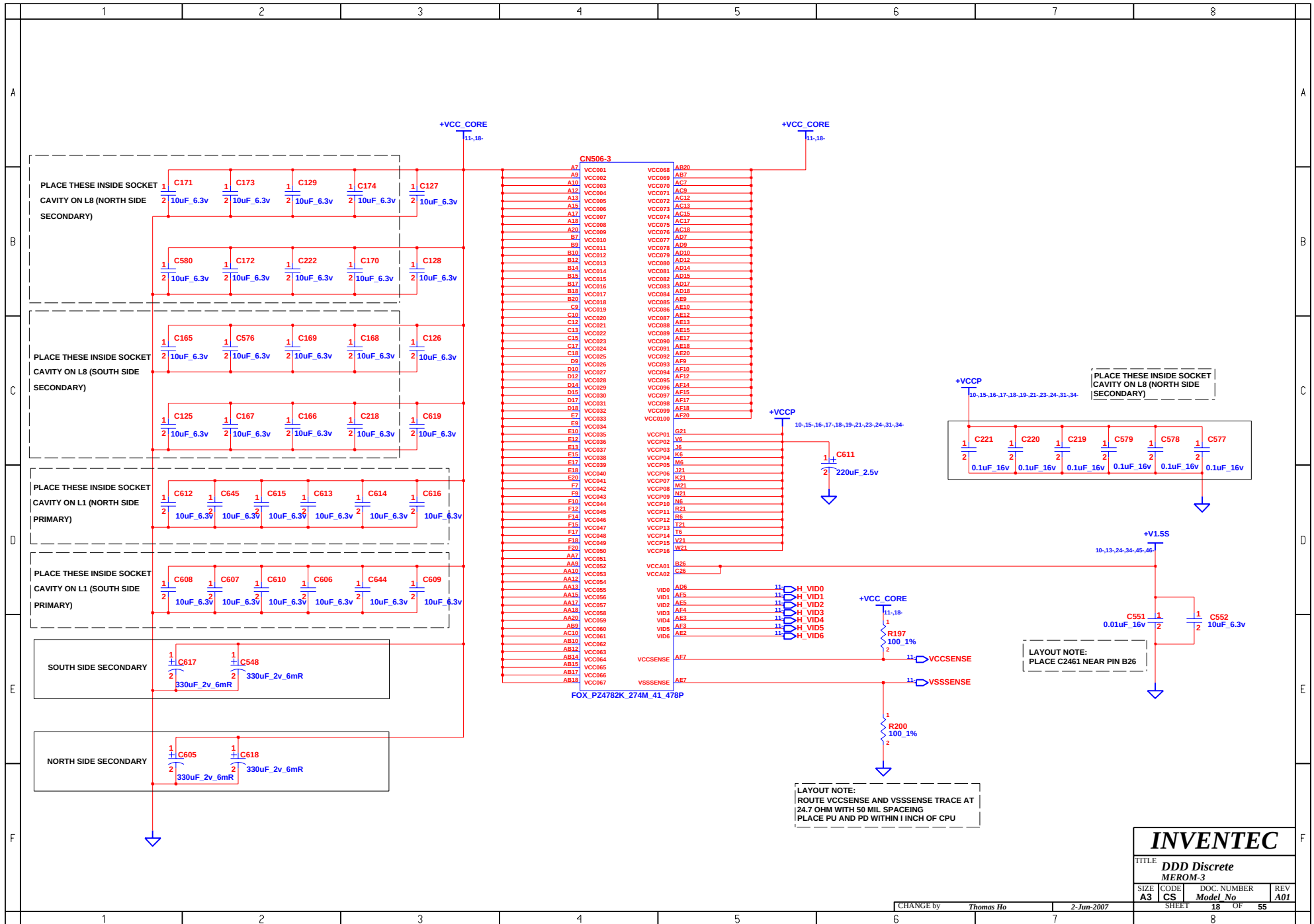
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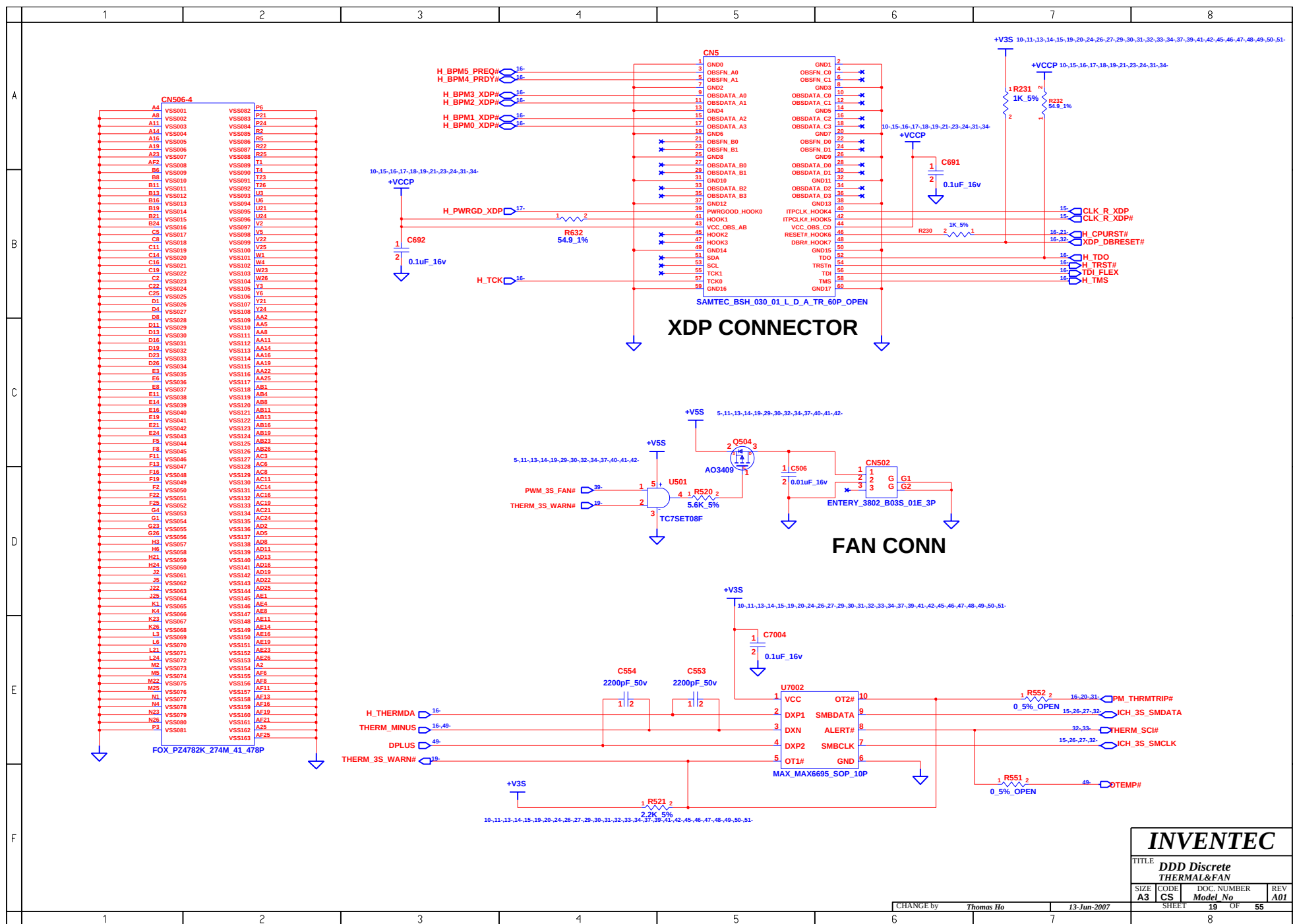
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DDD Discrete
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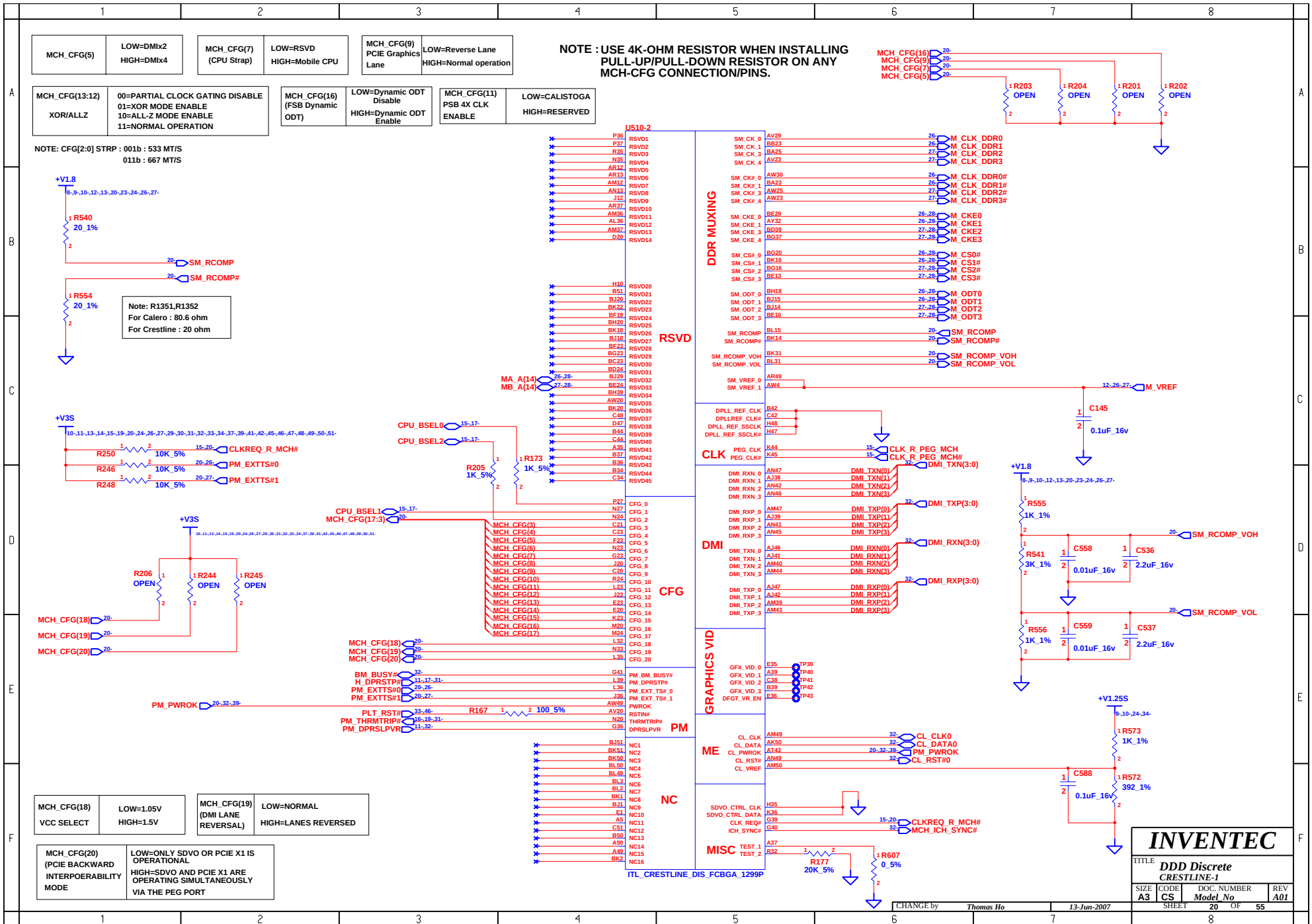
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A3	CS	Model No	A01

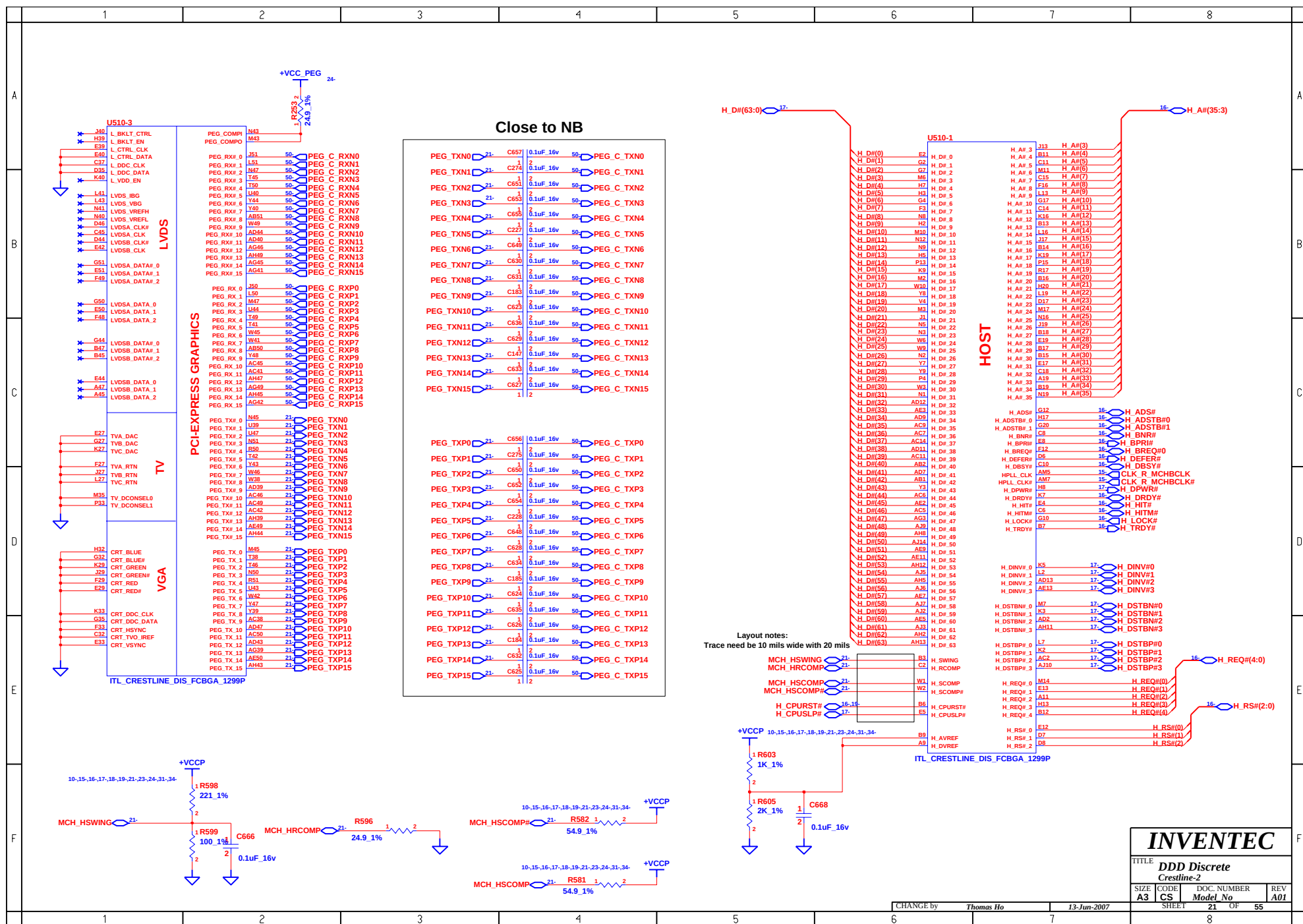
CHANGE by Thomas Ho 2-Jun-2007

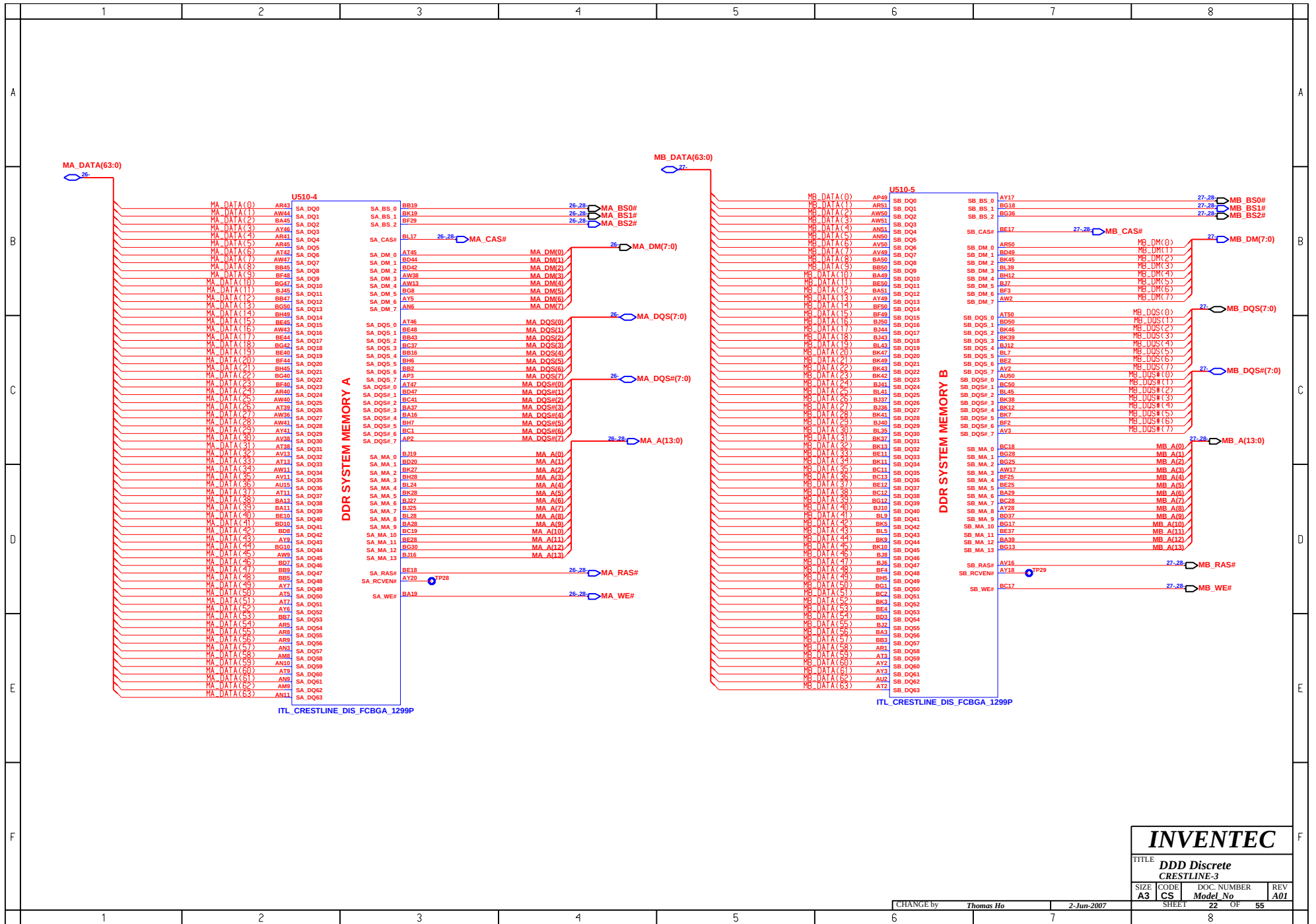
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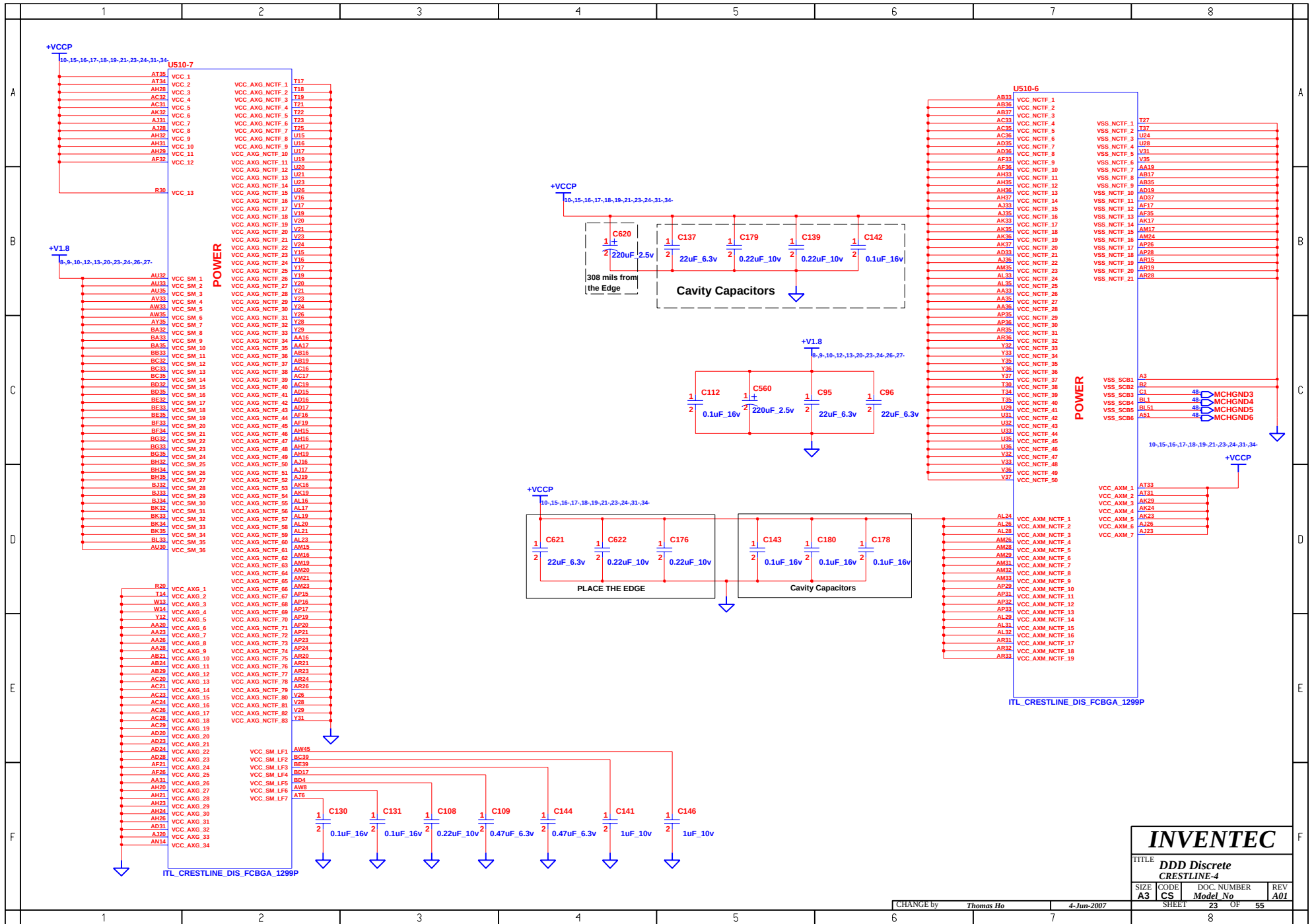
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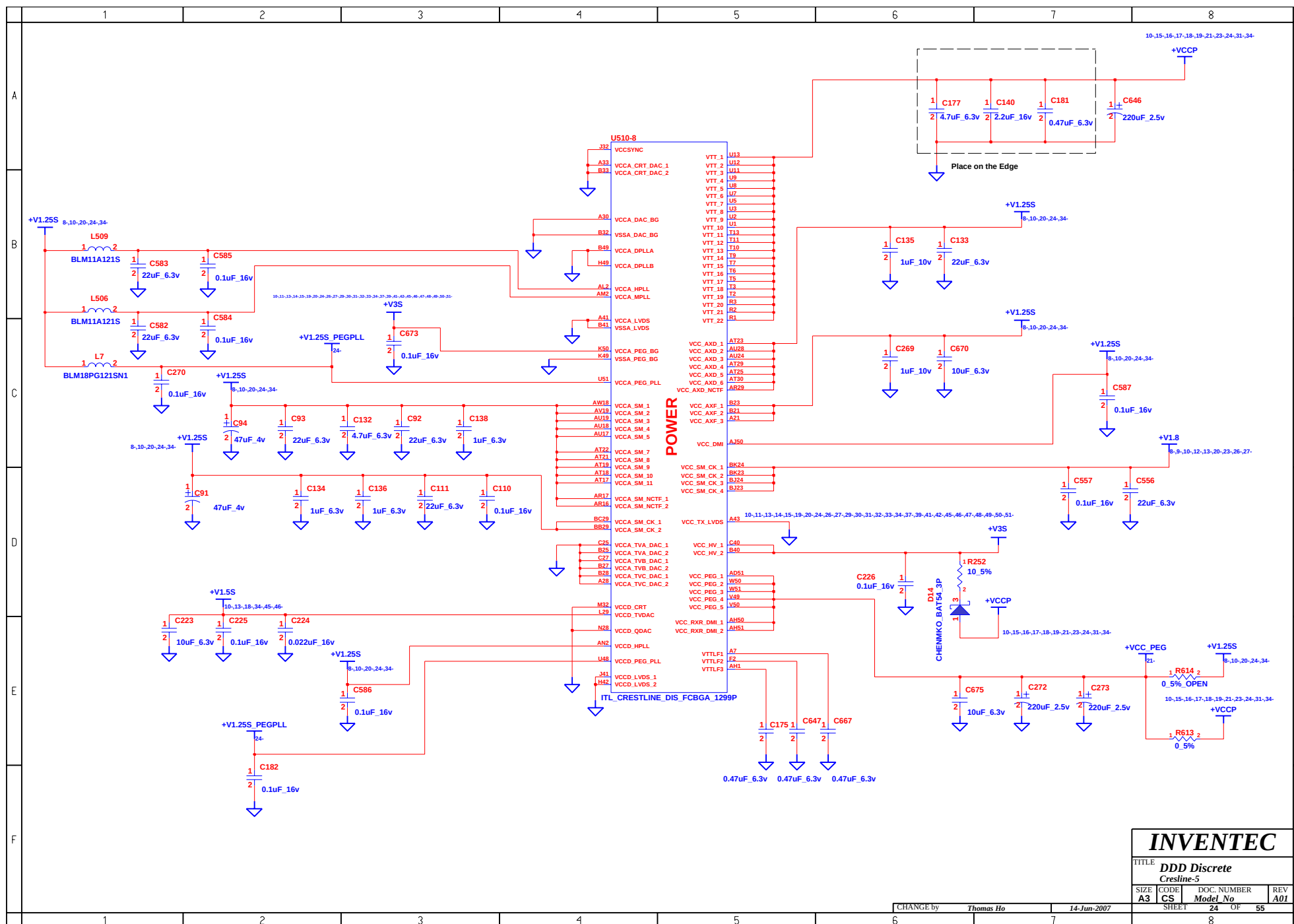
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DDD Discrete
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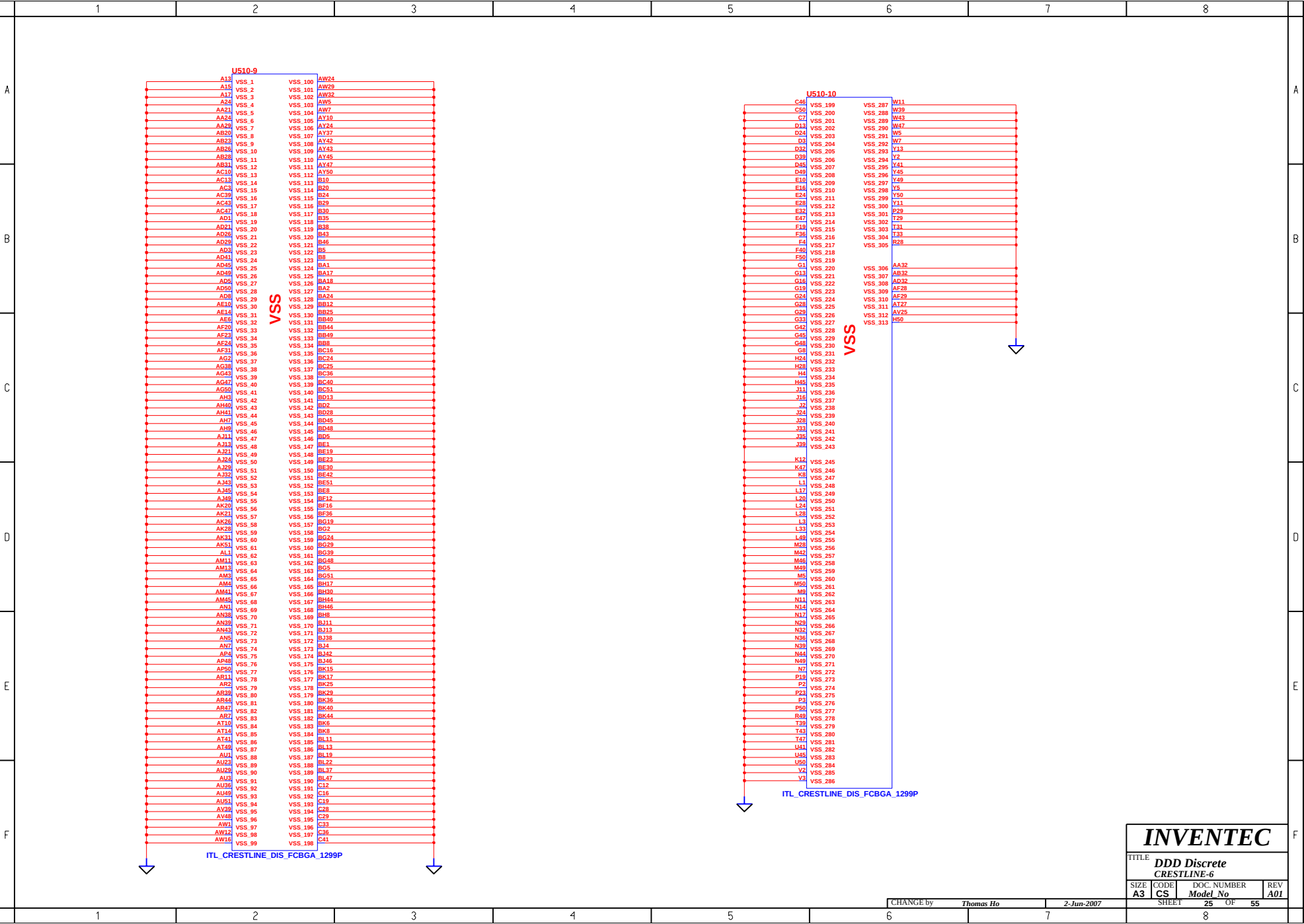
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A3	CS	22	A01

CHANGE by Thomas Ho 2-Jun-2007

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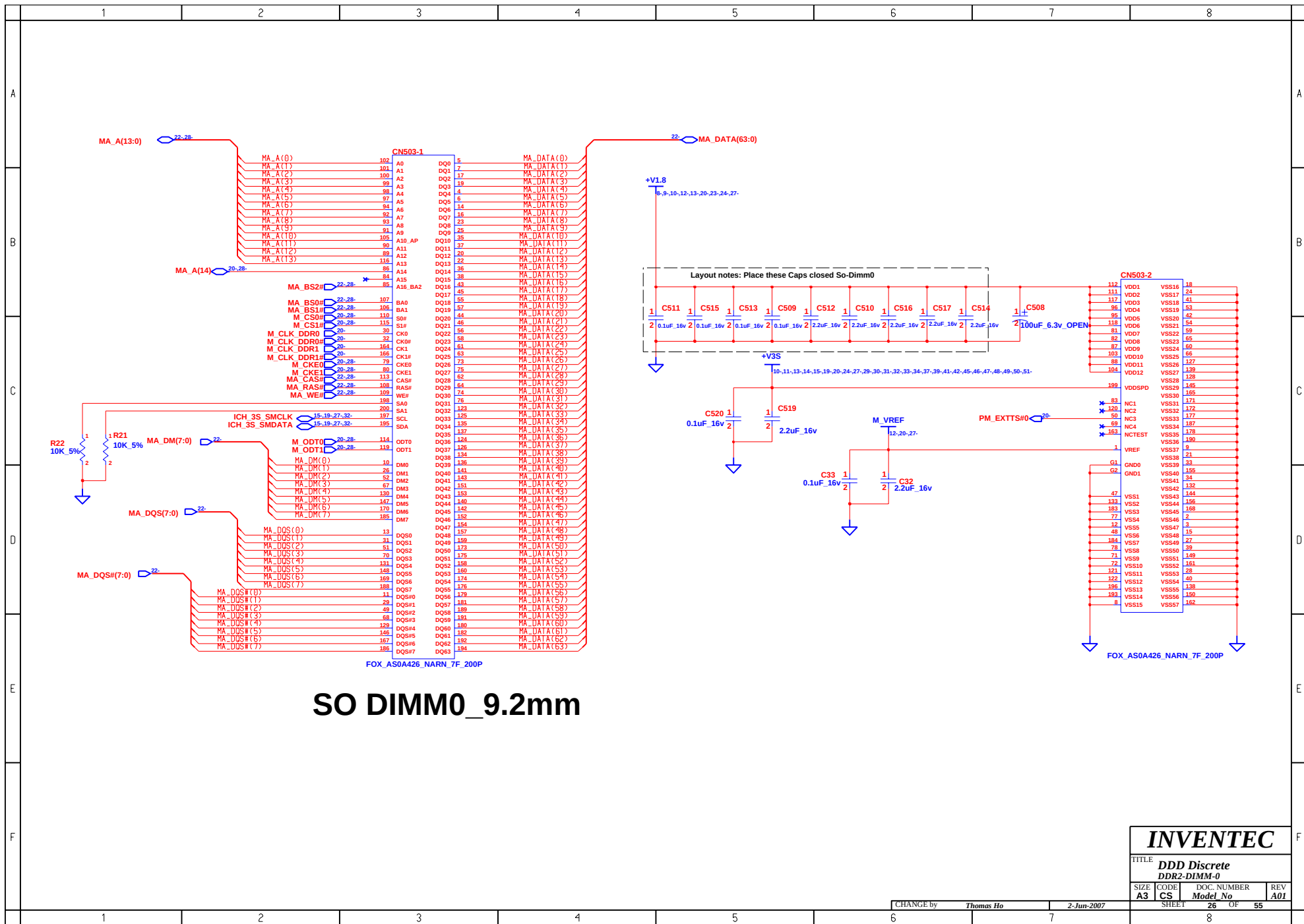


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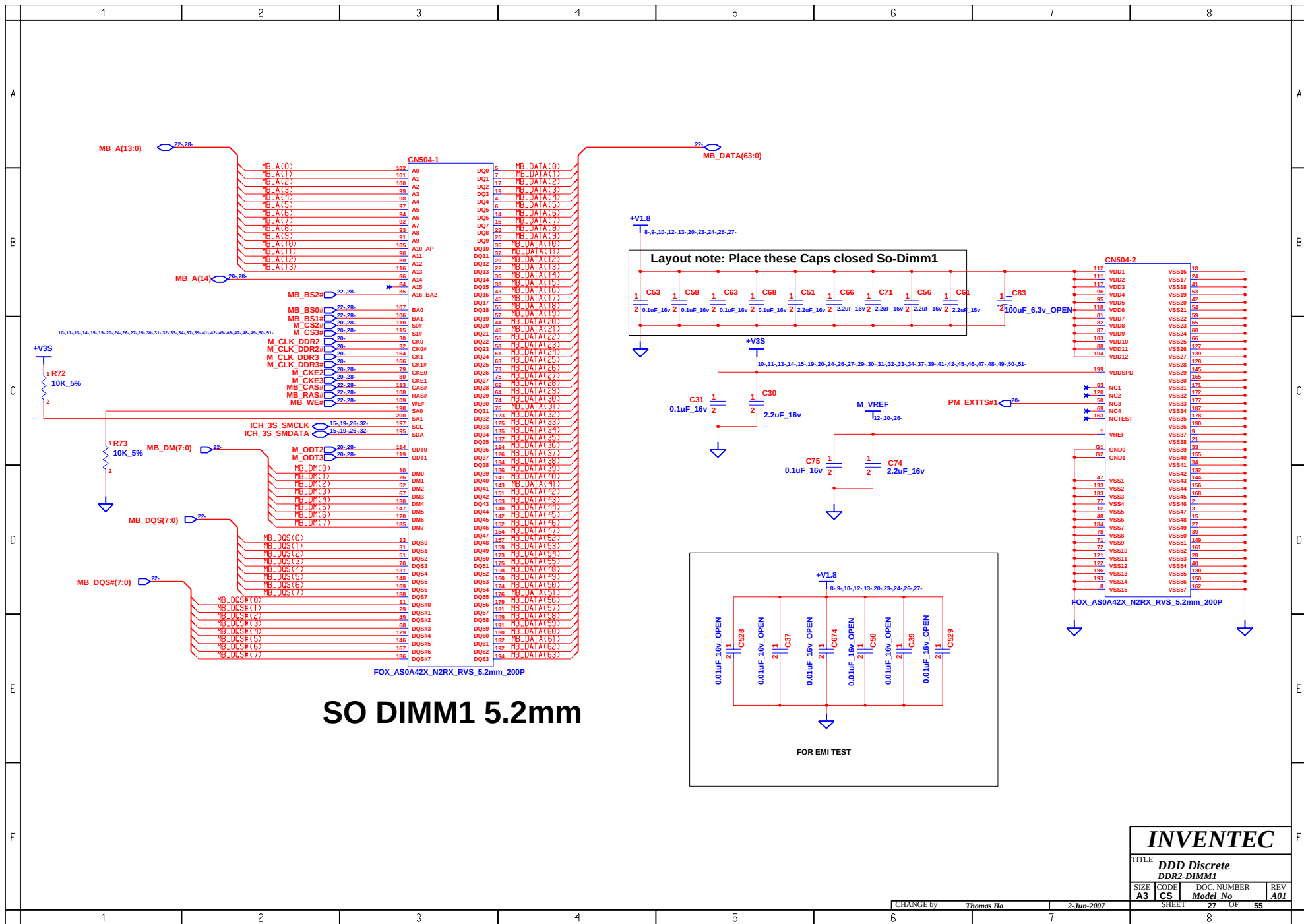
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DDD Discrete			
CRESTLINE-6			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01

CHANGE by Thomas Ho 2-Jun-2007

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INVENTEC			
TITLE DDD Discrete DDR2-DIMM-0			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A01



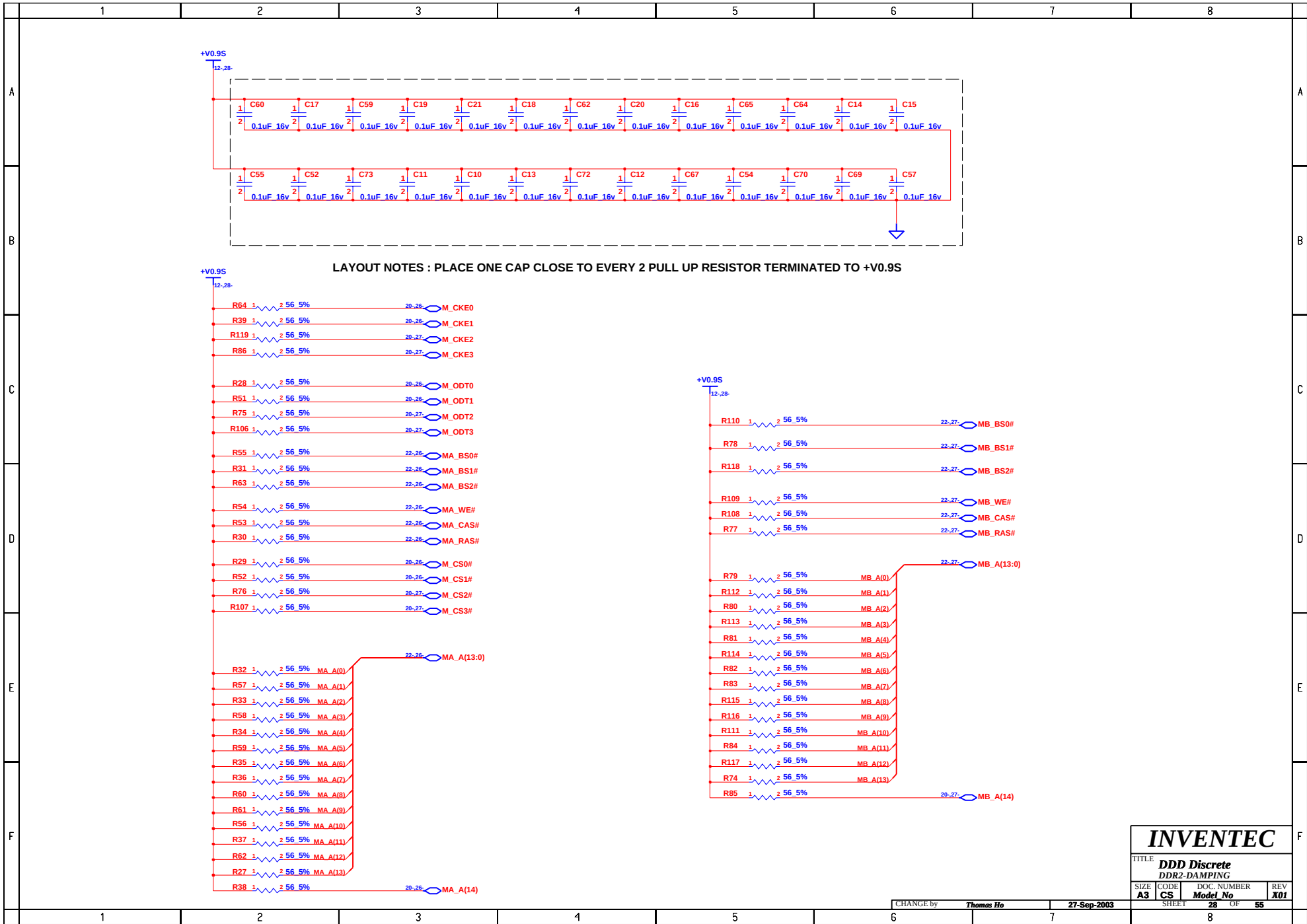
SO DIMM1 5.2mm

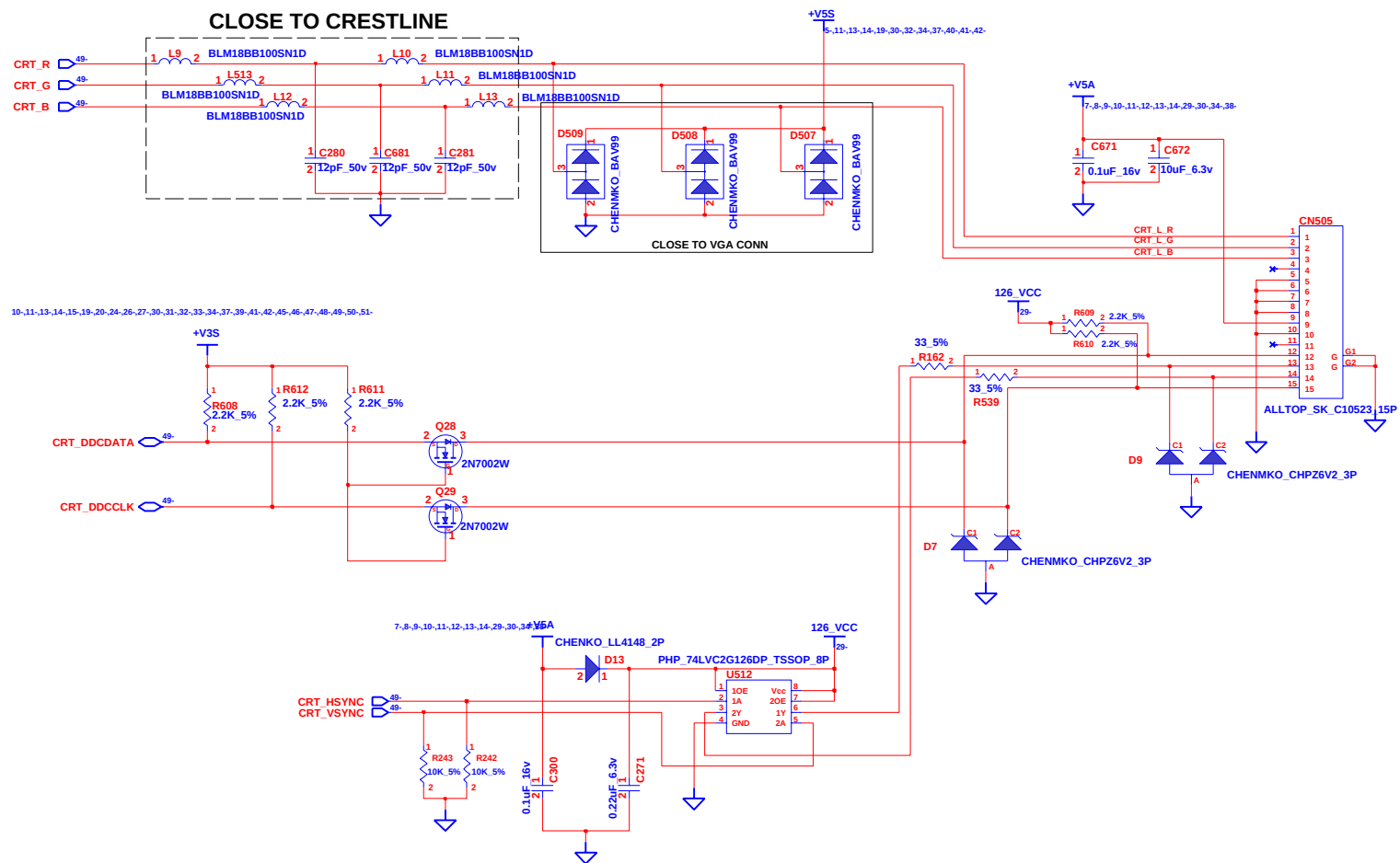
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TITLE
DDD Discrete
DDR2-DIMM1

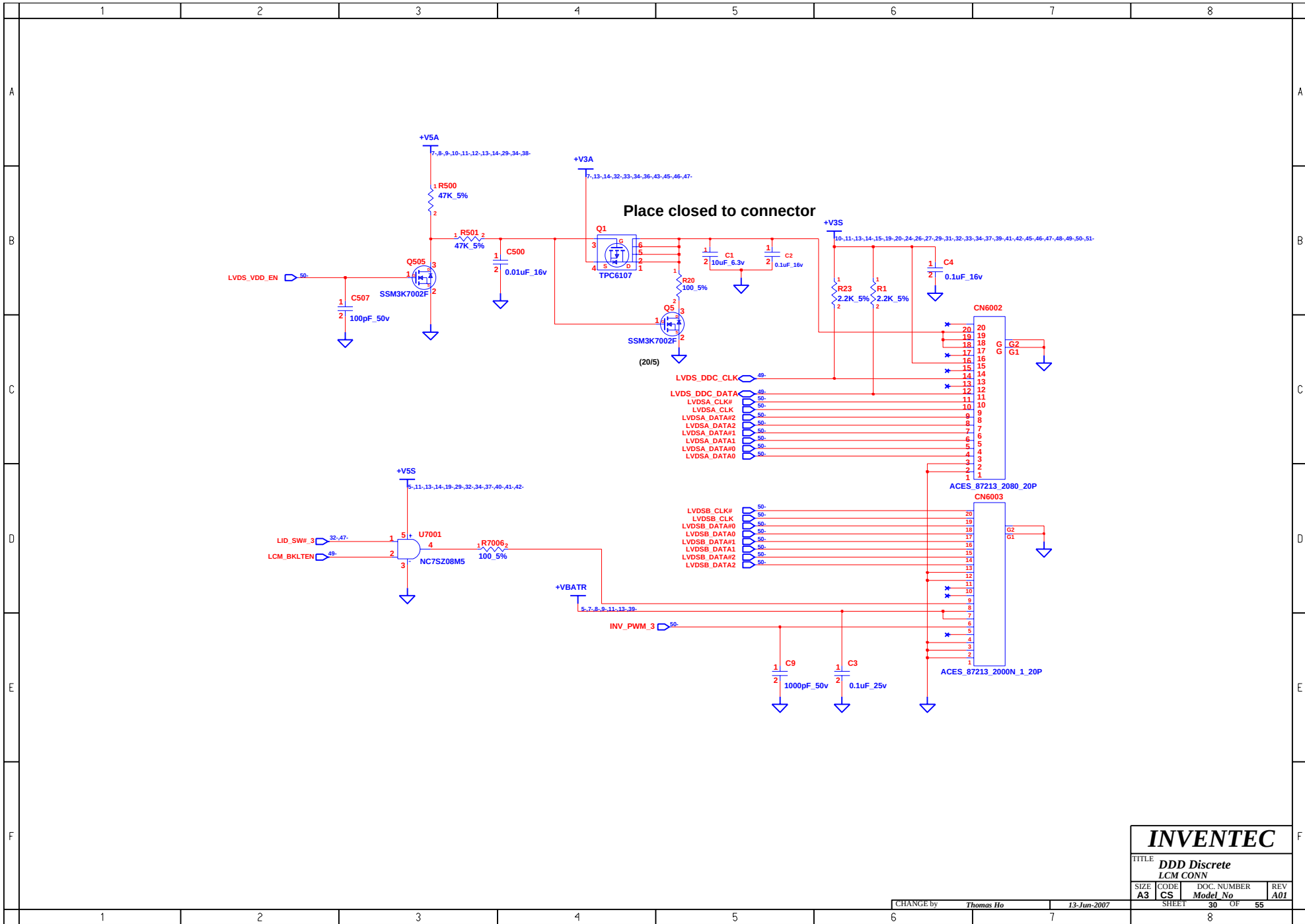
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A3	CS	Model No	A01

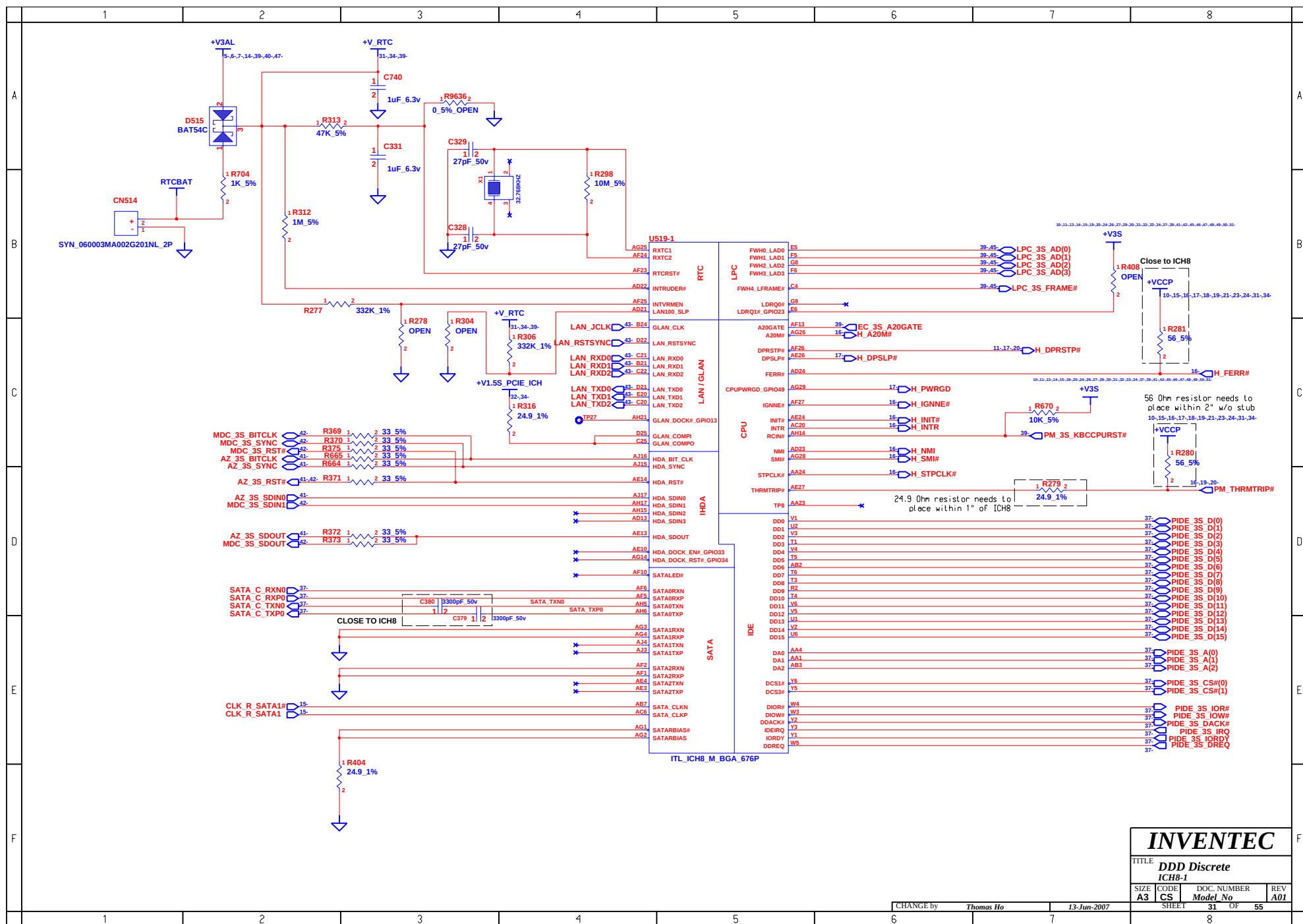
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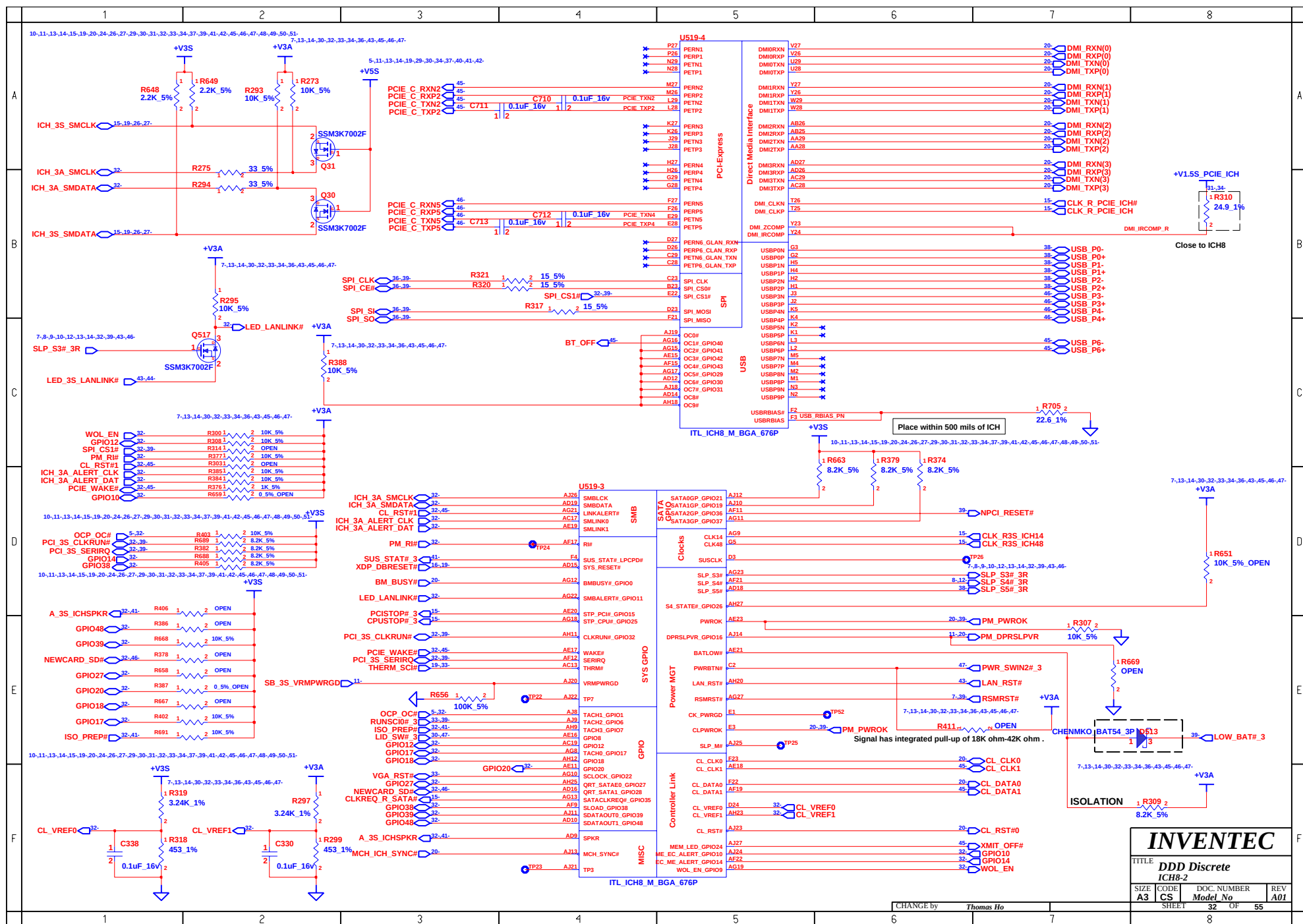




INVENTEC			
TITLE DDD Discrete VGA CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A01
CHANGE by Thomas Ho		2-Jun-2007	
SHEET 29		OF 55	







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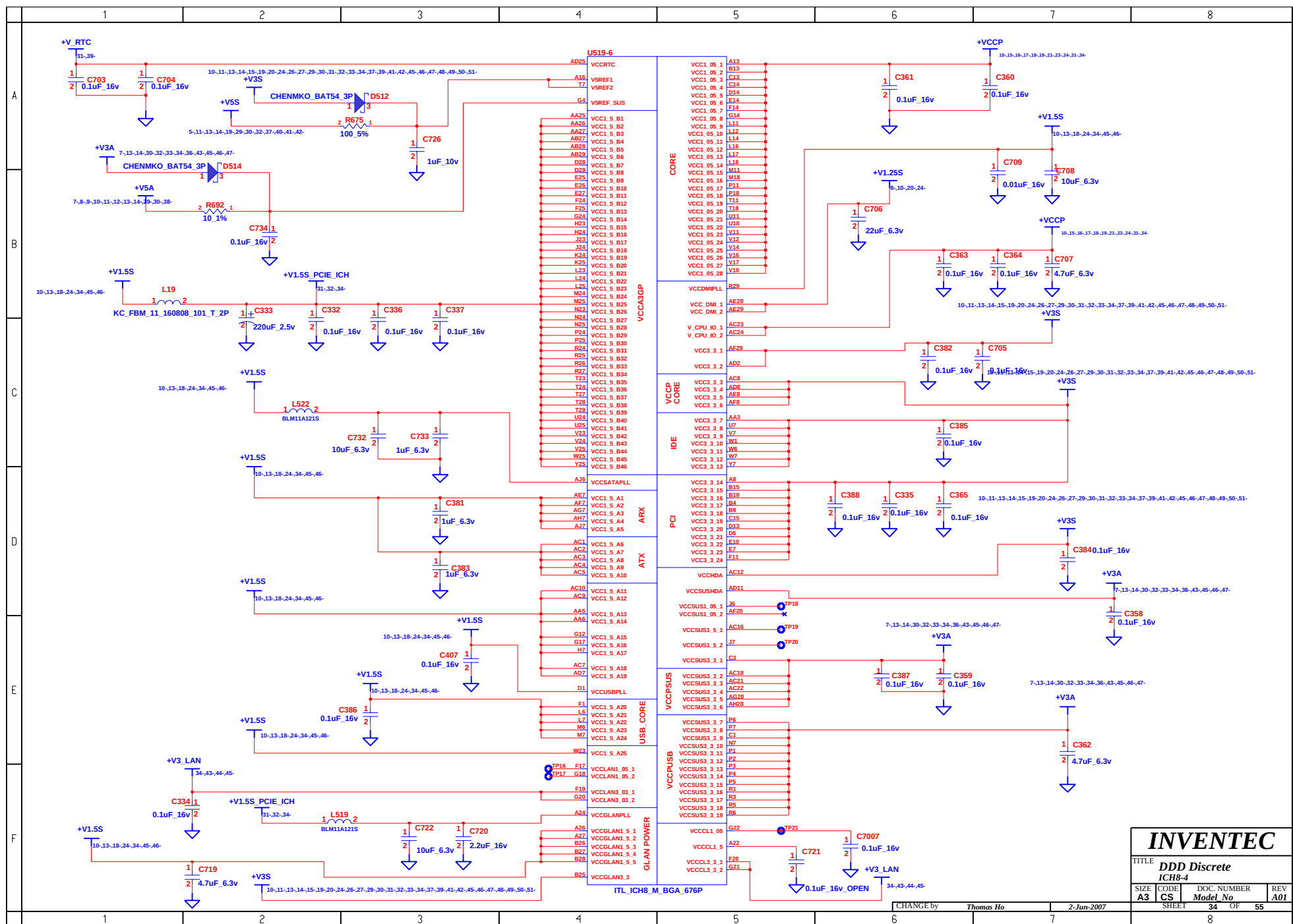
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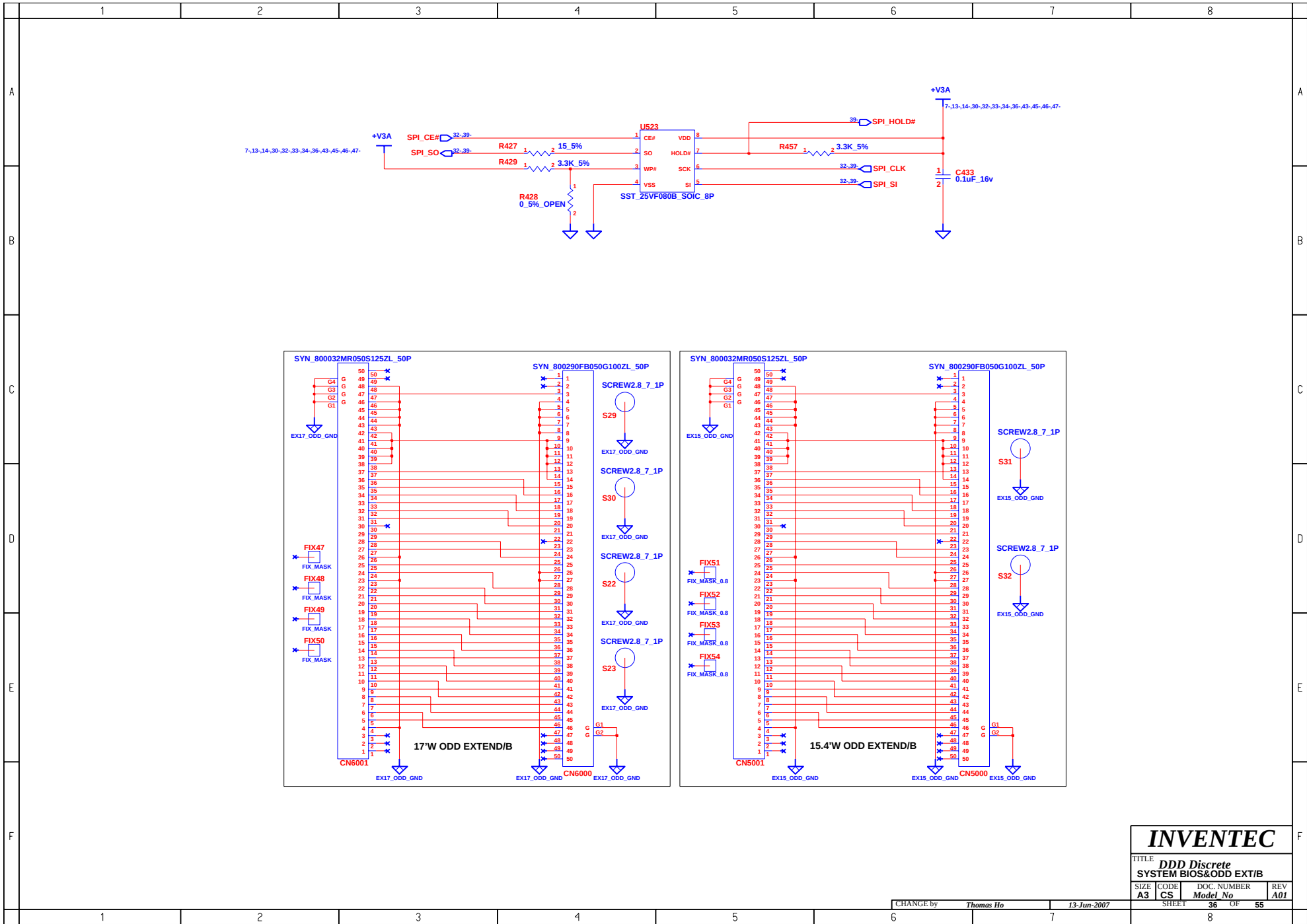
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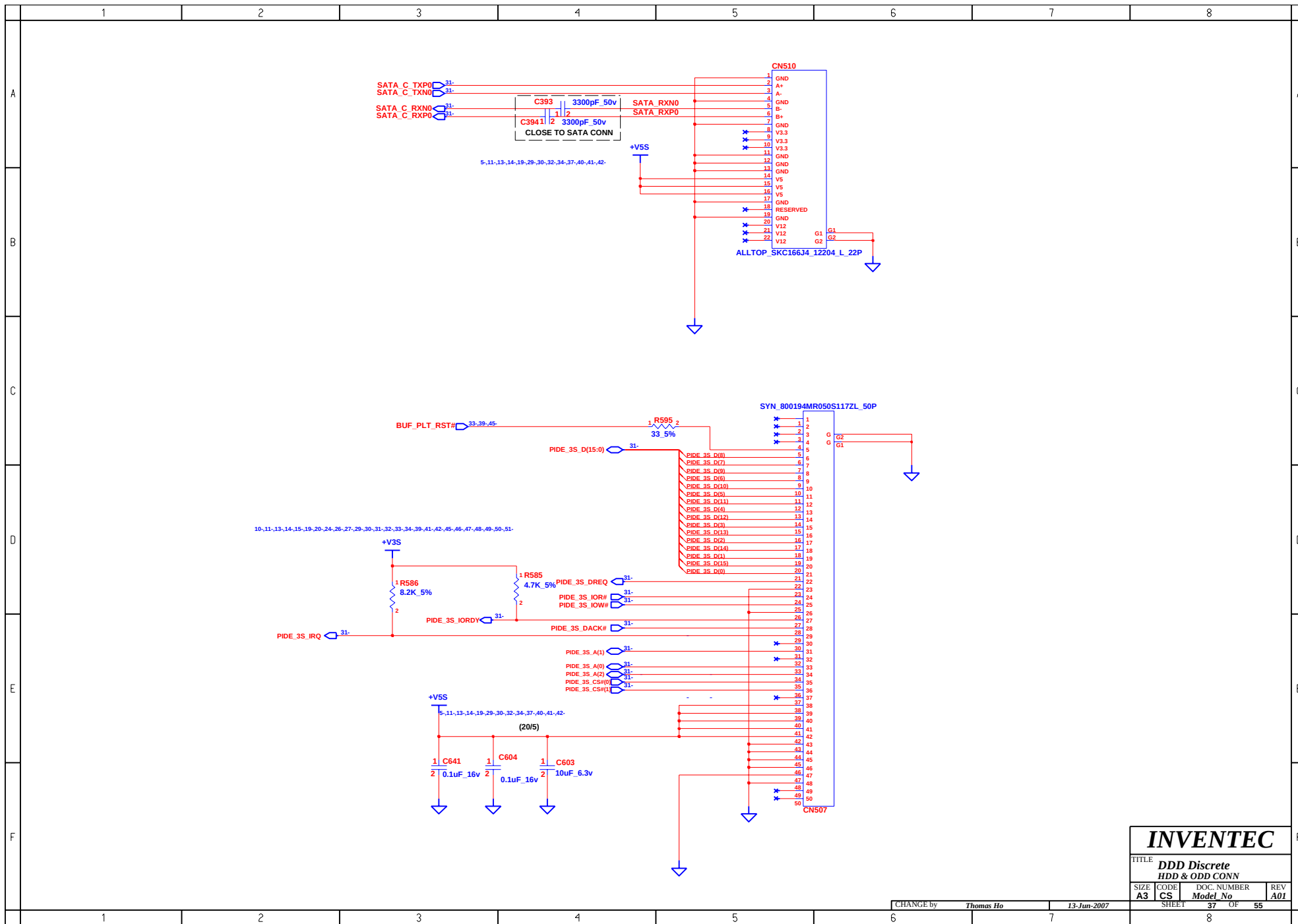
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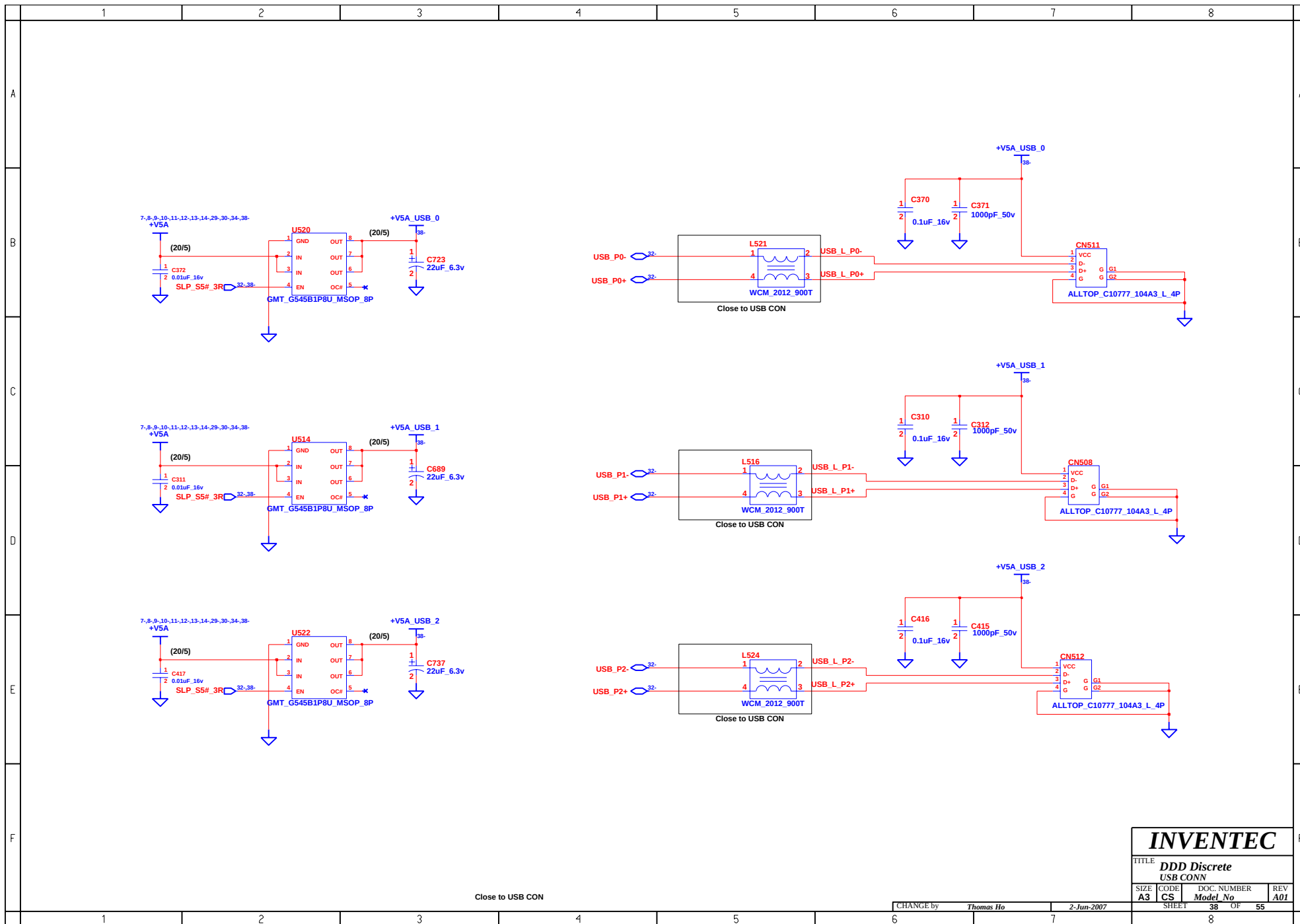
	1	2	3	4	5	6	7	8
A								
B								
C								
D								
E								
F								

The diagram shows the pinout for the U519-5 component, which is an ITL_ICH8_M_BGA_676P. The pins are arranged in a grid, with the component name and pin numbers (A23 to K8) listed on the left. The pins are connected to various components, including VSS001 to VSS098, VSS100 to VSS177, VSS179 to VSS188, VSS189 to VSS198, VSS199 to VSS208, VSS209 to VSS218, VSS219 to VSS228, VSS229 to VSS238, VSS239 to VSS248, VSS249 to VSS258, VSS259 to VSS268, VSS269 to VSS278, VSS279 to VSS288, VSS289 to VSS298, VSS299 to VSS308, VSS309 to VSS318, VSS319 to VSS328, VSS329 to VSS338, VSS339 to VSS348, VSS349 to VSS358, VSS359 to VSS368, VSS369 to VSS378, VSS379 to VSS388, VSS389 to VSS398, VSS399 to VSS408, VSS409 to VSS418, VSS419 to VSS428, VSS429 to VSS438, VSS439 to VSS448, VSS449 to VSS458, VSS459 to VSS468, VSS469 to VSS478, VSS479 to VSS488, VSS489 to VSS498, VSS499 to VSS508, VSS509 to VSS518, VSS519 to VSS528, VSS529 to VSS538, VSS539 to VSS548, VSS549 to VSS558, VSS559 to VSS568, VSS569 to VSS578, VSS579 to VSS588, VSS589 to VSS598, VSS599 to VSS608, VSS609 to VSS618, VSS619 to VSS628, VSS629 to VSS638, VSS639 to VSS648, VSS649 to VSS658, VSS659 to VSS668, VSS669 to VSS678, VSS679 to VSS688, VSS689 to VSS698, VSS699 to VSS708, VSS709 to VSS718, VSS719 to VSS728, VSS729 to VSS738, VSS739 to VSS748, VSS749 to VSS758, VSS759 to VSS768, VSS769 to VSS778, VSS779 to VSS788, VSS789 to VSS798, VSS799 to VSS808, VSS809 to VSS818, VSS819 to VSS828, VSS829 to VSS838, VSS839 to VSS848, VSS849 to VSS858, VSS859 to VSS868, VSS869 to VSS878, VSS879 to VSS888, VSS889 to VSS898, VSS899 to VSS908, VSS909 to VSS918, VSS919 to VSS928, VSS929 to VSS938, VSS939 to VSS948, VSS949 to VSS958, VSS959 to VSS968, VSS969 to VSS978, VSS979 to VSS988, VSS989 to VSS998, VSS999 to VSS1008, VSS1009 to VSS1018, VSS1019 to VSS1028, VSS1029 to VSS1038, VSS1039 to VSS1048, VSS1049 to VSS1058, VSS1059 to VSS1068, VSS1069 to VSS1078, VSS1079 to VSS1088, VSS1089 to VSS1098, VSS1099 to VSS1108, VSS1109 to VSS1118, VSS1119 to VSS1128, VSS1129 to VSS1138, VSS1139 to VSS1148, VSS1149 to VSS1158, VSS1159 to VSS1168, VSS1169 to VSS1178, VSS1179 to VSS1188, VSS1189 to VSS1198, VSS1199 to VSS1208, VSS1209 to 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VSS2218, VSS2219 to VSS2228, VSS2229 to VSS2238, VSS2239 to VSS2248, VSS2249 to VSS2258, VSS2259 to VSS2268, VSS2269 to VSS2278, VSS2279 to VSS2288, VSS2289 to VSS2298, VSS2299 to VSS2308, VSS2309 to VSS2318, VSS2319 to VSS2328, VSS2329 to VSS2338, VSS2339 to VSS2348, VSS2349 to VSS2358, VSS2359 to VSS2368, VSS2369 to VSS2378, VSS2379 to VSS2388, VSS2389 to VSS2398, VSS2399 to VSS2408, VSS2409 to VSS2418, VSS2419 to VSS2428, VSS2429 to VSS2438, VSS2439 to VSS2448, VSS2449 to VSS2458, VSS2459 to VSS2468, VSS2469 to VSS2478, VSS2479 to VSS2488, VSS2489 to VSS2498, VSS2499 to VSS2508, VSS2509 to VSS2518, VSS2519 to VSS2528, VSS2529 to VSS2538, VSS2539 to VSS2548, VSS2549 to VSS2558, VSS2559 to VSS2568, VSS2569 to VSS2578, VSS2579 to VSS2588, VSS2589 to VSS2598, VSS2599 to VSS2608, VSS2609 to VSS2618, VSS2619 to VSS2628, VSS2629 to VSS2638, VSS2639 to VSS2648, VSS2649 to VSS2658, VSS2659 to VSS2668, VSS2669 to VSS2678, VSS2679 to VSS2688, VSS2689 to VSS2698, VSS2699 to VSS2708, VSS2709 to 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INVENTEC			
TITLE DDD Discrete HDD & ODD CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A01
CHANGE by Thomas Ho		13-Jun-2007	
SHEET 37		OF 55	
1		8	

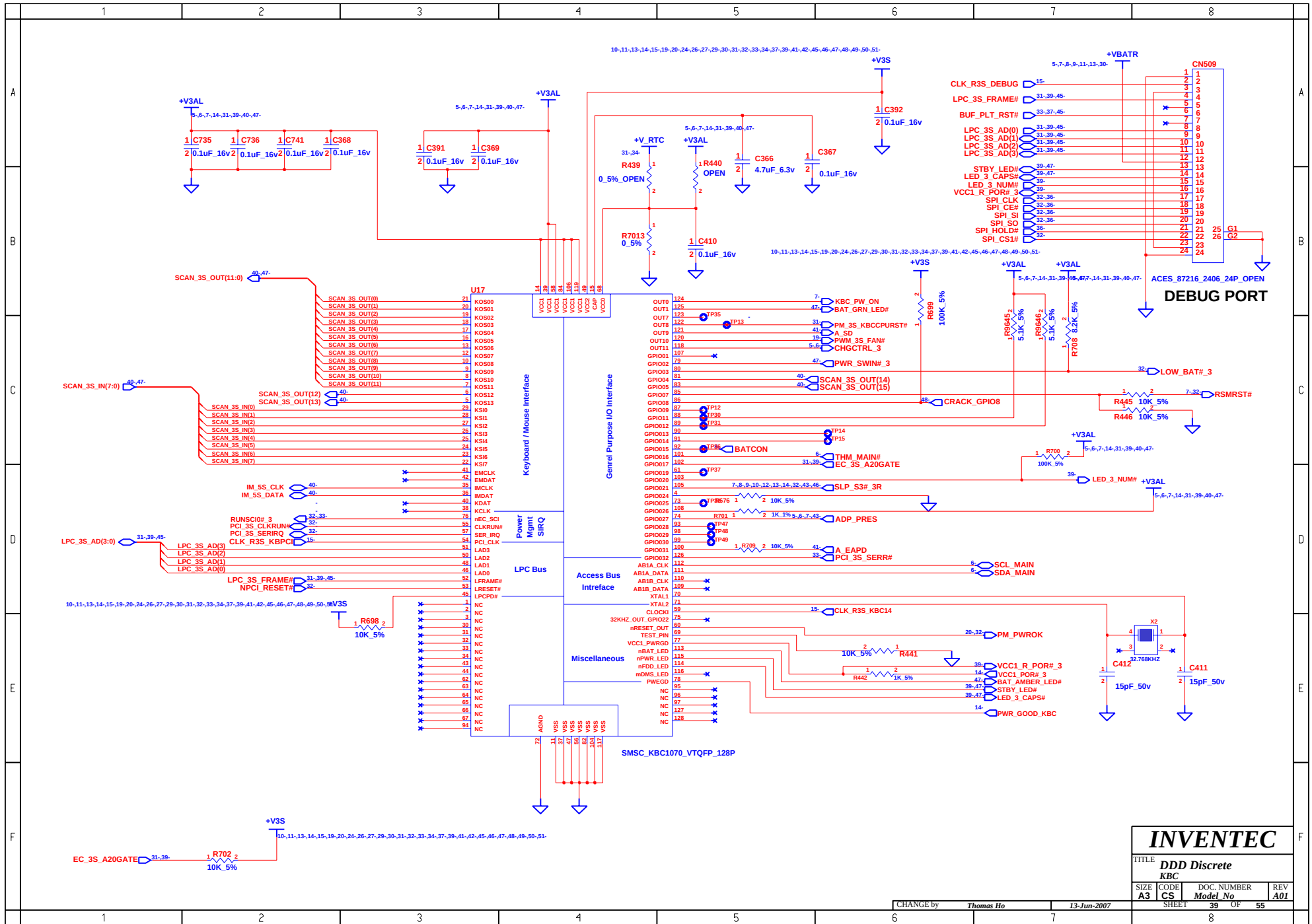


INVENTEC

TITLE DDD Discrete USB CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A01
SHEET 38		OF 55	

CHANGE by Thomas Ho 2-Jun-2007

Close to USB CON



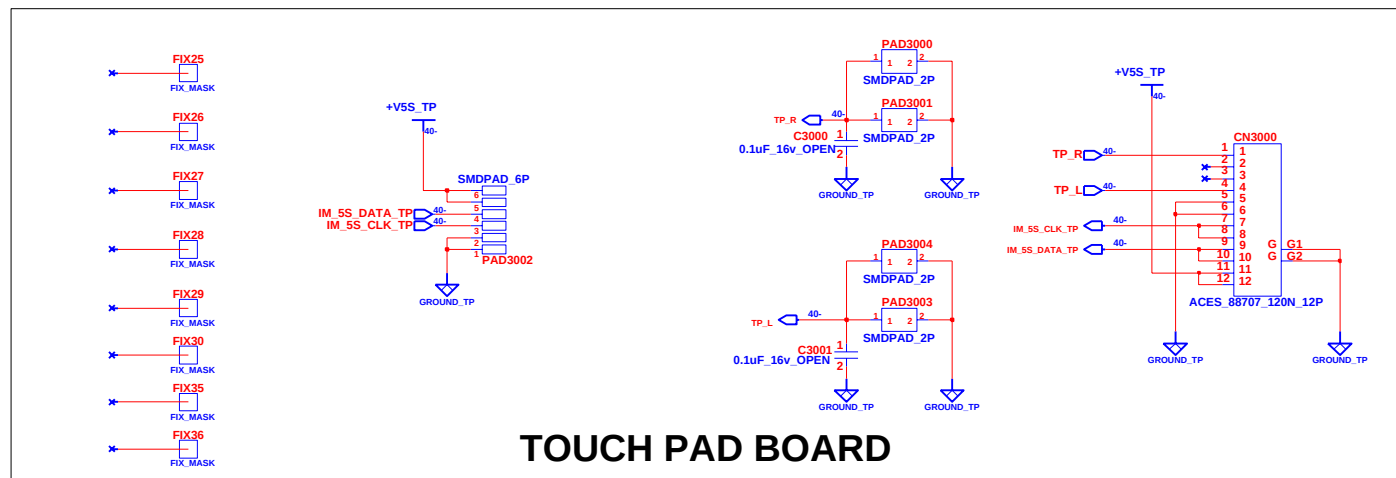
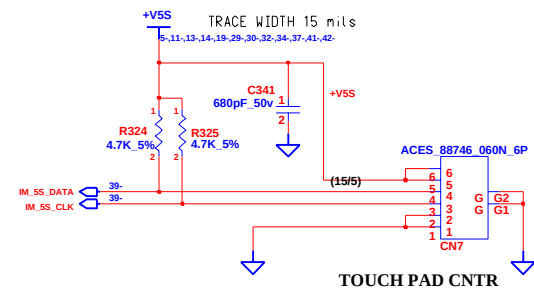
INVENTEC

TITLE
DDD Discrete

SIZE CODE DOC. NUMBER REV
A3 CS Model No A01

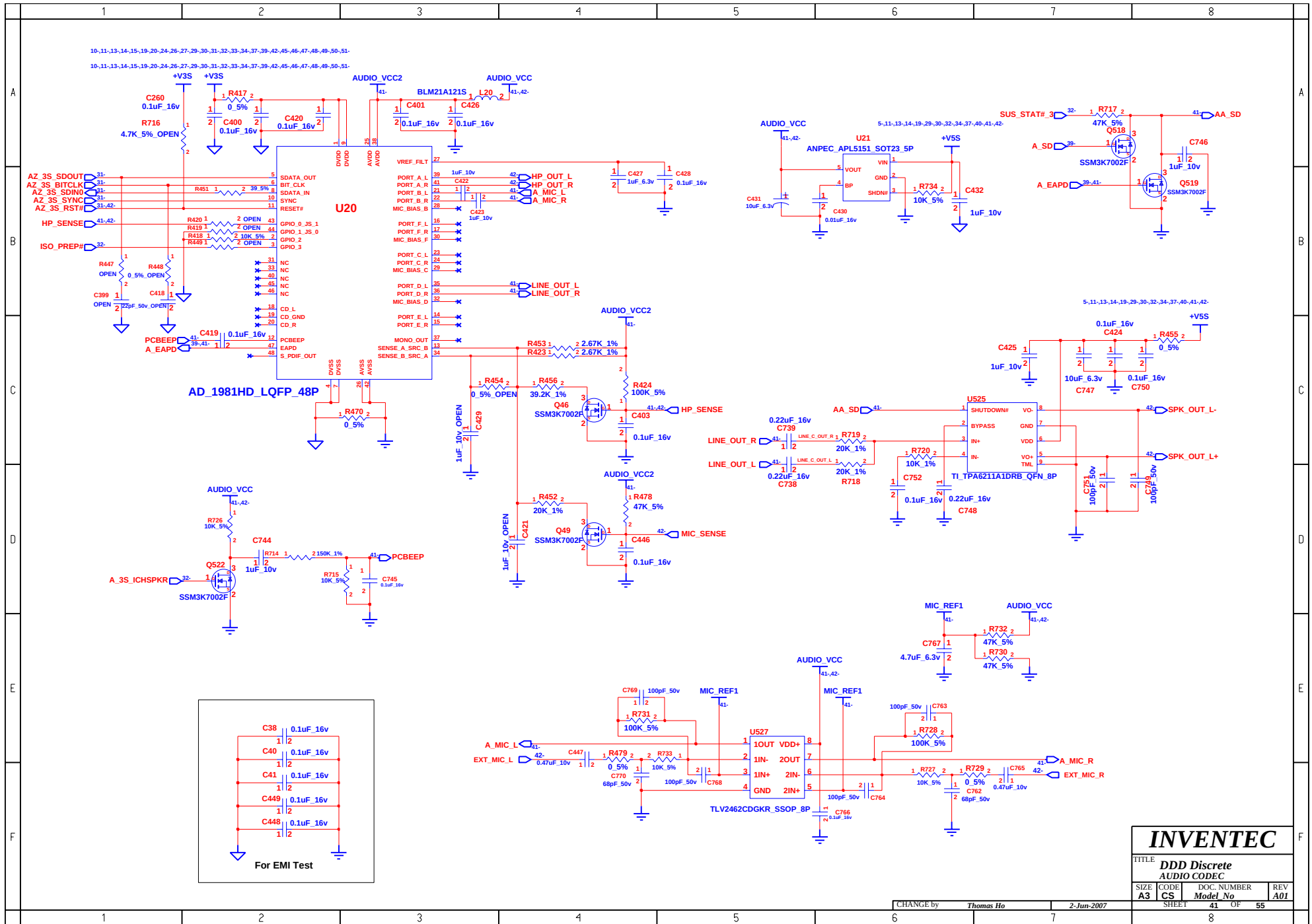
CHANGE by **Thomas Ho** 13-Jun-2007

SHEET **39** OF **55**



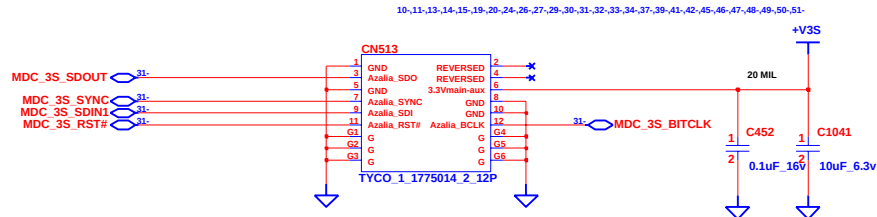
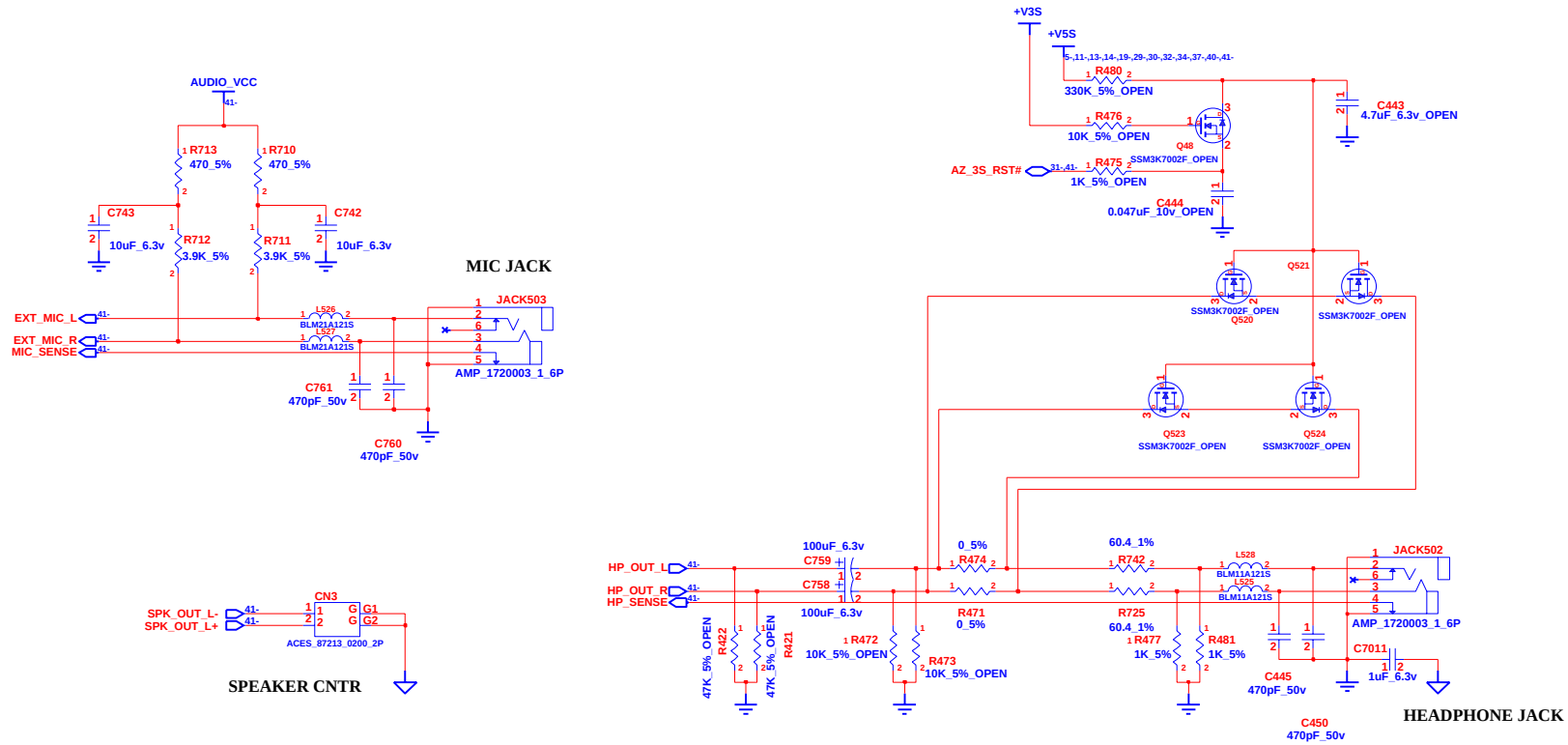
TITLE DDD Discrete KB & TP CONN			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV A01
SHEET 40 OF 55			

CHANGE by	<i>Thomas Ho</i>	2-Jun-2007
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1 2 3 4 5 6 7 8

10-11-13-14-15-19-20-24-26-27-29-30-31-32-33-34-37-39-41-42-45-46-47-48-49-50-51-

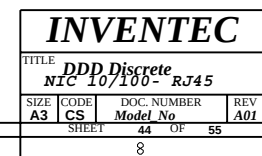


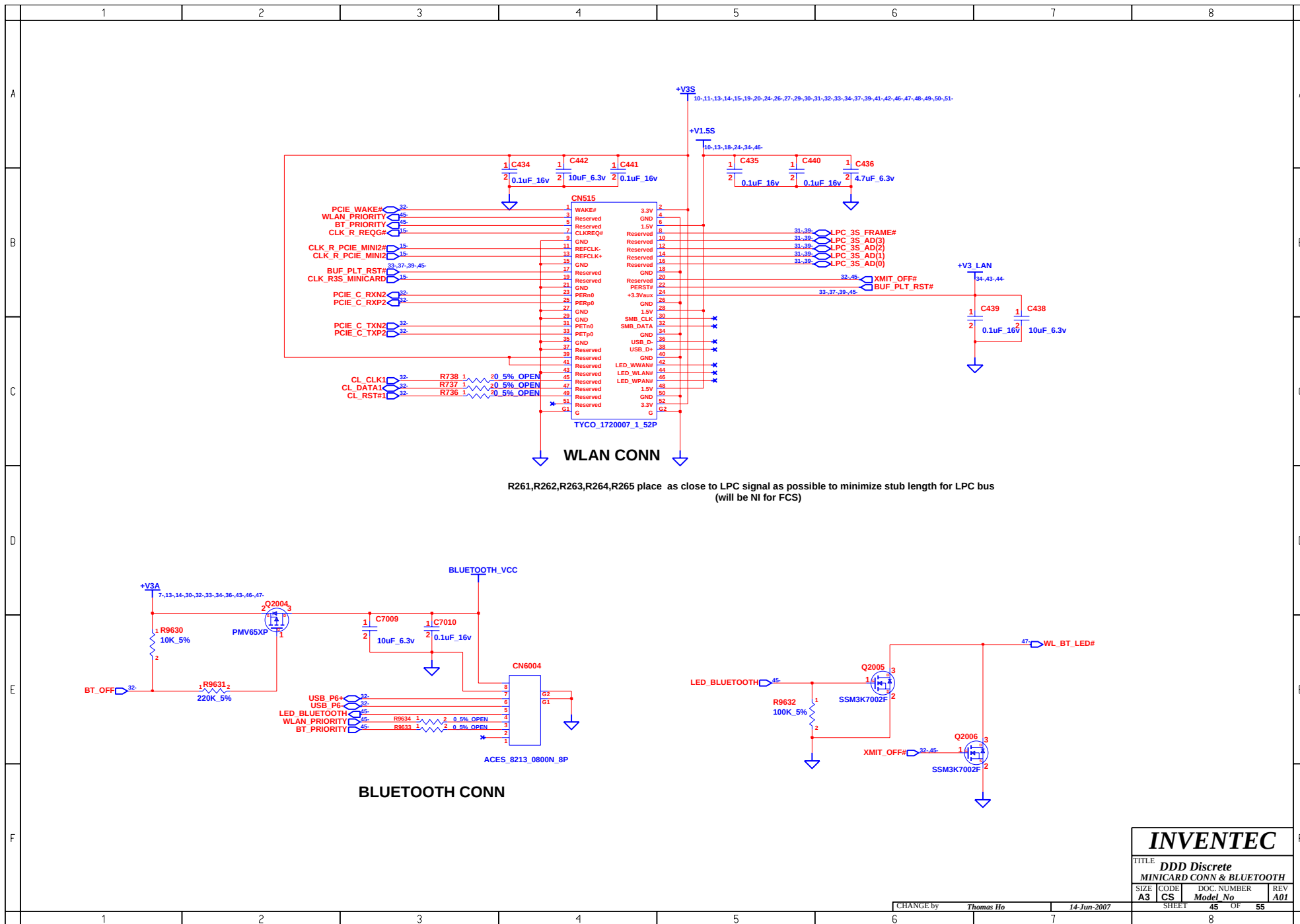
MDC CNTR

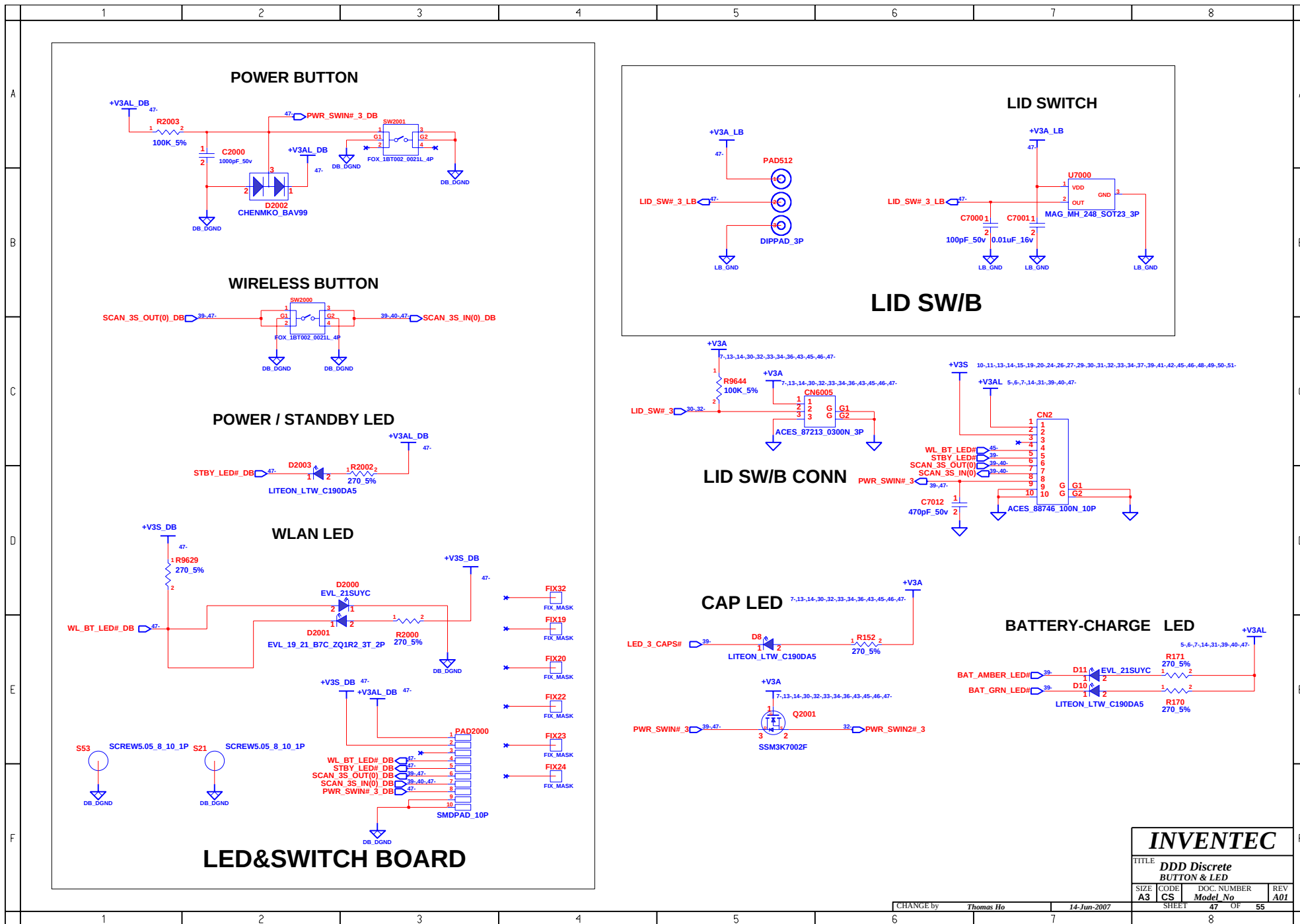
INVENTEC

TITLE DDD Discrete MDC CNTR			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A01
CHANGE by Thomas Ho		4-Jun-2007	
SHEET 42		OF 55	

1 2 3 4 5 6 7 8







INVENTEC

TITLE DDD Discrete BUTTON & LED			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A01

CHANGE by Thomas Ho 14-Jun-2007

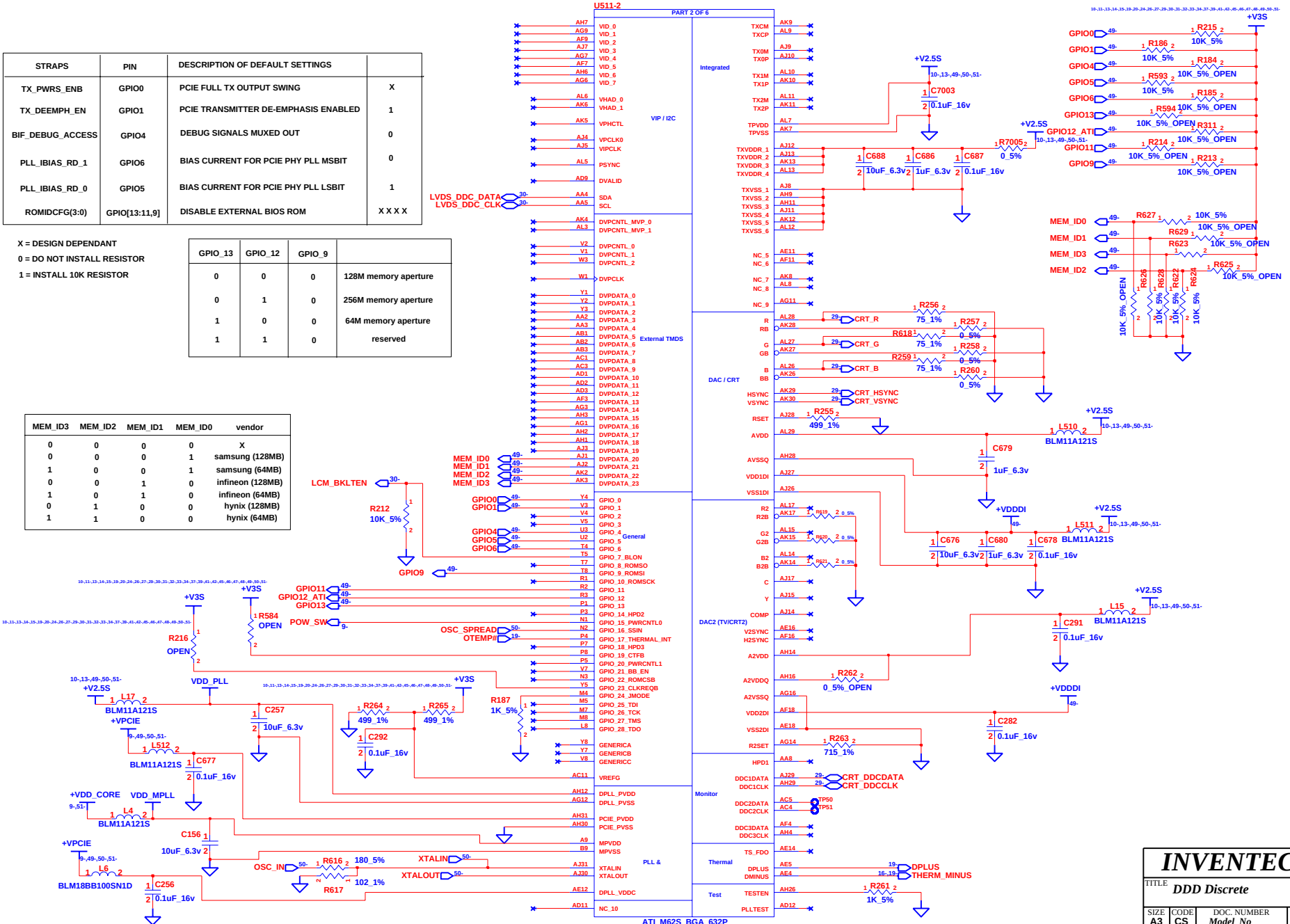
SHEET 47 OF 55

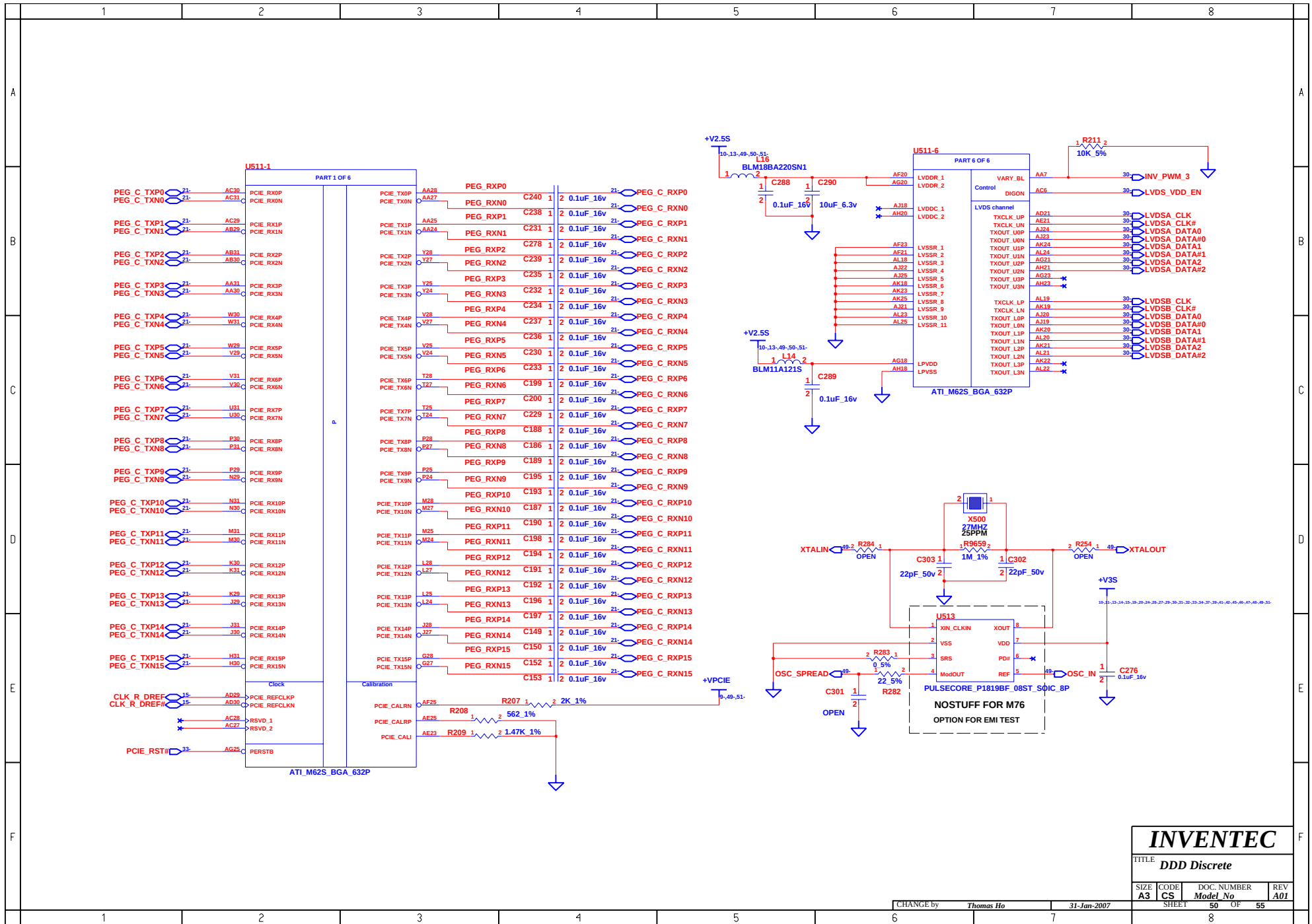
STRAPS	PIN	DESCRIPTION OF DEFAULT SETTINGS	
TX_PWRS_ENB	GPIO0	PCIE FULL TX OUTPUT SWING	X
TX_DEEMPH_EN	GPIO1	PCIE TRANSMITTER DE-EMPHASIS ENABLED	1
BIF_DEBUG_ACCESS	GPIO4	DEBUG SIGNALS MUXED OUT	0
PLL_IBIAS_RD_1	GPIO6	BIAS CURRENT FOR PCIE PHY PLL MSBIT	0
PLL_IBIAS_RD_0	GPIO5	BIAS CURRENT FOR PCIE PHY PLL LSBIT	1
ROMIDCFG(3:0)	GPIO[13:11,9]	DISABLE EXTERNAL BIOS ROM	X X X X

X = DESIGN DEPENDANT
0 = DO NOT INSTALL RESISTOR
1 = INSTALL 10K RESISTOR

GPIO_13	GPIO_12	GPIO_9	
0	0	0	128M memory aperture
0	1	0	256M memory aperture
1	0	0	64M memory aperture
1	1	0	reserved

MEM_ID3	MEM_ID2	MEM_ID1	MEM_ID0	vendor
0	0	0	0	X
0	0	0	1	samsung (128MB)
1	0	0	1	samsung (64MB)
0	0	1	0	infineon (128MB)
1	0	1	0	infineon (64MB)
0	1	0	0	hynix (128MB)
1	1	0	0	hynix (64MB)

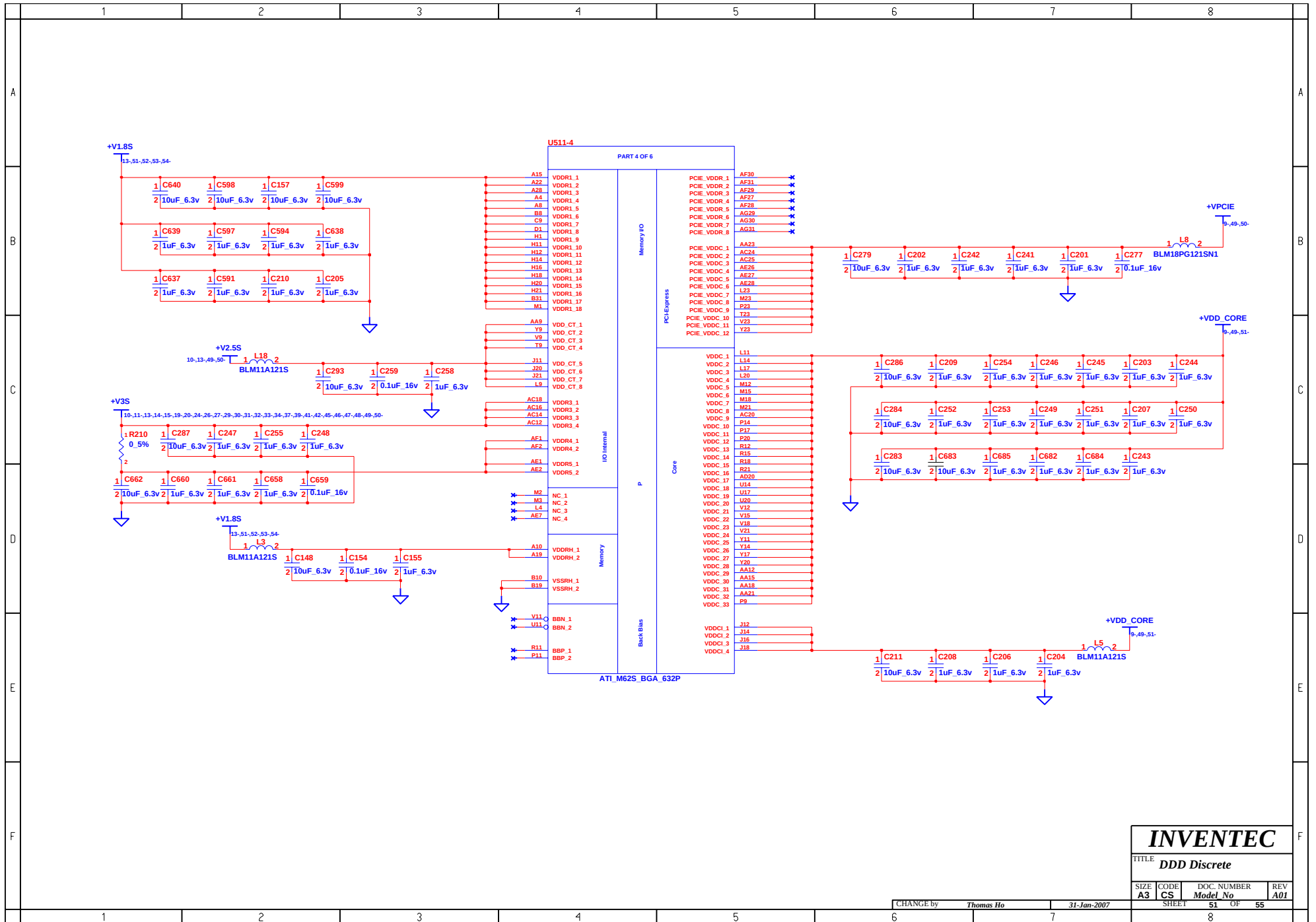




INVENTEC

TITLE
DDD Discrete

SIZE CODE CS DOC. NUMBER REV
A3 Model No 50 OF 55 A01



INVENTEC

TITLE
DDD Discrete

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01

CHANGE by Thomas Ho 31-Jan-2007

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