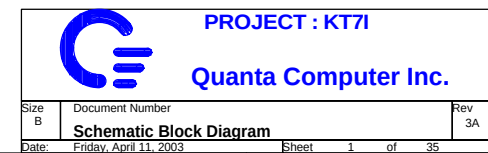
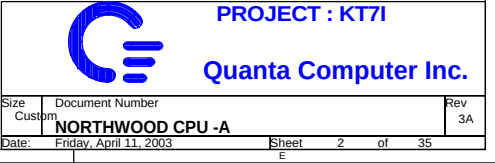
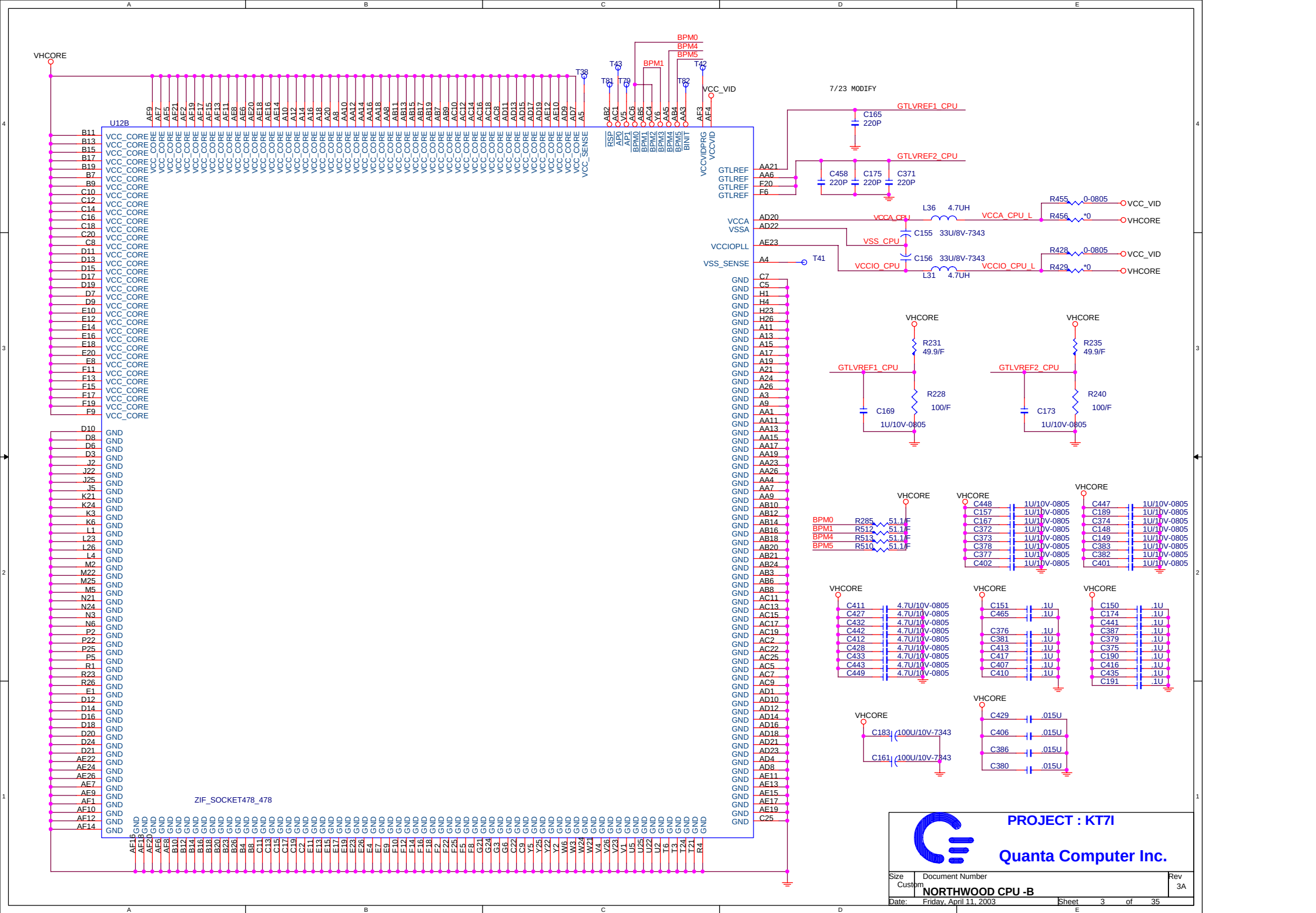


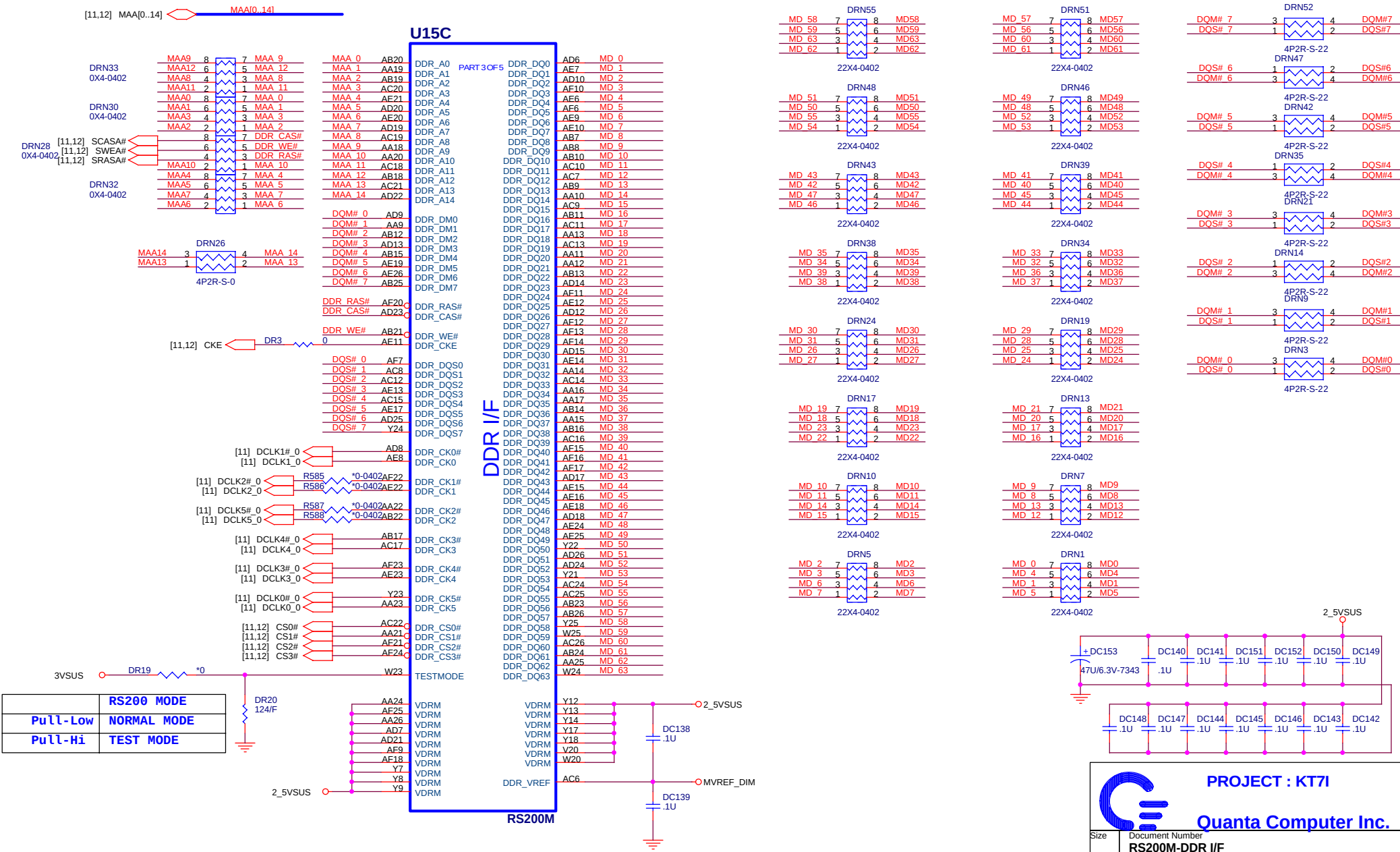
		7	8
MINI PCI	REQ 0	GNT 0	INT C, D
PCI HUB	REQ 1	GNT 1	
LAN	REQ 2	GNT 2	INT B
1394	REQ 3	GNT 3	INT F
PCMCIA6912	REQ 4	GNT 4	INT F
PCMCIA6933	REQ 4	GNT 4	INT F
USB 2.0	REQ 5	GNT 5	INT C, D, E

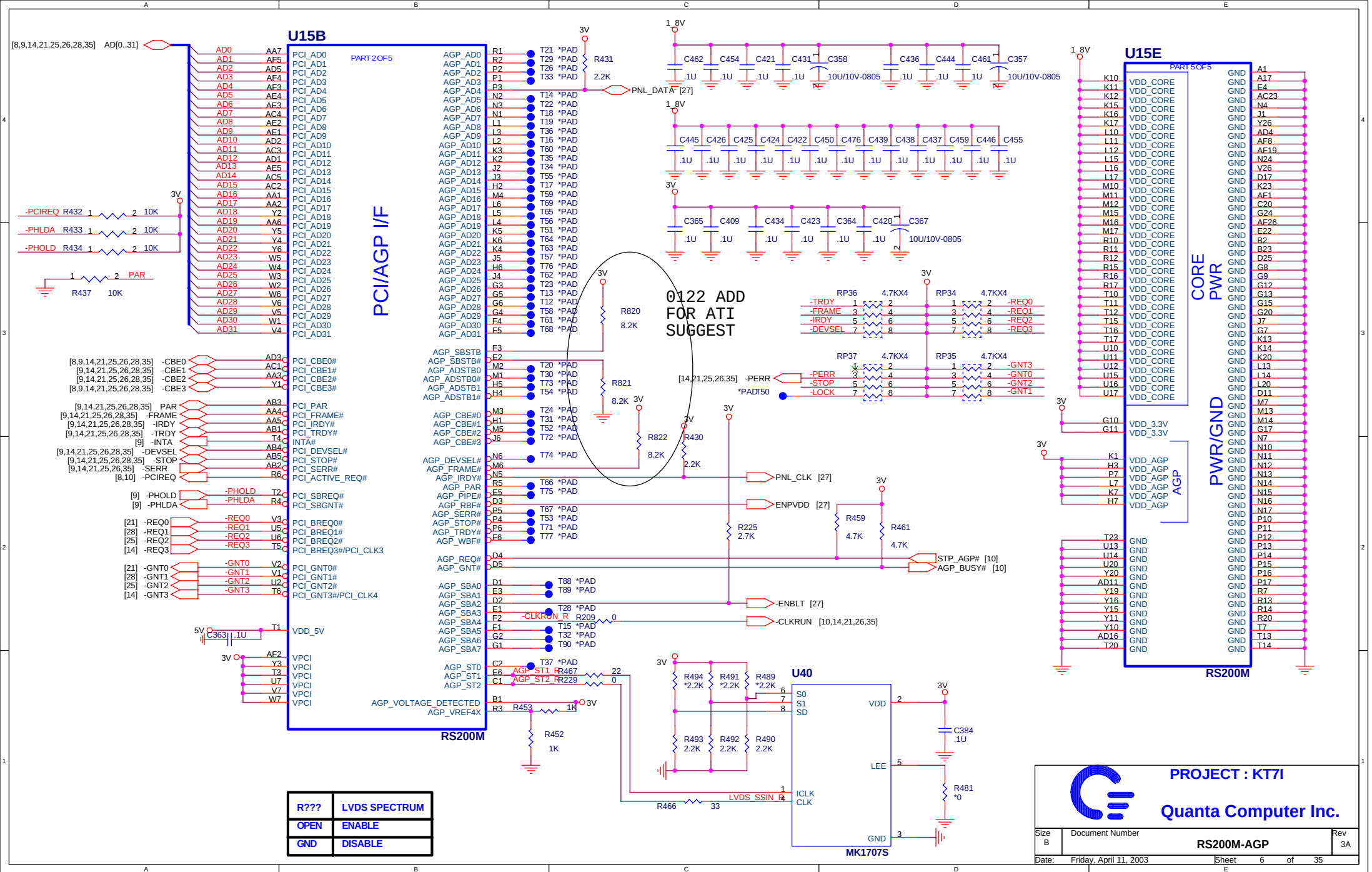




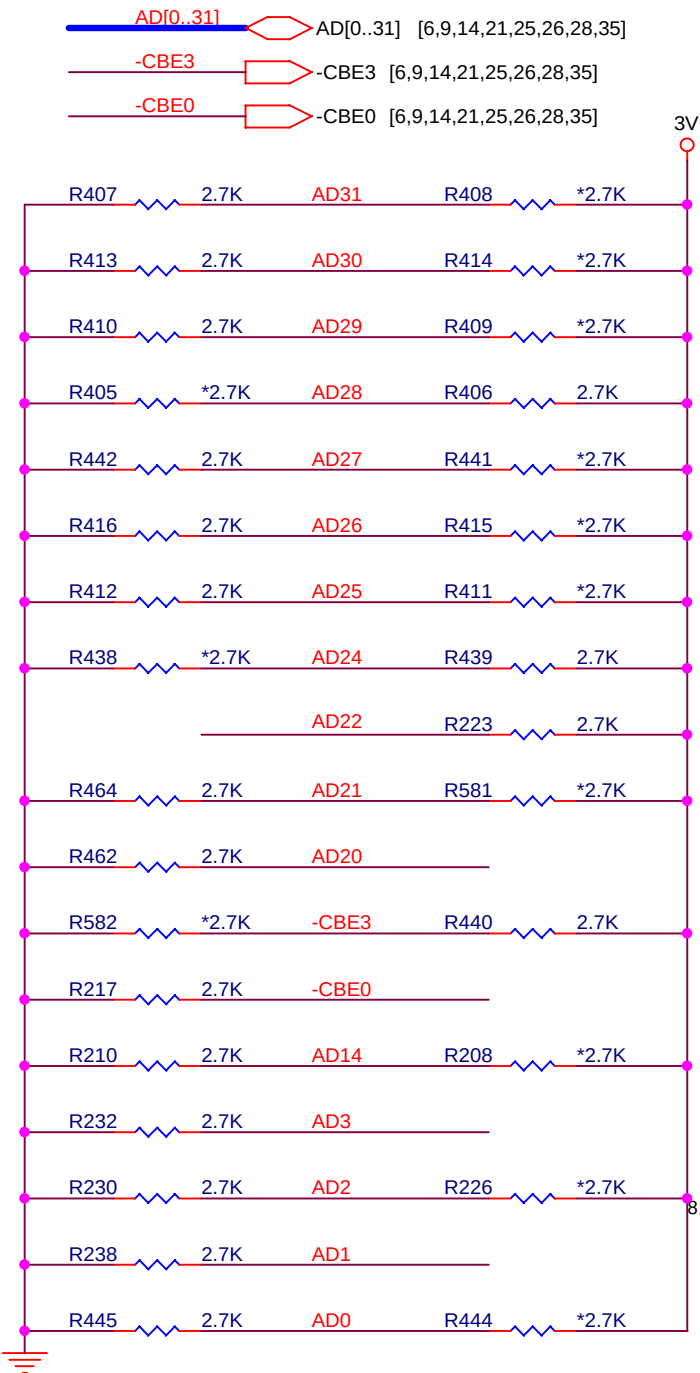


ATI_RS200M - 2 (DDR Interface)



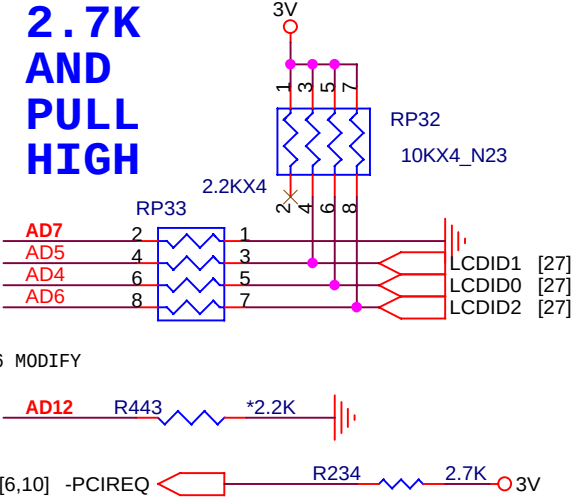


SYSTEM CONFIG. & N.B H/W STRAPPING



AD[30..31] : CLK SPEED DEFAULT: 00		00: 100 MHZ 01: 133 MHZ	10: 166MHZ 11: 66 MHZ	AD15 : RESERVED	
AD29: RS200M FULL CONFIGURATION DEFAULT:1		0: FULL CONFIGURATION 1: USES DEFAULT VALUES		AD14 : AGTL+ VDDQ DEFAULT: 0	
AD28: SPREAD SPECTRUM ENABLE DEFAULT:0		0: DISABLE 1: ENABLE		AD13: RESERVED	
AD27: MCERR OBSERVATION DEFAULT: 1		0: DISABLE 1: ENABLE		AD[12] : FLAT PANEL ID MSB	
AD26: QUICK START SELECT DEFAULT : 1		0: DISABLE 1: ENABLE		AD10 : RESERVED	
AD25: INIT OBSERVATION DEFAULT : 1		0: DISABLE 1: ENABLE		AD09 : RESERVED	
AD24: IOQ ENABLE DEFAULT : 1		0: DISABLE 1: ENABLE		AD[7..4] : FLAT PANEL ID	
AD23 : RESERVED				AD3: PCI66 MODE DEFAULT: 0	
AD22: 27MHz/33 MHz CLOCK SELECTION DEFAULT : 1		0: 33 MHz CLOCK OUTPUT 1: 27 MHz CLOCK INPUT		AD2: CAL DEFAULTS FOR CPU DEFAULT: 1 (FOR BRING UP)	
AD21 : RESERVED		0: PLL Bypass mode disable(Normal mode) 1: PLL Bypass mode enable(Debug only)		AD1: FrcShortReset DEFAULT: 0	
AD20 : PCICLK EXPANSION DEFAULT : 0		1: PCI_REQ#3, PCI_GNT#3 USED AS PCICLK 0: PCI_REQ#3, PCI_GNT#3 USED AS REQ/GNT		AD0: PCI/AGP CONFIG. DEFAULT : 1	
CBE#3: PROD TEST DEFAULT : 1		0: SHORT TIMERS 1: NORMAL OPERATION		PCI_PREQACT#: INTERNAL CLOCK GEN. DEFAULT : 1	
				0: DISABLE 1: ENABLE	

2.7K
AND
PULL
HIGH



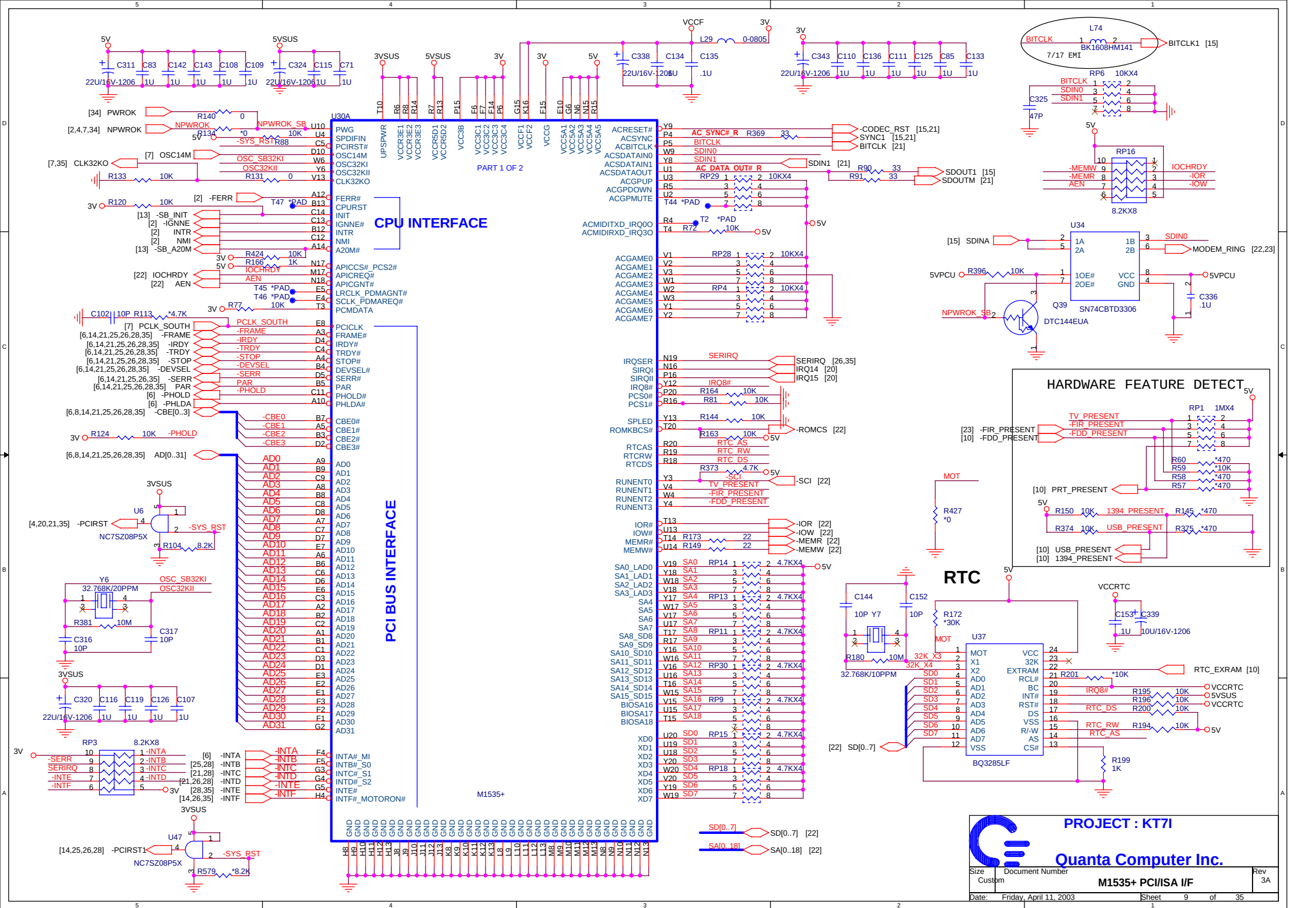
Panel IDs

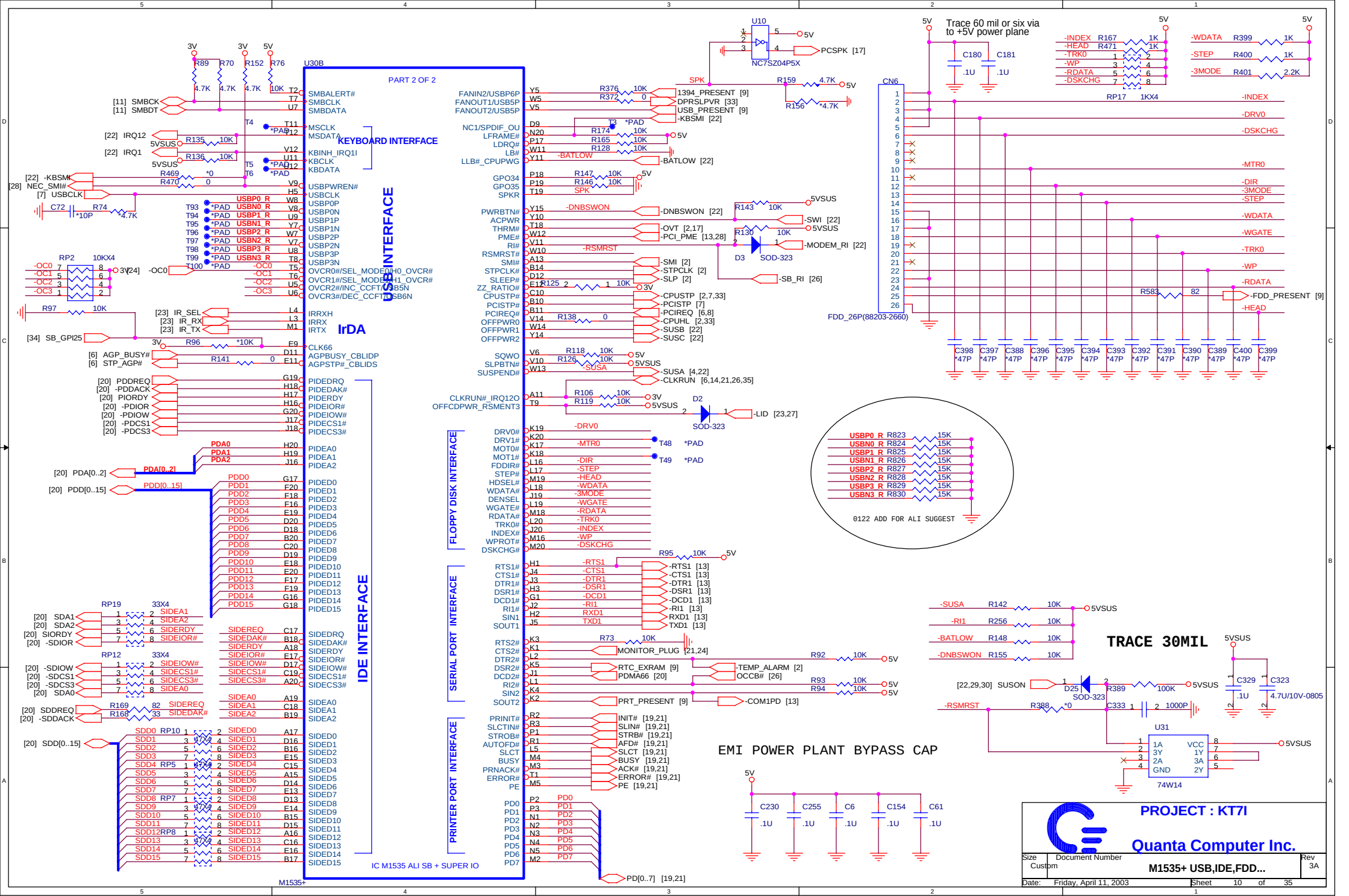


PROJECT : KT7I

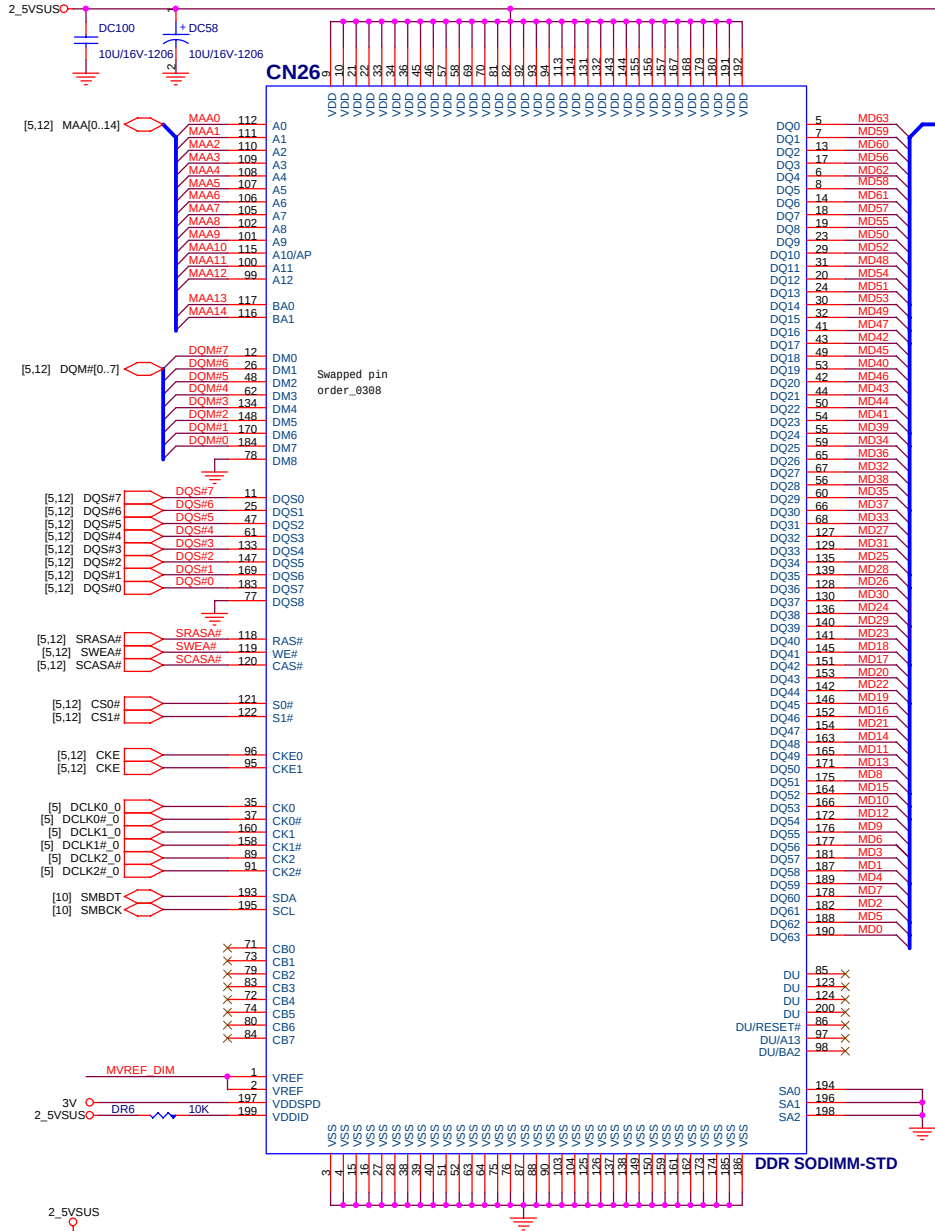
Quanta Computer Inc.

Size Custom	Document Number RS200M config setting	Rev 3A
Date: Friday, April 11, 2003	Sheet 8 of 35	

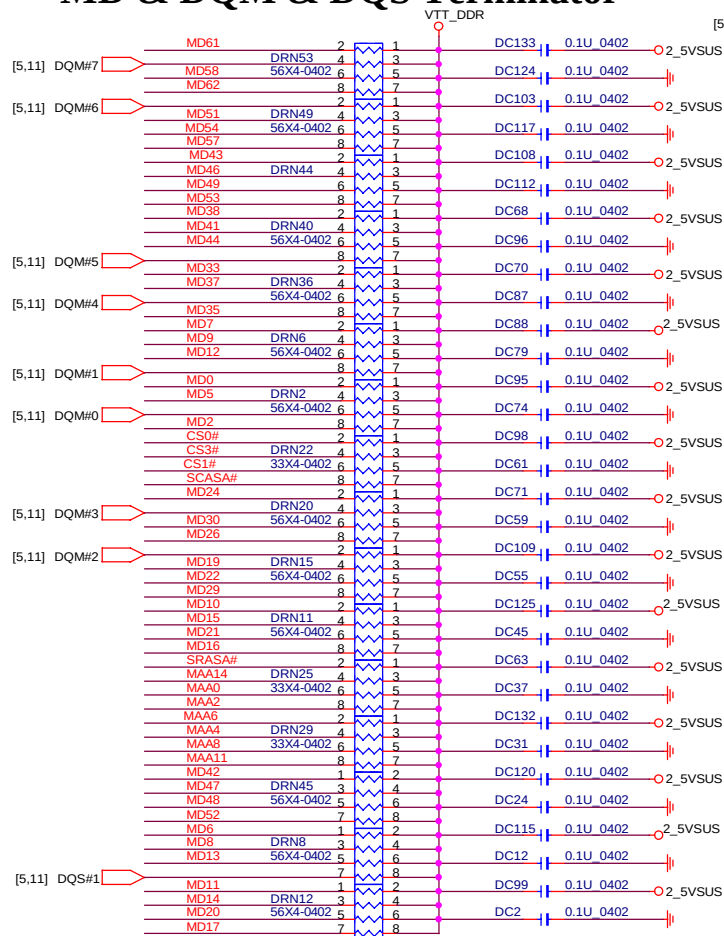




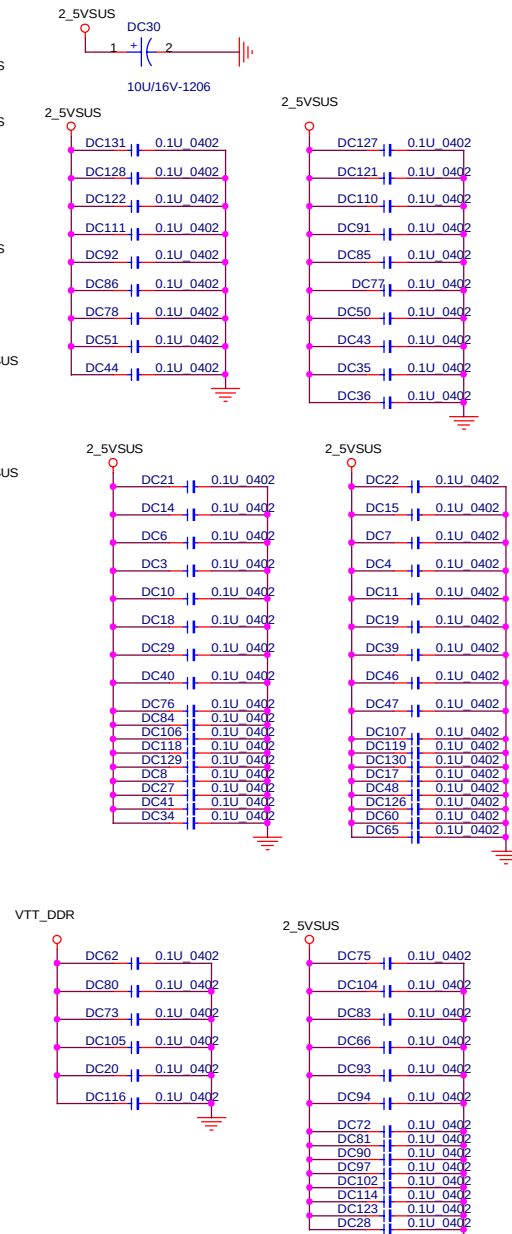
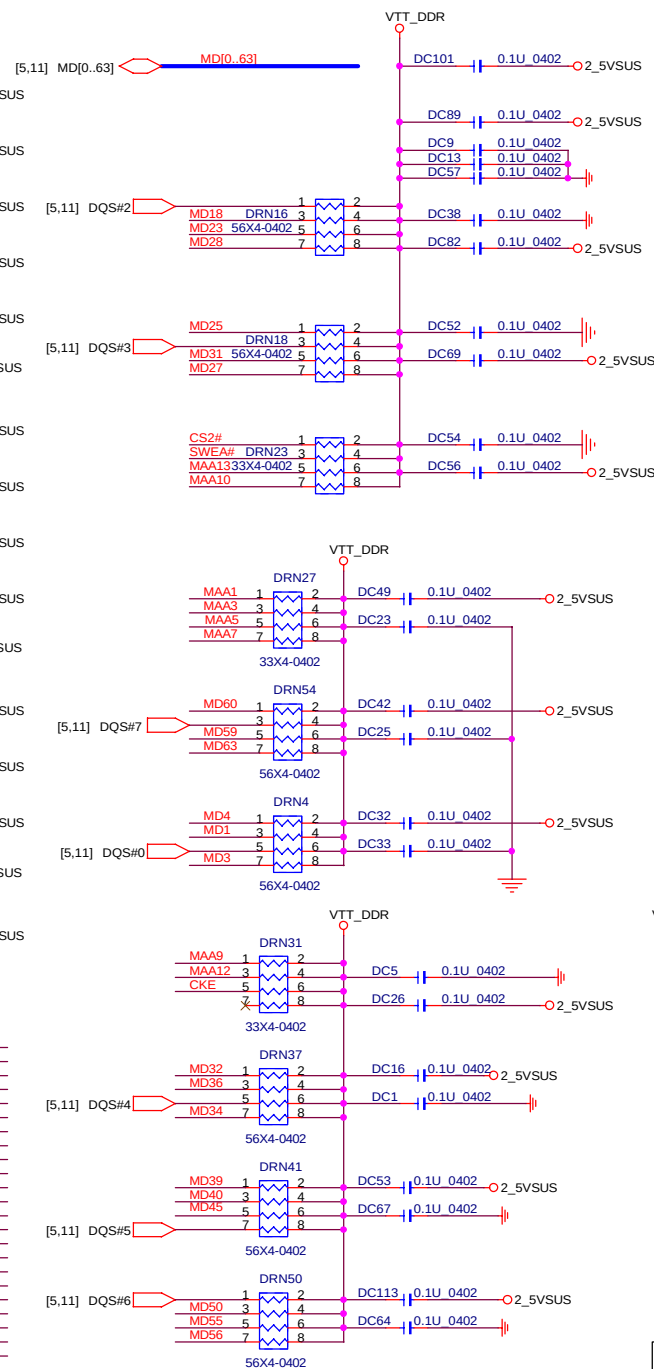
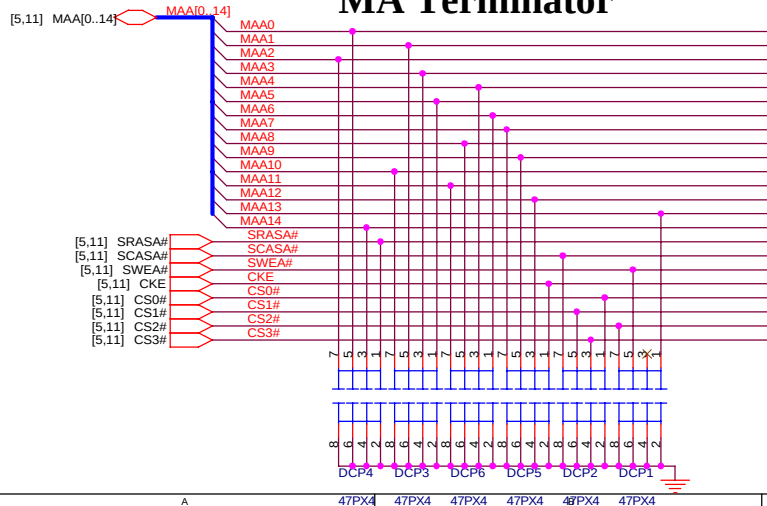
DDR - Modules



MD & DQM & DQS Terminator



MA Terminator

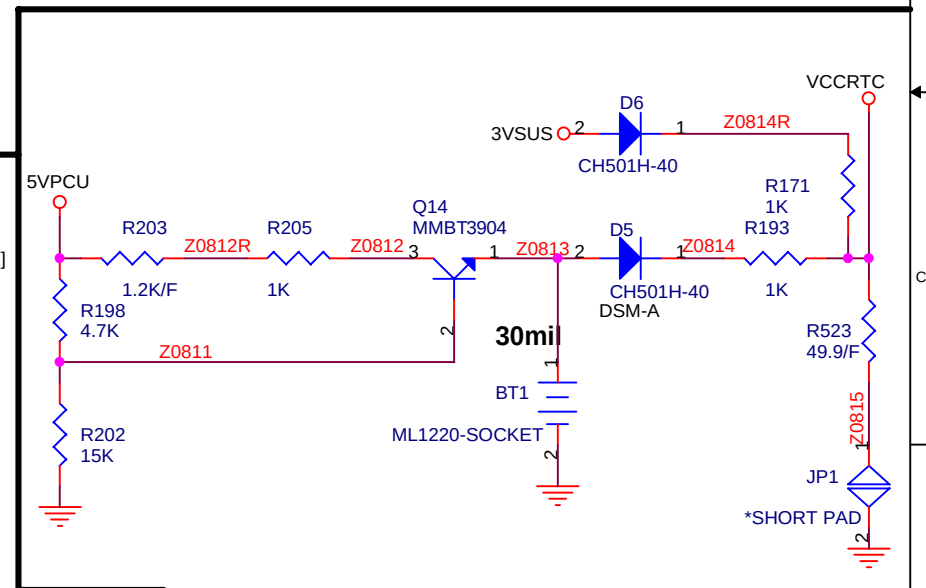
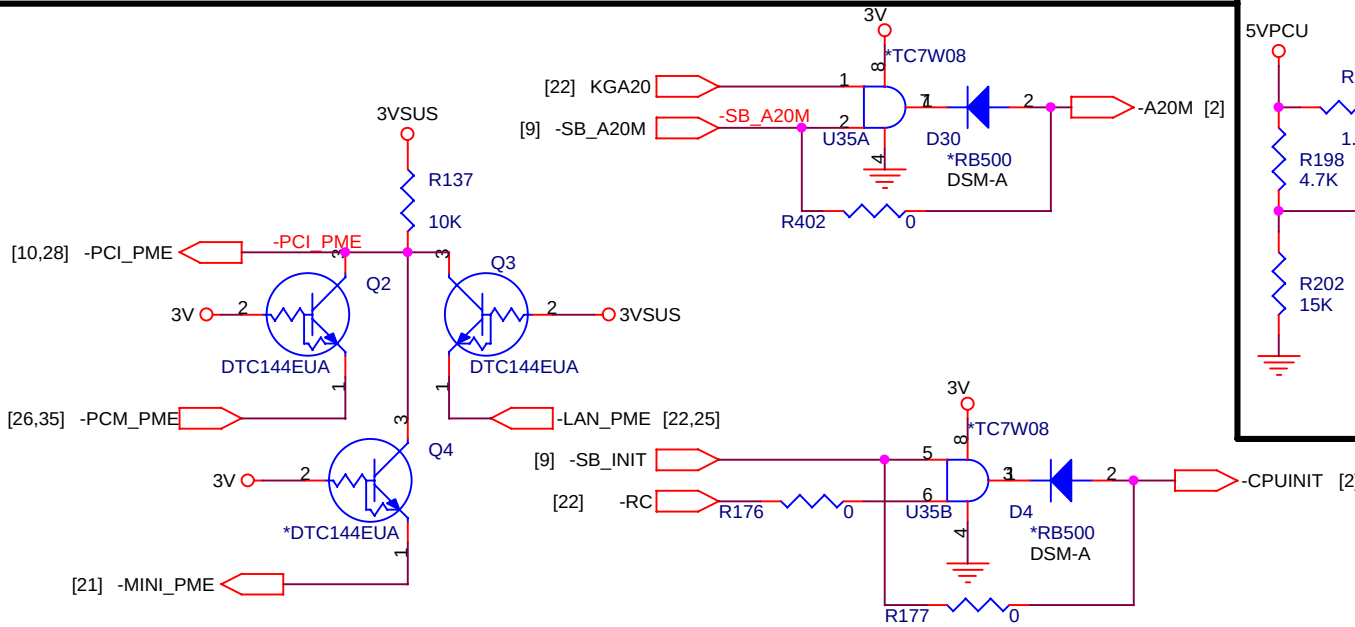
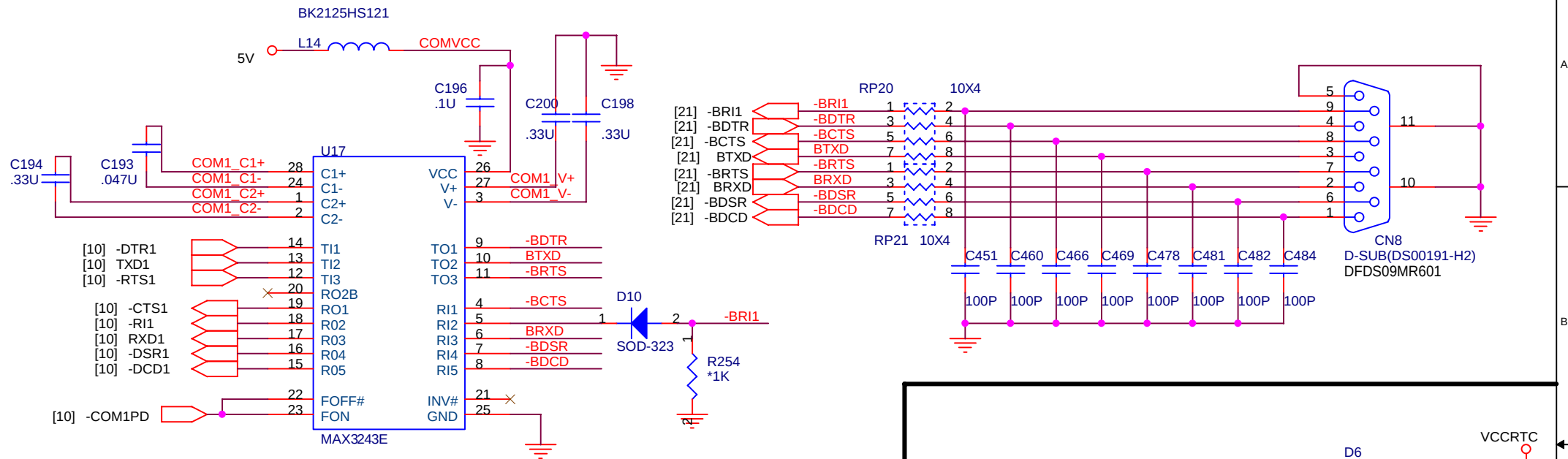


PROJECT : KT7I

Quanta Computer Inc.

Size	Document Number DDR TERMINATOR	Rev 3A
Date:	Friday, April 11, 2003	Sheet 12 of 35

SERIAL PORT

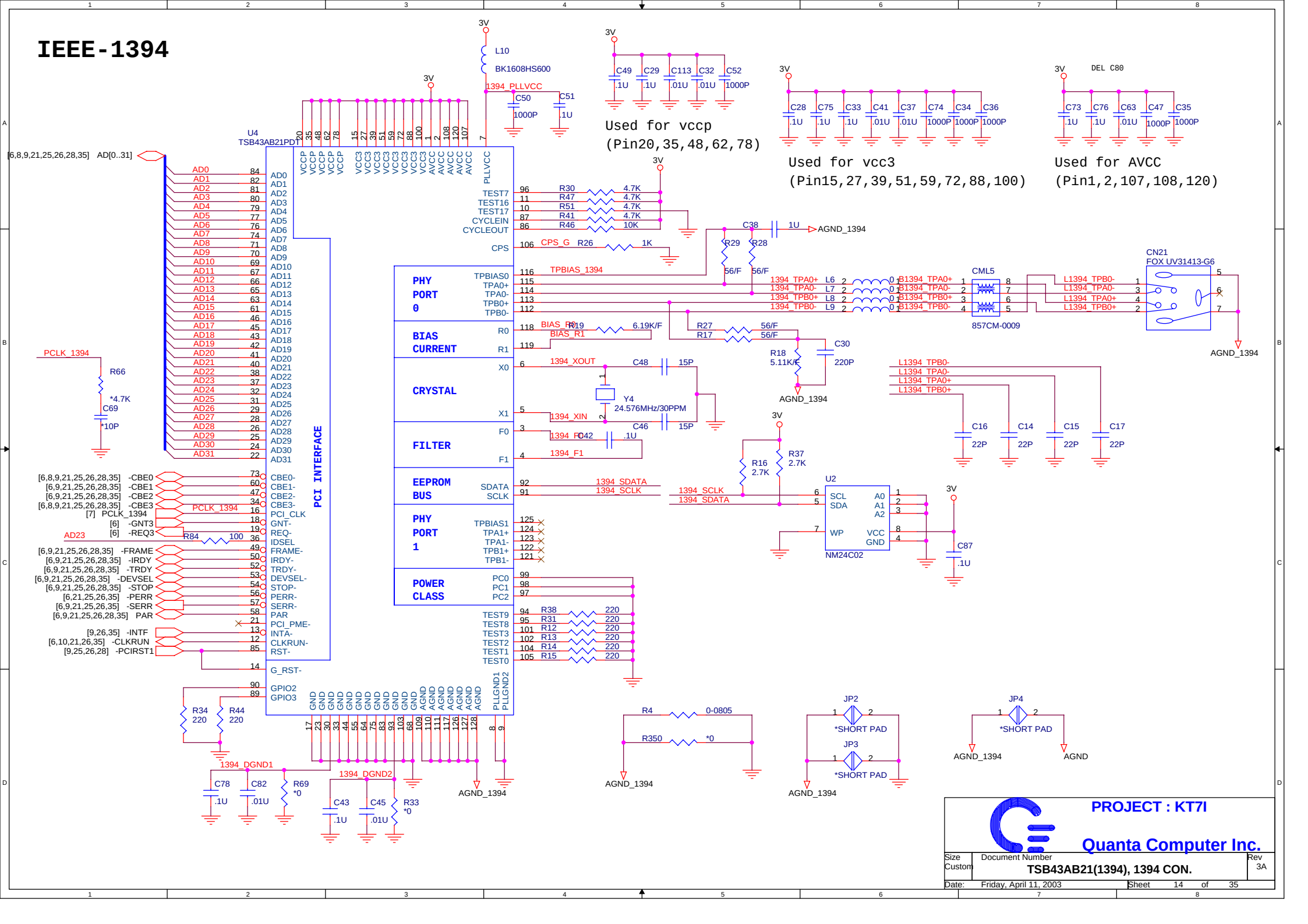


PROJECT : KT71

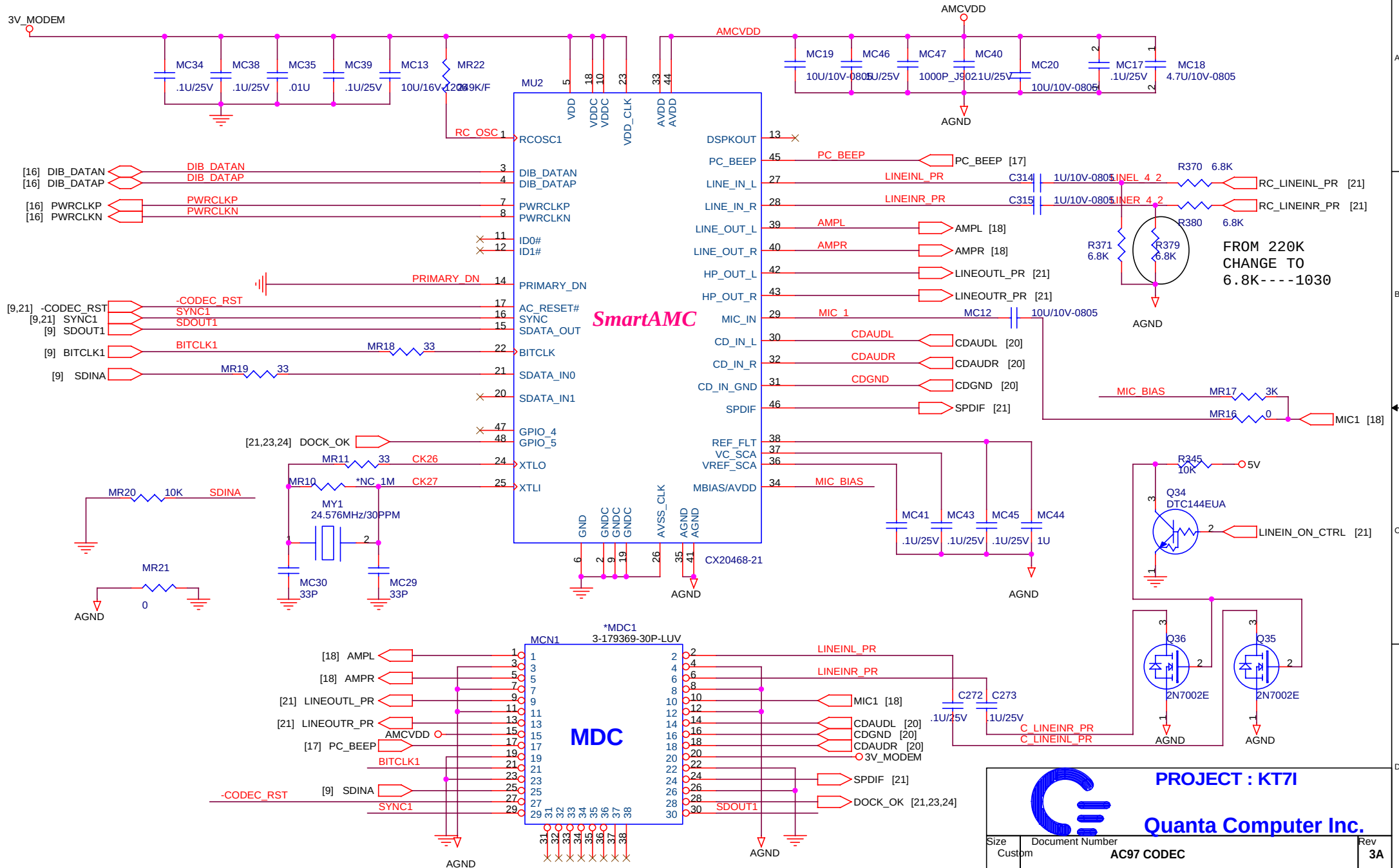
Quanta Computer Inc.

Size Custom	Document Number	Rev 3A
SERIAL PORT&RTC		
Date: Friday, April 11, 2003	Sheet 13 of 35	

IEEE - 1394



The AMC20493-001 modem is used for mother board family MBAMC20493-005



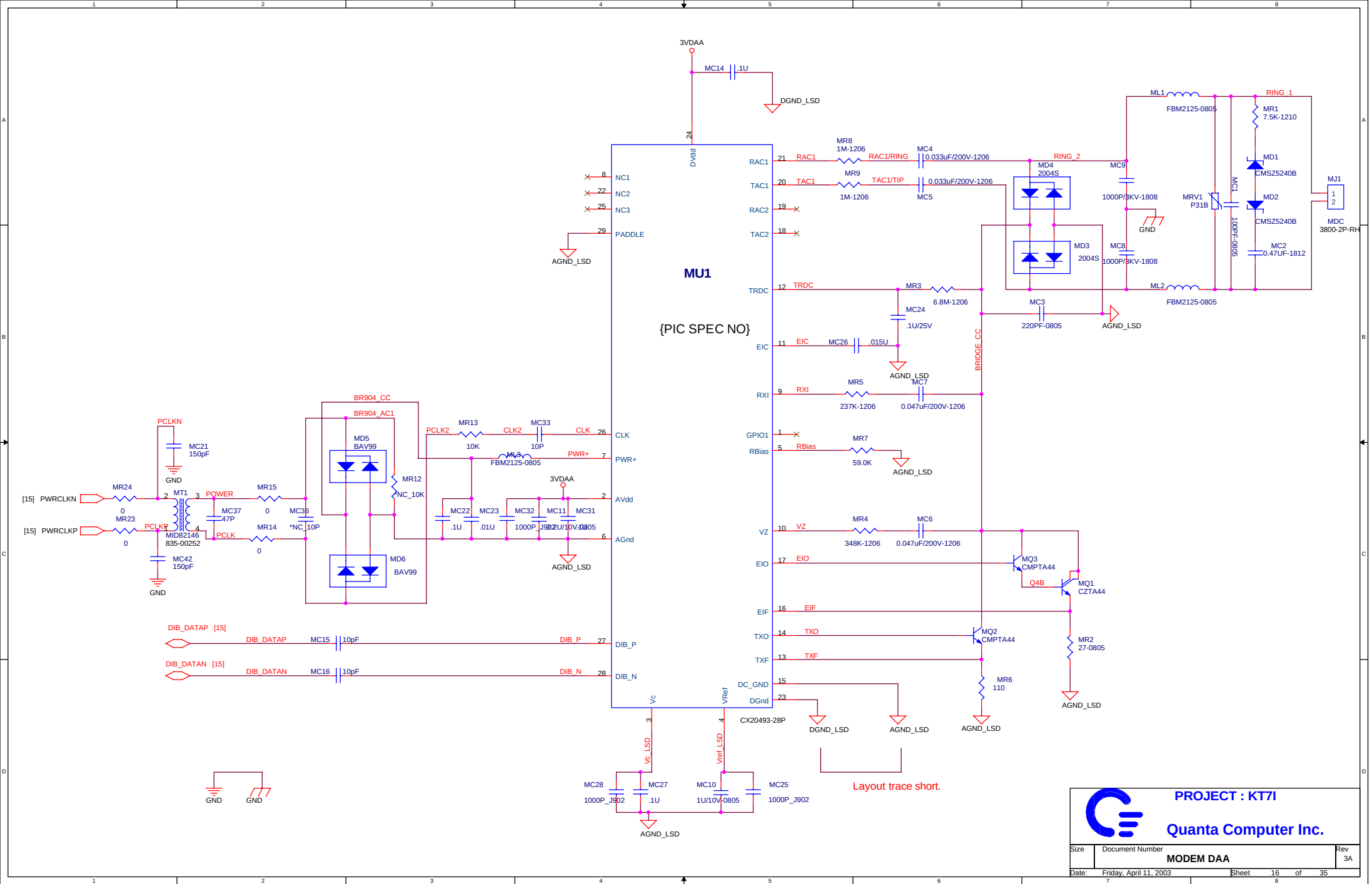
FROM 220K
CHANGE TO
6.8K----



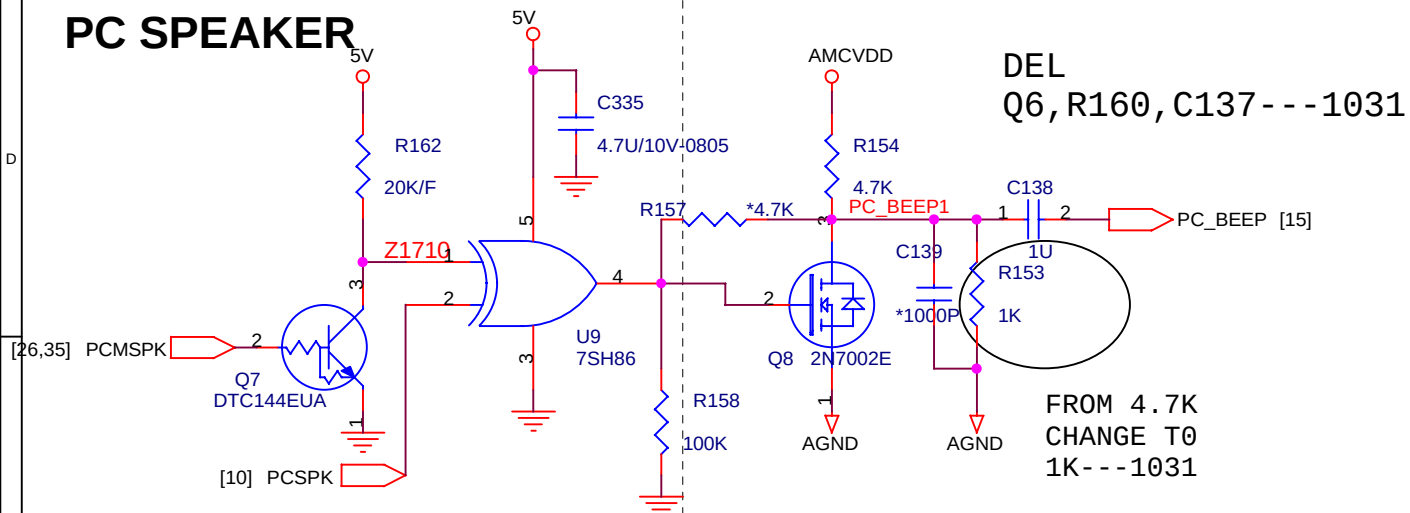
PROJECT : KT7I

Quanta Computer Inc.

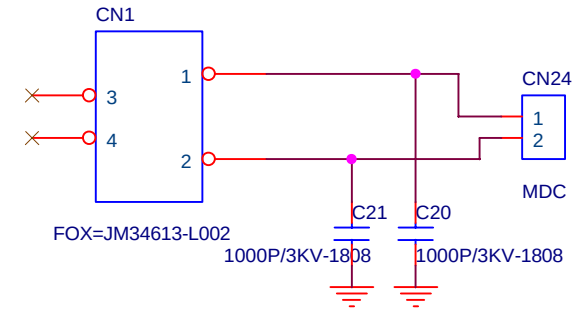
Size	Document Number	Rev
Custom	AC97 CODEC	3A
Date:	Friday, April 11, 2003	Sheet 15 of 35



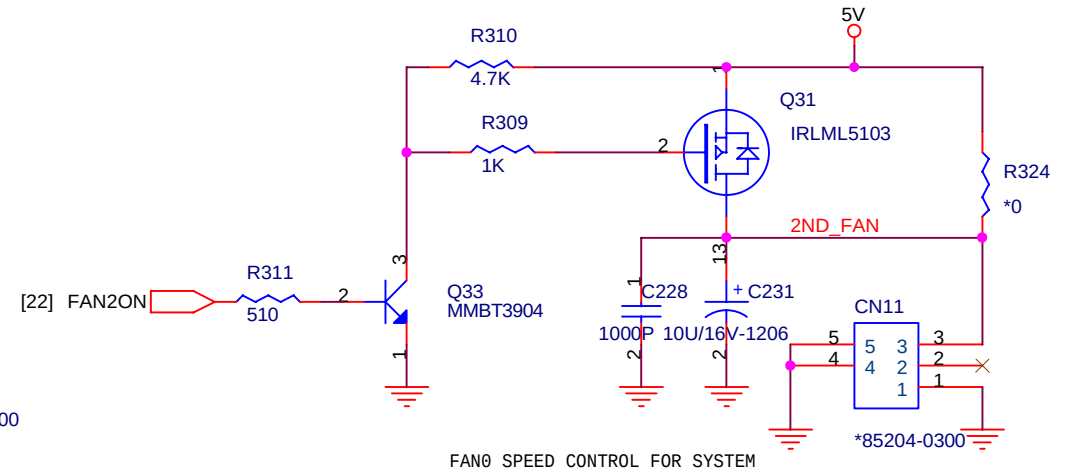
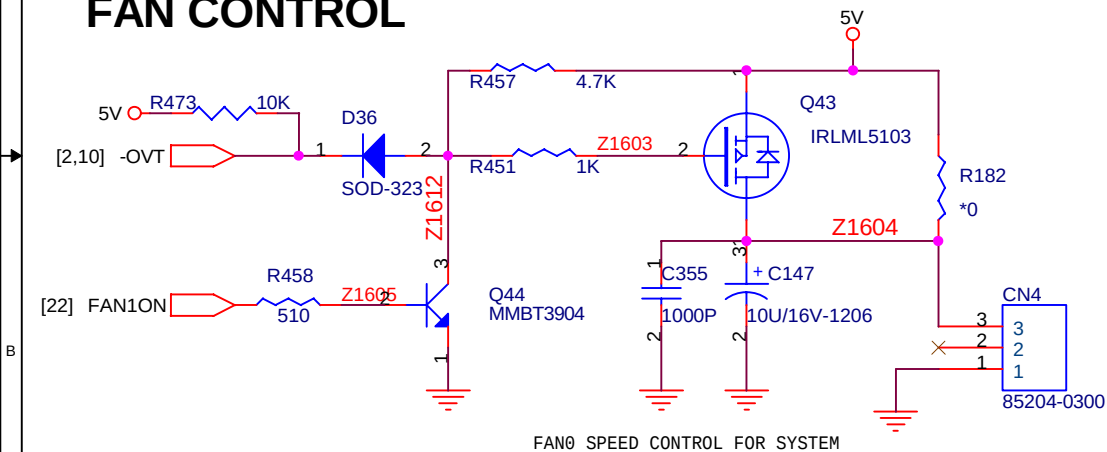
PC SPEAKER



RJ11 CONNECTOR

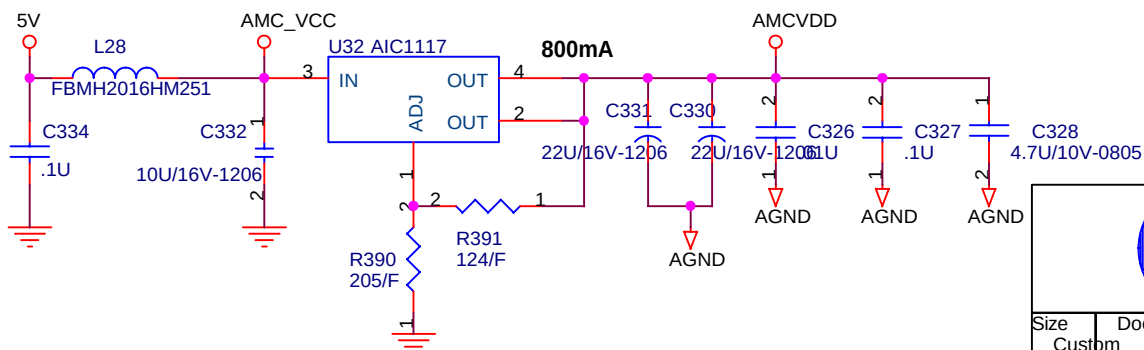


FAN CONTROL



MODEM VDD

R362, R361, R343, R32
MOVE TO PAGE 18



$$V_{out} = 1.25 * (R_{429}/R_{428} + 1) + 55u * R_{429} = 3.3V$$



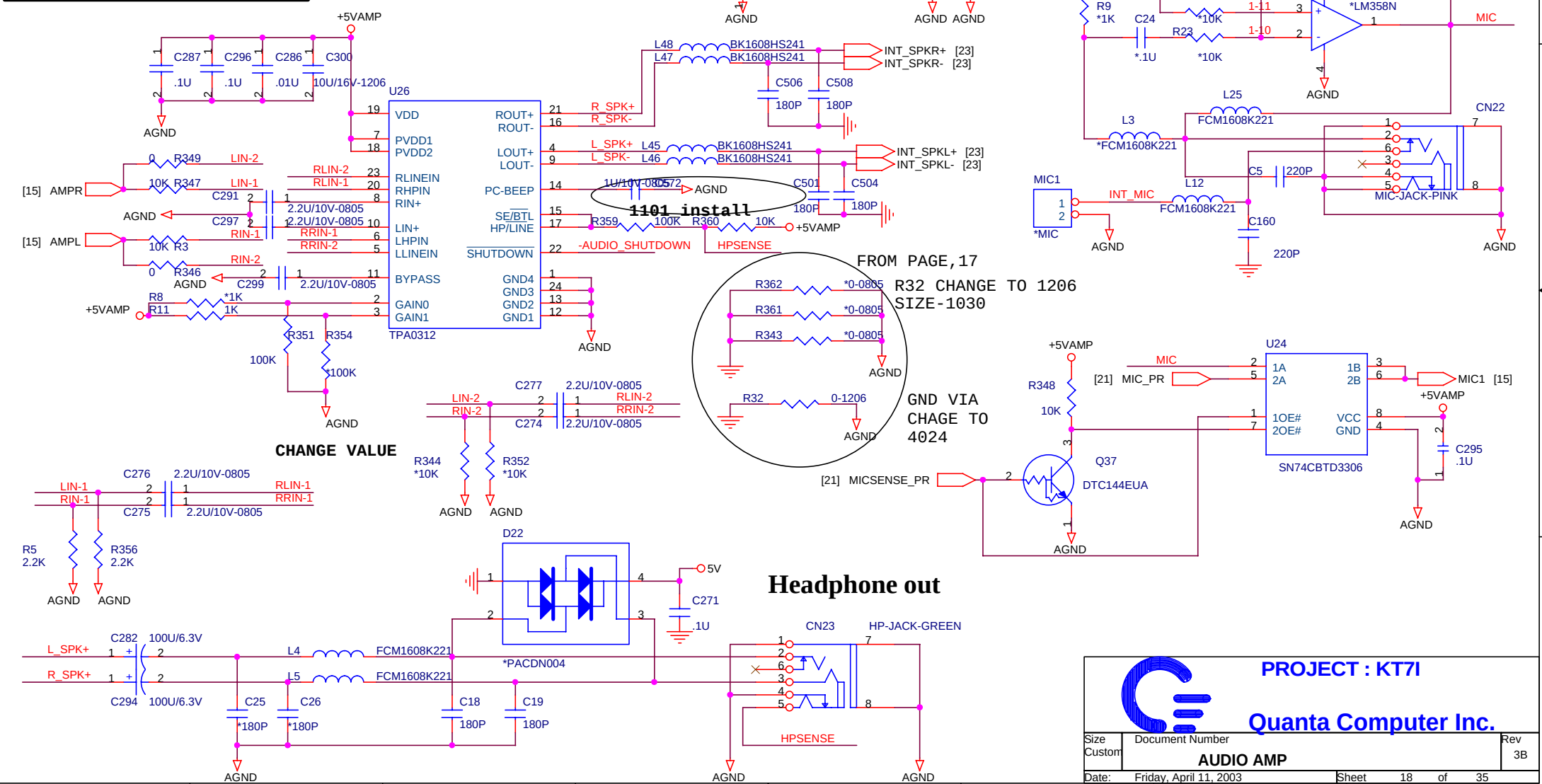
PROJECT : KT7I

Quanta Computer Inc.

Size Custom	Document Number FAN/RJ11/BEEP	Rev 3A
Date: Friday, April 11, 2003	Sheet 17 of 35	

Audio amplifier

Gain0	Gain1	Av
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB



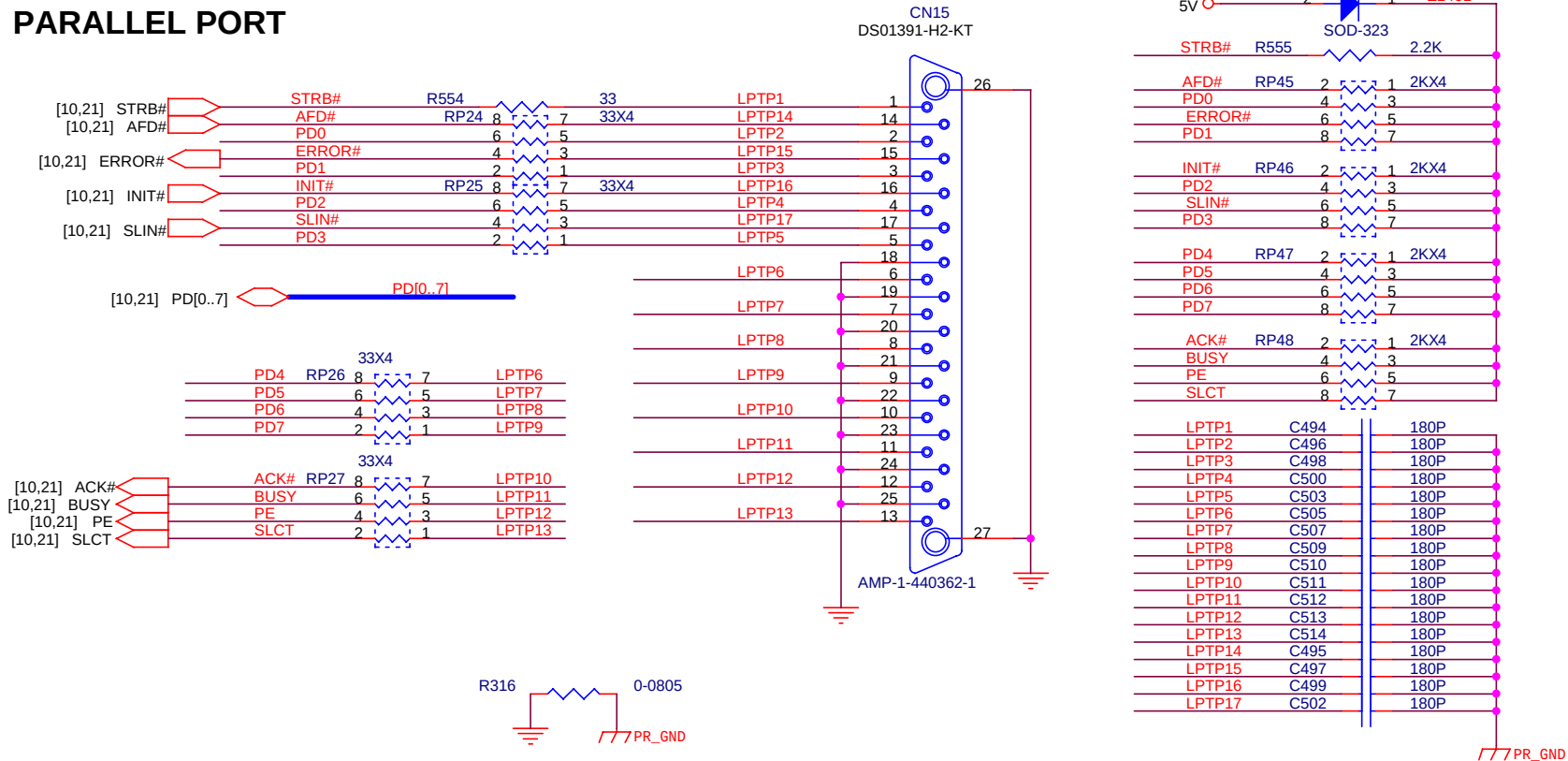


PROJECT : KT7I

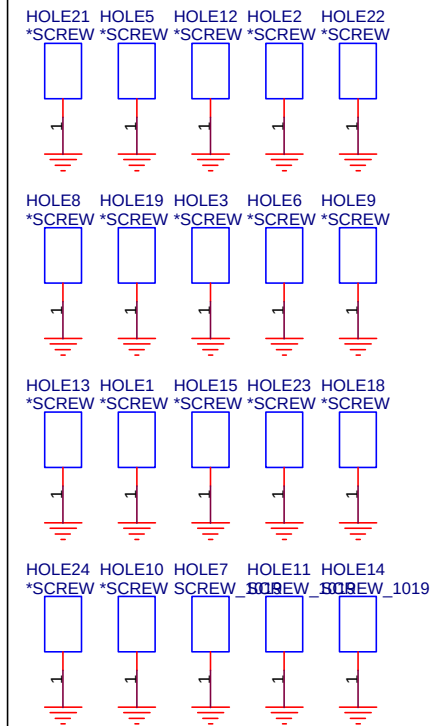
Quanta Computer Inc.

Size Custom	Document Number	Rev 3B
AUDIO AMP		
Date: Friday, April 11, 2003	Sheet 18 of 35	

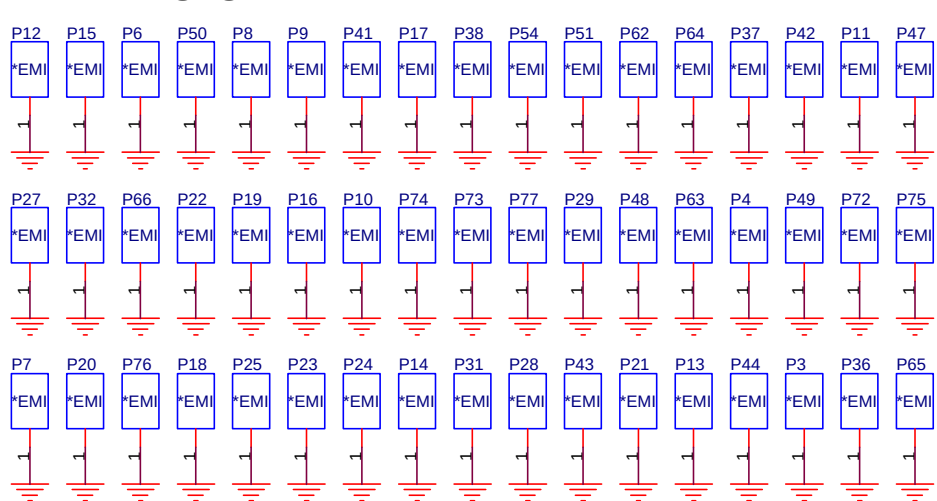
PARALLEL PORT



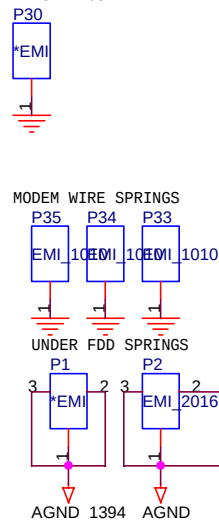
SCREW HOLES



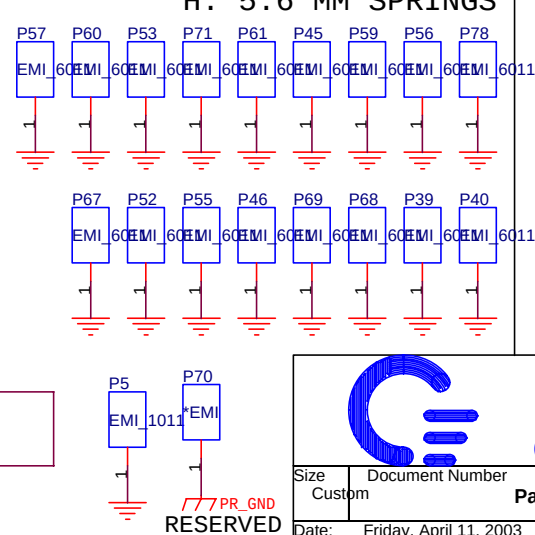
EMI FENSES



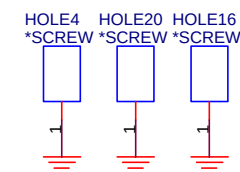
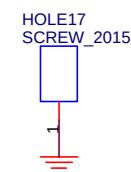
ANTENNA WIRE SPRINGS



H: 5.6 MM SPRINGS



THERMAL
MODULE
NUT

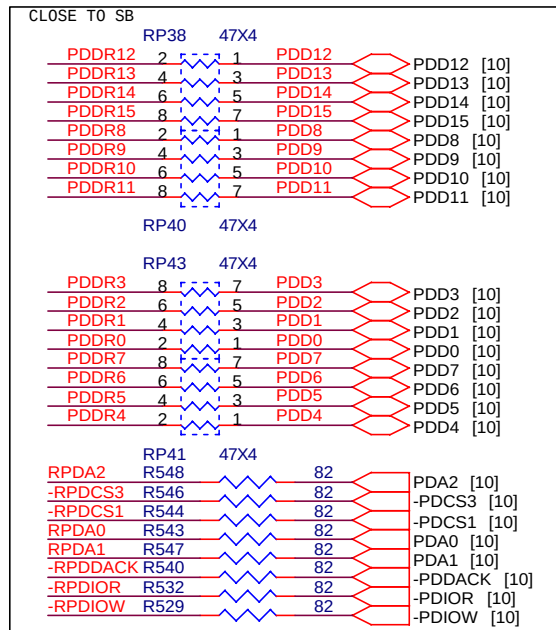
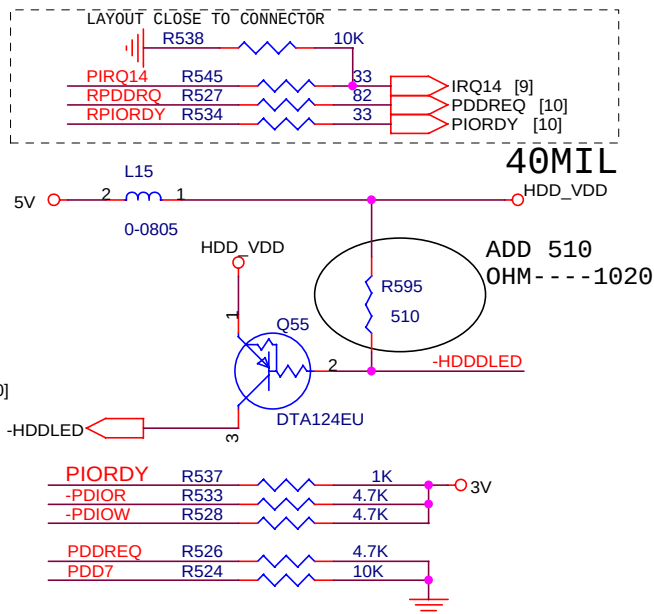
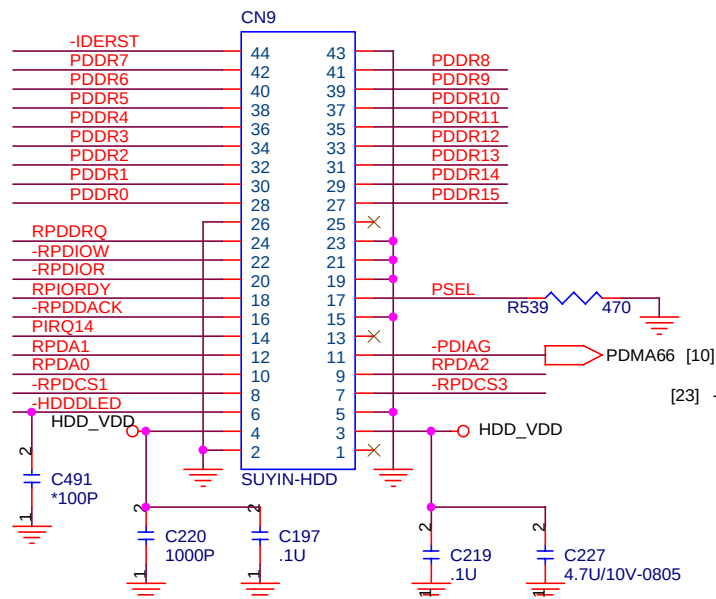
CDROM
NUT

PROJECT : KT7I

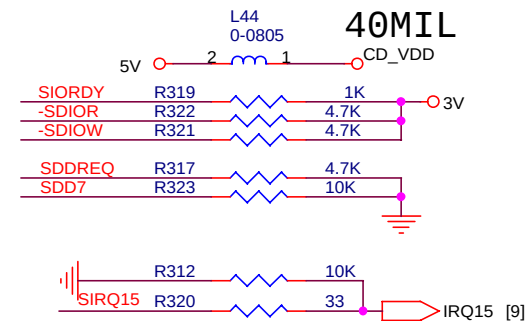
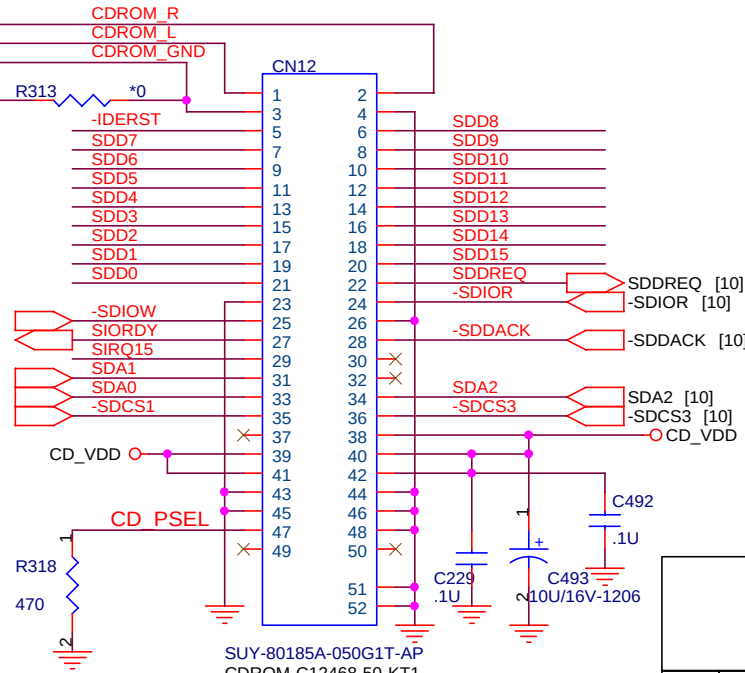
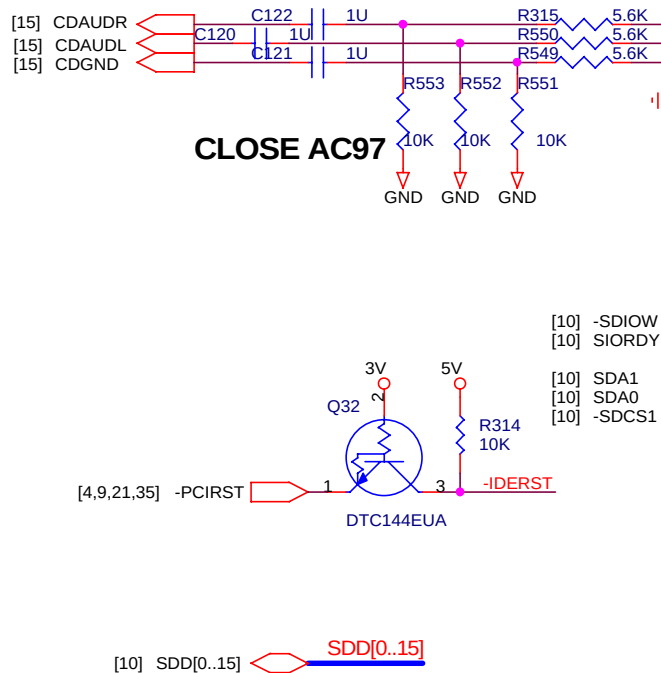
Quanta Computer Inc.

Size Custom	Document Number Parallel PORT/SCREW HOLE	Rev 3A
Date: Friday, April 11, 2003	Sheet 19 of 35	

HDD CONNECTOR



CD-ROM CONNECTOR

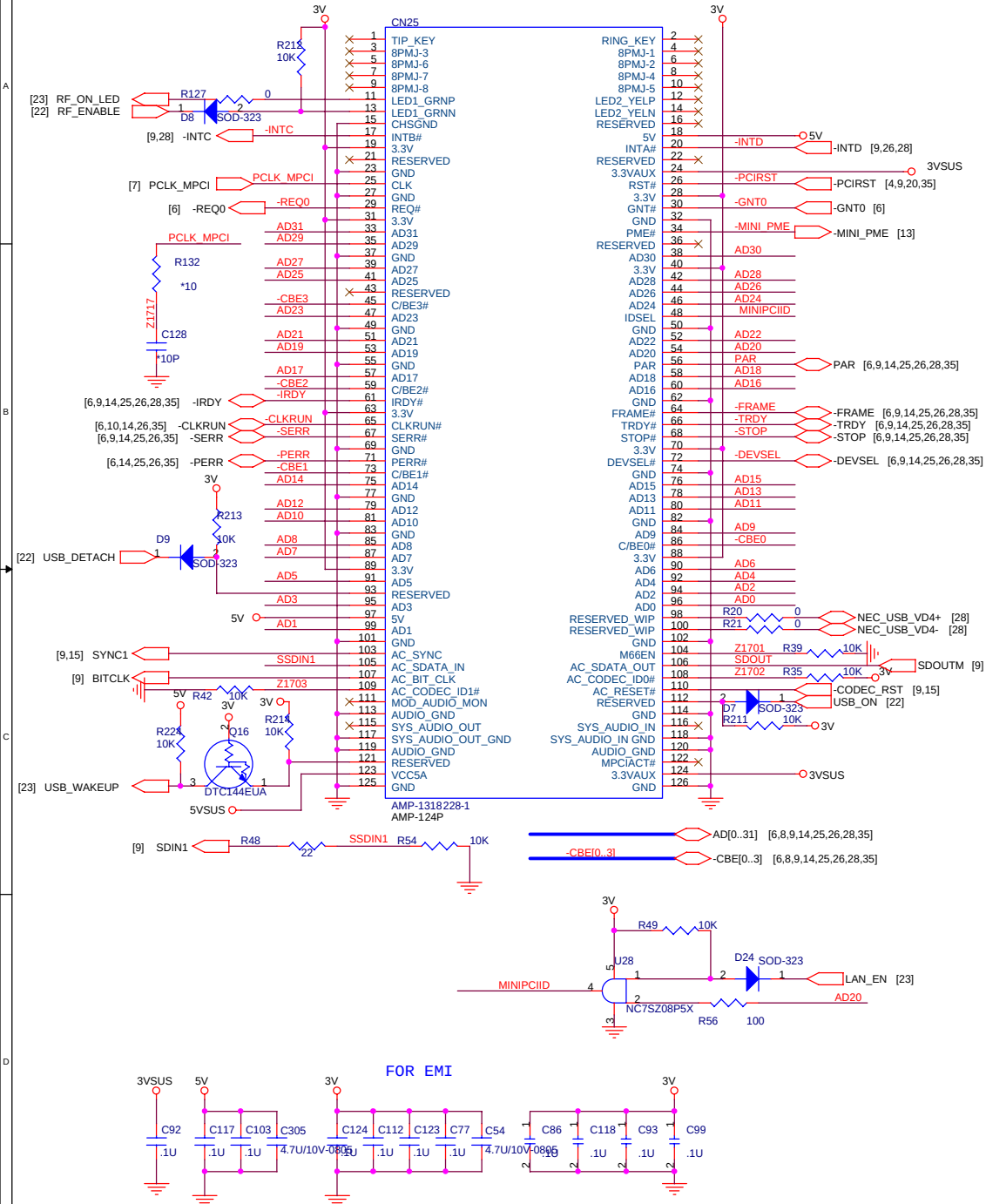


PROJECT : KT7I

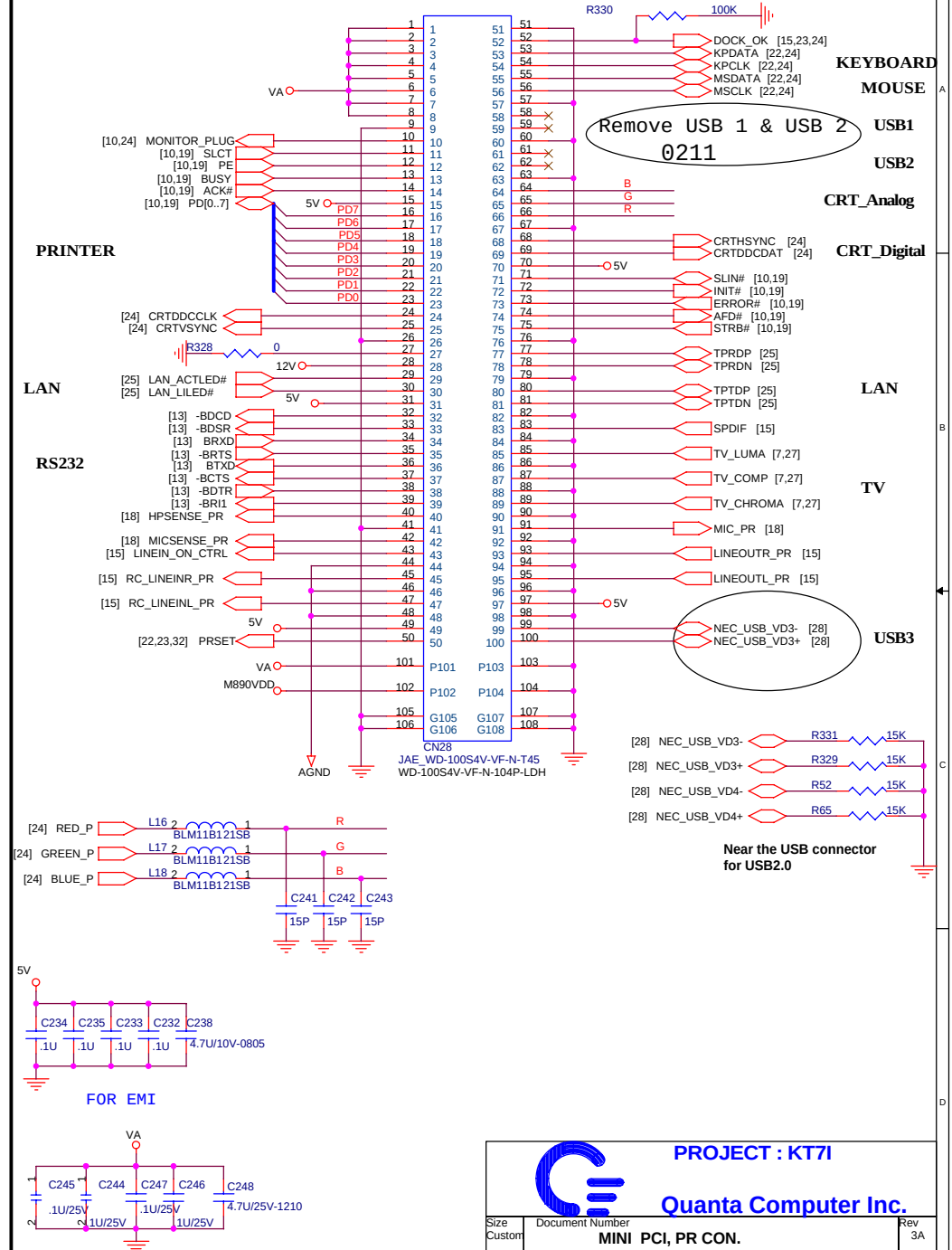
Quanta Computer Inc.

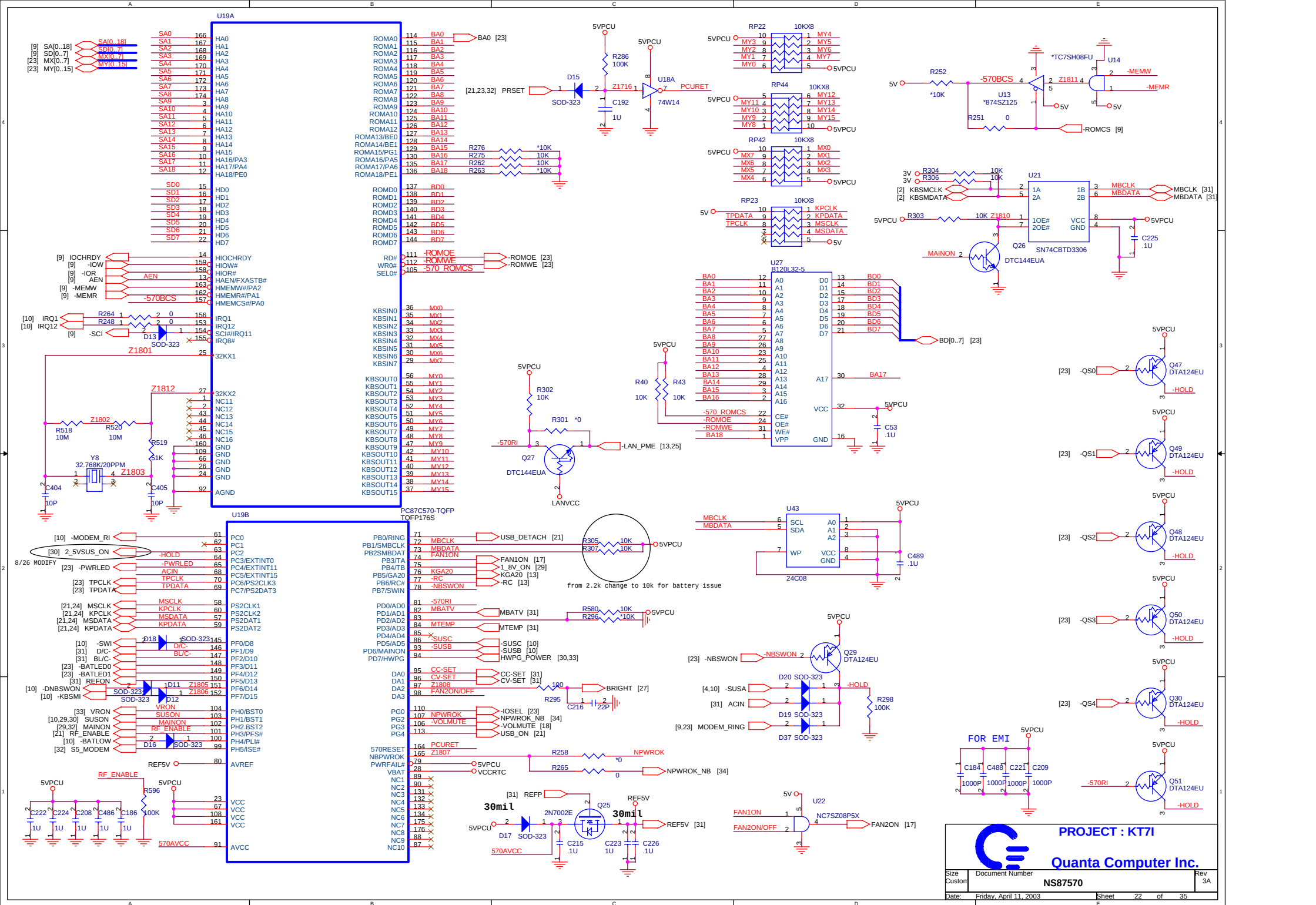
Size	Document Number	Rev
Custom	CD-ROM/HDD CONNECTOR	3A
Date:	Friday, April 11, 2003	Sheet 20 of 35

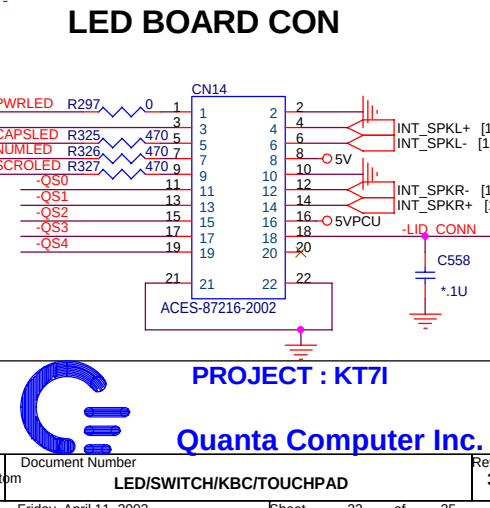
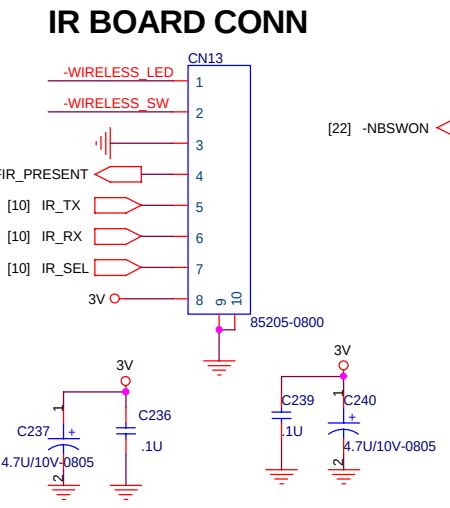
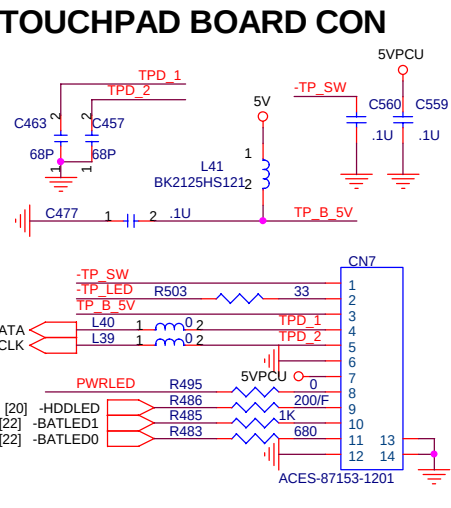
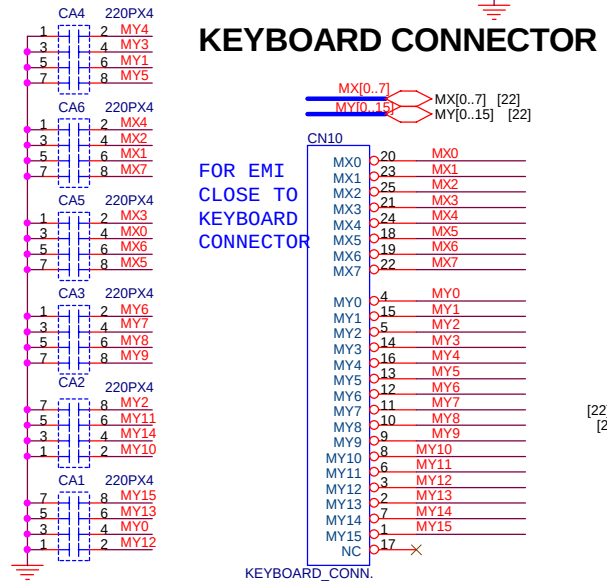
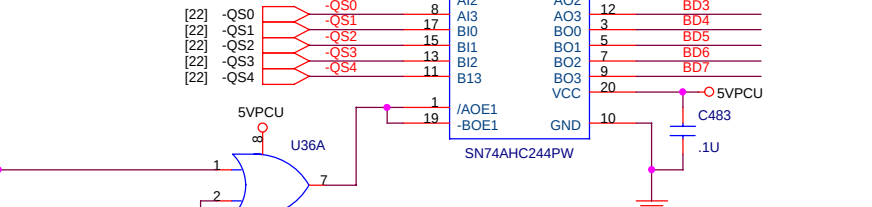
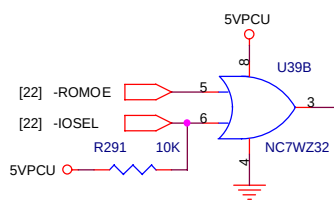
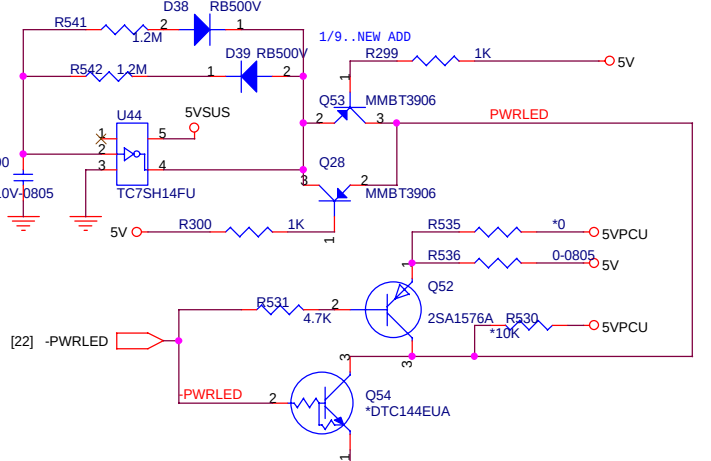
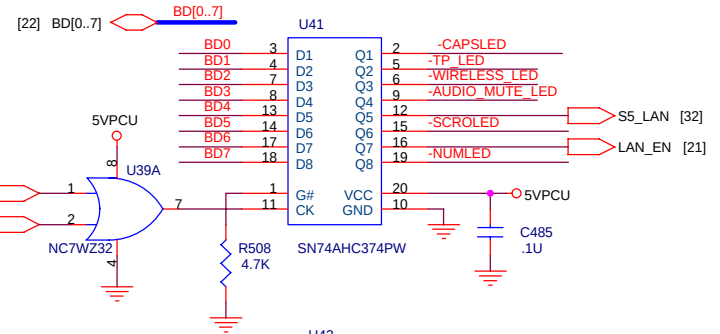
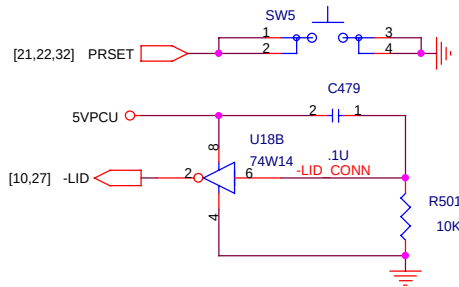
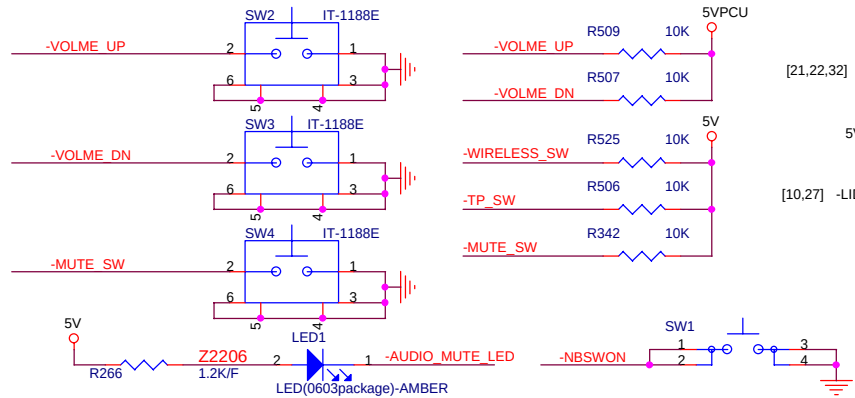
TYPE III MINI PCI SOCKET



PORT REPLICATOR Connector









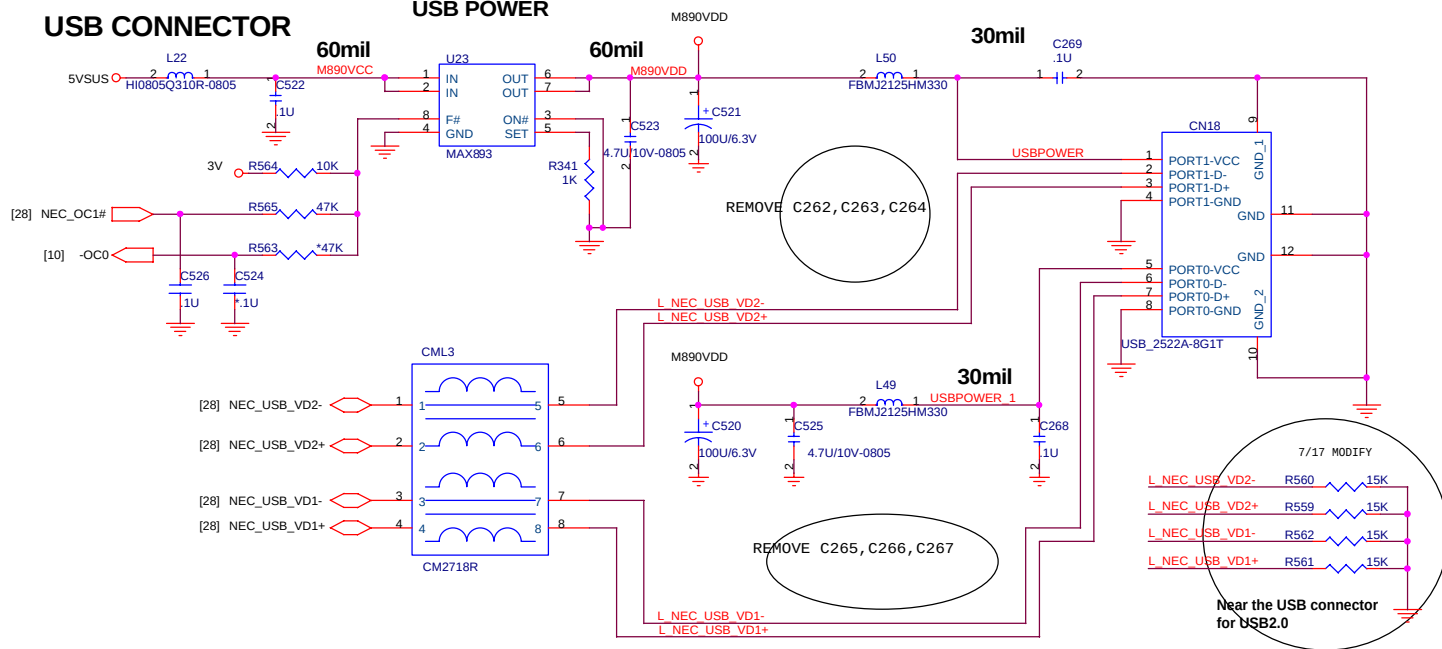
PROJECT : KT71

Quanta Computer Inc.

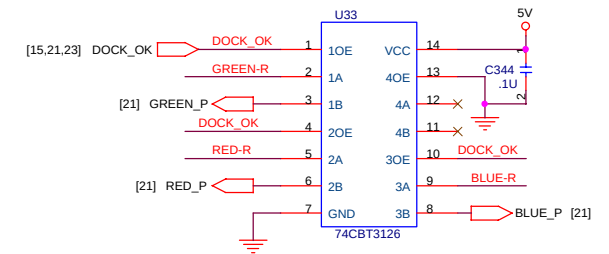
Size	Document Number	Rev
Custom	LED/SWITCH/KBC/TOUCHPAD	3A
Date:	Friday, April 11, 2003	Sheet 23 of 35

USB CONNECTOR

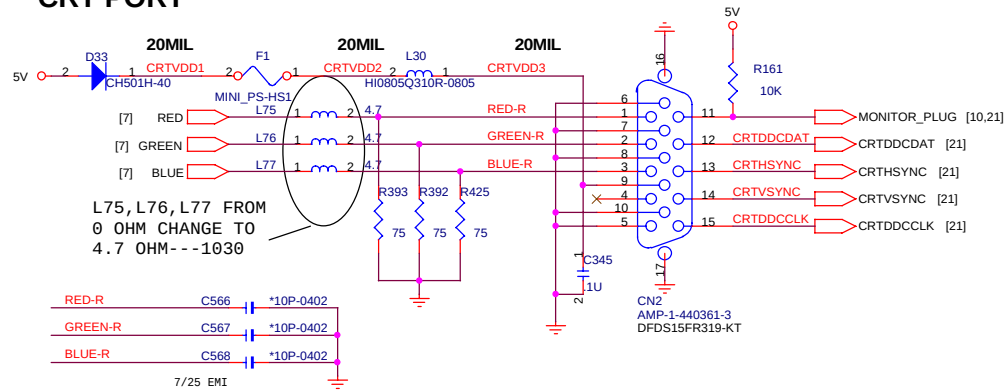
USB POWER



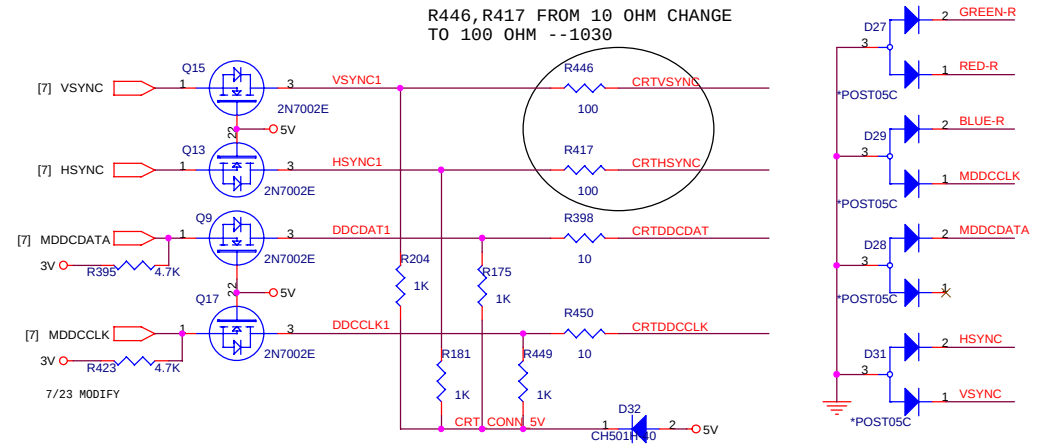
SWITCHING THE C.R.T. SIGNAL



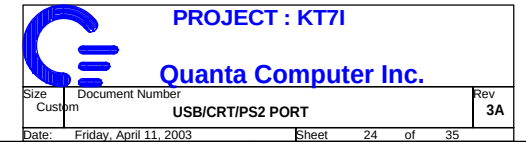
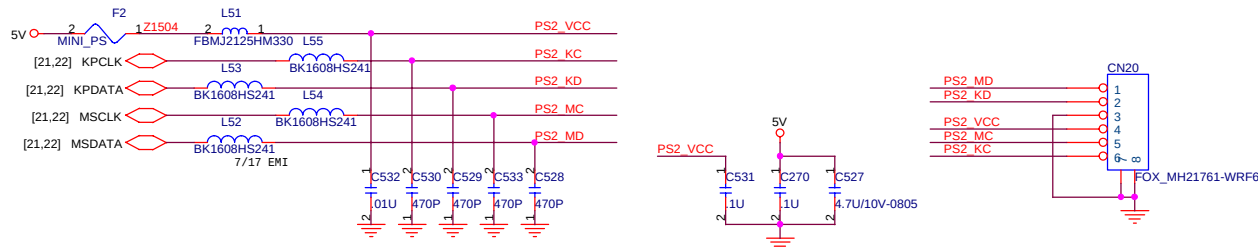
CRT PORT

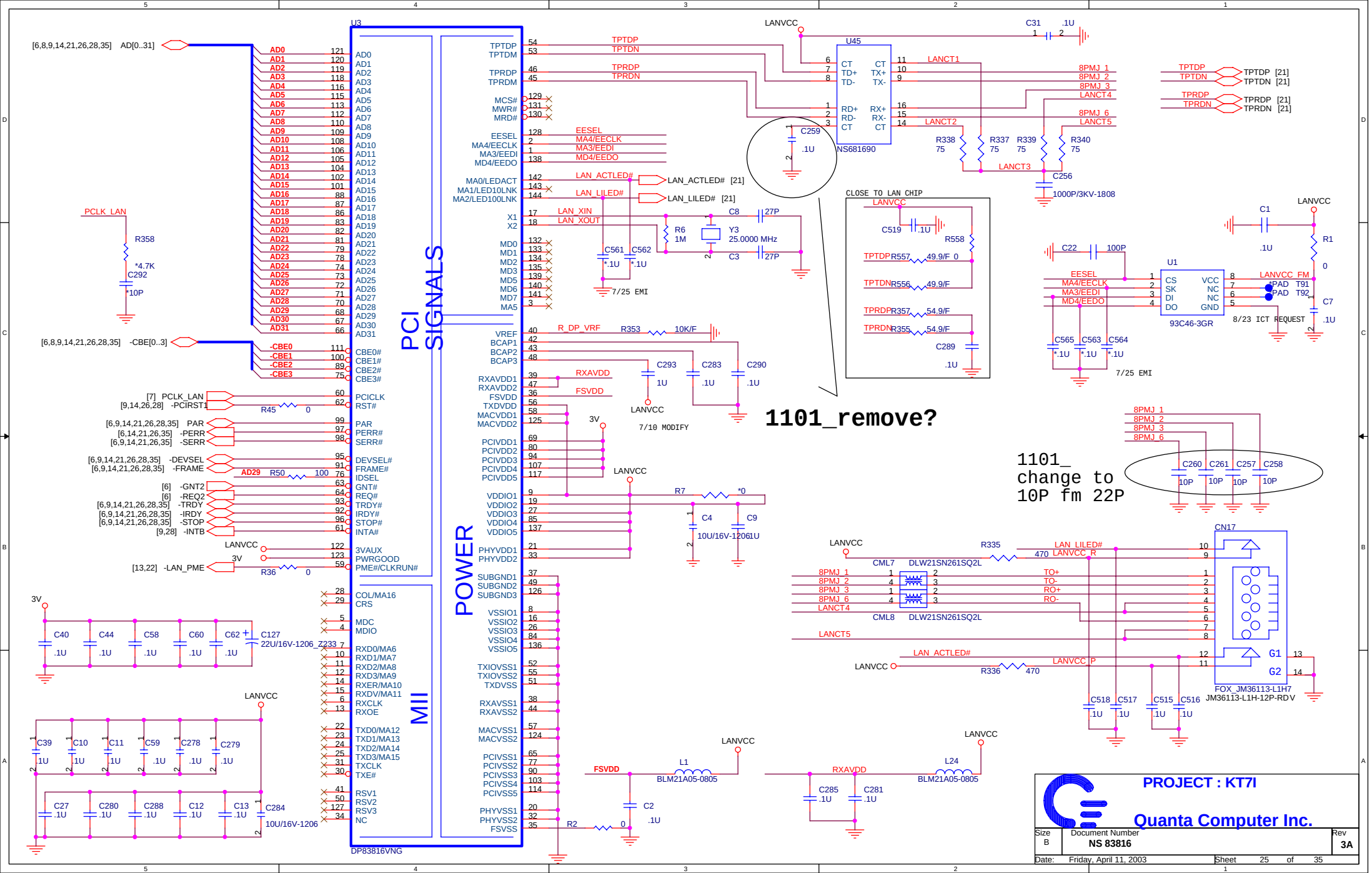


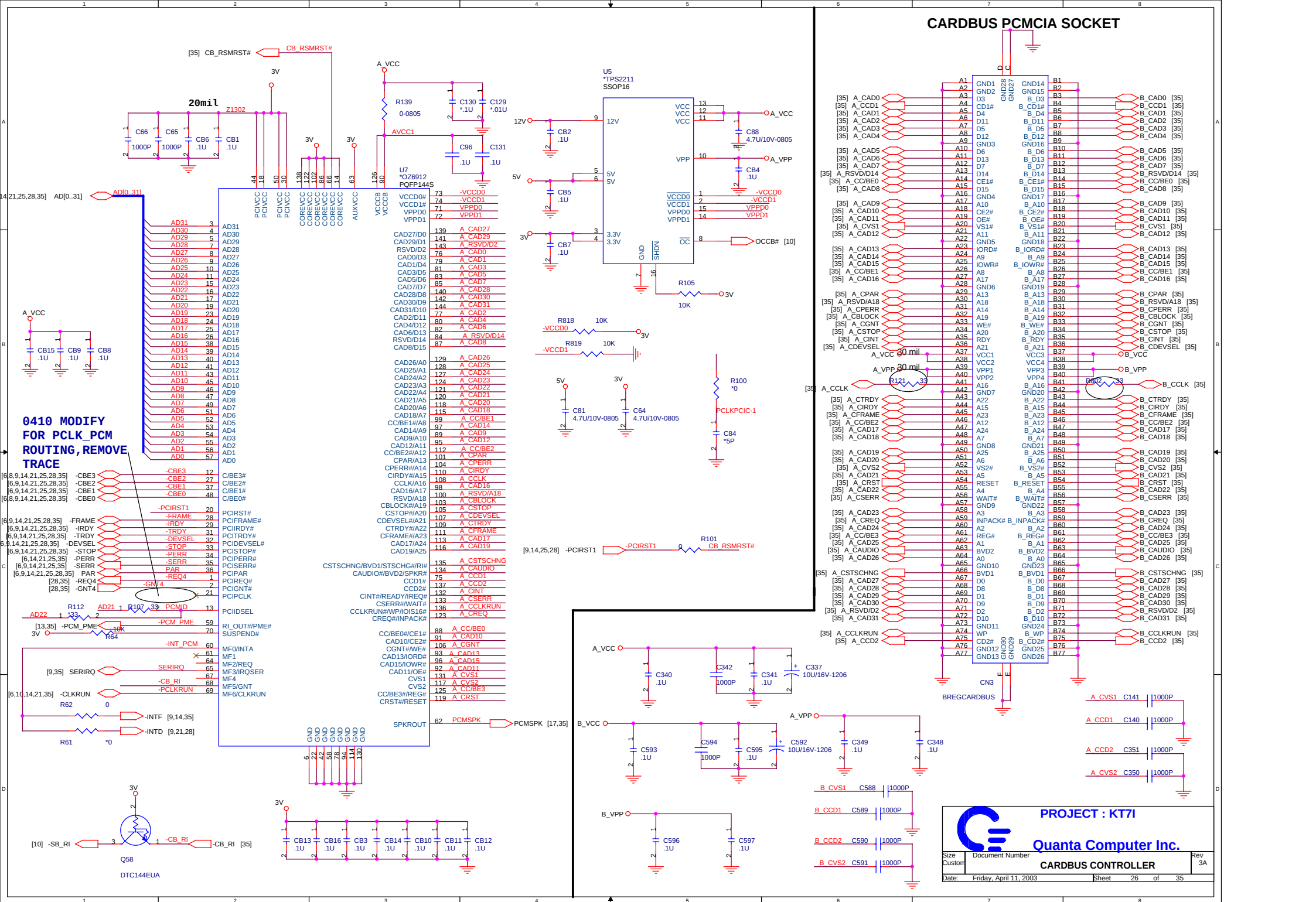
R446,R417 FROM 10 OHM CHANGE
TO 100 OHM --1030

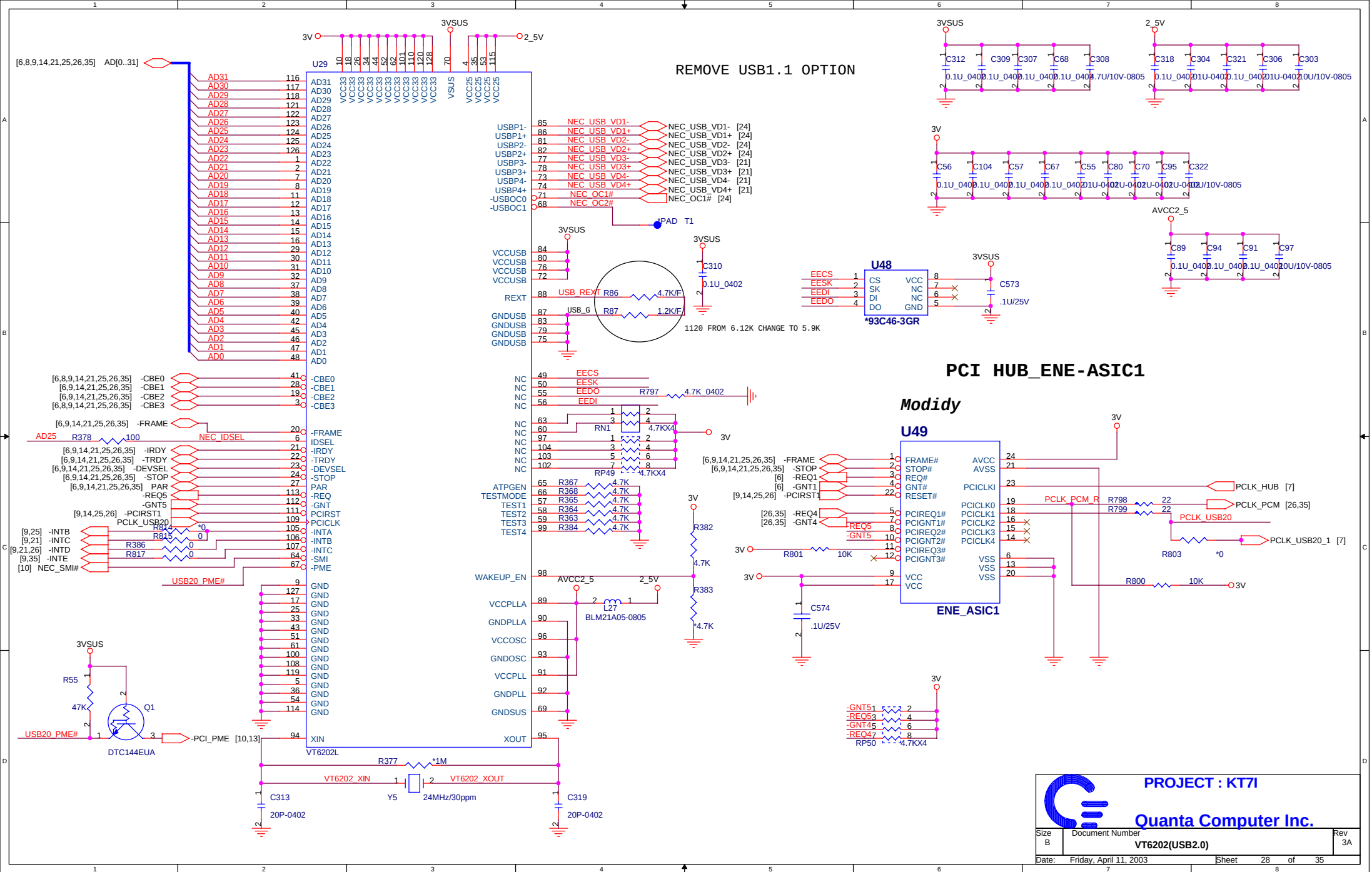


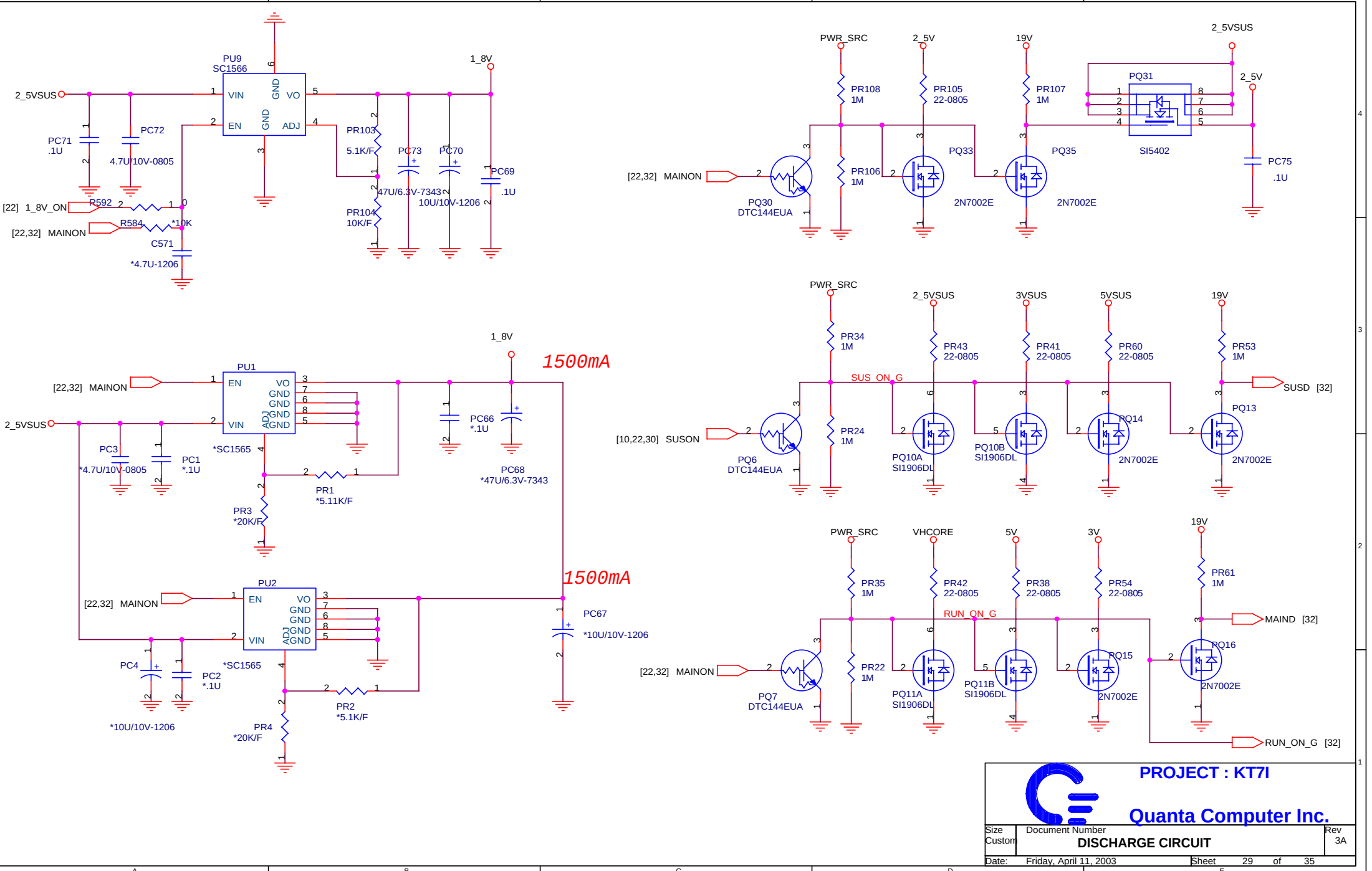
PS/2 PORT

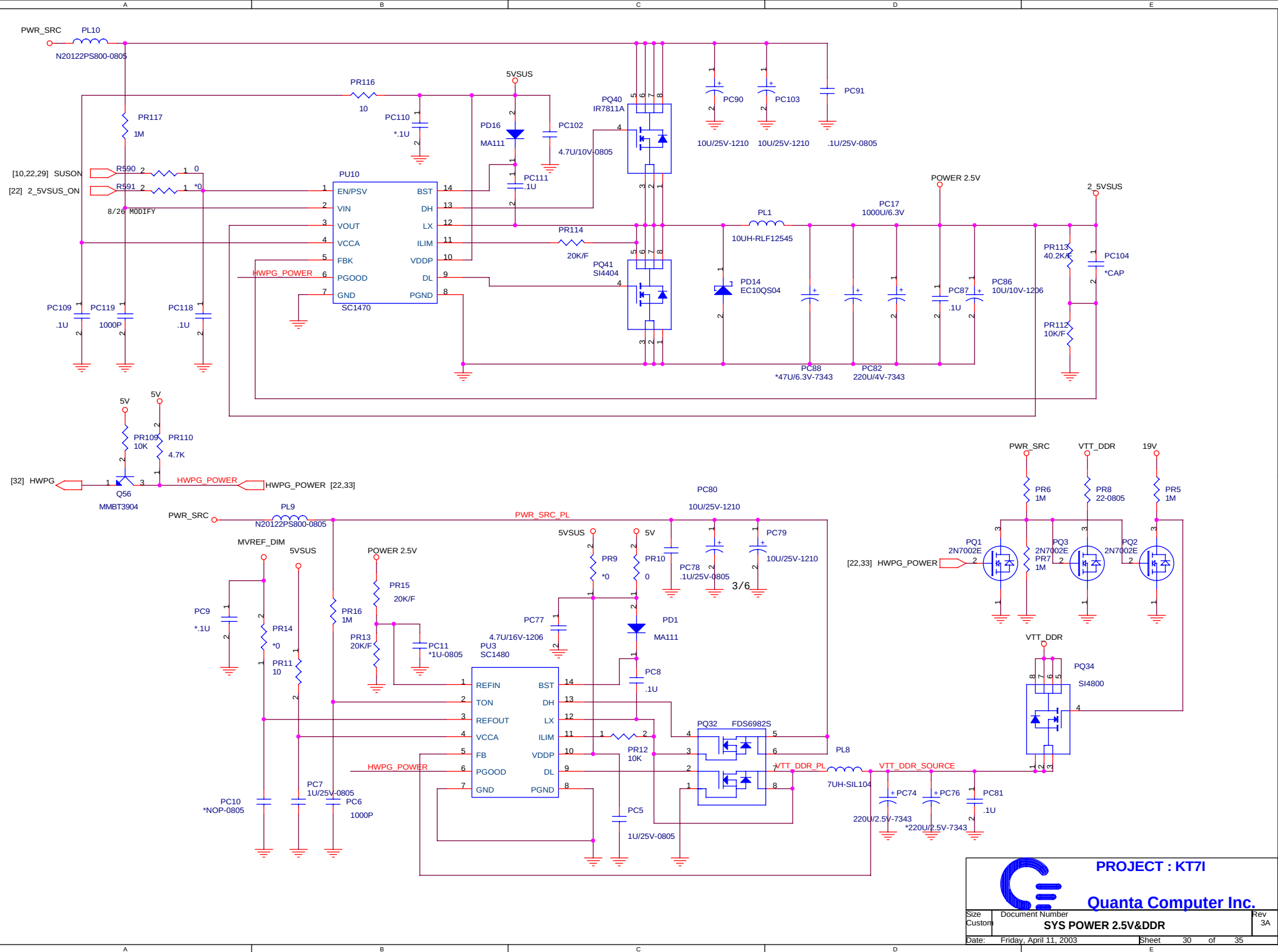


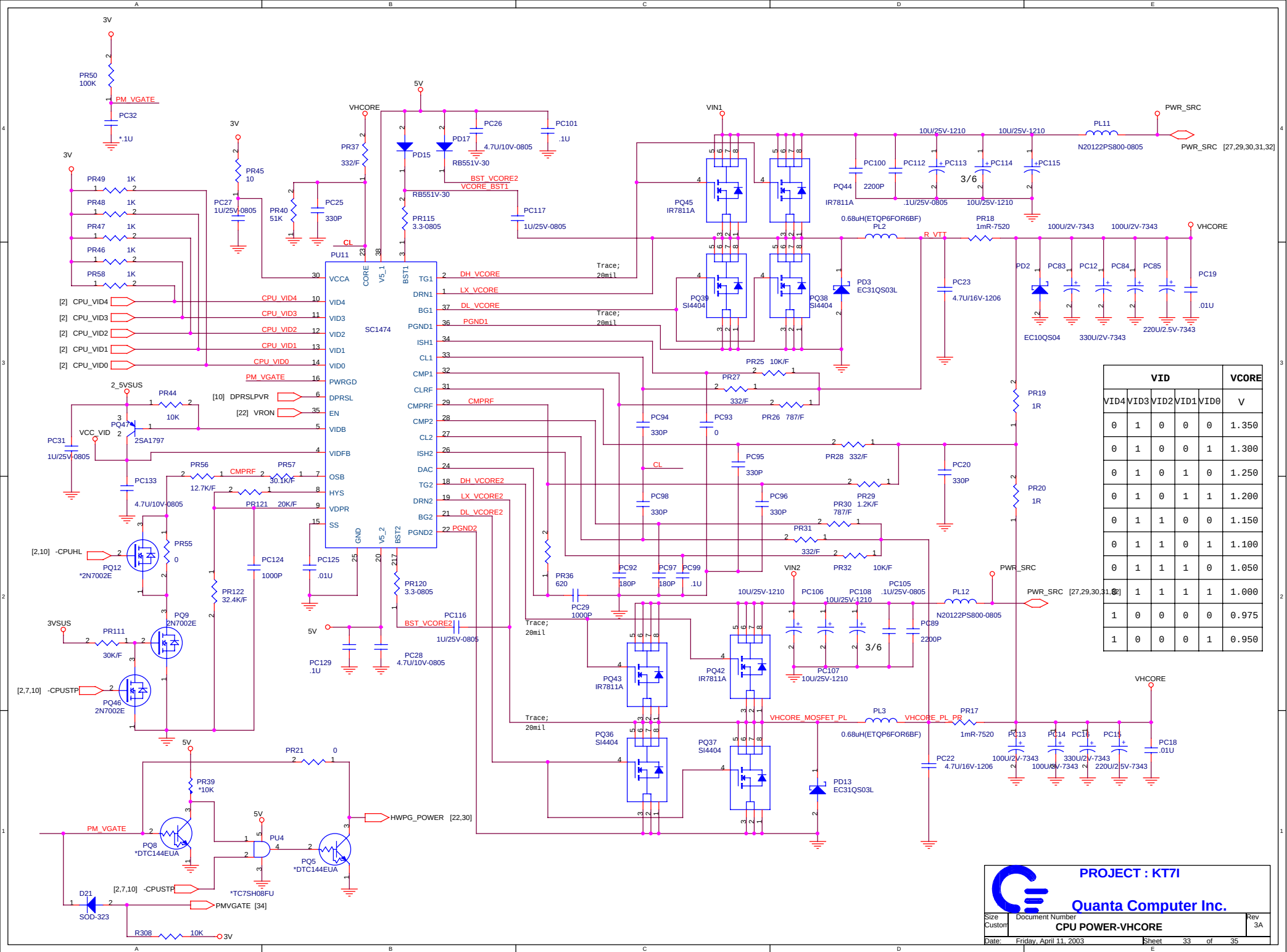


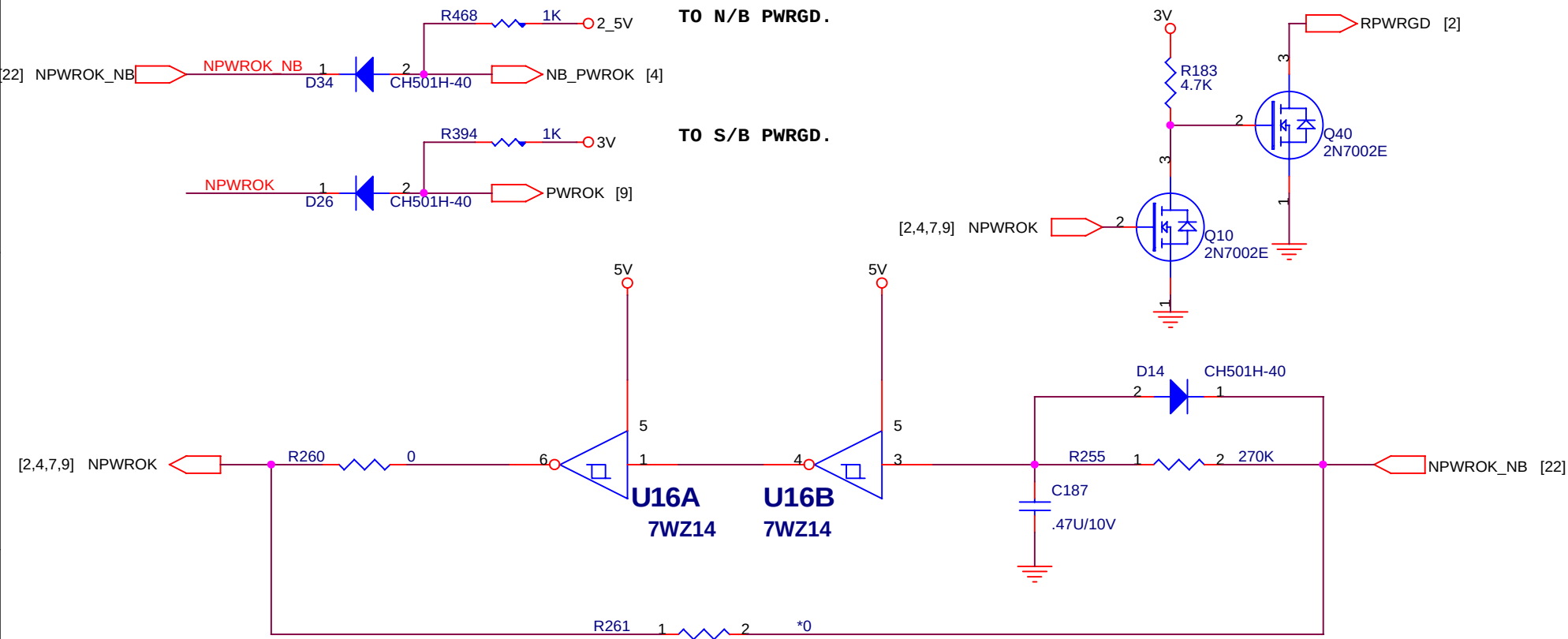




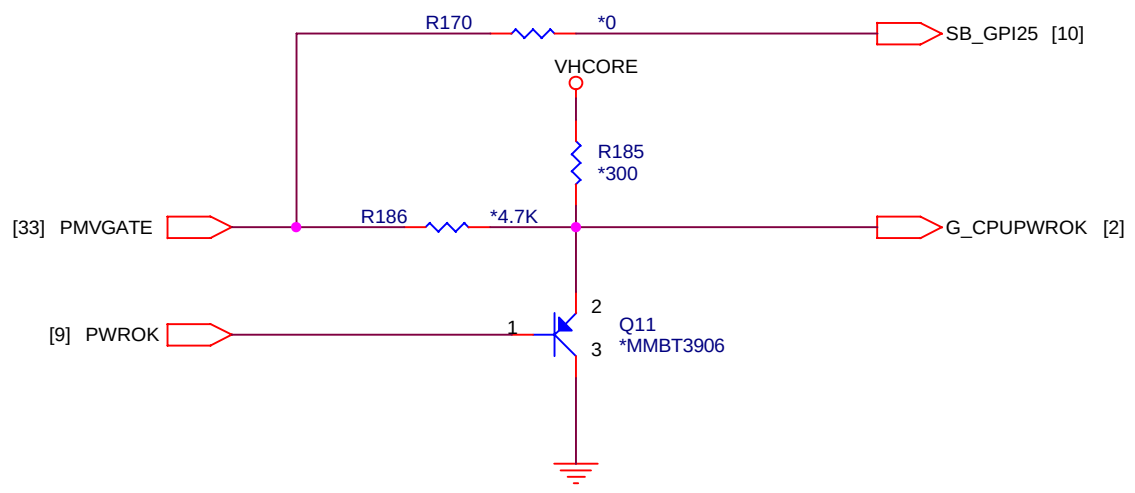








For_ATI_PWG function





PROJECT : KT7I

Quanta Computer Inc.

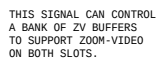
Size A	Document Number POWER GOOD CIRCUIT	Rev 3A
Date: Friday, April 11, 2003	Sheet 34 of 35	

[26] CB_RSMRST#  CB_RSMRST# R804  2 0

3V                   

3V                   

3V                   



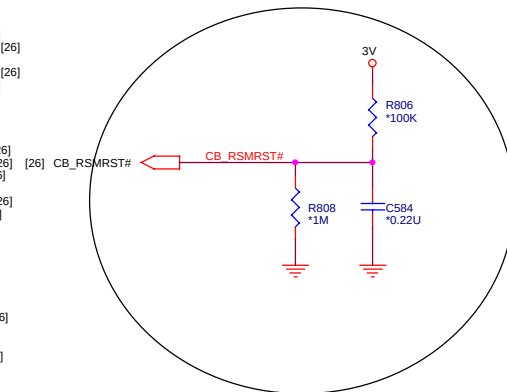
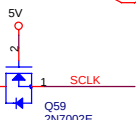
NOTES:

THE SERIAL IRQ SIGNAL MUST BE PULLED-UP BY 10K ON THE MLB.

THE TWO SOCKET ACTIVITY SIGNALS MAY BE ORED TOGETHER FOR A SINGLE SIGNAL.

THE STRAPPING RESISTORS ON SLATCH AND SDATA CONFIGURE 026933 FOR SERIAL POWER. (SEE BOTTOM OF PAGE)

THE 026933 DEFAULTS TO PCI INT. MODE. (THE BIOS must program PCI/WAY mode)



[7,9] CLK32KO ☐

REV
2A

Document Number

Sheet 35 of 35

REV
2A