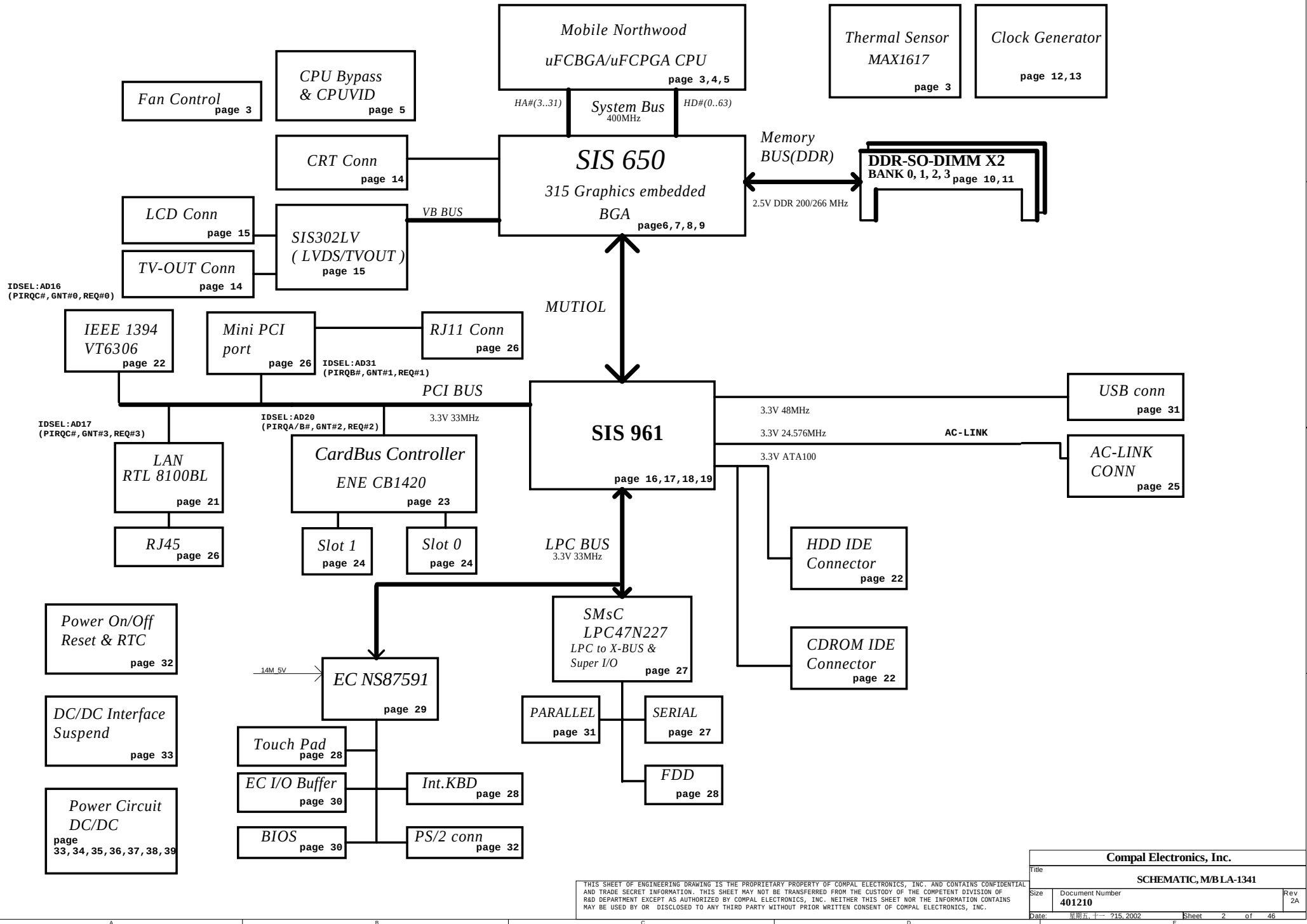
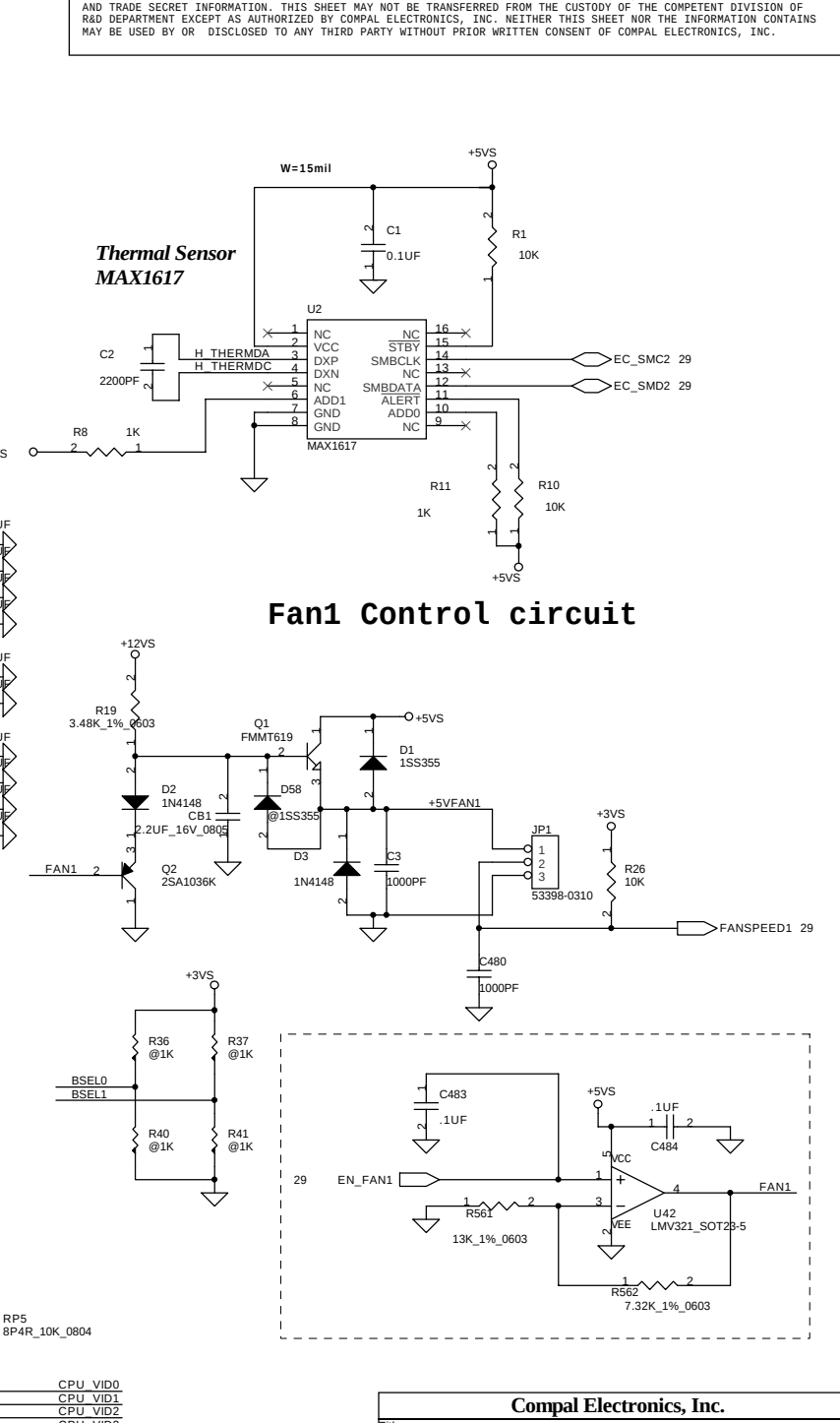
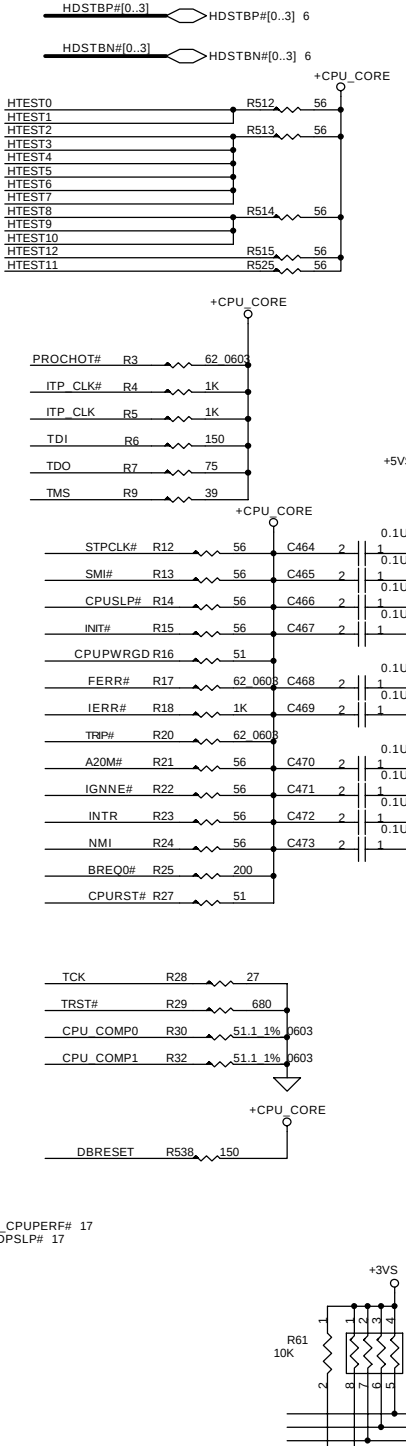
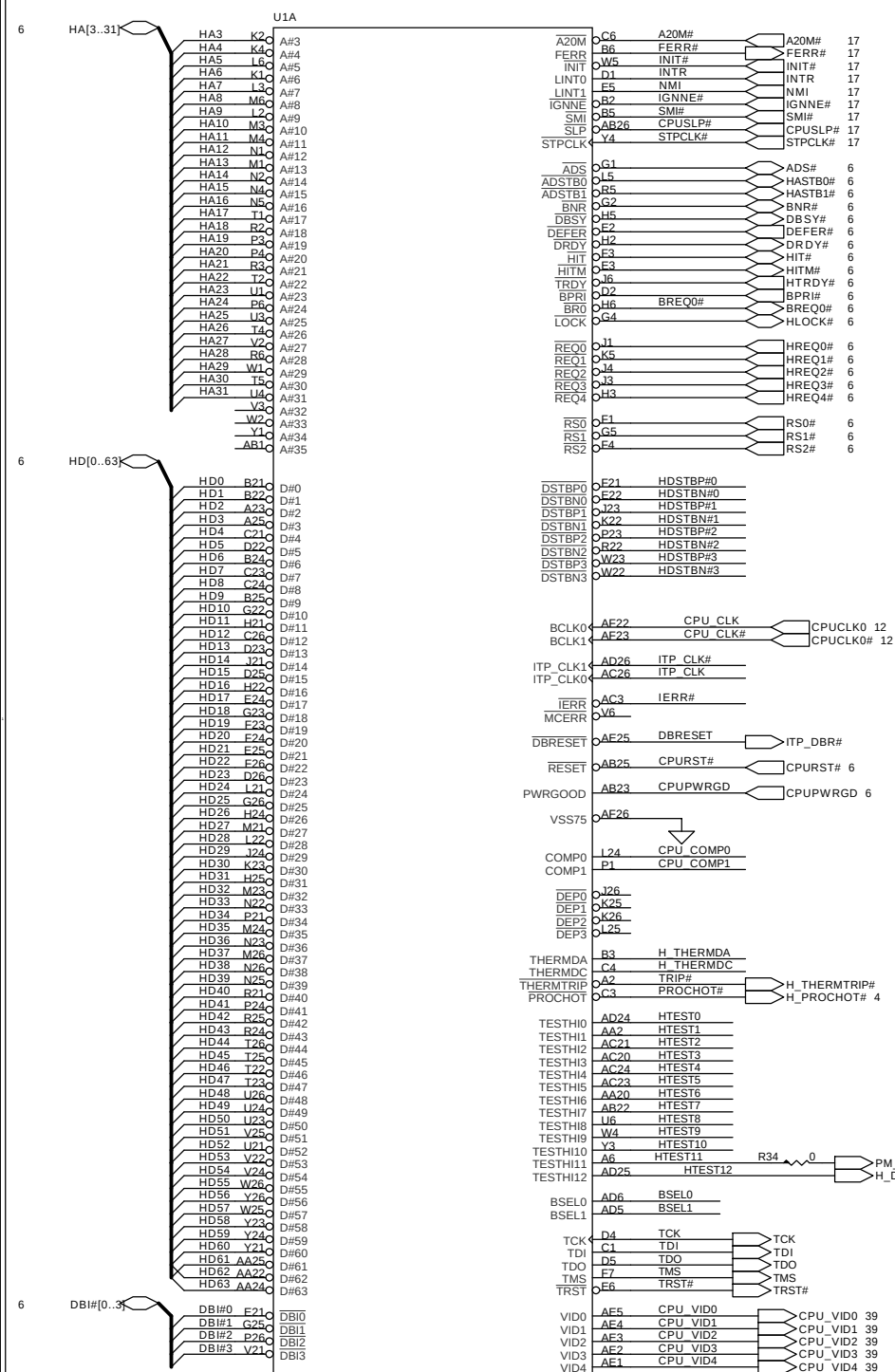
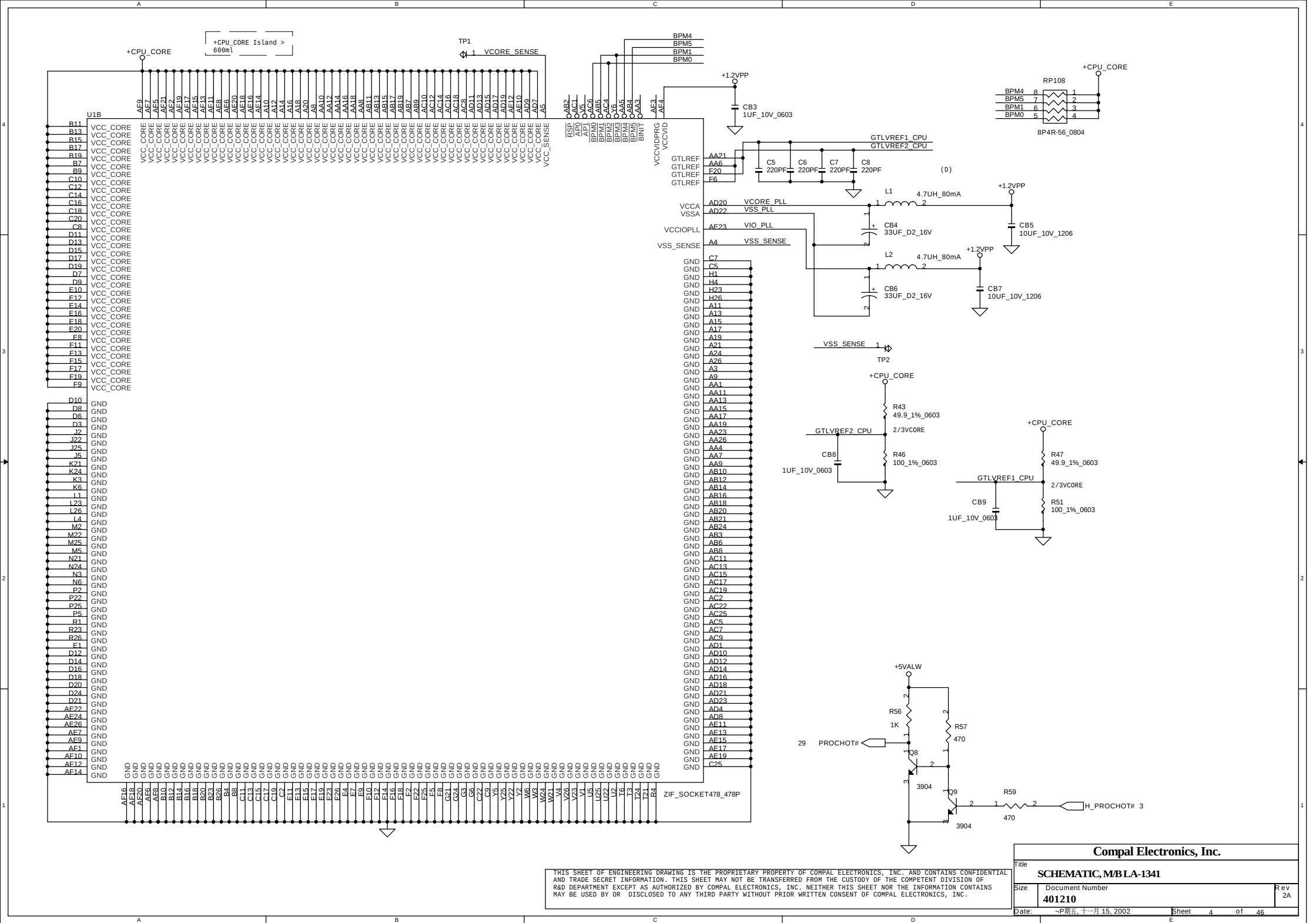


File Name : LA-1341

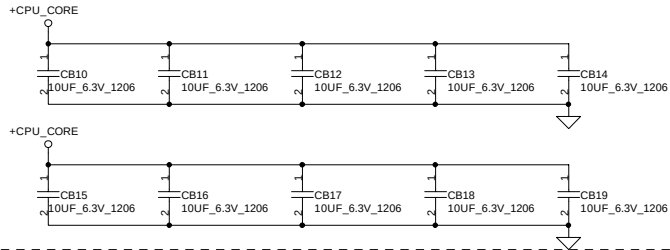




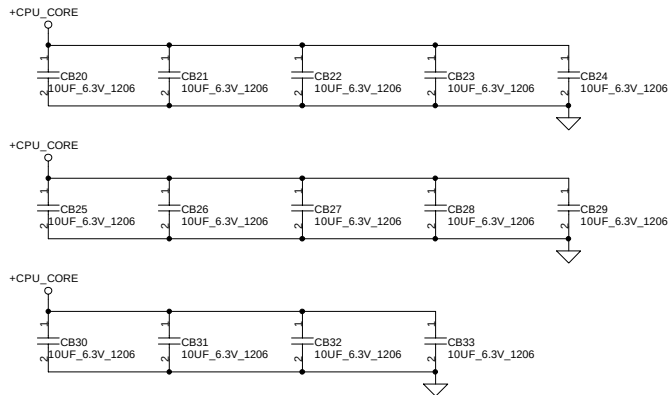


Layout note :
Place close to CPU. Use 2-3 vias per PAD.
Place 22uF caps underneath balls on solder side.
Place 10uF caps on the peripheral near balls.
Use 2-3 vias per PAD.

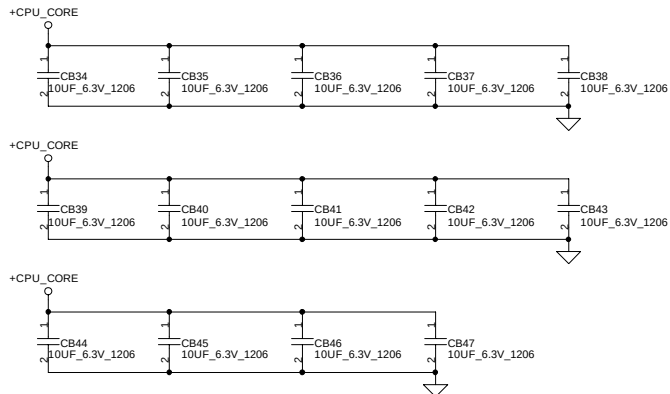
Please place these cap in the socket cavity area



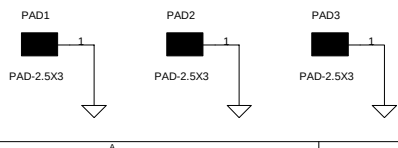
Please place these cap on the socket north side



Please place these cap on the socket south side

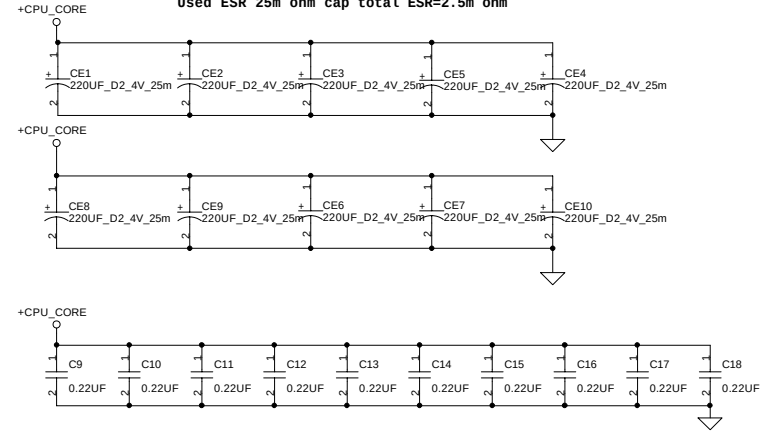


EMI Clip PAD for CPU



Layout note :
Place close to CPU power and
ground pin as possible
(<1inch)

Used ESR 25m ohm cap total ESR=2.5m ohm



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3 HD[0..63] HD[0..63]

3 HA[3..31] HA[3..31]

12 CPUCLK1 CPUCLK1# A126 CPUCLK CPUCLK#

12 CPUCLK1# CPUCLK1# AH26

3 HLOCK# HLOCK# U26 HLOCK#

3 DEFER# DEFER# U26 DEFER#

3 HTRDY# HTRDY# U26 HTRDY#

3 CPURST# CPURST# C20 CPURST#

3 CPUPWRGD CPUPWRGD D19 CPUPWRGD

3 BPRI# BPRI# T27 BPRI#

3 BREQ0# BREQ0# U25 BREQ0#

3 RS2# RS2# T24 RS#2

3 RS1# RS1# T26 RS#1

3 RS0# RS0# U29 RS#0

3 ADS# ADS# V28 ADS#

3 HITM# HITM# T28 HITM#

3 HIT# HIT# U28 HIT#

3 DRDY# DRDY# W28 DRDY#

3 DBSY# DBSY# V24 DBSY#

3 BNR# BNR# V27 BNR#

3 HREQ4# HREQ4# W28 HREQ#4

3 HREQ3# HREQ3# W29 HREQ#3

3 HREQ2# HREQ2# W24 HREQ#2

3 HREQ1# HREQ1# W25 HREQ#1

3 HREQ0# HREQ0# Y27 HREQ#0

3 HASTB1# HASTB1# AD24 HASTB#1

3 HASTB0# HASTB0# AD24 HASTB#0

+CPU_CORE R81 HNCOMP

20_1%_0603 Rds-on(n) = 10 ohm

HNCVERF = 1/3 VCCP

R83 HPCOMP

113_1%_0603 Rds-on(p) = 56 ohm

HPCVERF = 2/3 VCCP

+CPU_CORE

R84 150_1%_0603 C26 0.01UF

HNCVREF

R85 75_1%_0603 C27 0.01UF

HNCVREF G

+CPU_CORE

R86 75_1%_0603 C28 0.01UF

R89 150_1%_0603 C29 0.01UF

HVREF

C30 0.1UF

place this capacitor under 650 solder side

HA31 AE26 HA#31

HA30 AE25 HA#30

HA29 AH28 HA#29

HA28 AD26 HA#28

HA27 AG25 HA#27

HA26 AE28 HA#26

HA25 AC24 HA#25

HA24 AG26 HA#24

HA23 AC26 HA#23

HA22 AE29 HA#22

HA21 AD28 HA#21

HA20 AC25 HA#20

HA19 AE27 HA#19

HA18 AE29 HA#18

HA17 AE27 HA#17

HA16 AB24 HA#16

HA15 AB26 HA#15

HA14 AC28 HA#14

HA13 AC26 HA#13

HA12 AC26 HA#12

HA11 AA26 HA#11

HA10 AB28 HA#10

HA9 AB27 HA#9

HA8 AA25 HA#8

HA7 AA29 HA#7

HA6 Y24 HA#6

HA5 Y26 HA#5

HA4 Y24 HA#4

HA3 Y26 HA#3

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HOST

A4XAVDD 1 L3 2 +3VS

HB-1M2012-121JT

C31 0.1UF C32 0.01UF CB48 4.7UF_10V_0805

A4XAVSS

C4XAVDD 1 L4 2 +3VS

HB-1M2012-121JT

C33 0.1UF C34 0.01UF CB49 4.7UF_10V_0805

C4XAVSS

+3VS

R87 4.7K R88 4.7K

BCAD BHCLK

VBCLK VBHCLK

650-1

LVDS

U5A

AC/BE#3 E6

AC/BE#2 E3

AC/BE#0 K5

VBCAD/AREQ# C9

AGNT# A6

AFRAMES# G2

AIRDY# G1

ATRDY# G3

ADEVSEL# G4

ASERR# H5

ASTOP# H1

APAR H3

VBHCLK/RBF# E8

VGPI02/WBF# E8

VGPI03/PIPE# D9

NC D10

B3 NC

C4 NC

SB_STB B5

SB_STB# A4

VAGCLK/AD_STB# K1 AGCLK

VAGCLKN/AD_STB# L1 AGCLKN

C1 BGCLK

D1 BGCLKN

VBGCLK/AD_STB# C1

VBGCLKN/AD_STB# D1

AGPCLK B10 AGPCLK0

AGPCLK0 AGPCLK0 12

AGPRCOMP M1 AGPRCOMP

B9

A1XAVDD A9

A1XAVSS

A4XAVDD B8

A4XAVSS A8

AGPVREF M3

AGPVSSREF M2

HDSTBN#3 E20 HDSTBN#3

HDSTBN#2 E23 HDSTBN#2

HDSTBN#1 K24 HDSTBN#1

HDSTBN#0 C24 HDSTBN#0

HDSTBP#3 E21 HDSTBP#3

HDSTBP#2 E24 HDSTBP#2

HDSTBP#1 L24 HDSTBP#1

HDSTBP#0 N25 HDSTBP#0

HDSTBN#[0..3] 3

HDSTBP#[0..3] 3

BCLK R64 10 VBCLK 15

BGCLKNR65 @10 VBCLK# 15

BGCLK R66 10 VBCLK 15

AGCLKNR67 @10 VAGCLK# 15

AGCLK R68 10 VAGCLK 15

BVSYNC R69 10 VBVSNC 15

BHSYNC R70 10 VBHSNC 15

AHSYNC R71 10 VAHSNC 15

AVSYNC R72 10 VAVSNC 15

VAD[0..11] VAD[0..11] 15

VBD[0..11] VBD[0..11] 15

VBCTL[0..1] VBCTL[0..1] 15

ADE R74 10 VADE 15

BDE R75 10 VBDE 15

VBGCLK#

VBGCLK

VAGCLK#

VAGCLK

AGPCLK0

AGPCLK0 12

AGPRCOMP

+3VS

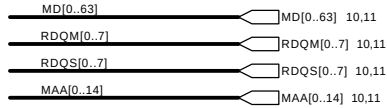
AGPRCOMP

60_1%_0603

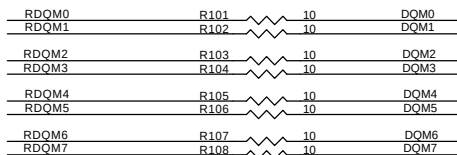
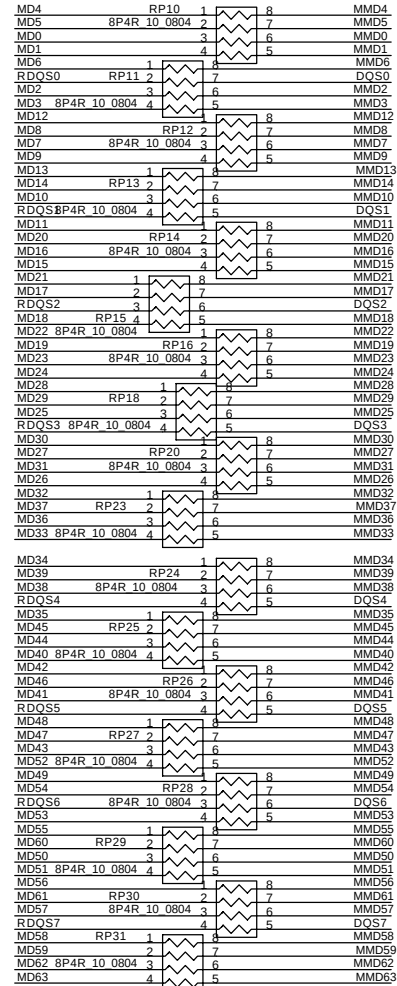
AVREFGC

8.2K

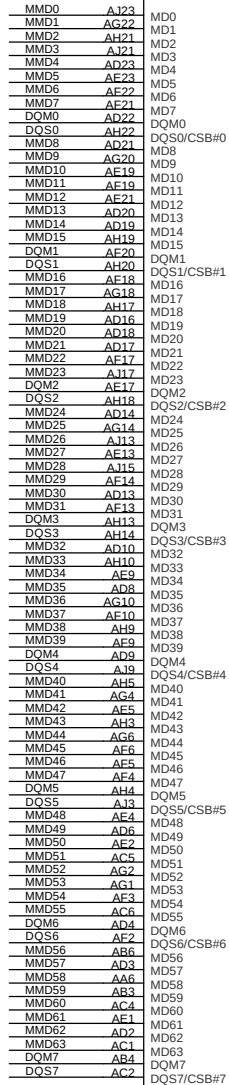
NOTE:This page is for pure DDR SDRAM PCB design



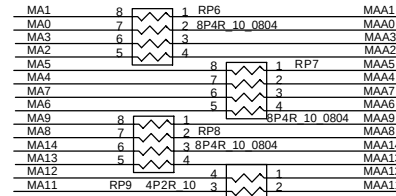
RP* PLACE CLOSE TO DIMM



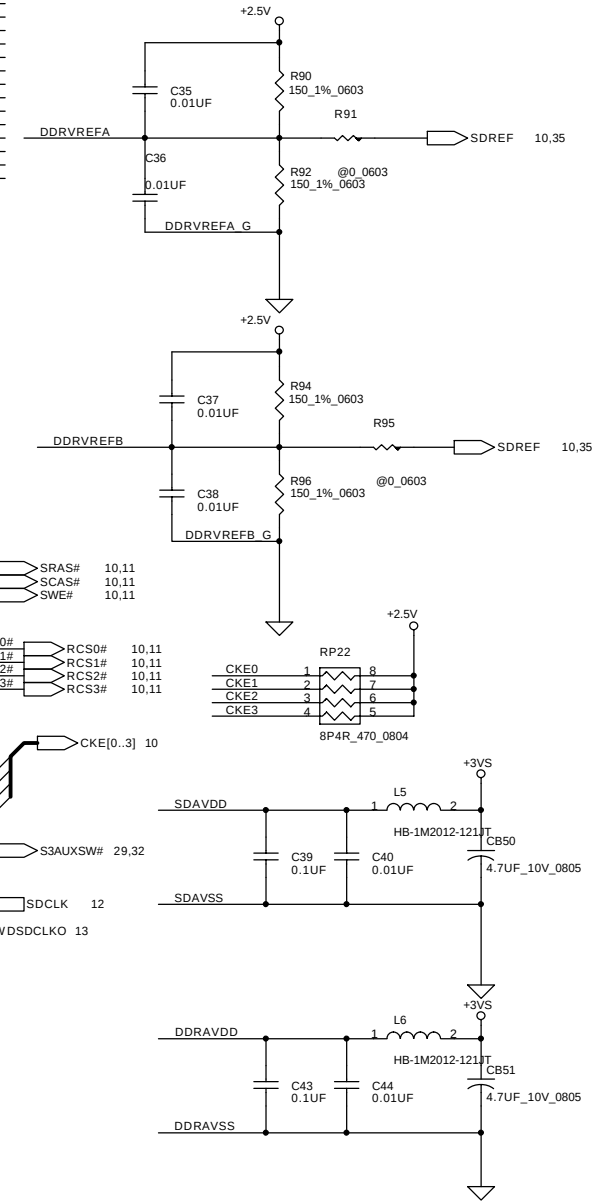
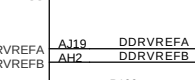
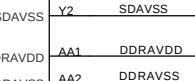
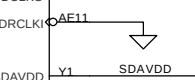
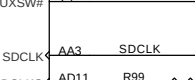
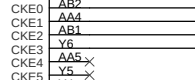
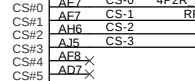
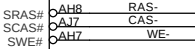
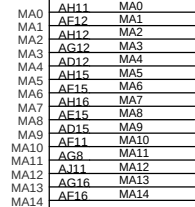
USB



SIS650



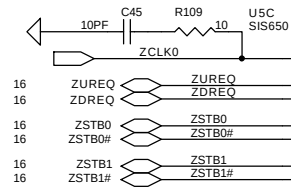
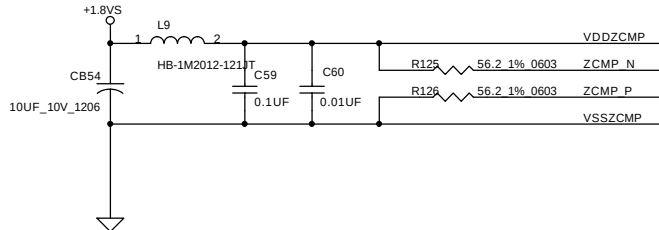
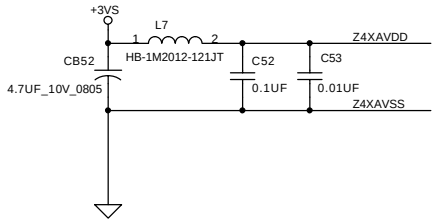
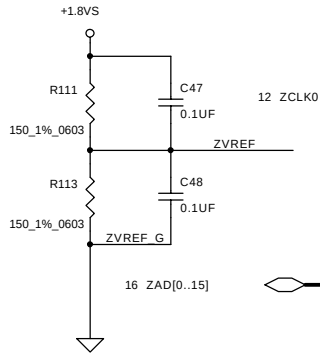
RP* place close to DIMM1



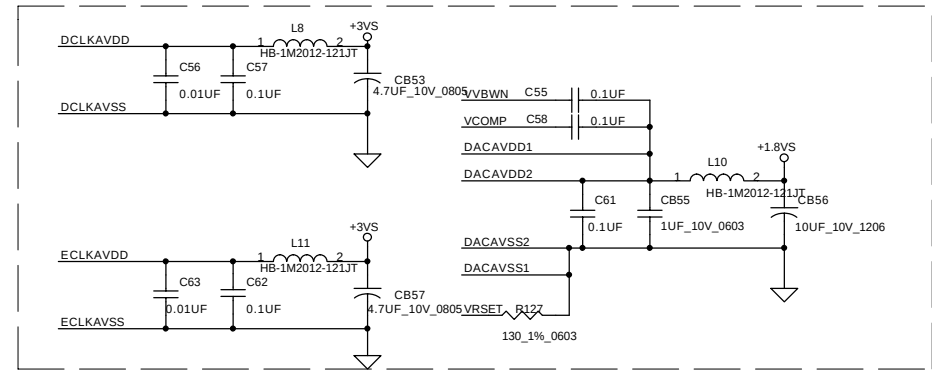
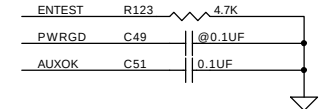
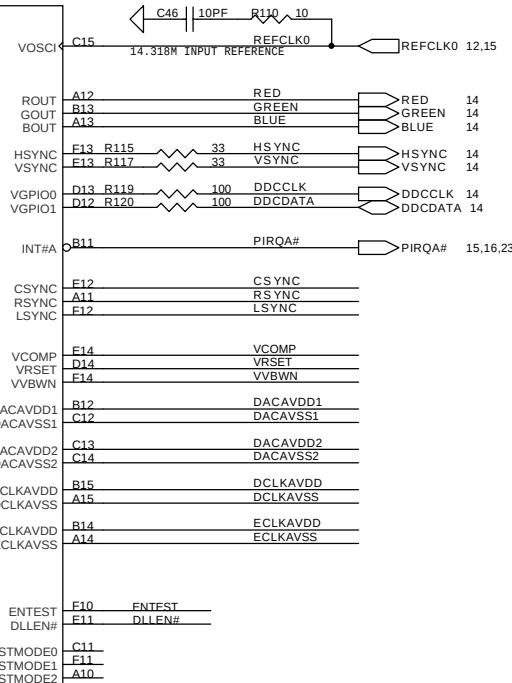
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16 ZSTB[0..1] ZSTB[0..1]
16 ZSTB[0..1]# ZSTB[0..1]#

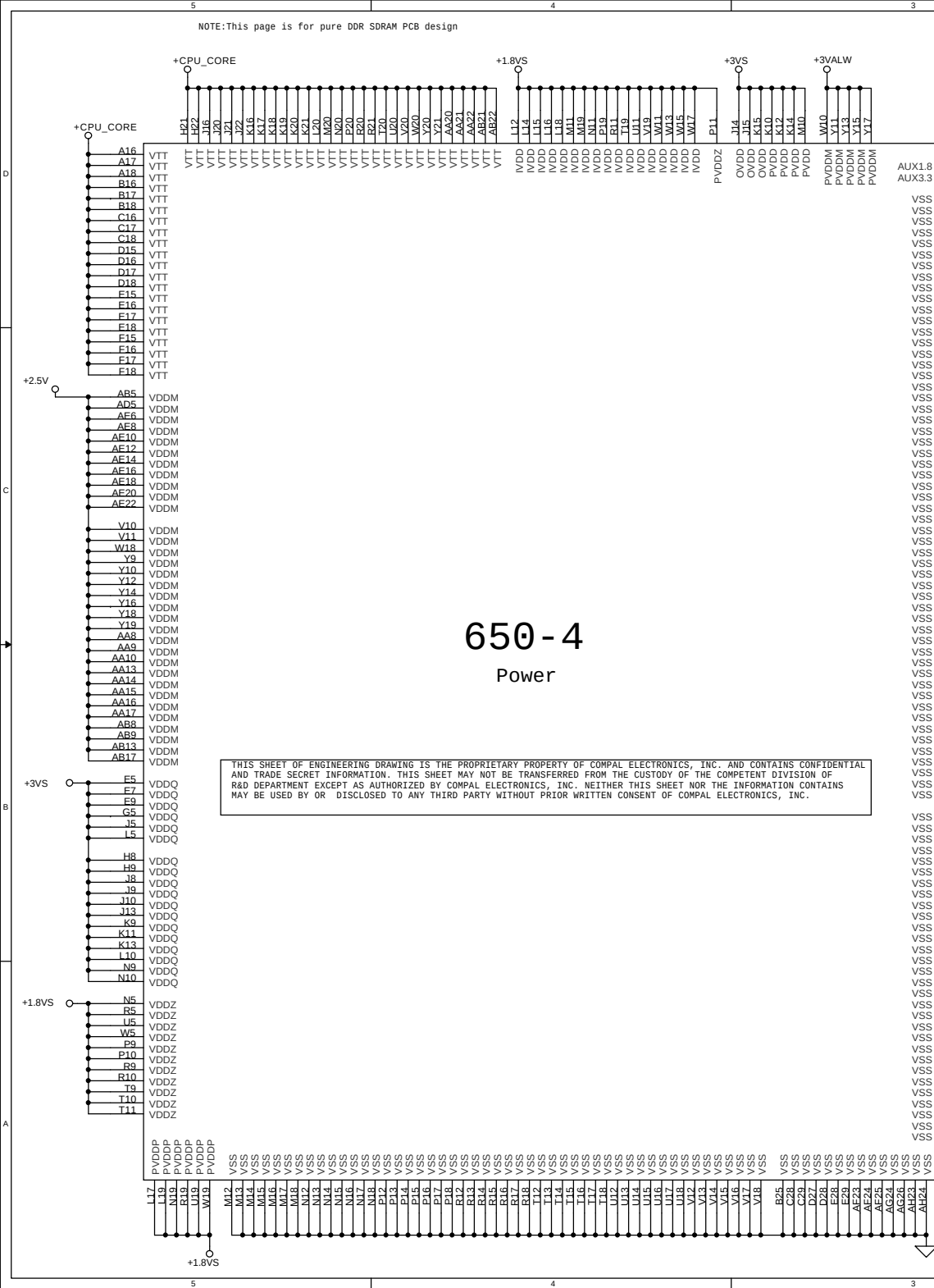


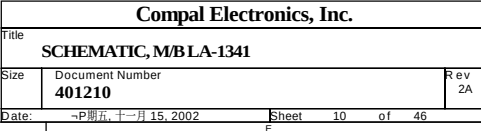
VGA
650-3
MuTIOL



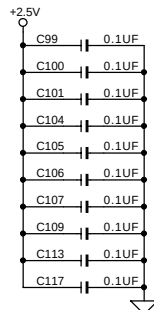
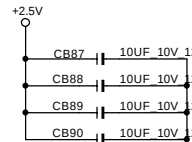
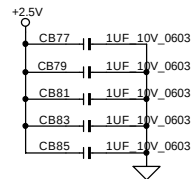
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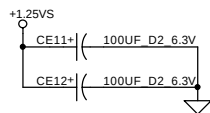
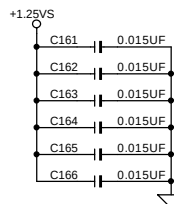
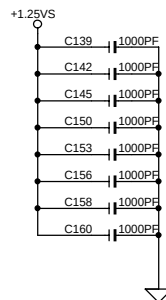
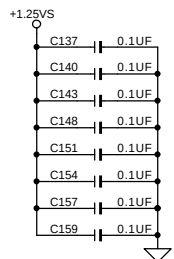




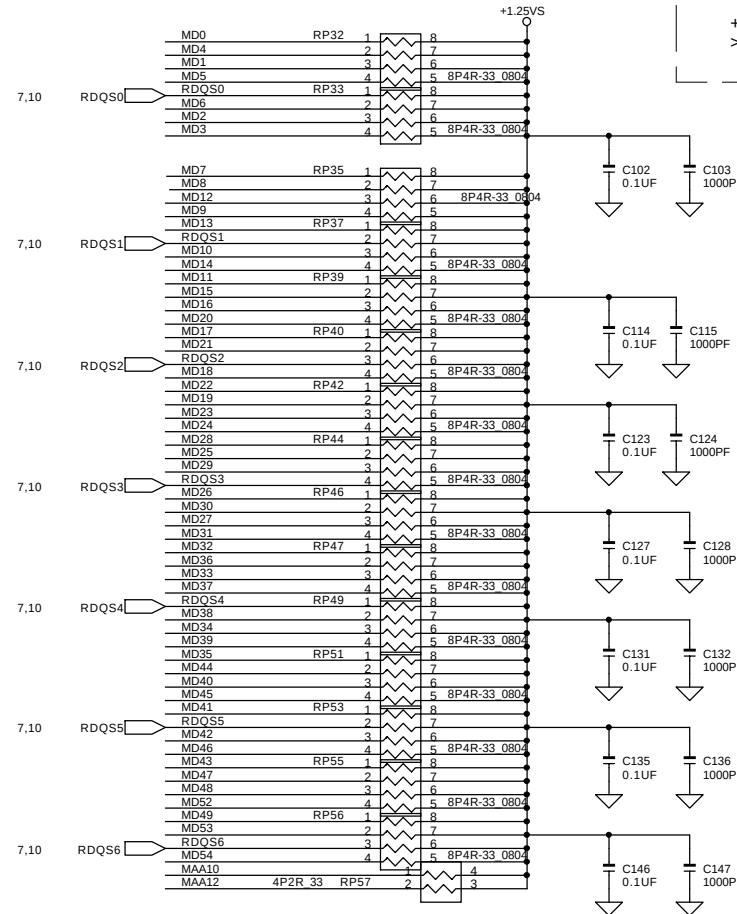
Decoupling capacitors (Place near DDR SODIMM)



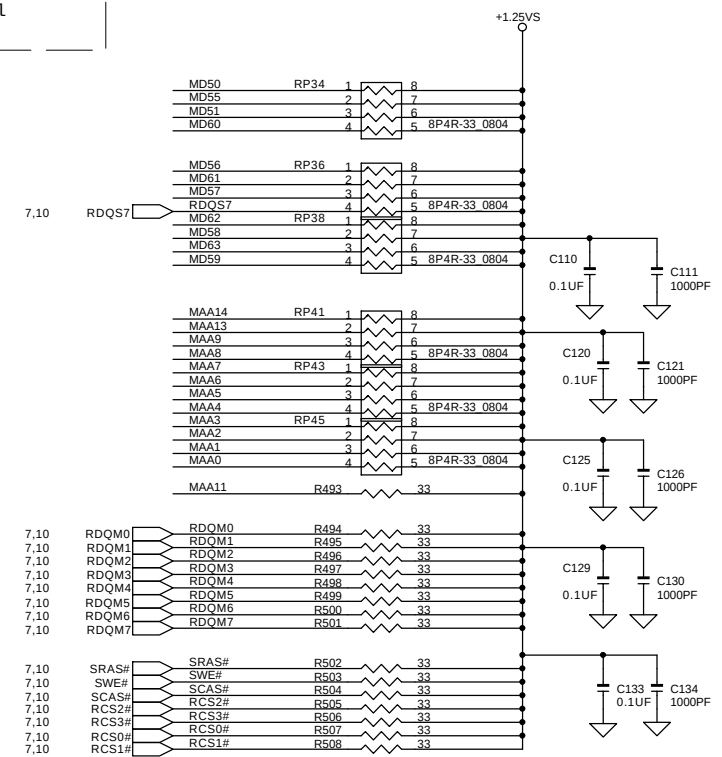
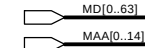
Place these decoupling capacitors close to VTT_MEM termination resistors.



Termination Network place closely to DIMM



7,10 MD[0..63]
7,10 MAA[0..14]

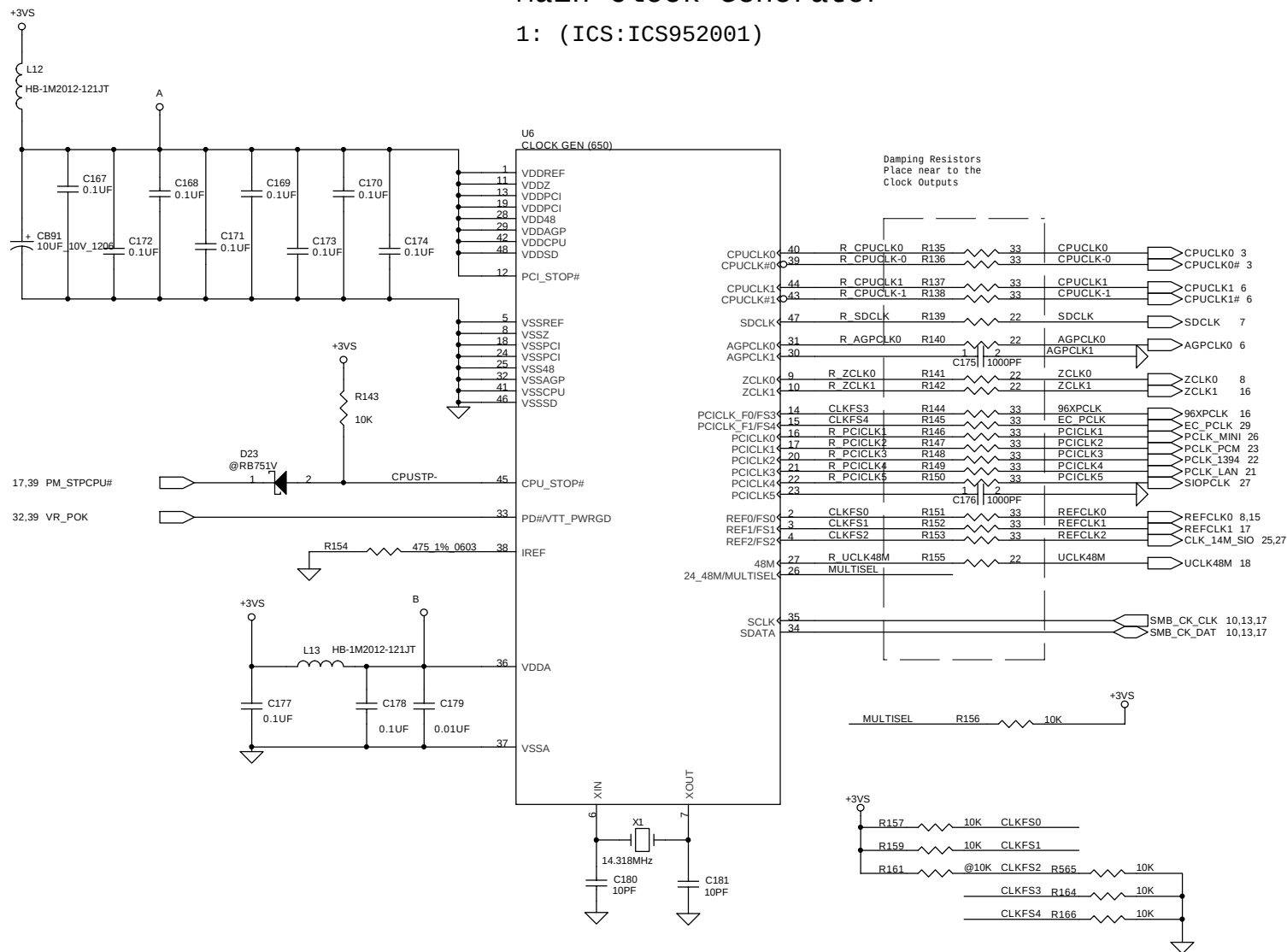


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Main Clock Generator

1: (ICS:ICS952001)



Place near to the Clock Outputs

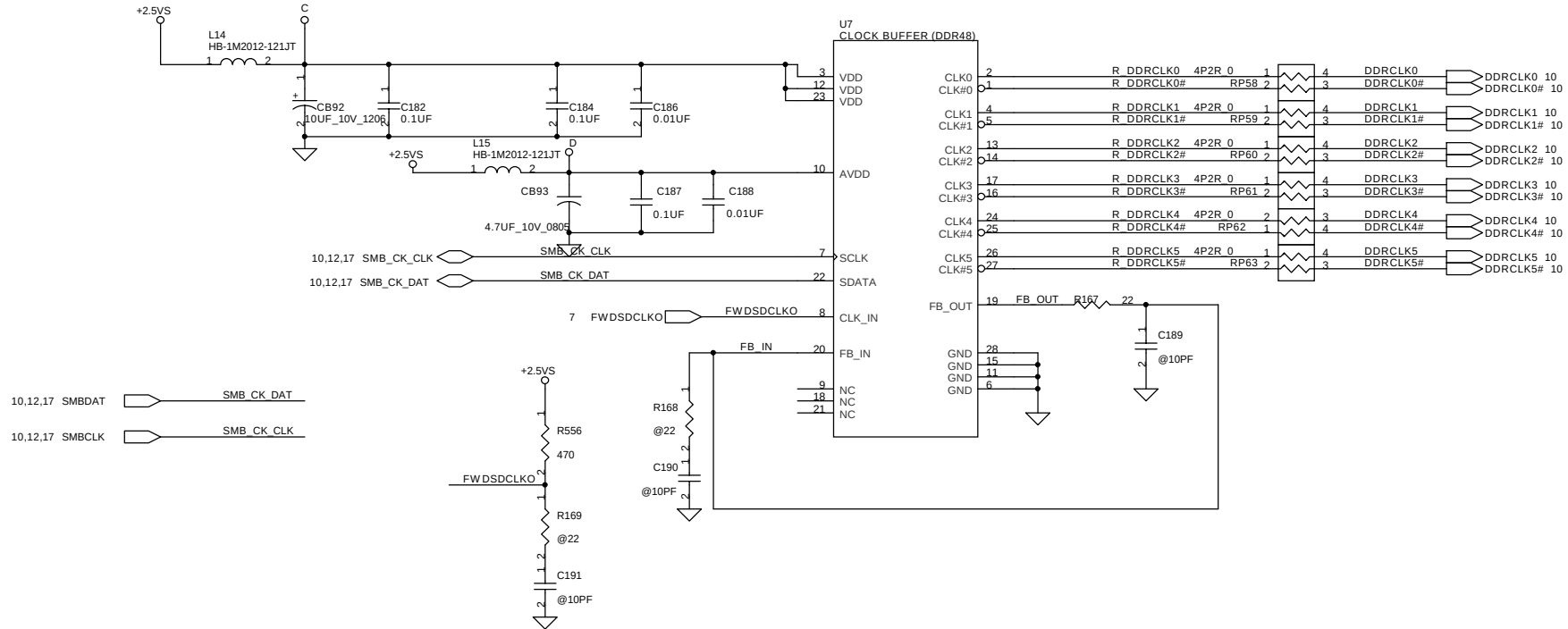
CPUCLK0	R131		49.9 1% 0603
CPUCLK-0	R132		49.9 1% 0603
CPUCLK1	R133		49.9 1% 0603
CPUCLK-1	R134		49.9 1% 0603

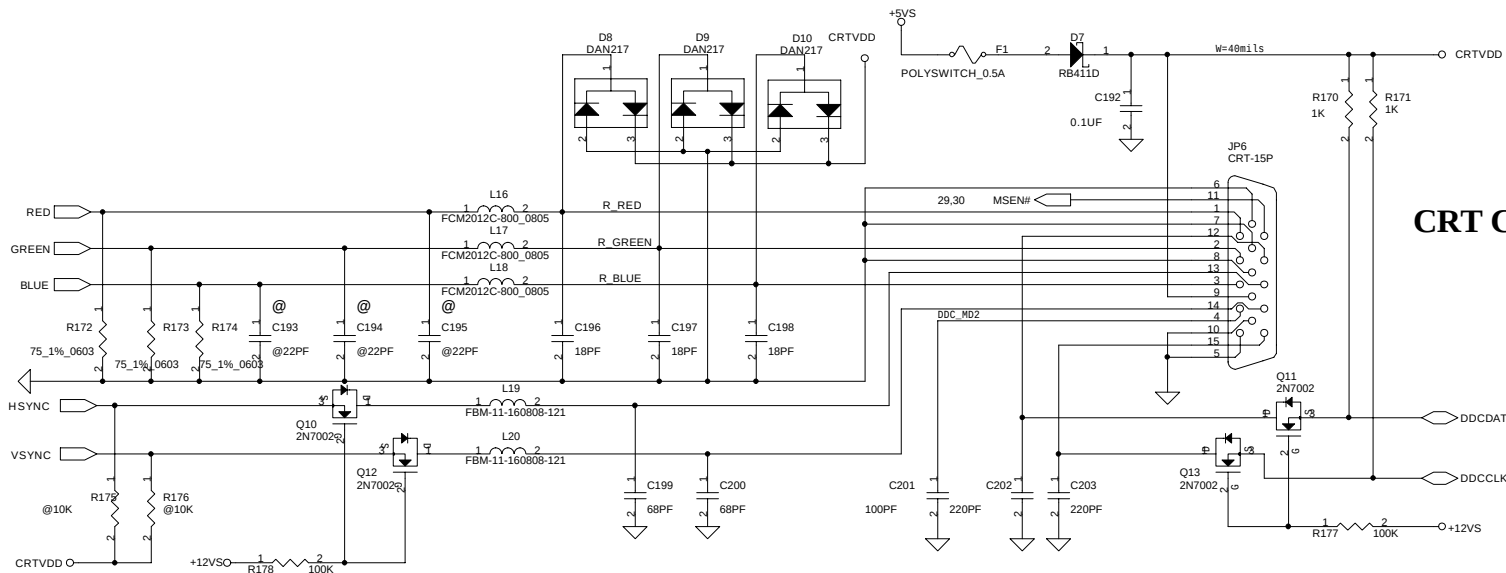
	FS4	FS3	FS2	FS1	FS0	CPU	DDR	ZCLK	AGP	PCI
	1	1	0	0	0	100.0	133.0	80.0	66.7	33.3
	1	1	0	0	1	100.0	100.0	80.0	66.7	33.3
*	0	0	1	1	1	100.0	133.3	80.0	66.7	33.3

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Title SCHEMATIC, MBLA-1341		
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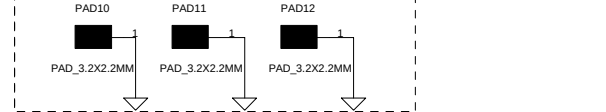
1: (ICS:ICS93722)



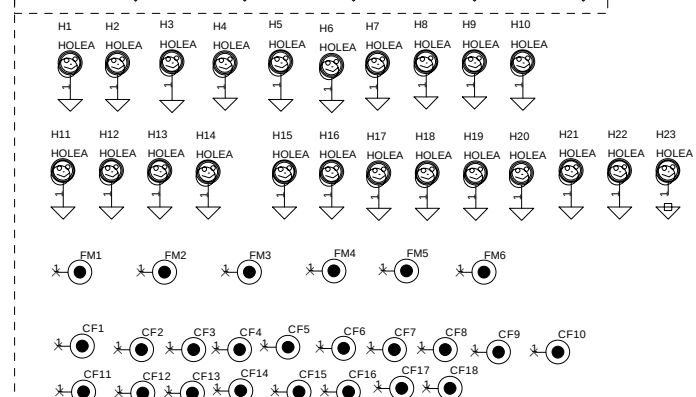
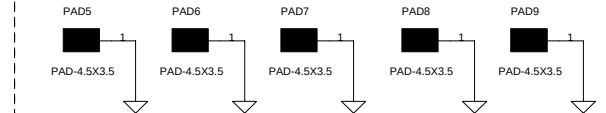


CRT Connector

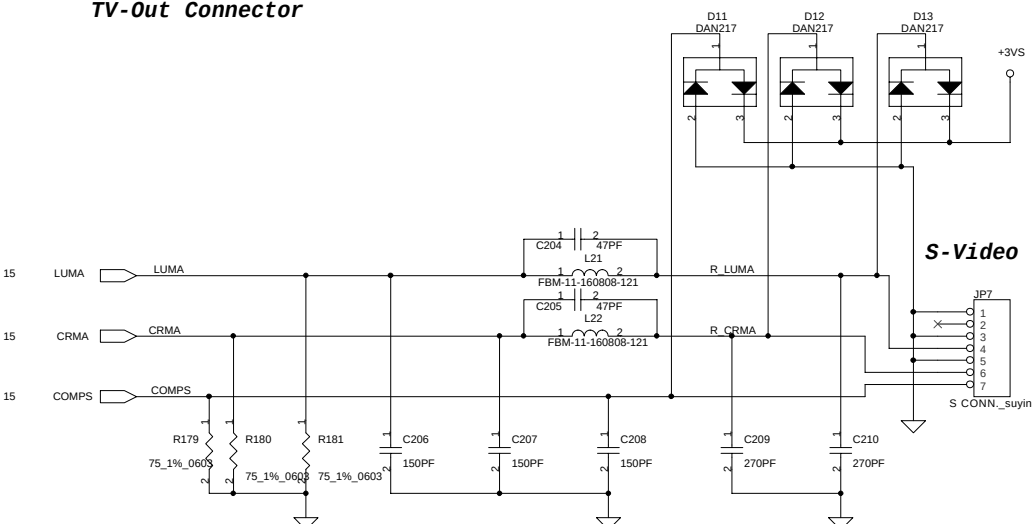
Cable guide



DDR EMI Clip



TV-Out Connector

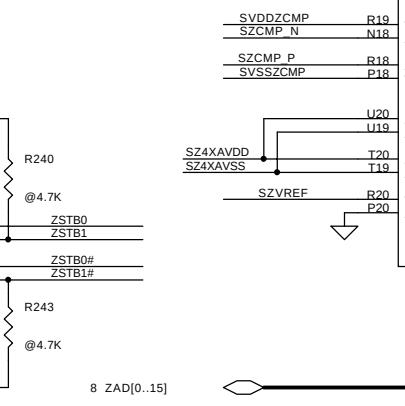
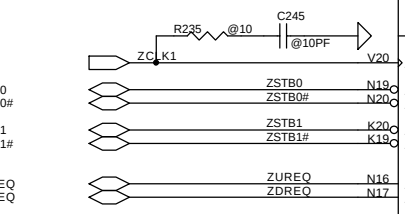
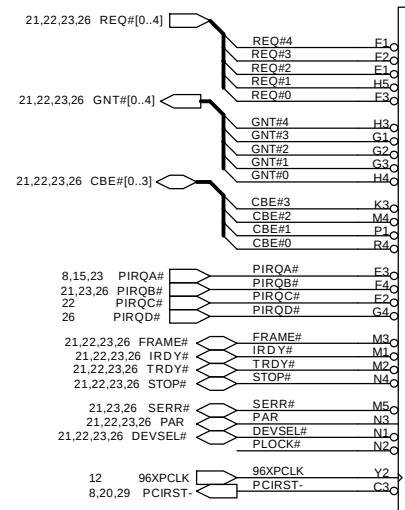
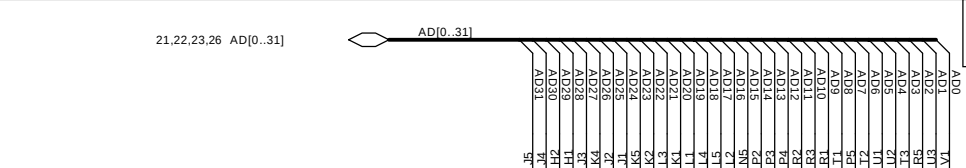
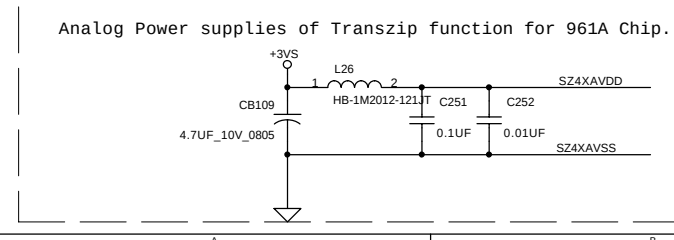
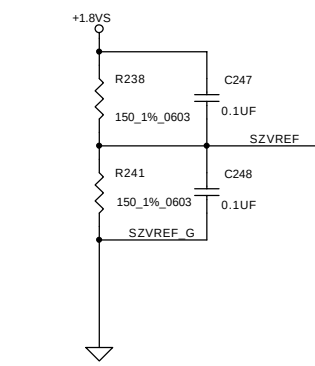
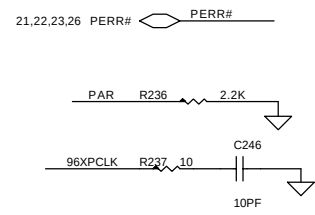
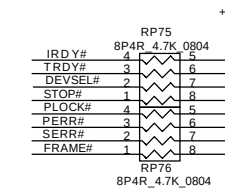
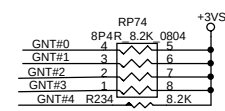
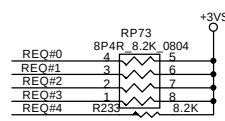
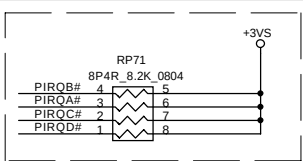


S-Video

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Title	SCHEMATIC, M/B LA-1341		
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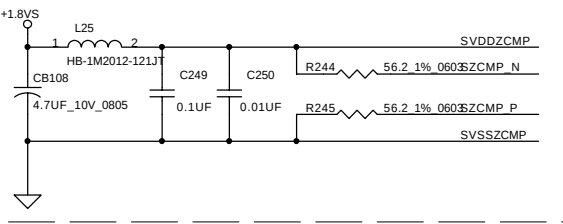
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND IS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



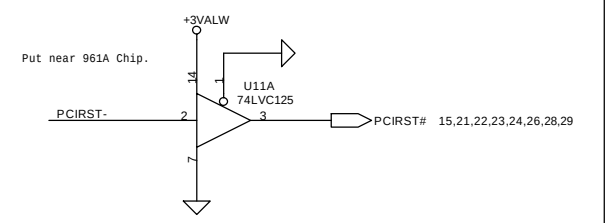
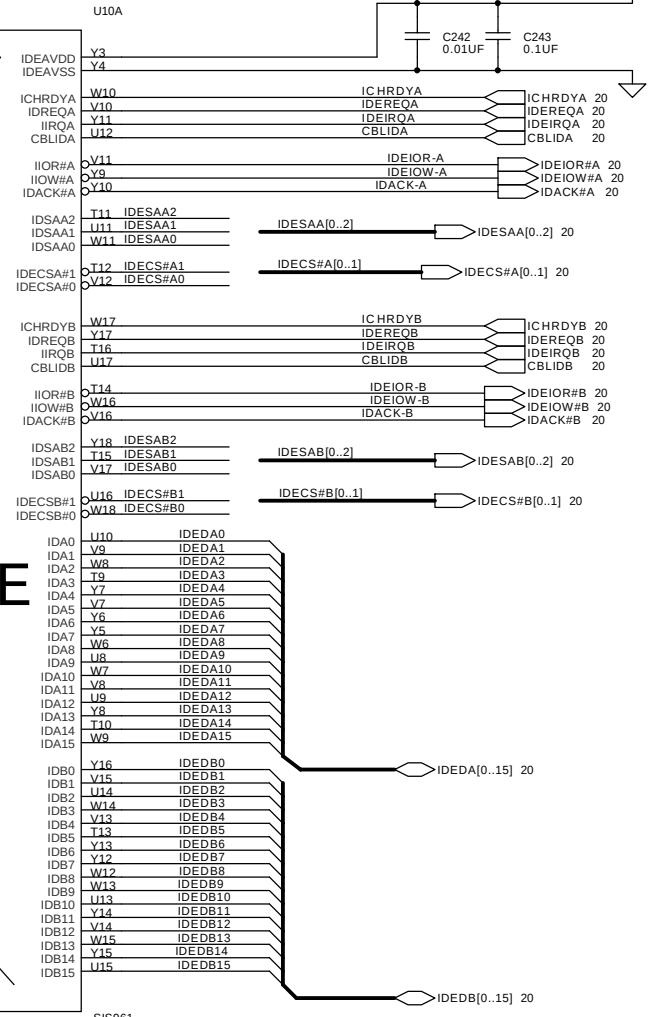
PCI

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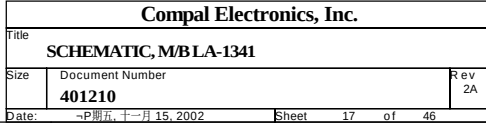


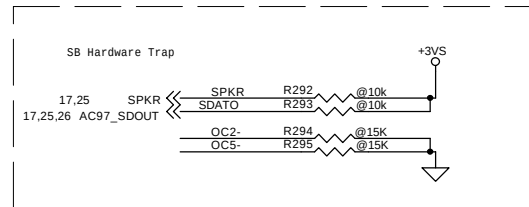
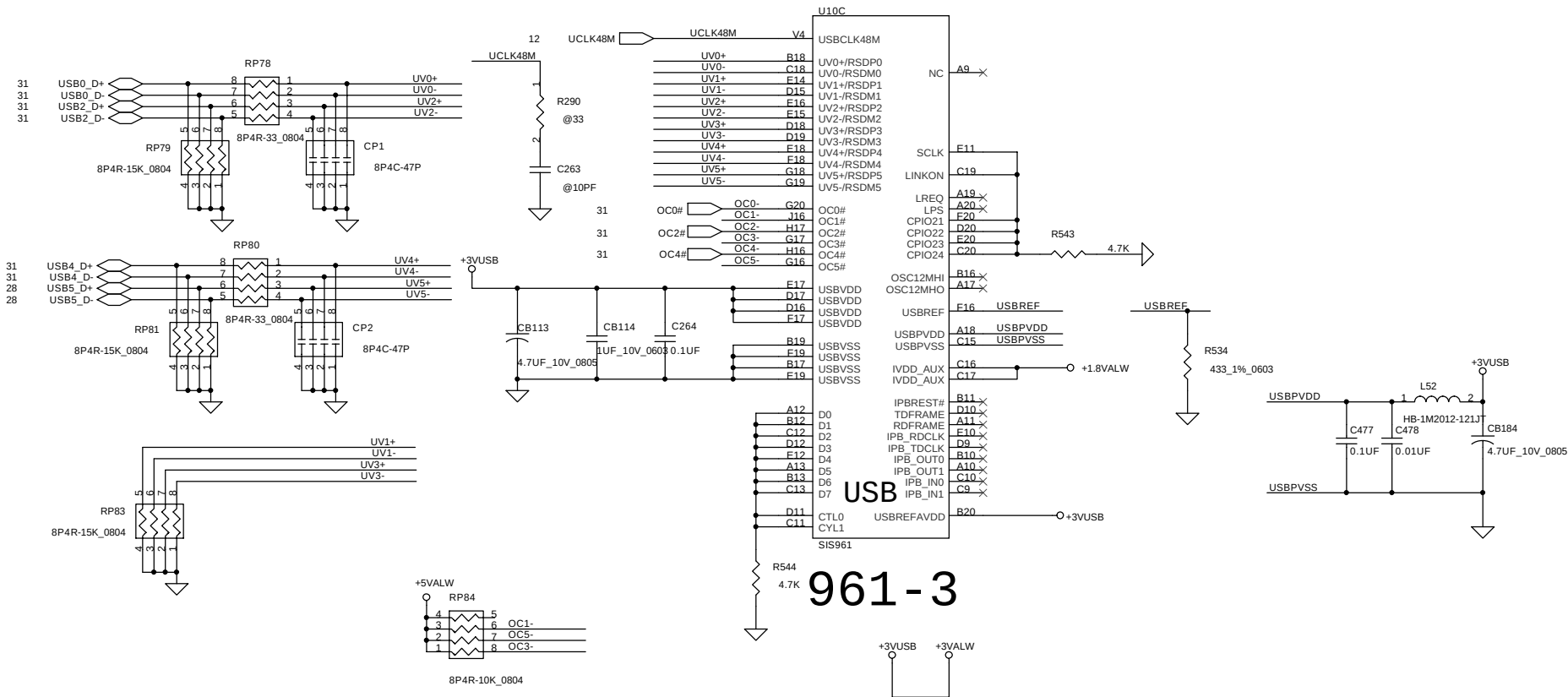
IDE



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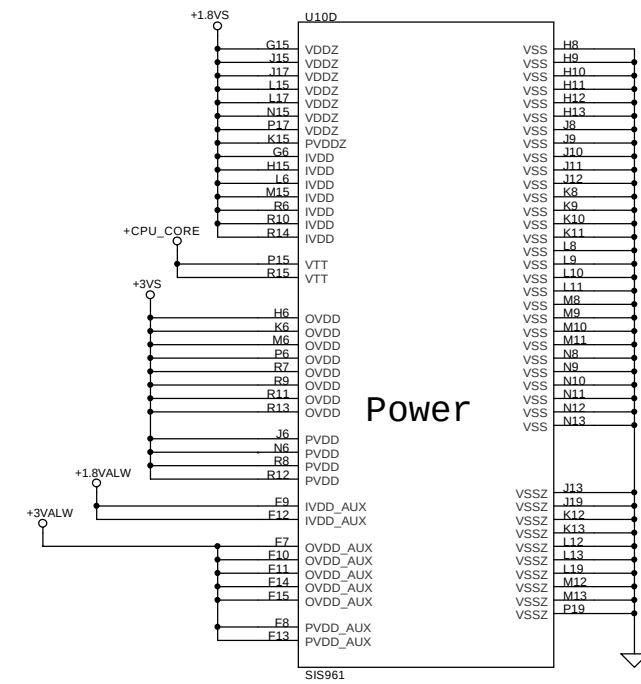
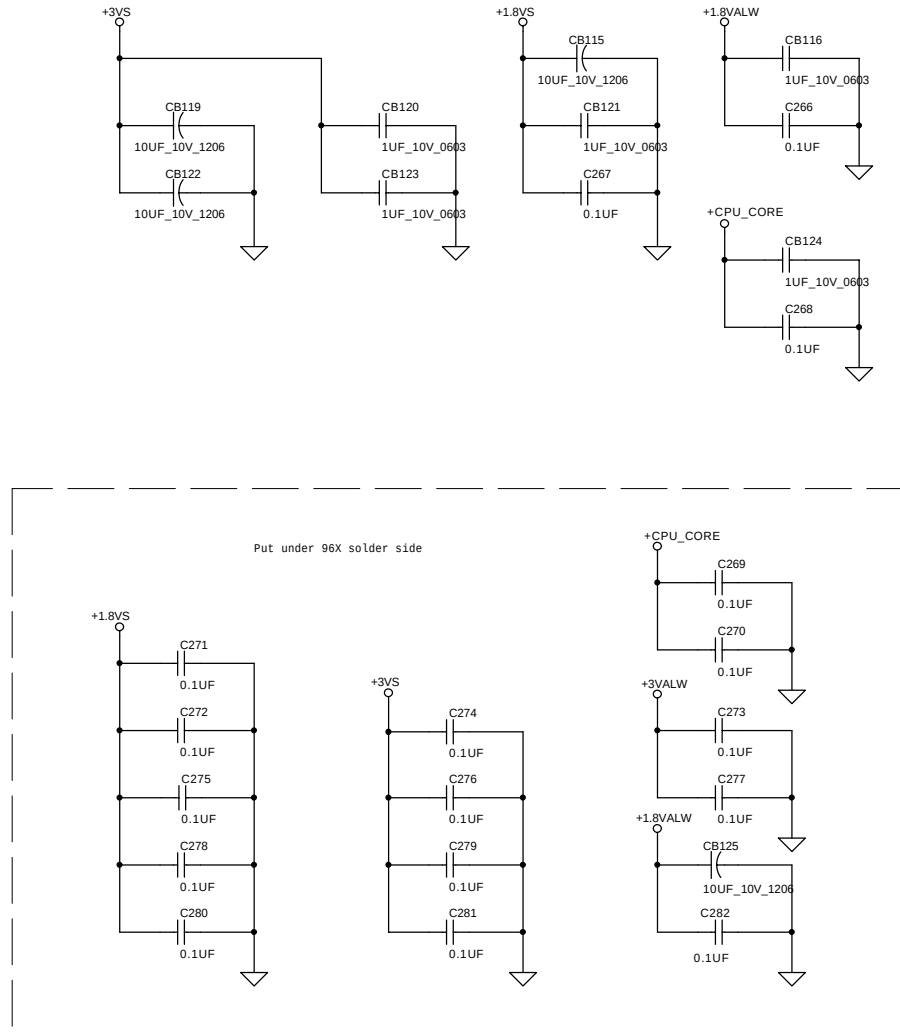




	0	1	Default
SPKR(LPC addr mapping)	disable	enable	0
SDATO(PCICLK PLL)	enable	disable	0
OC2-(SB debug mode)	enable	disable	1
OC5-(Trap mode)	PCI AD	ROM	1

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Title SCHEMATIC, MB LA-1341		
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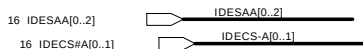
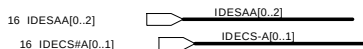
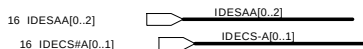
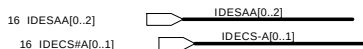
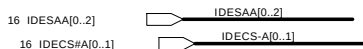
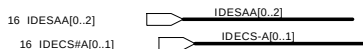
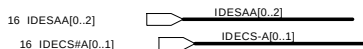
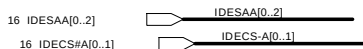
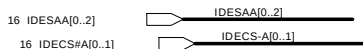
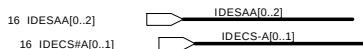
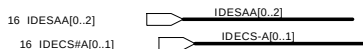
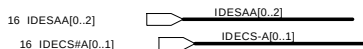
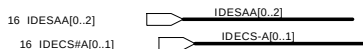
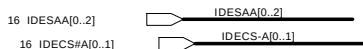
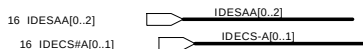
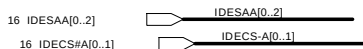
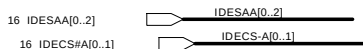
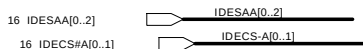
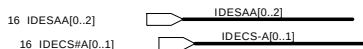
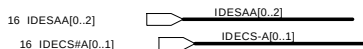
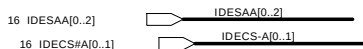
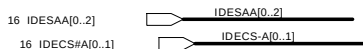
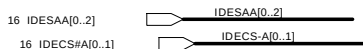
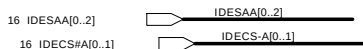
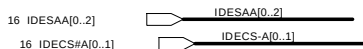
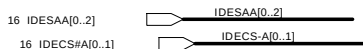
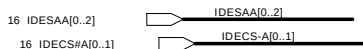
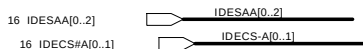
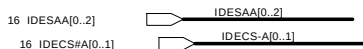
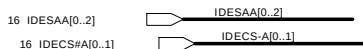
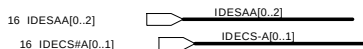
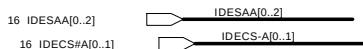
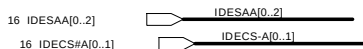
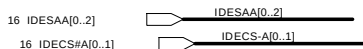
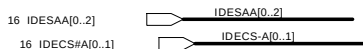
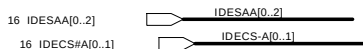
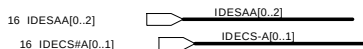
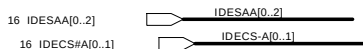
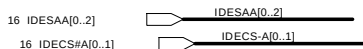
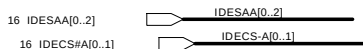
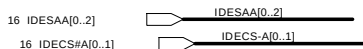
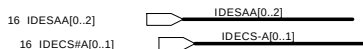
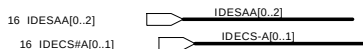
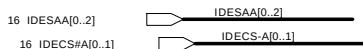
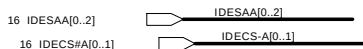
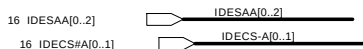
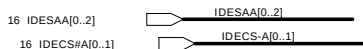
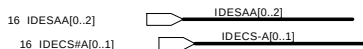
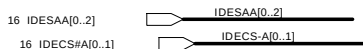
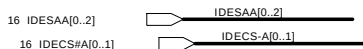
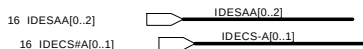
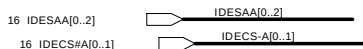
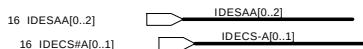
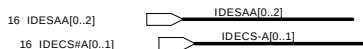
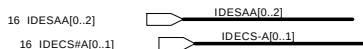
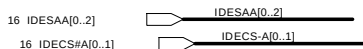
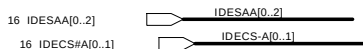
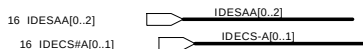
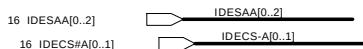
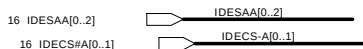
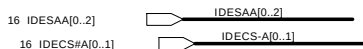
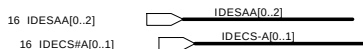
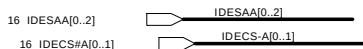
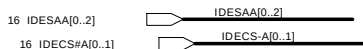
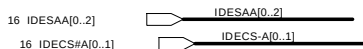
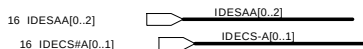
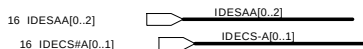
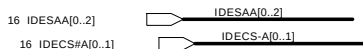
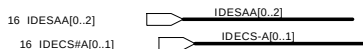
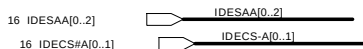
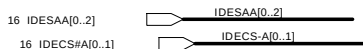
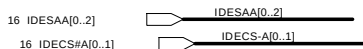
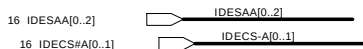
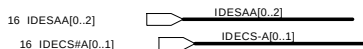
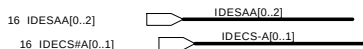
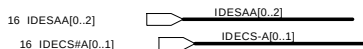
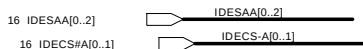
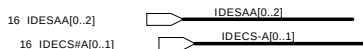
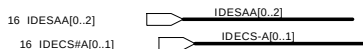
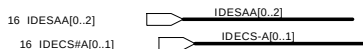
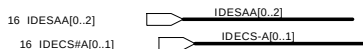
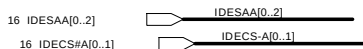
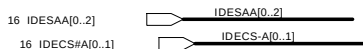
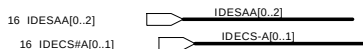
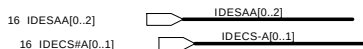
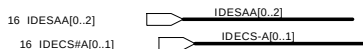
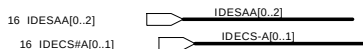
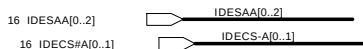
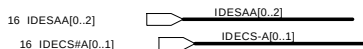
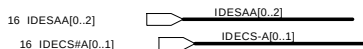
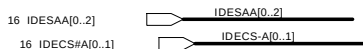
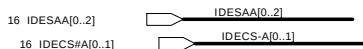
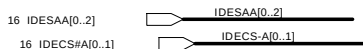
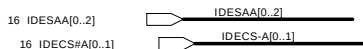
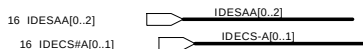
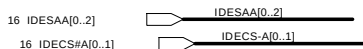
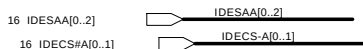
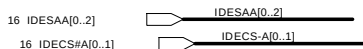
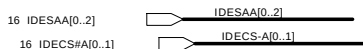
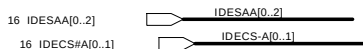
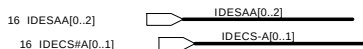
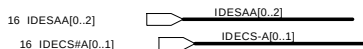
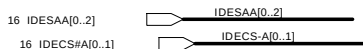
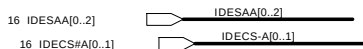
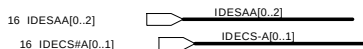
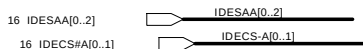
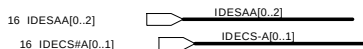
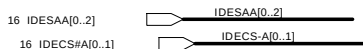
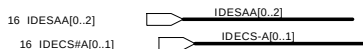
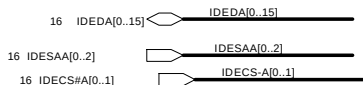
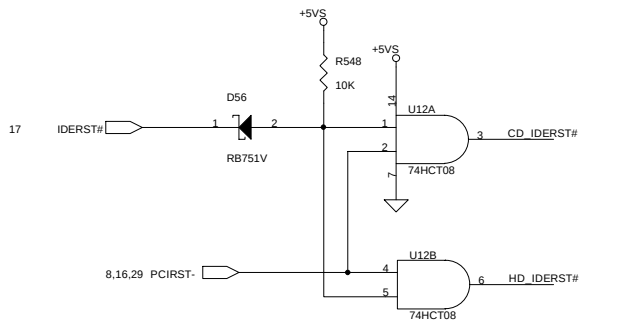


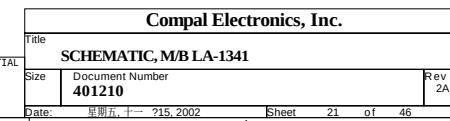
961-4

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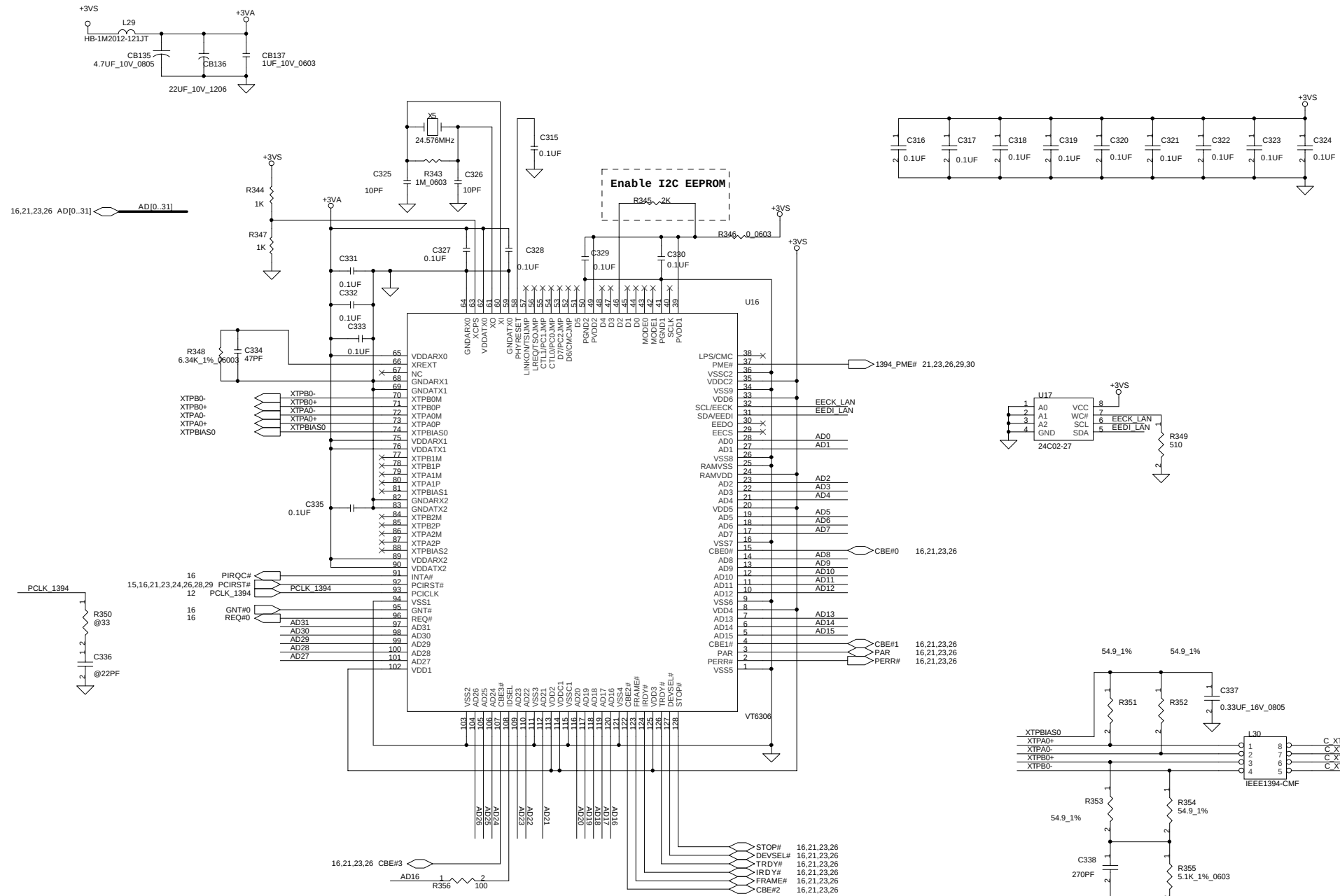
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HDD/CD-ROM Module





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@X3

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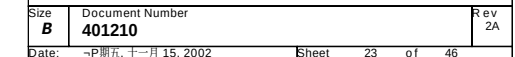
Title
SCHEMATIC, M/B LA-1341Size Document Number
401210Rev
2A

Date: 星期五, 十一月 21, 2002

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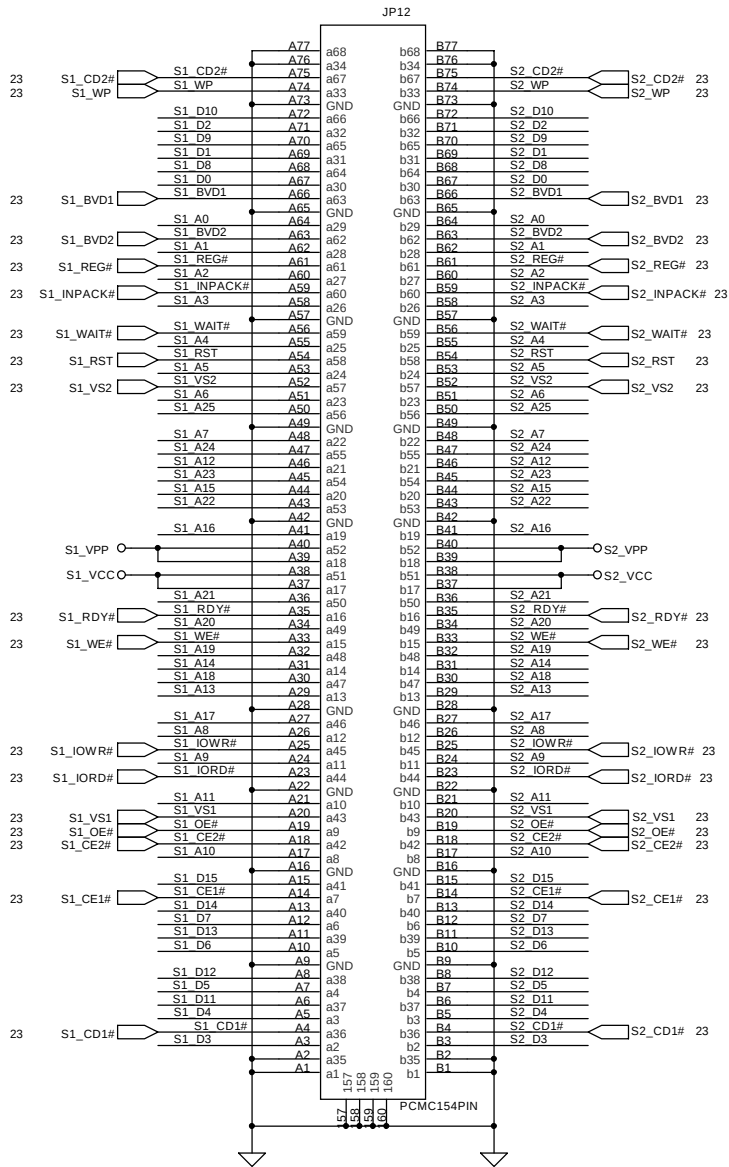
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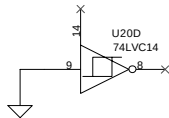
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CARDBUS

SOCKET



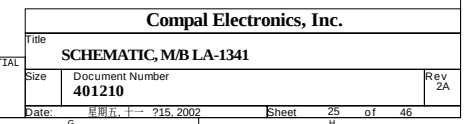


The diagram illustrates the pin connections for the CODEC CONN. The connector has 30 pins, numbered 1 through 30. The connections are as follows:

- Pin 1:** CLK 14M_SIO
- Pin 2:** AC97_BCLK
- Pin 3:** HPLUG
- Pin 4:** AC97_RST#
- Pin 5:** AC97_SYNC
- Pin 6:** AC97_SDOU
- Pin 7:** AC97_SDI0
- Pin 8:** MD_MIC
- Pin 9:** MONO IN
- Pin 10:** MOD_AUDIO_MONR
- Pin 11:** +3VS_O
- Pin 12:** CDROM_L
- Pin 13:** CDROM_R
- Pin 14:** INTMIC
- Pin 15:** PS2_DATA
- Pin 16:** PS2_CLK
- Pin 17:** EC_MUTE
- Pin 18:** FULL_LED#
- Pin 19:** ACT_LED#
- Pin 20:** CHARGING_LED#
- Pin 21:** POWER1_LED#
- Pin 22:** POWER2_LED#
- Pin 23:** LED2
- Pin 24:** +5VS_O
- Pin 25:** +5VALW_O
- Pin 26:** +3VALW_O
- Pin 27:** GND - MIC
- Pin 28:** AGND
- Pin 29:** (Unused)
- Pin 30:** (Unused)

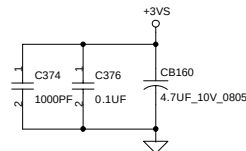
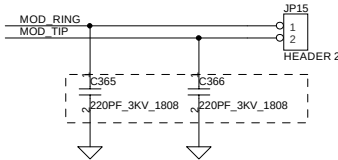
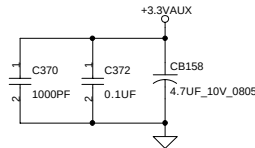
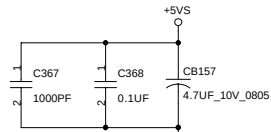
Additional components shown include:

- Resistors:** R559 (@33) and R560 (@33) connected to CLK 14M_SIO and AC97_BCLK respectively.
- Capacitors:** C481 (@22PF) and C482 (@22PF) connected to +3VS_O and +3VALW_O respectively.

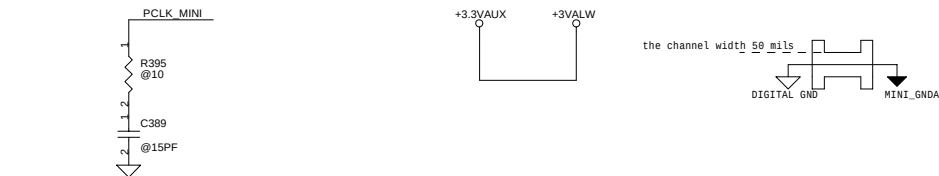
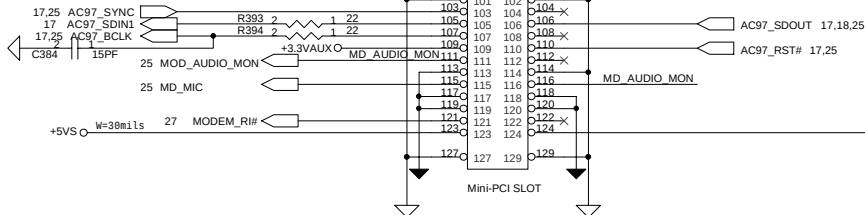
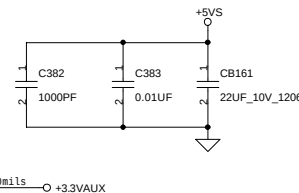
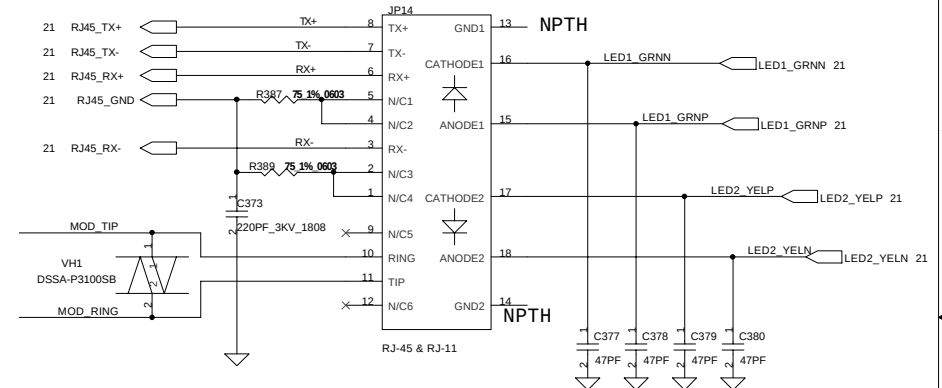
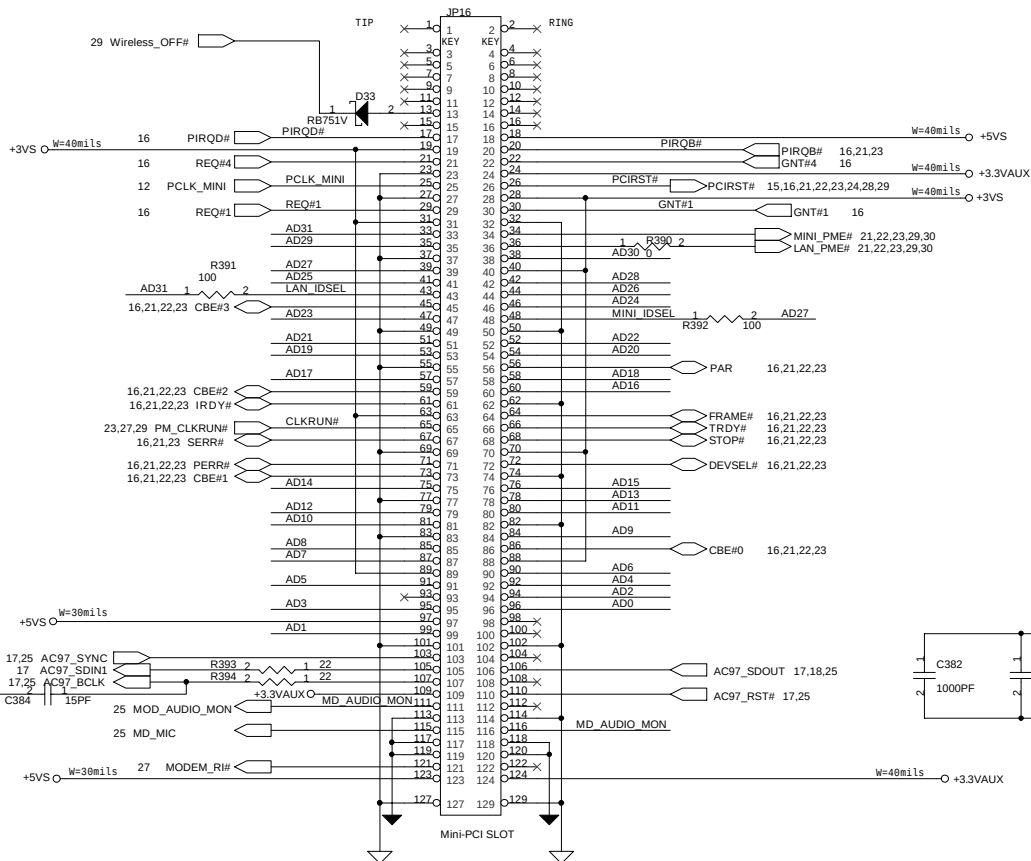


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Mini-PCI Slot



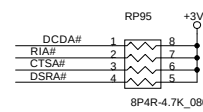
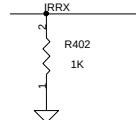
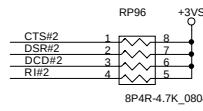
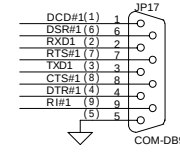
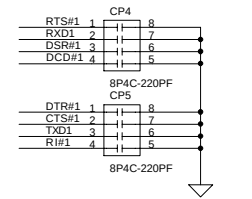
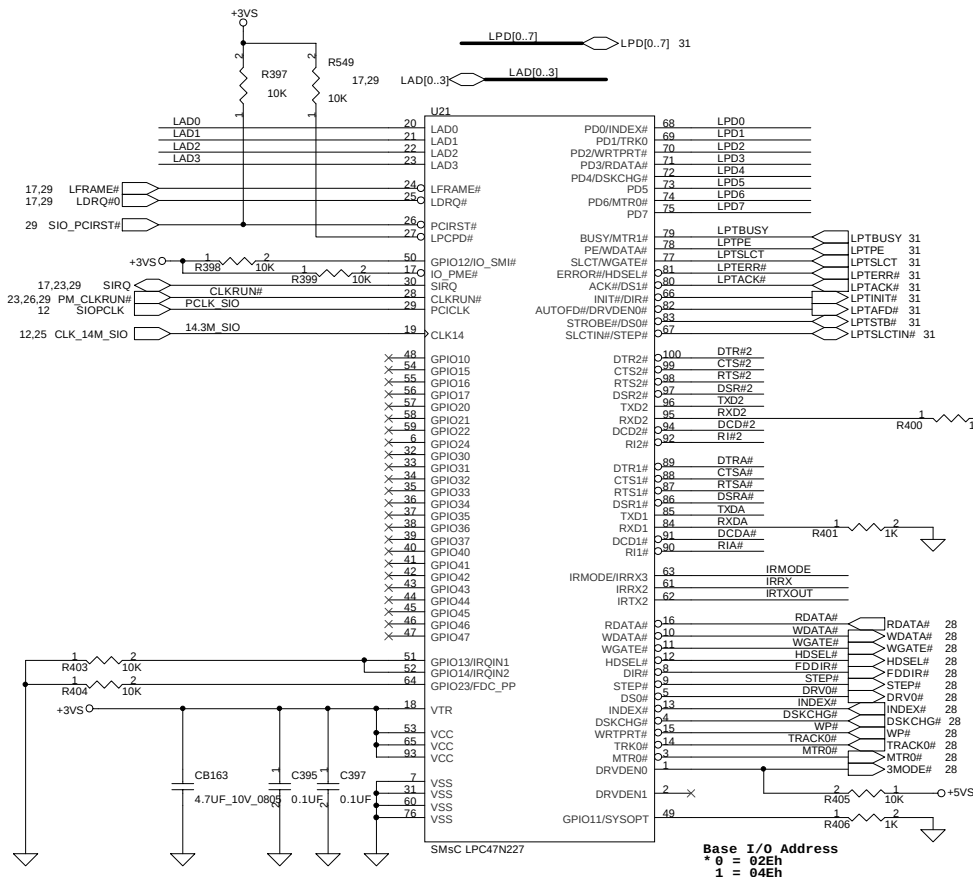
AD[0..31] AD[0..31] 16,21,22,23



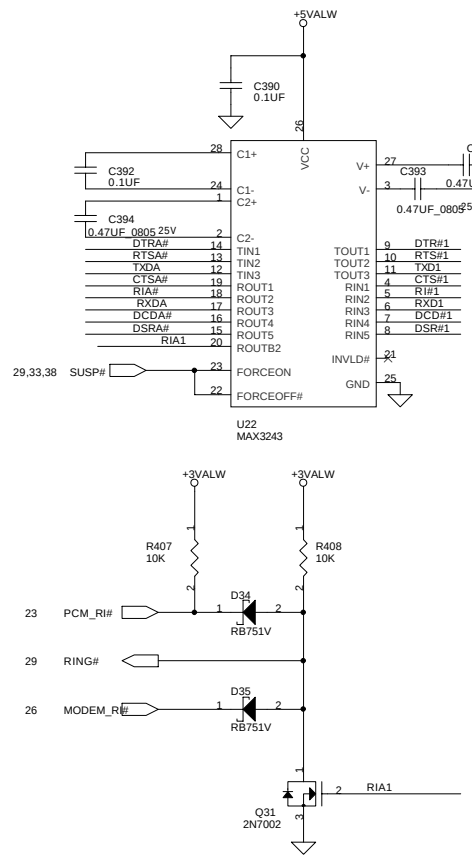
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SUPER I/O SMsC LPC47N227



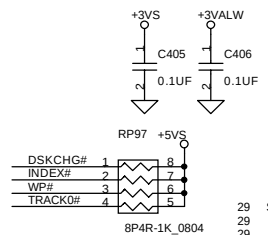
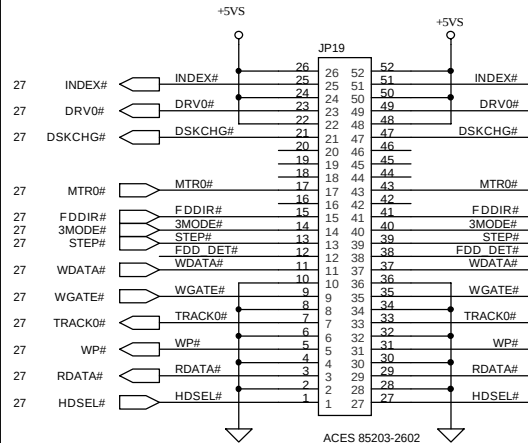
SERIAL PORT



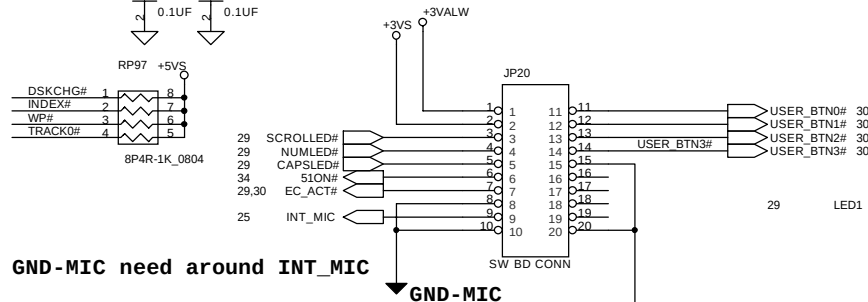
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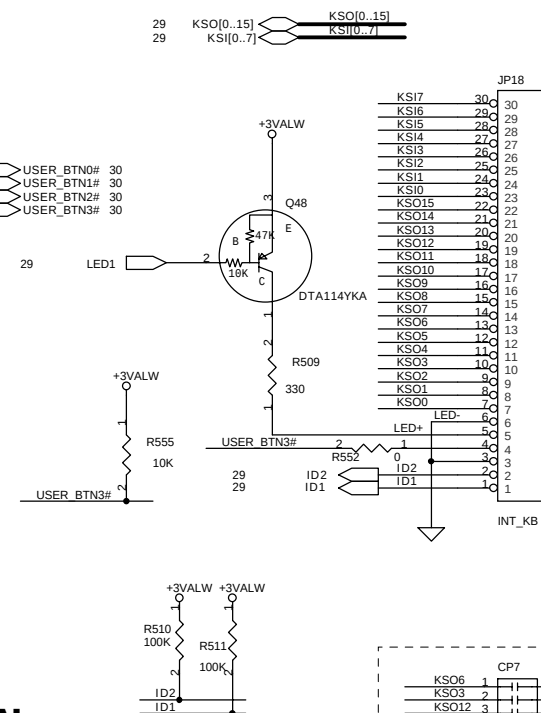
FDD CONN.



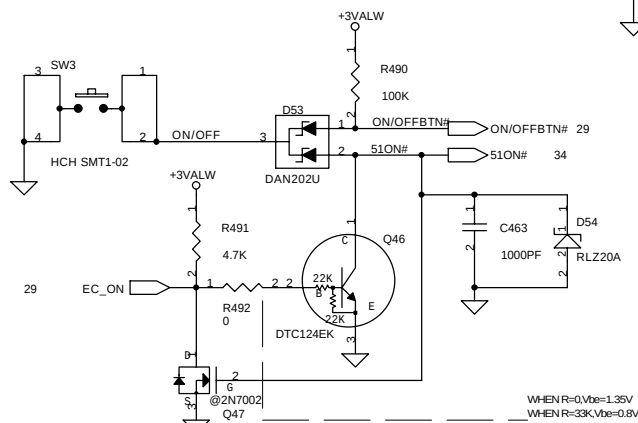
SWITCH BOARD CONN.



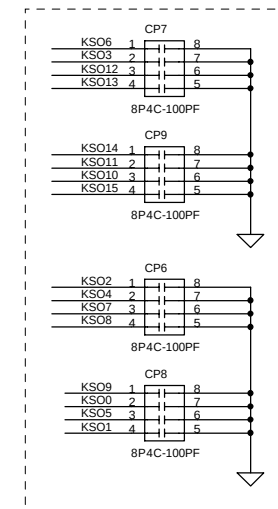
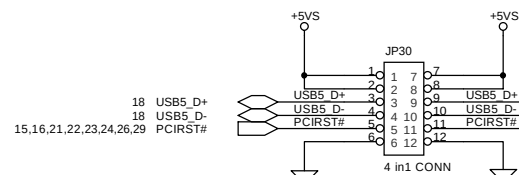
INT KBD CONN.



ON/OFF BUTTON

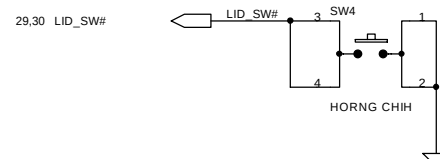


4 IN 1 CONN

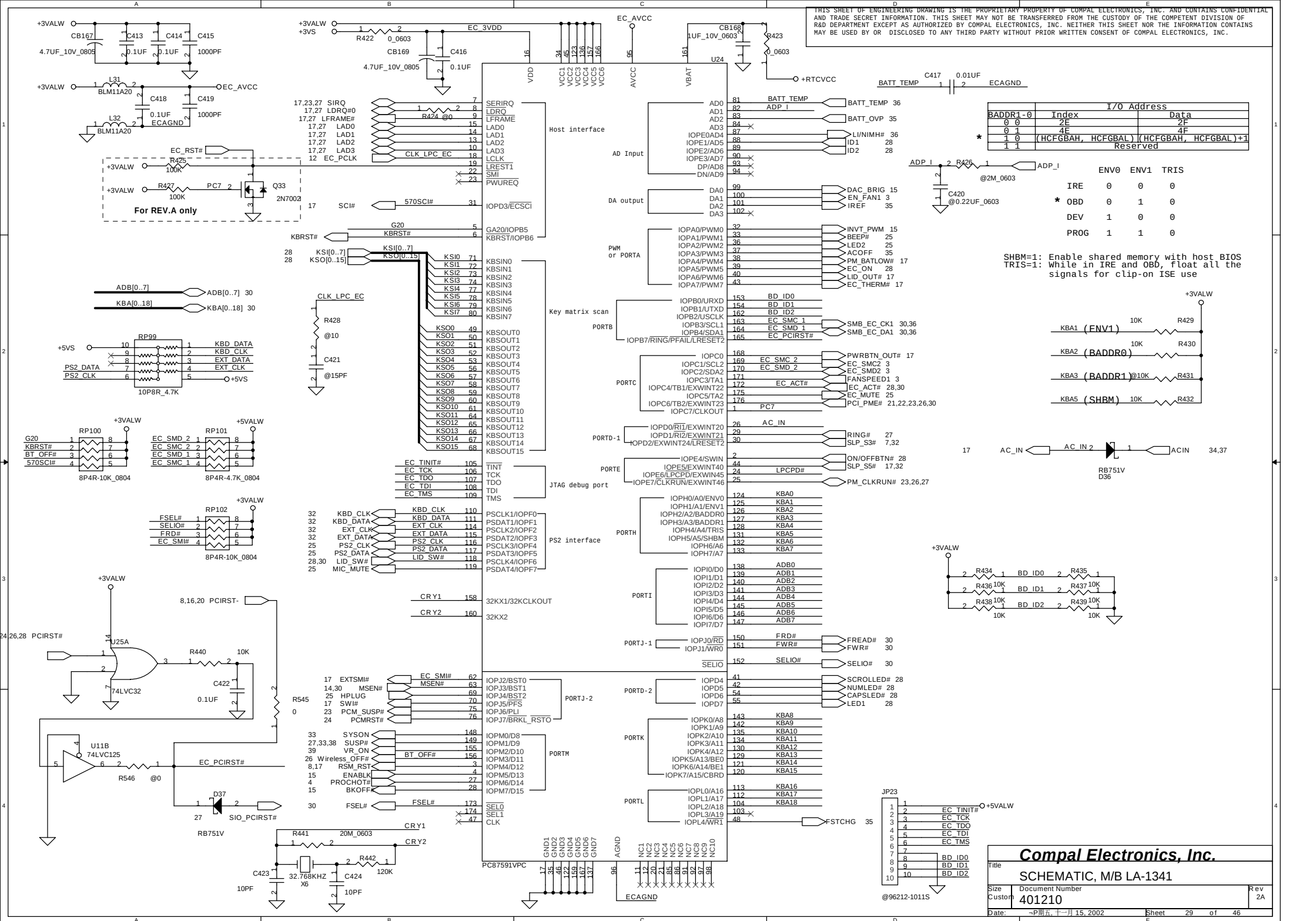


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LID SW

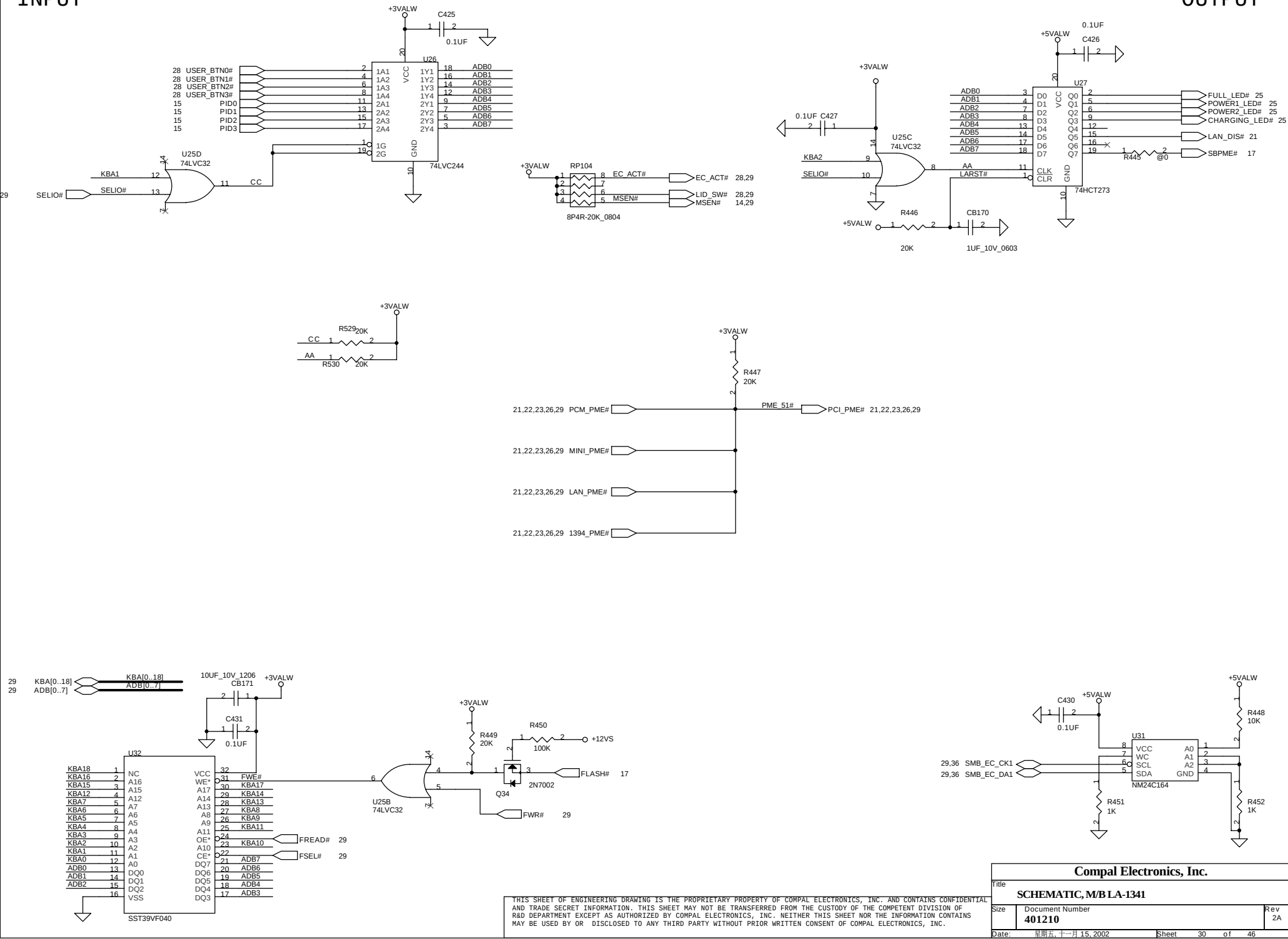


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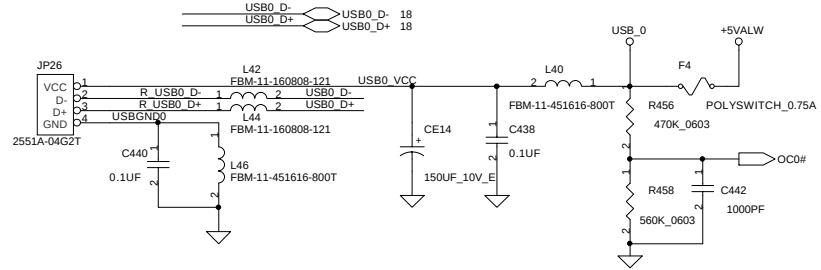


INPUT

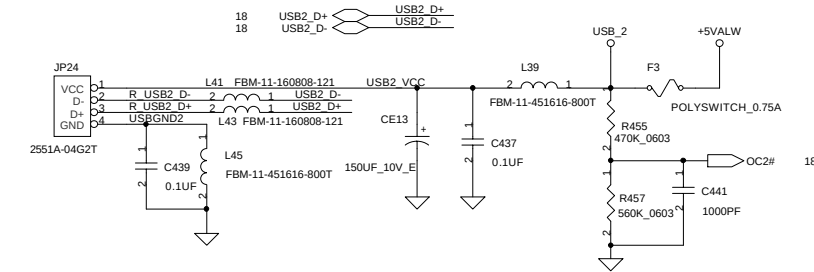
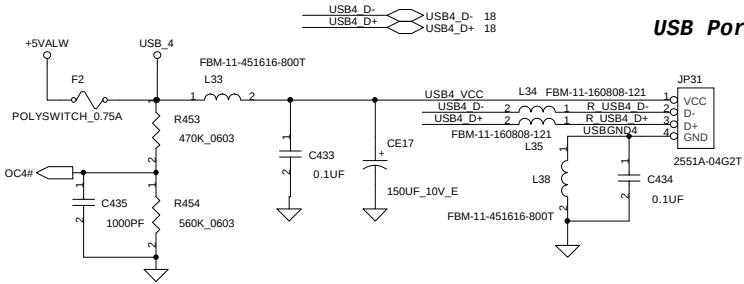
OUTPUT



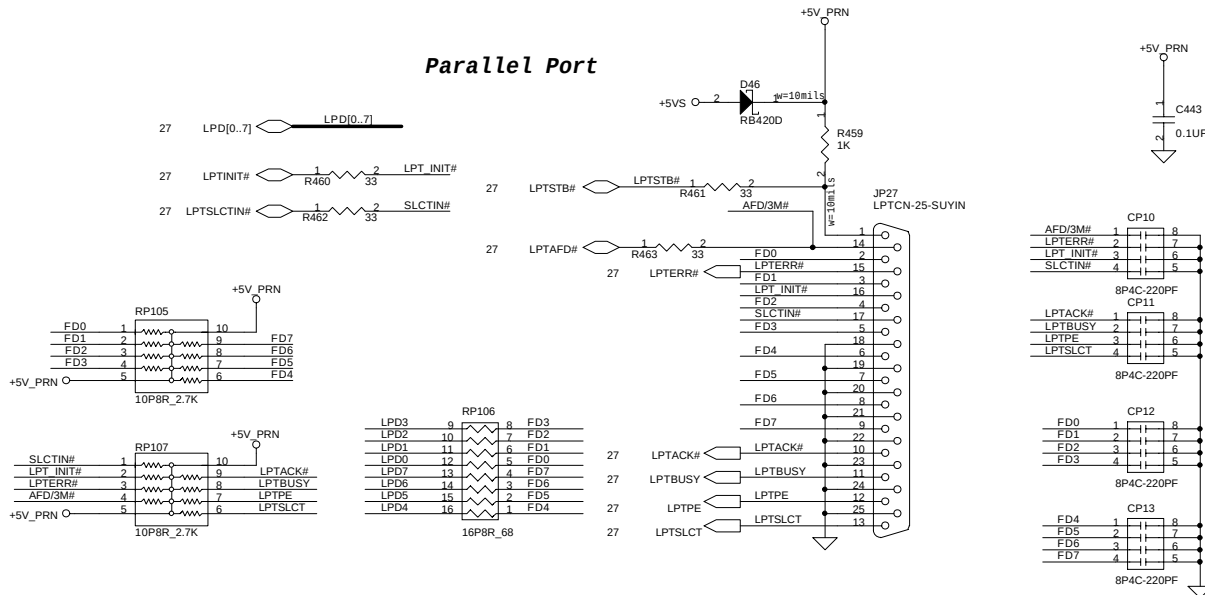
Printer Port/USB



USB Port

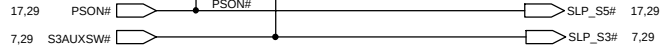
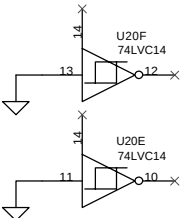


Parallel Port

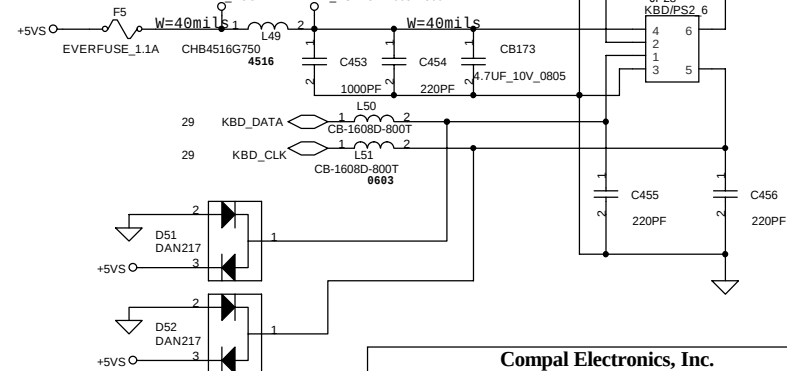


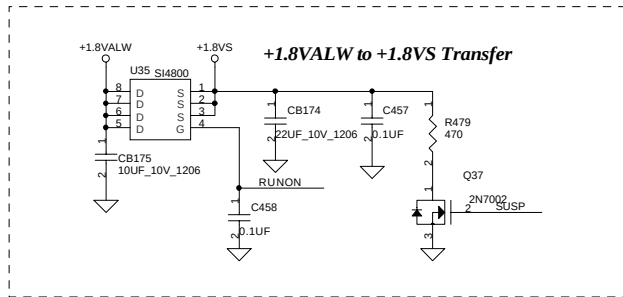
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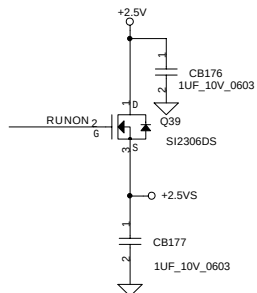


S3AUXSW#	1	0	1
PSON#	0	1	1
SLP_S3#	1	0	0
SLP_S5#	1	1	0



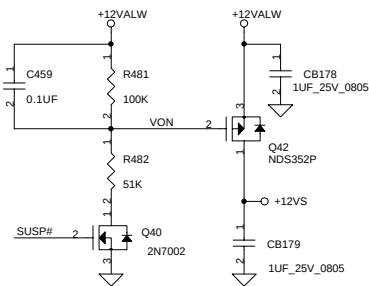


+2.5V to +2.5VS Transfer

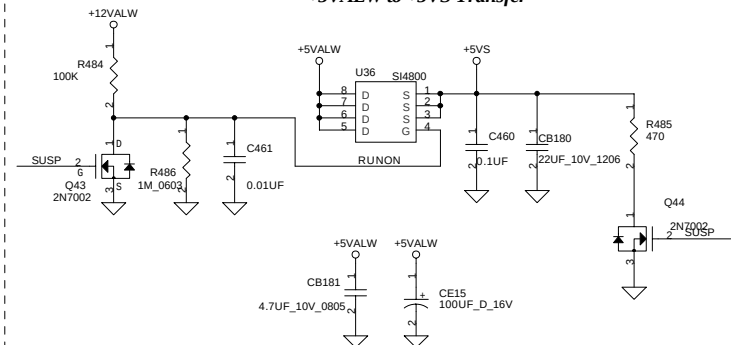


1.8VALW/+1.5VS Power direct provide

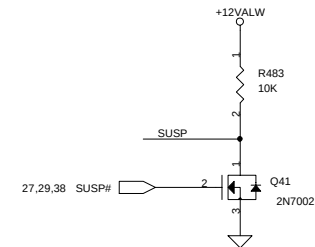
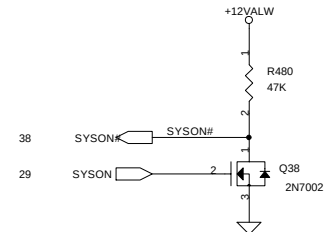
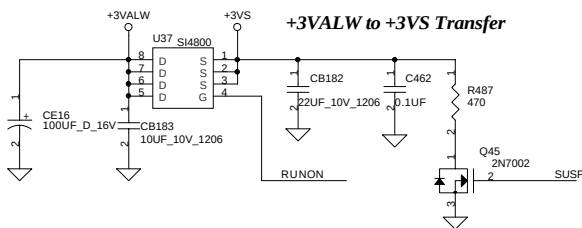
+12VALW TO +12VS Transfer



+5VALW to +5VS Transfer

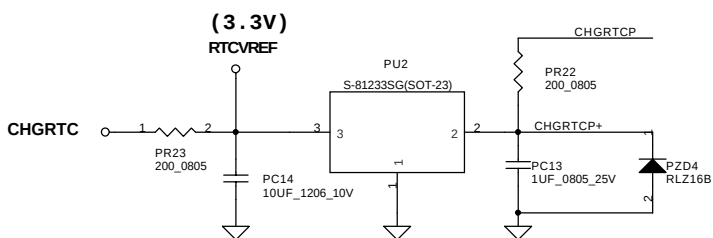
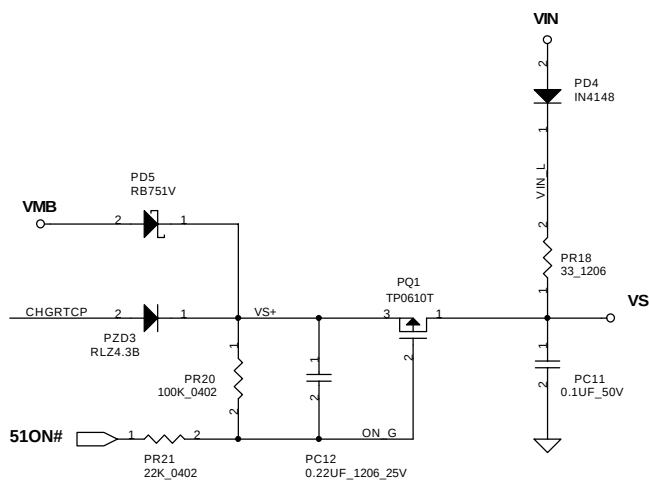
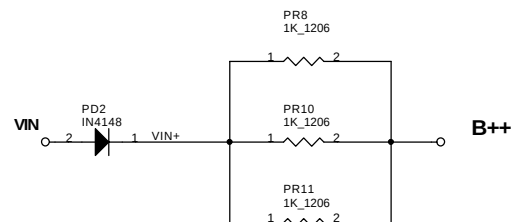
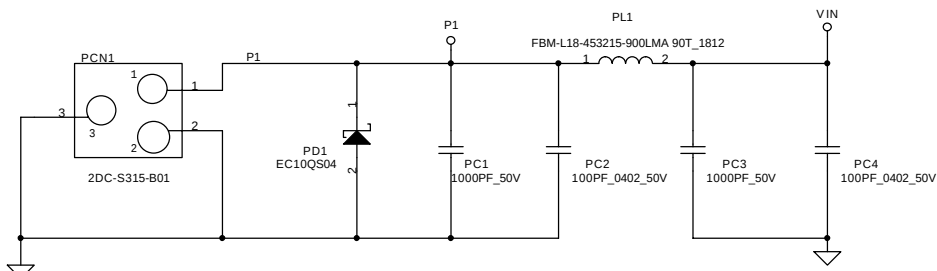


+3VALW to +3VS Transfer



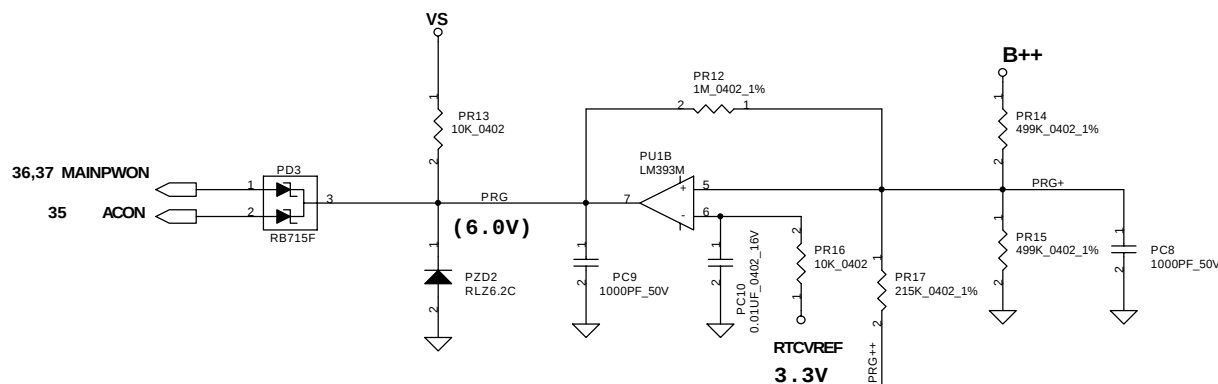
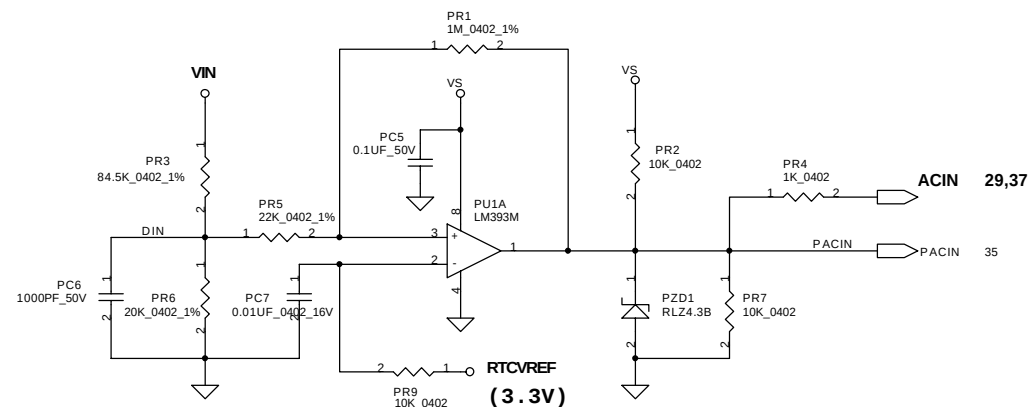
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Vin Detector

High 18.784 17.901 17.077 V
Low 17.877 17.043 16.195 V



ACIN

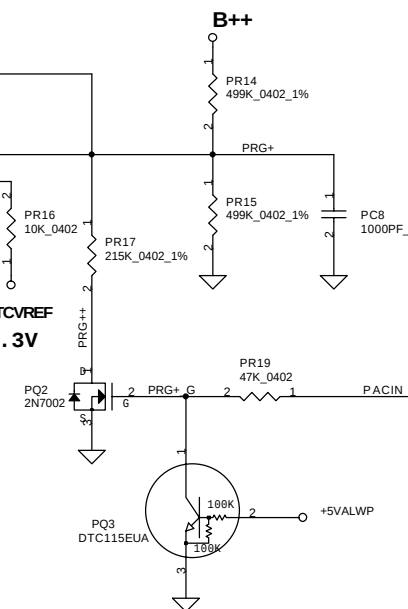
Precharge detector

16.6 15.9 15.2
13.48 12.93 12.09

BAT ONLY

Precharge detector

8.597 8.247 7.904
6.310 6.101 5.683



Compal Electronics, Inc.

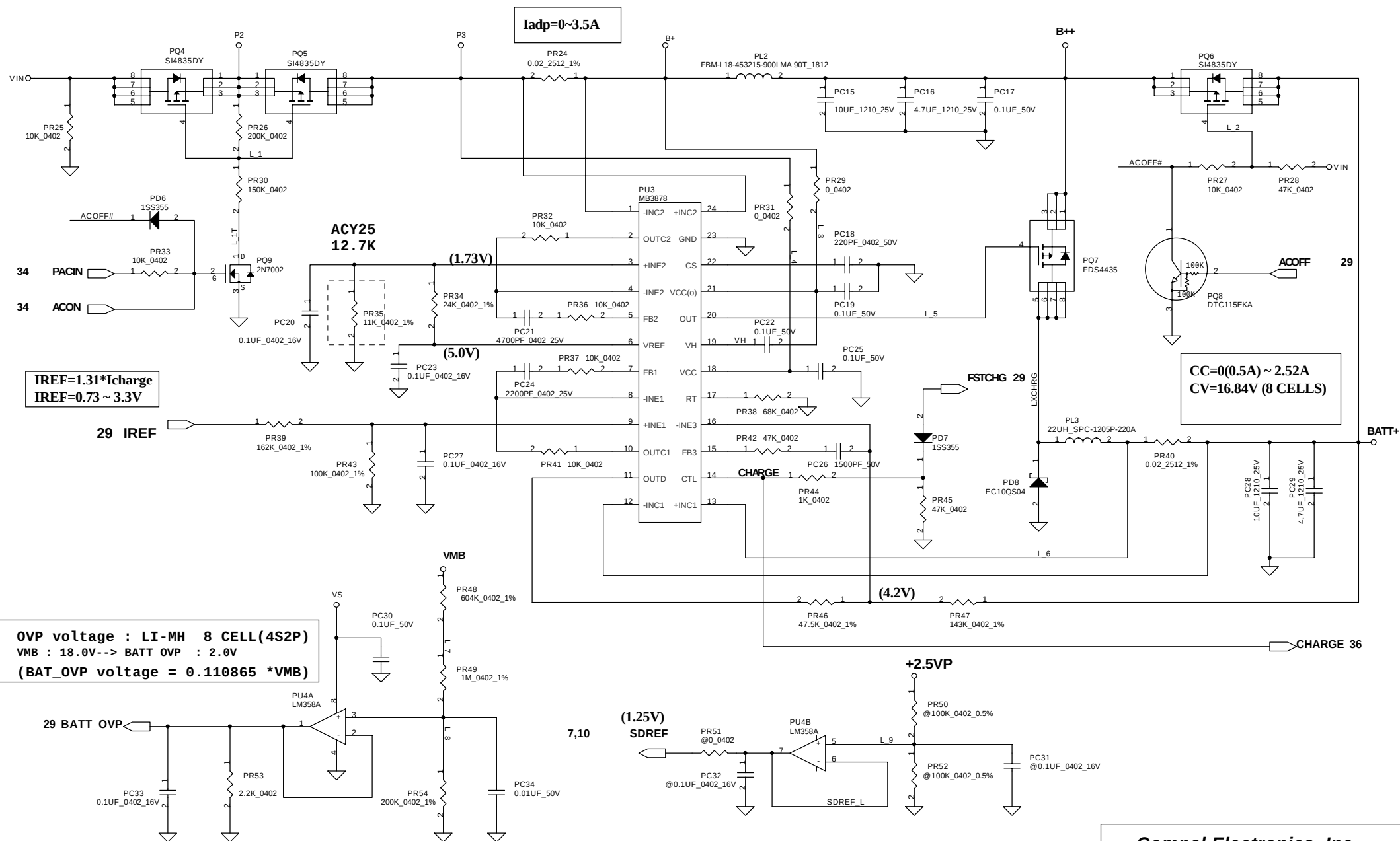
Title
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Rev
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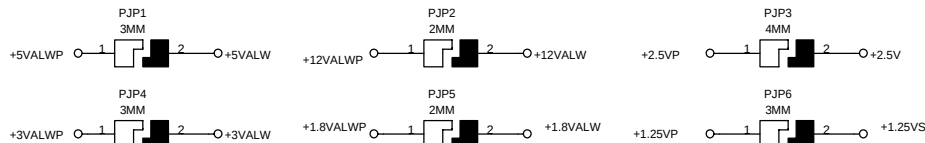


Compal Electronics, Inc.

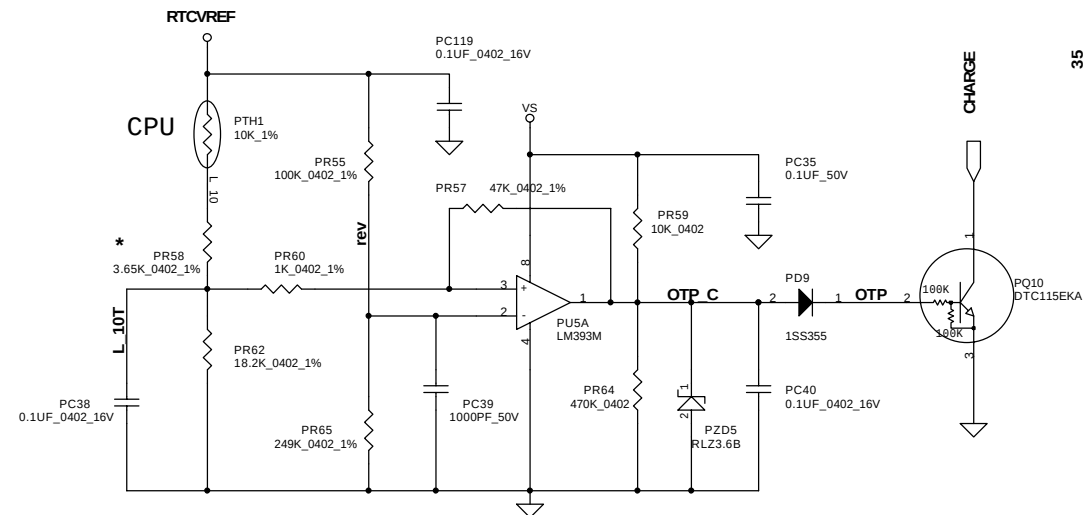
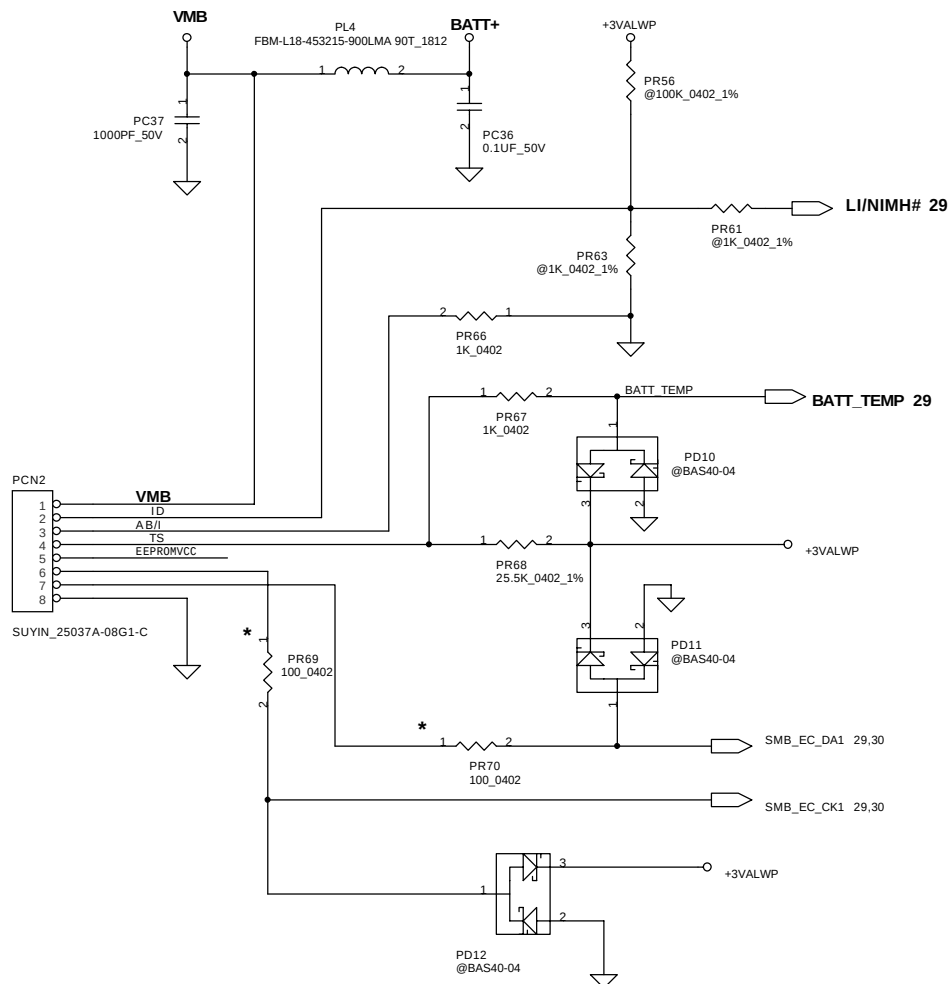
SCHEMATIC, M/B LA-1341

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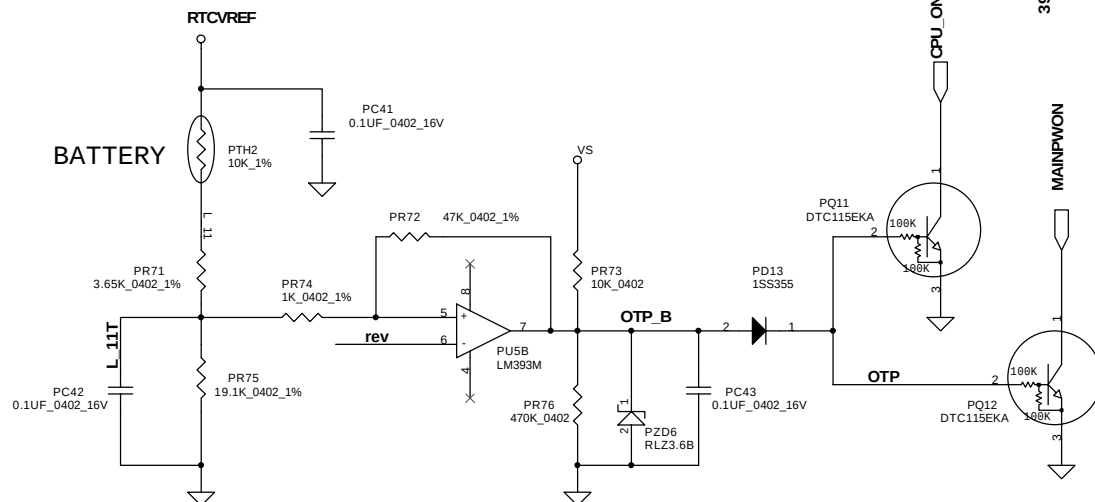
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PH1 under CPU botten side :
 CPU thermal protection at 90(91)+-3 degree C
 Recovery at 50(51)+-3 degree C



PH2 near main Battery CONN :
 BAT. thermal protection at 85(86)+-3 degree C
 Recovery at 50(51) degree C



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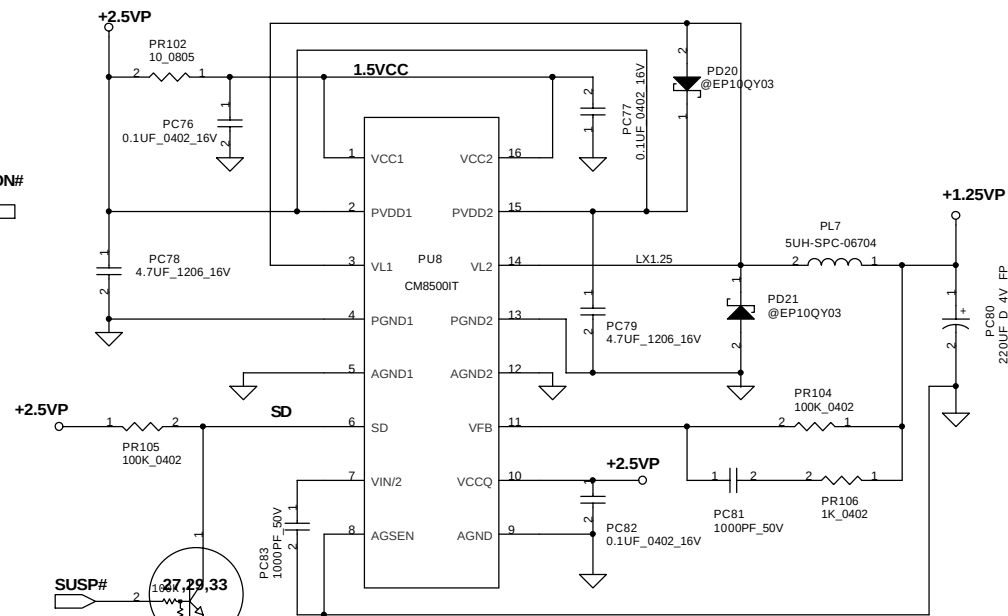
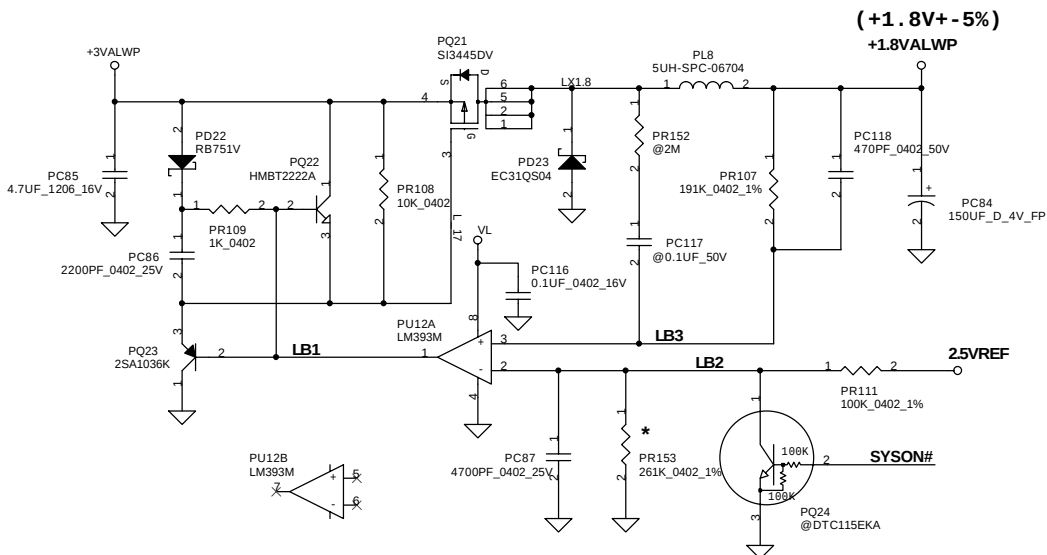
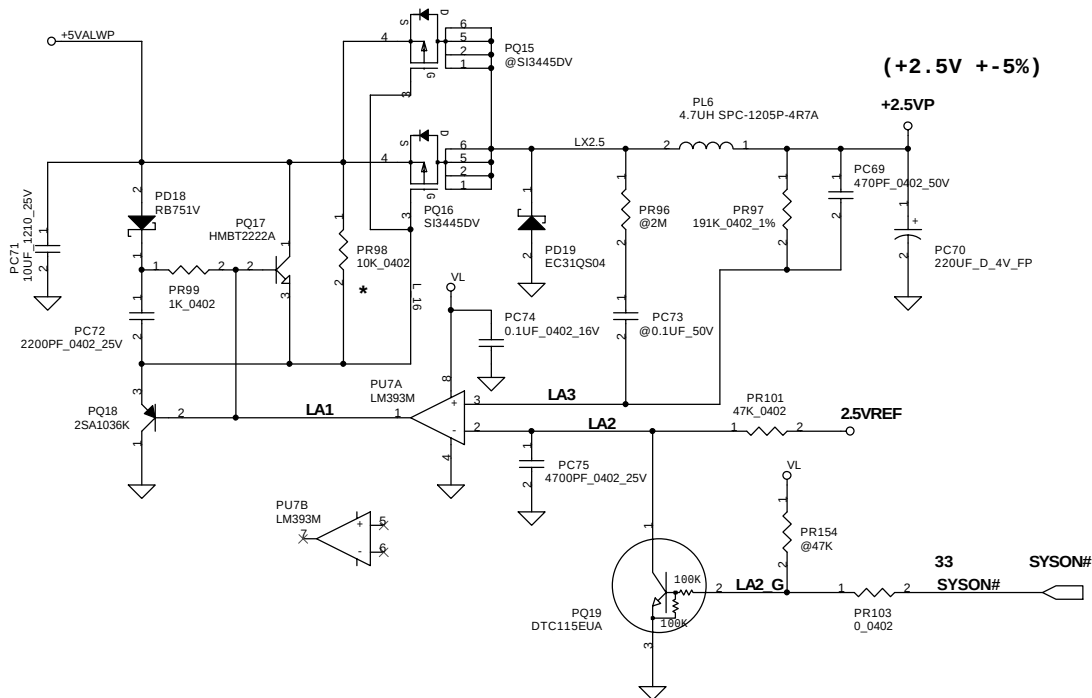
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+3.3V Ipeak = 6.66A ~ 10A

+5V Ipeak = 6.66A ~ 10A

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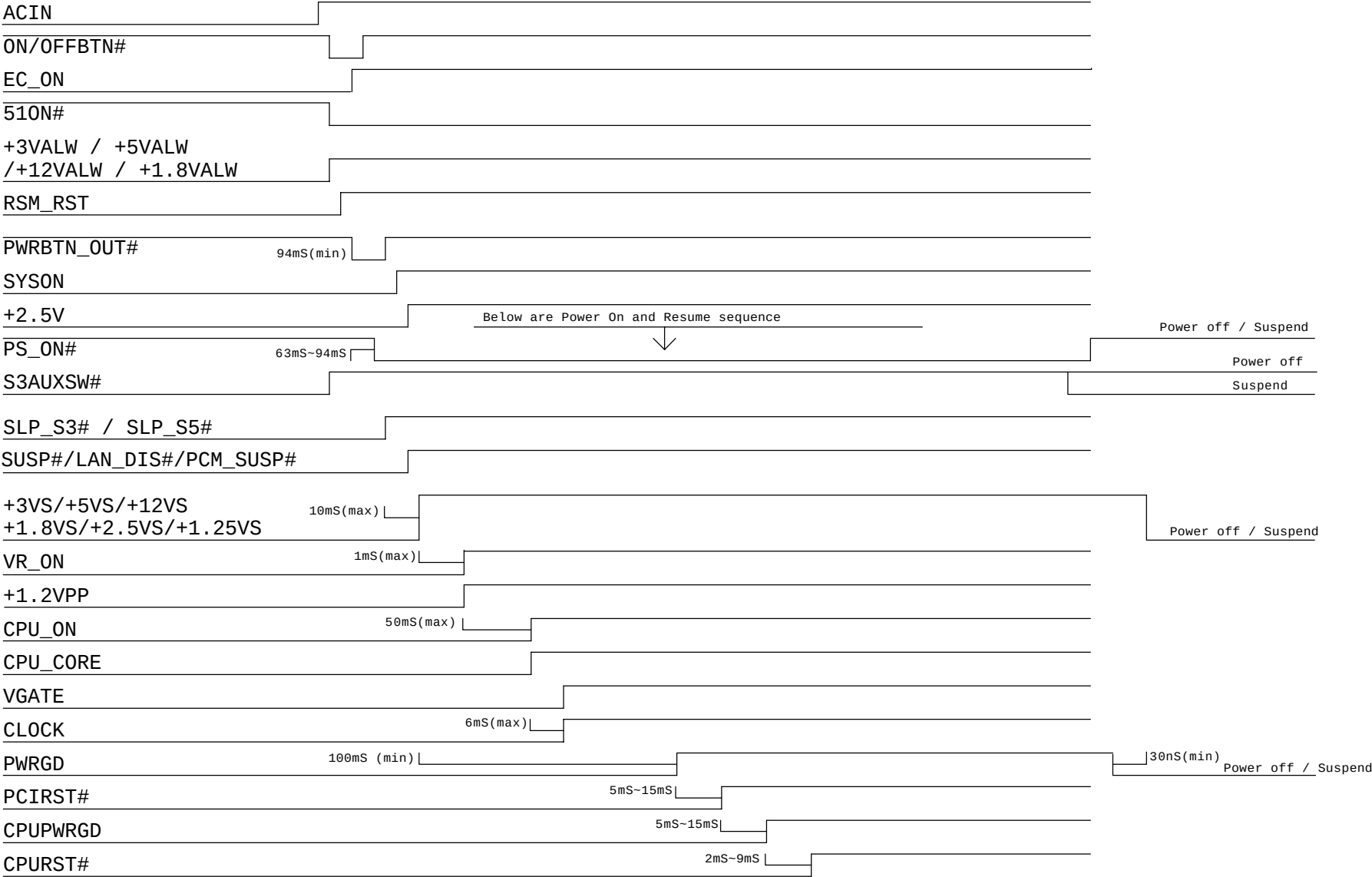
Layout : "Compensation network close to FB pin"

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Power ON sequence

Power Off sequence

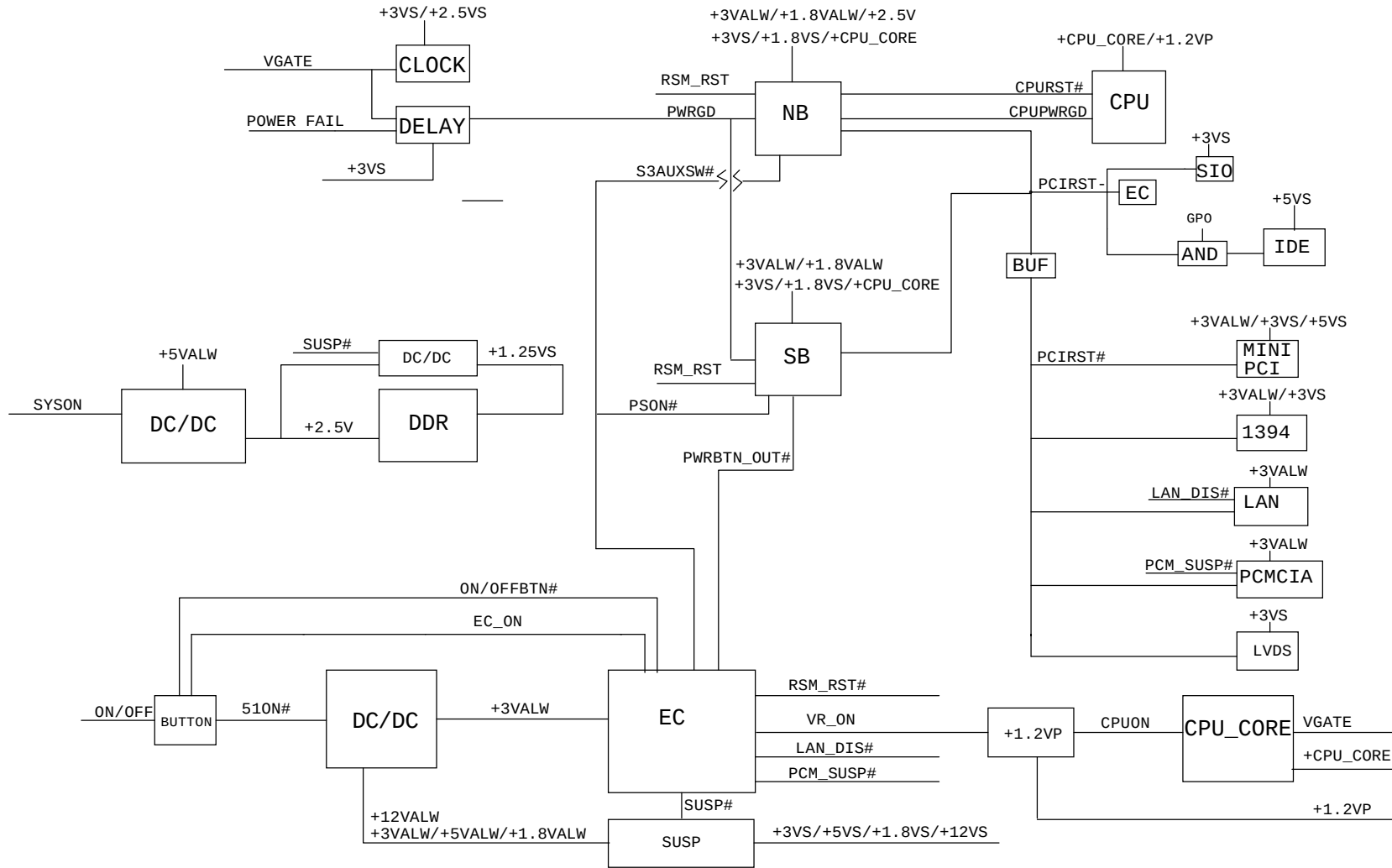


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ACY25 RESET MAP DIAGRAM



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Power PIR						
Item	Fixed Issue	Reason for change	Page	Modify item	MB_Ver.	Phase
1	NO	Improve CPU_CORE voltage regulator.	39		0.3	C test
2						
3						
4						
5						
6						
7						
8						
9						
10						
11						
12						
13						
14						
15						
16						
17						
18						

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		5		4		3	2		1																		
UNIT		IDSEL	IRQ	GNT	REQ		THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.																				
1394	U16	AD16	PIRQC#	GNT#0	REQ#0																						
MINIPCI	JP16	AD31	PIRQB#	GNT#1	REQ#1																						
LAN	U13	AD17	PIRQB#	GNT#3	REQ#3																						
CARDBUS	U40	AD20	PIRQA/B#	GNT#2	REQ#2																						
SMBUS from EC																											
EC_SMD2,EC_SMC2 : U2 (MAX1617) AD : 1001 110																											
SMB_EC_CK1,SMB_EC_DA1: U31(EEPROM) , PCN2 (BATT CONN)																											
SMBUS from SIS961																											
SMBCLK , SMBDAT: JP5 , JP4 , CLK GEN (U6) , CLK BUF(U7).																											
ACY25 A-TEST Modify																											
Change to 310LV : Add R64,R73,R76. Del R77,R78,Q49,Q50,R539																											
Add DPSLP# : Add Q49 ,R551																											
DPRSLPVR : Change to pull Low =Del R536 Add R284 ; Add U41 ,R554.																											
AUXOK : Del CB112																											
IDERST# : Add D56 , R548																											
LAN : VDD2.5 add L27,C294,C297,C299																											
1394 : +3VA change to +3VS power , R351,R352,R353,R3354 change to 54.9_1% , R355 change to 5.1K_1% , C337 change to .33UF , C338 change to 270PF																											
KB : Add R552 to disconnect ON/OFF																											
PSON# : Connect to SLP_S5# directly. Del U33																											
S3AUXSW# : Connect to SLP_S3# directly																											
PWRGD : Add R550 transfer 5V to 3V																											
+2.5V : Q39 using SI2306 to transfer +2.5VS																											
H_DPSLP# : Add Q49 , R551 to connect with PM_STPCPU#																											
		5		4		3	2		1																		
							<table><tr><td colspan="3">Compal Electronics, Inc.</td></tr><tr><td colspan="3">Title</td></tr><tr><td colspan="3">SCHEMATIC, M/B LA-1341</td></tr><tr><td>Size</td><td>Document Number</td><td>Rev</td></tr><tr><td>B</td><td>401210</td><td>2A</td></tr><tr><td>Date:</td><td>星期五, 十一月 15, 2002</td><td>Sheet 44 of 46</td></tr></table>			Compal Electronics, Inc.			Title			SCHEMATIC, M/B LA-1341			Size	Document Number	Rev	B	401210	2A	Date:	星期五, 十一月 15, 2002	Sheet 44 of 46
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CDLED# : Add R553 to disconnect with JP10

USER_BTN3# : Add R552 to disconnect with JP18,R555 pull high to +3VALW

DDR termination : RP32~RP57 , R494~R508 chanage to 33 ohm

PWRGD : Del C42 , C256

SUSP# : R487 change to 75 ohm

FOR C TEST

LED1 : Q48 PIN3 CHANGE TO +3VALW

FWDSCLK0 : Add pull high resistor R556

PWRGD : Add pass through resistor R557

CRTVDD : R170,R171 change from 2.2K to 1K

GPI019,20 : Add pull high resistor R566,R567 , del R269

EC_ON : Del Q47. R492 change to 0 ohm

EN_FAN1 : Add U42 , C483 , C484 , R561 ,R562

CLKFS2 :Del R161 , Add R565

AUXOK : Del D18 , R254

For MP

+5VFAN1 : Add D58 for reserve

S1_RST# ,S2_OE# , S2_RST# , S2_OE# :Add R568,R569,R570,R571 for reserve

LAN_DIS# : Del R333 , add D57 for reserve

MINI PCI : Change to REQ#4 & GNT#4

For MP 5/3

R248 change to 47K , R251 change to 15K

For MP 6/6

R278 change from 0 to 47 ohm

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