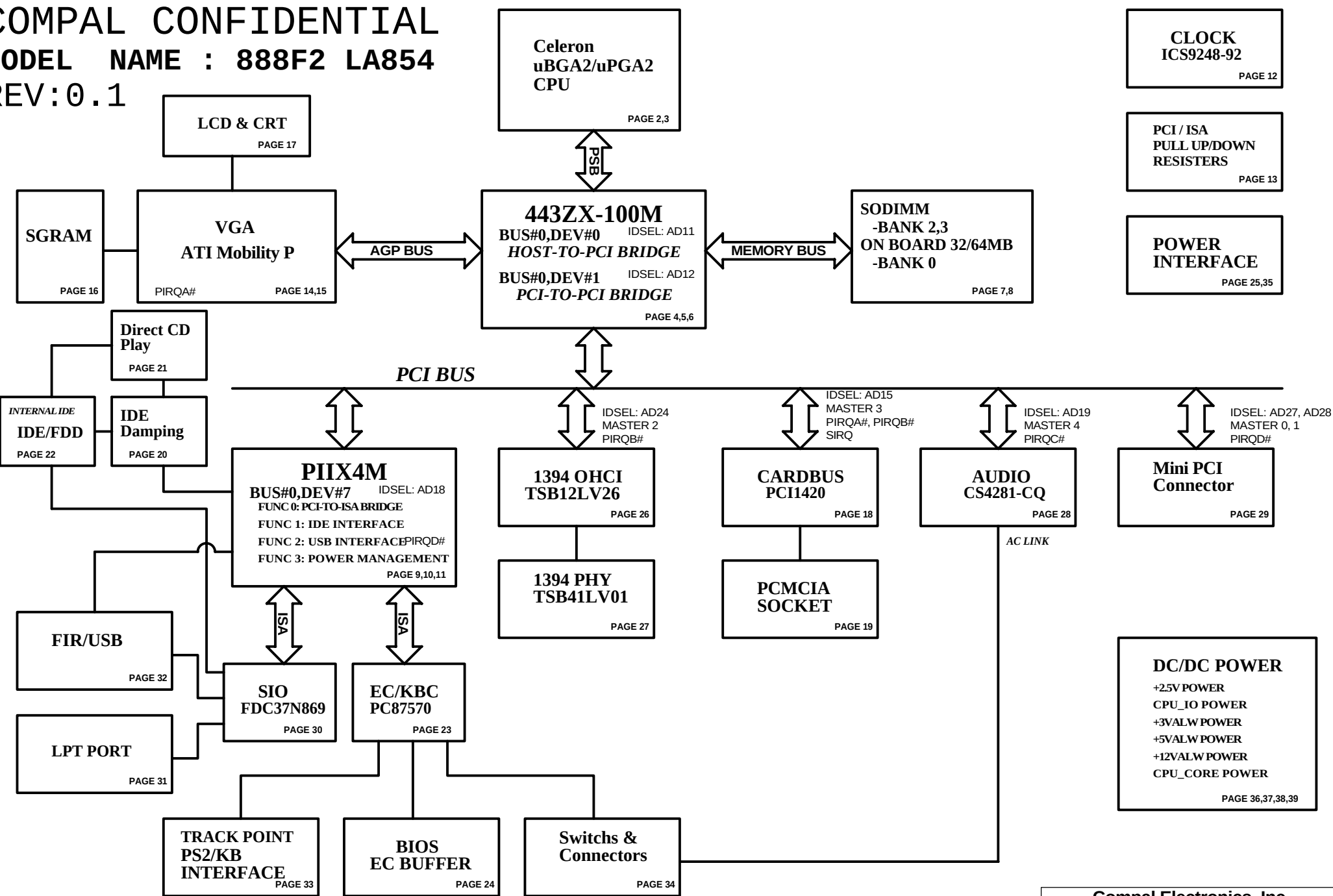
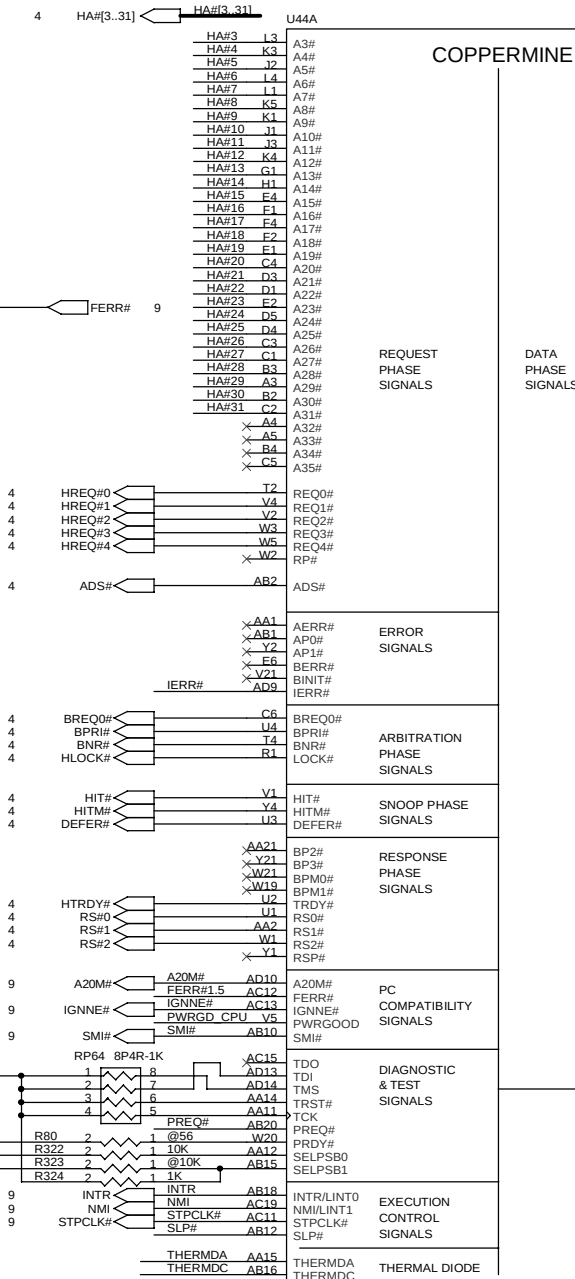
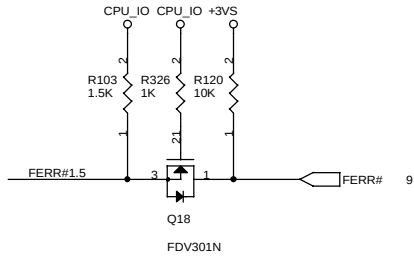


COMPAL CONFIDENTIAL
MODEL NAME : 888F2 LA854
REV:0.1

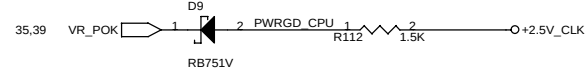
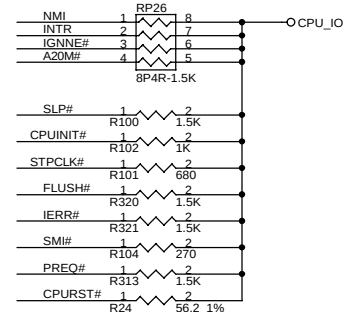
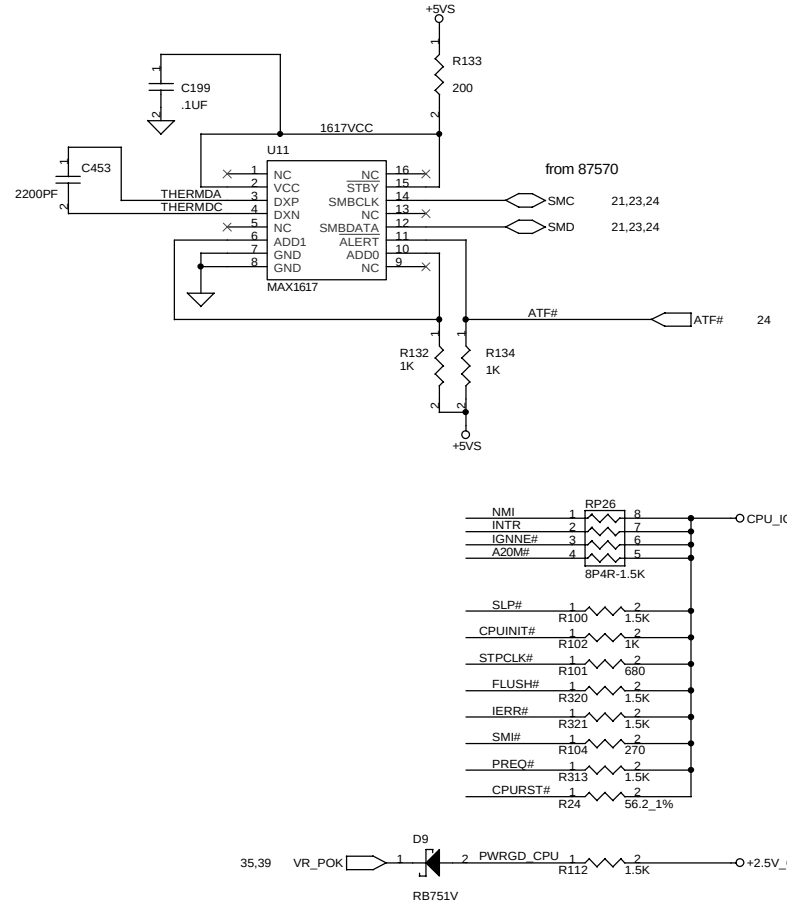
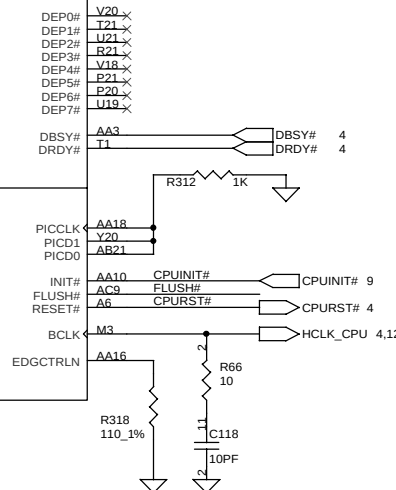


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SELPSB[1:0]	STSEM BUS FREQUENCY
00	66MHZ
01	100MHZ
10	RESERVED
11	133MHZ



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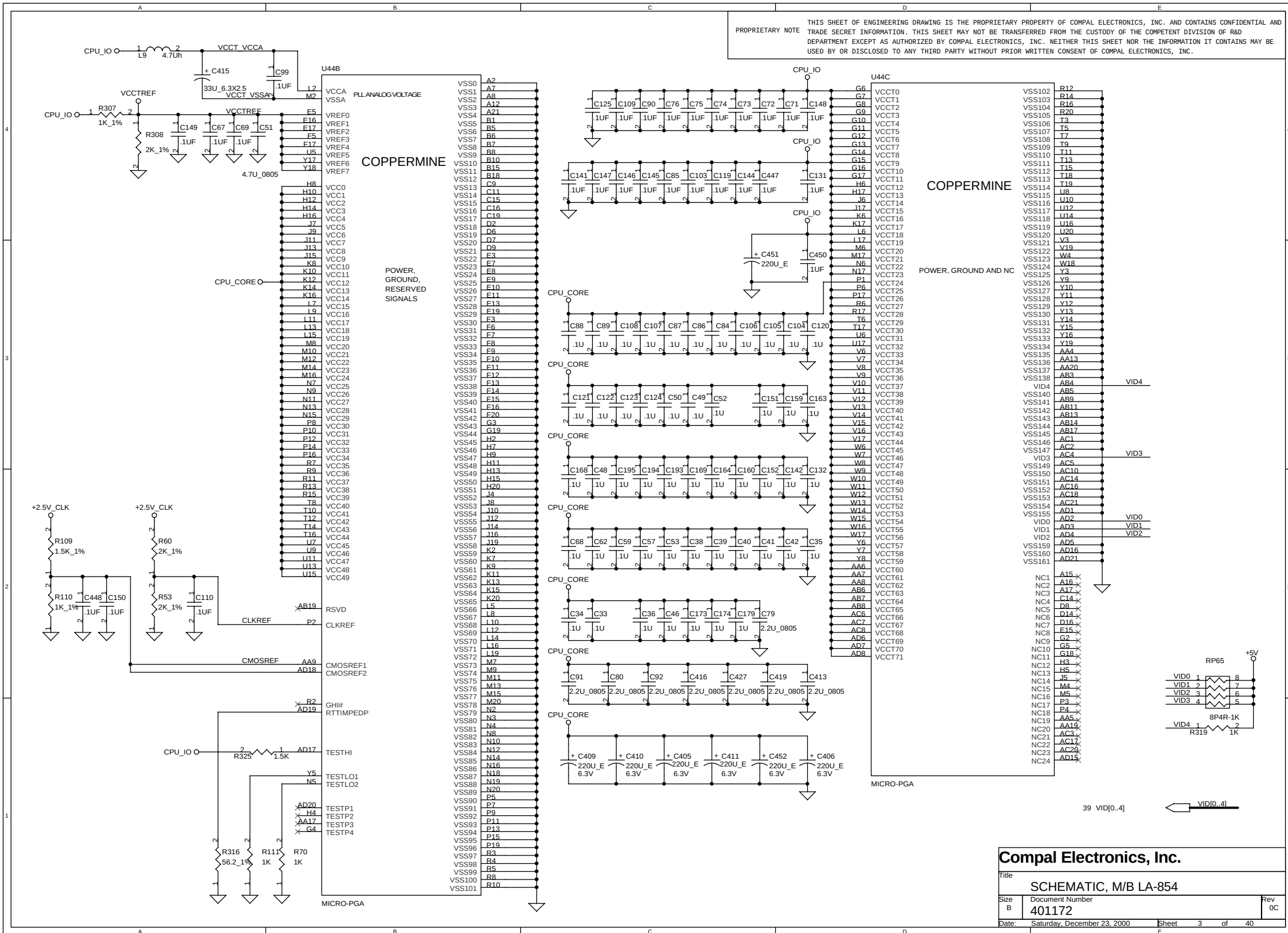
Compal Electronics, Inc.

SCHEMATIC, M/B LA-854

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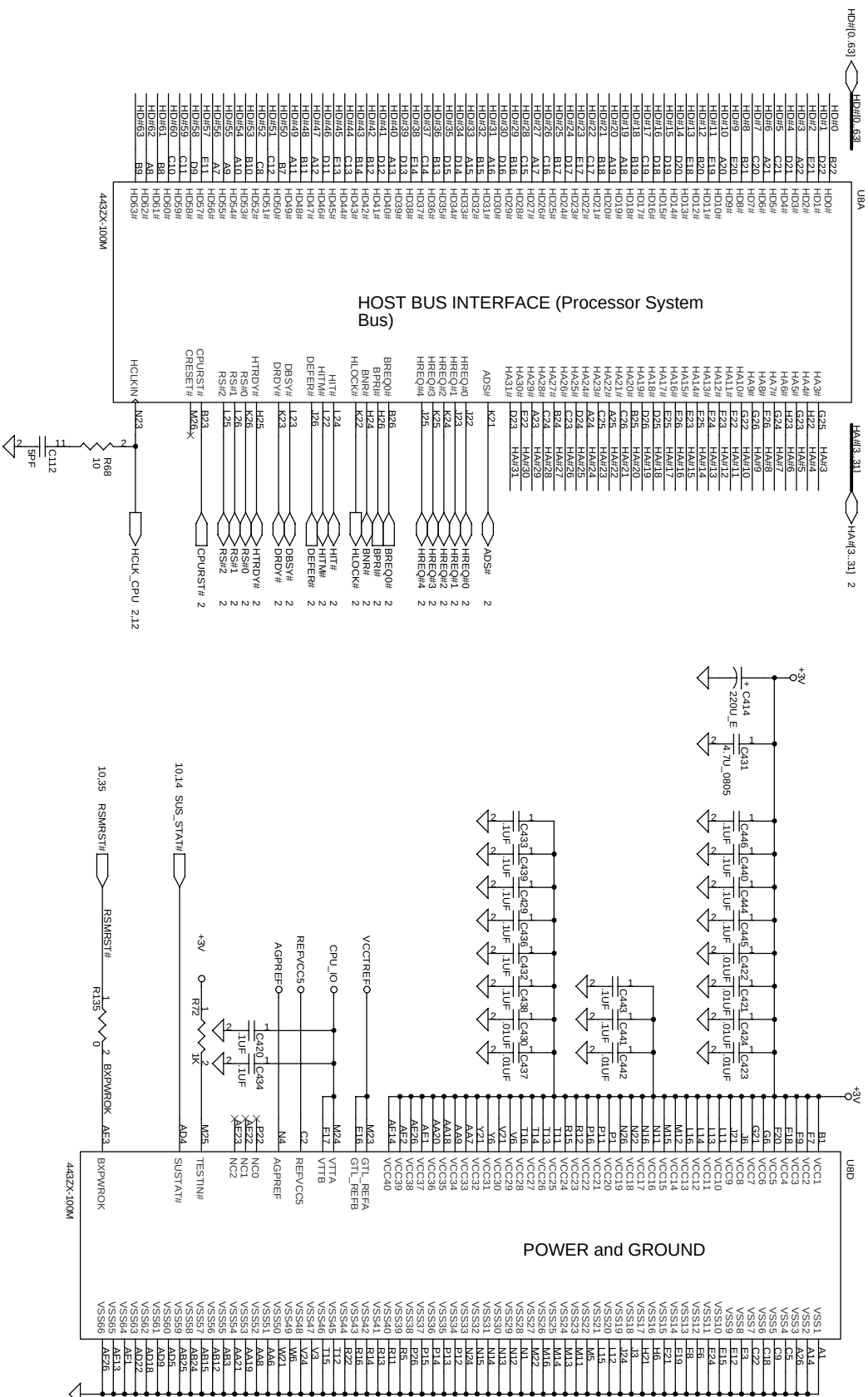
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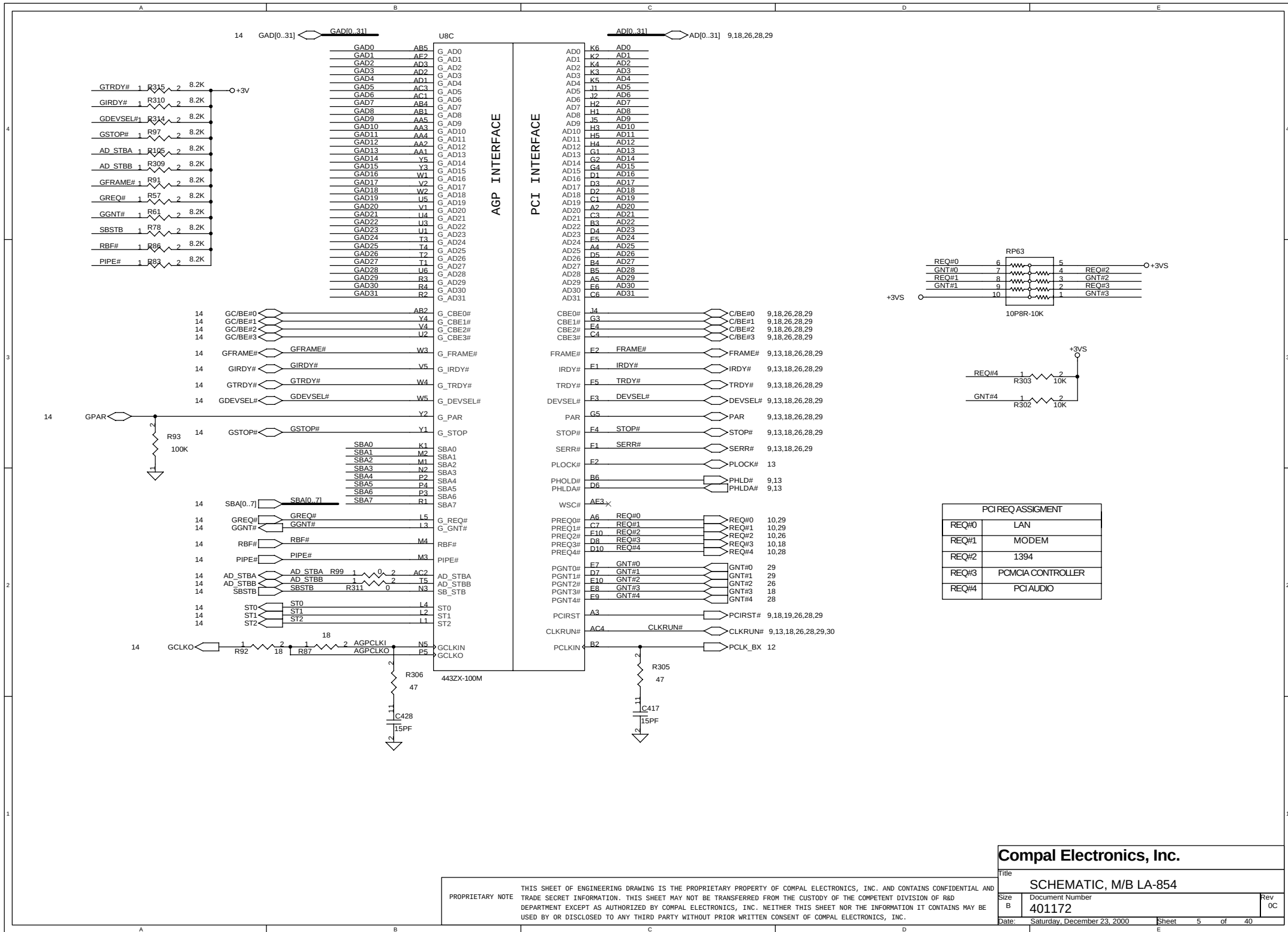
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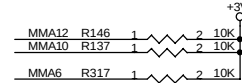
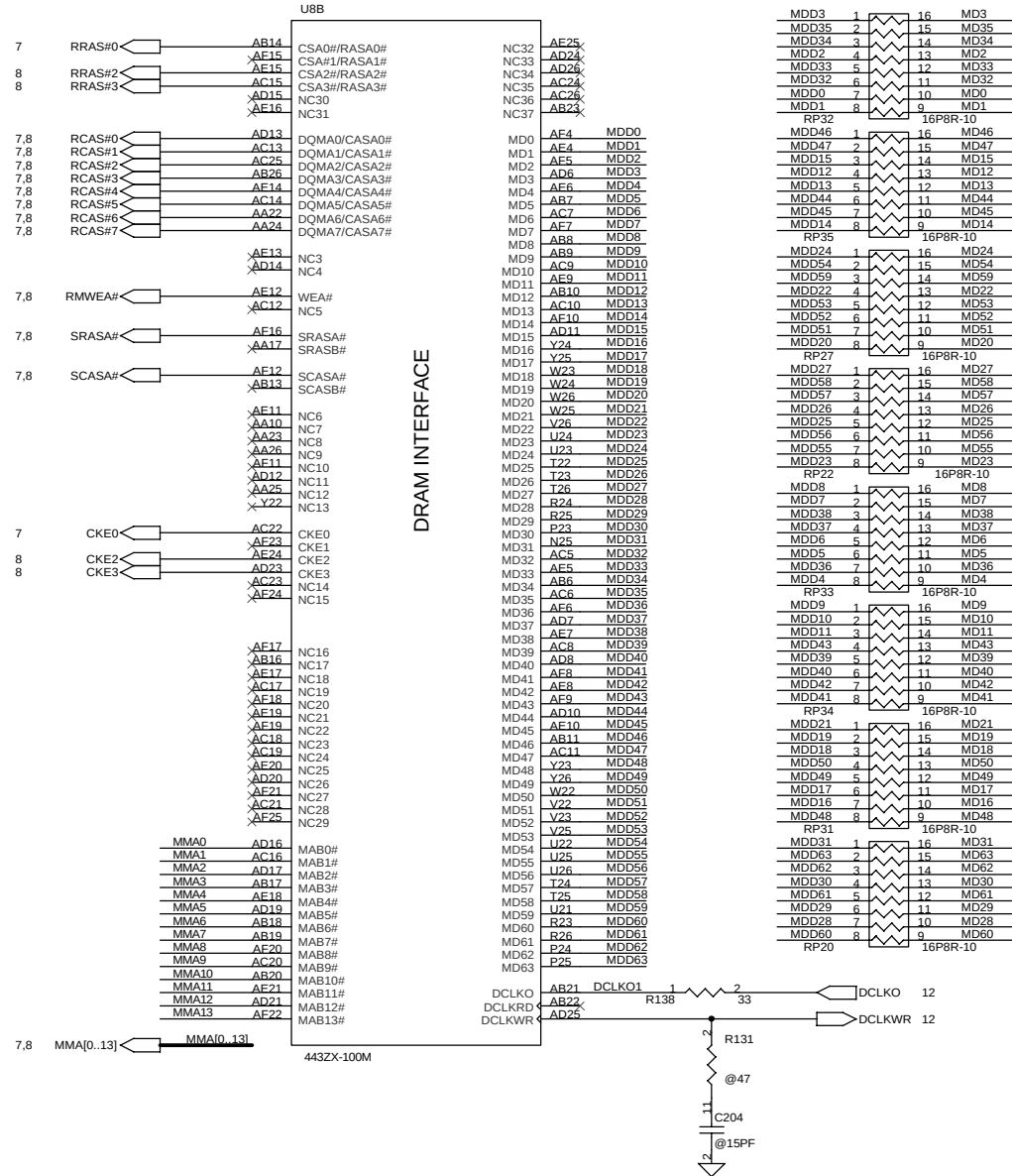


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Pin Name	Function	Low	High	Internal Resistor	Status Register
MAB12#	Host Frequency Select	66MHz	100MHz	Pull-down	NBXCFC[13]
MAB10#	Quick Start Select	Stop Clock Mode	Quick Start Mode	Pull-down	PMCR[3]
MAB6#	Host Bus Buffer Mode Select	Desktop GTL+	Mobile Low Power GTL+	Pull-down	none

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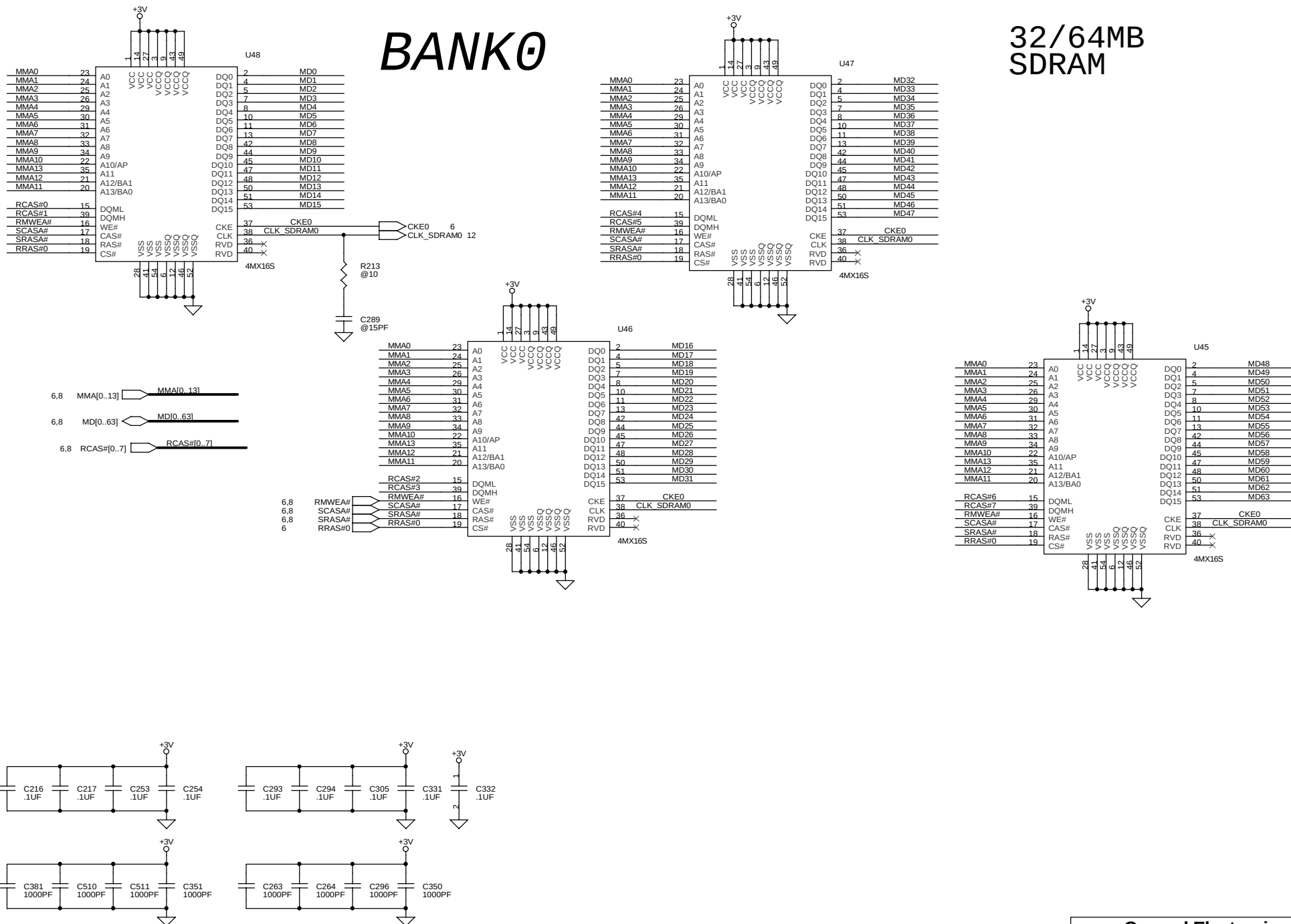
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BANK0

32/64MB
SDRAM



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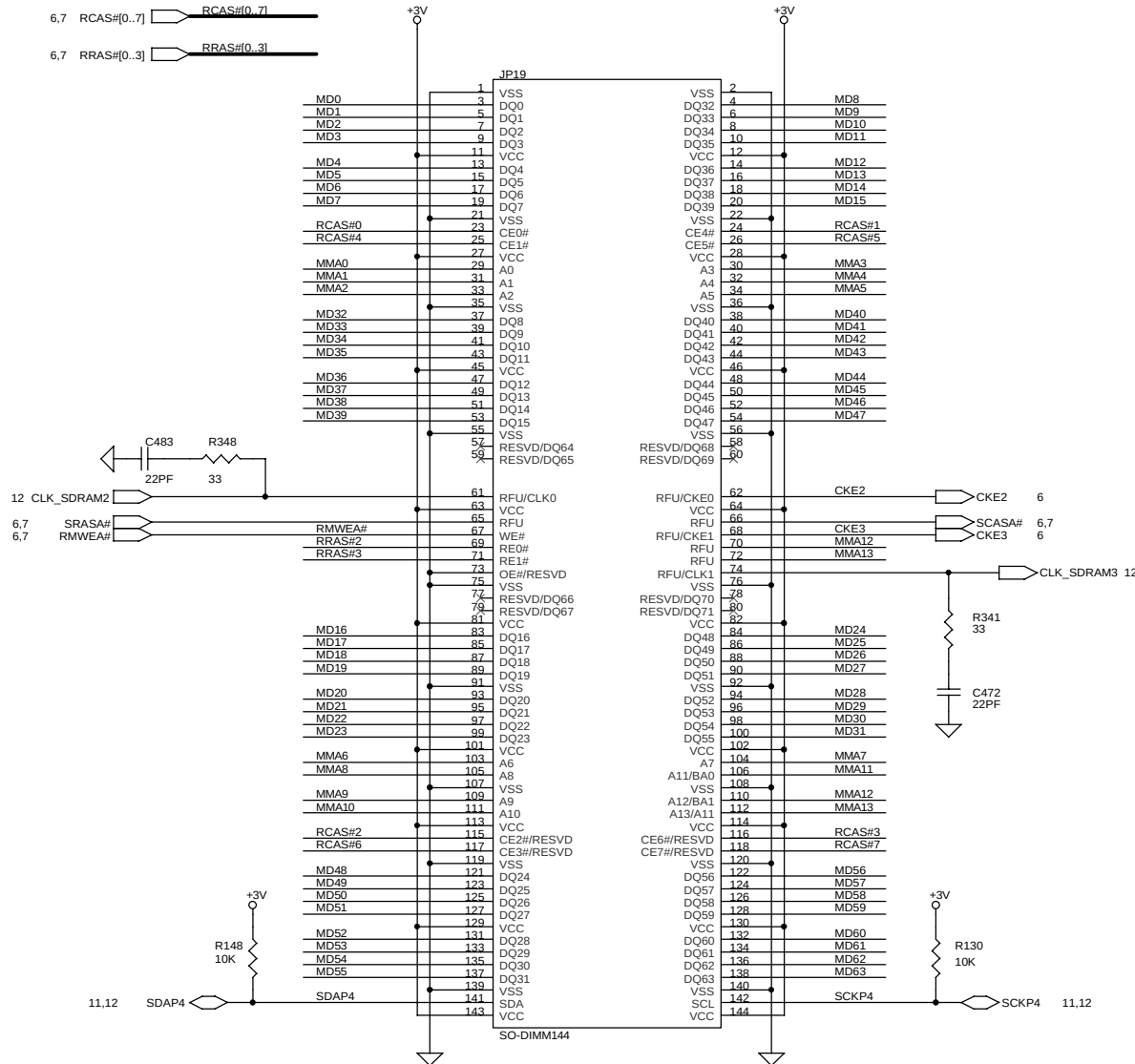
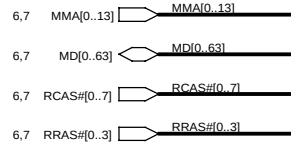
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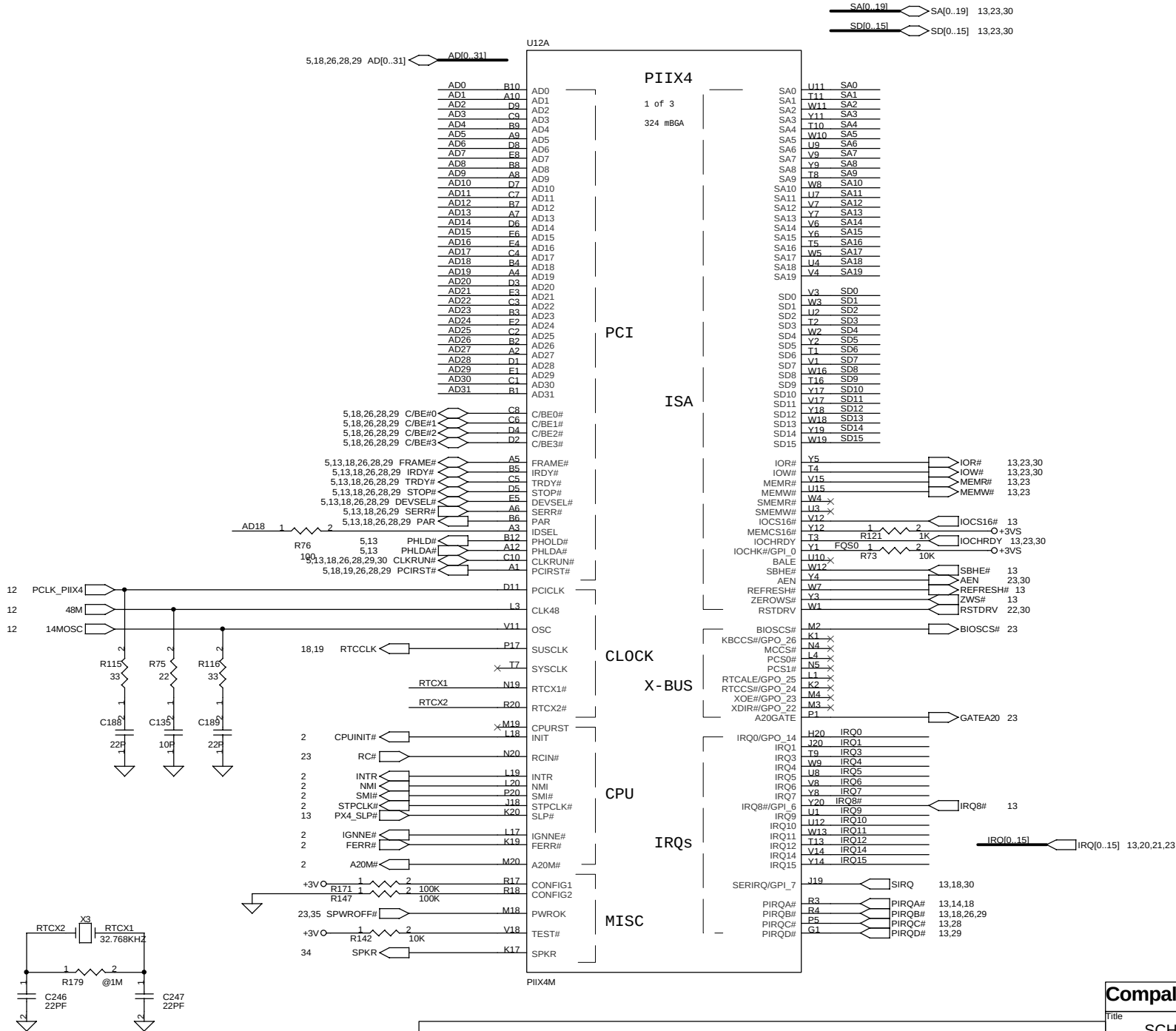
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SO-DIM 144 PINS
RAM MODULE CONN.

BANK2/3



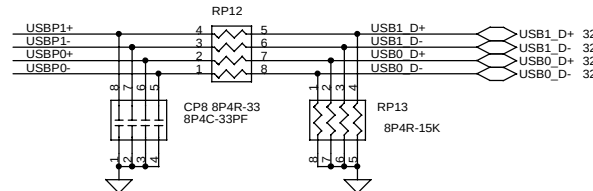


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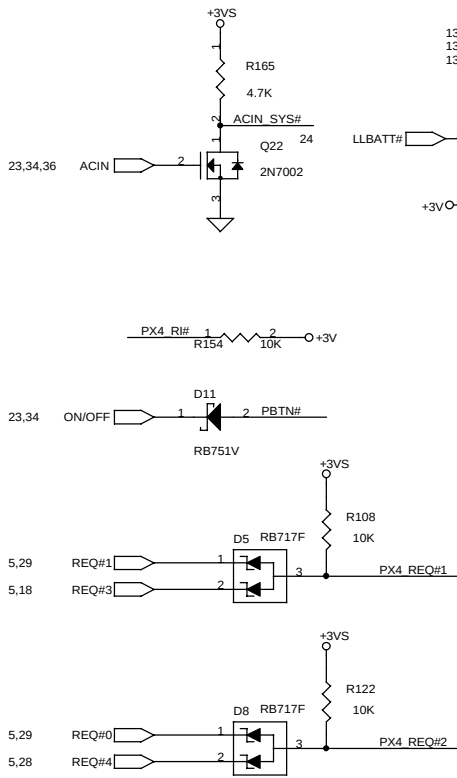
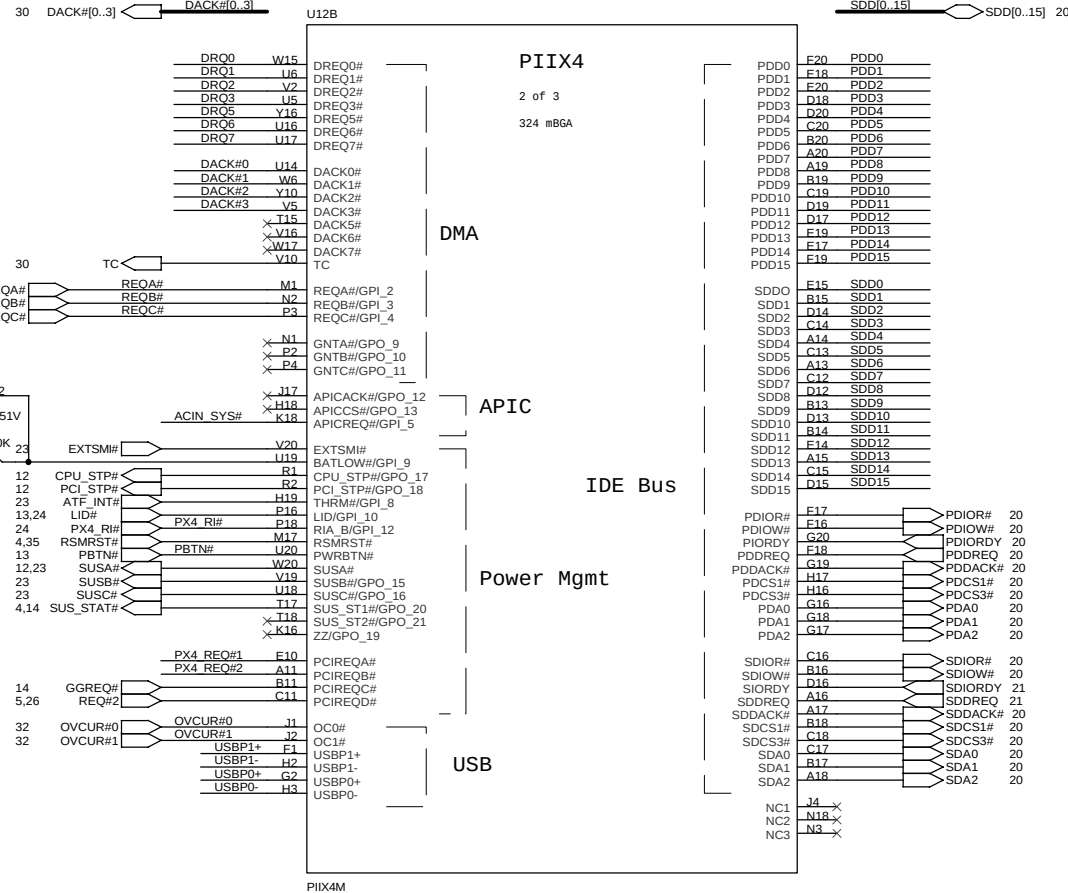


USB Host Termination should be close to PIIX4

USB TRACE ROUTING 6 MIL

13,30 DRQ[0..7] DRO[0..7]
30 DACK#[0..3] DACK#[0..3]

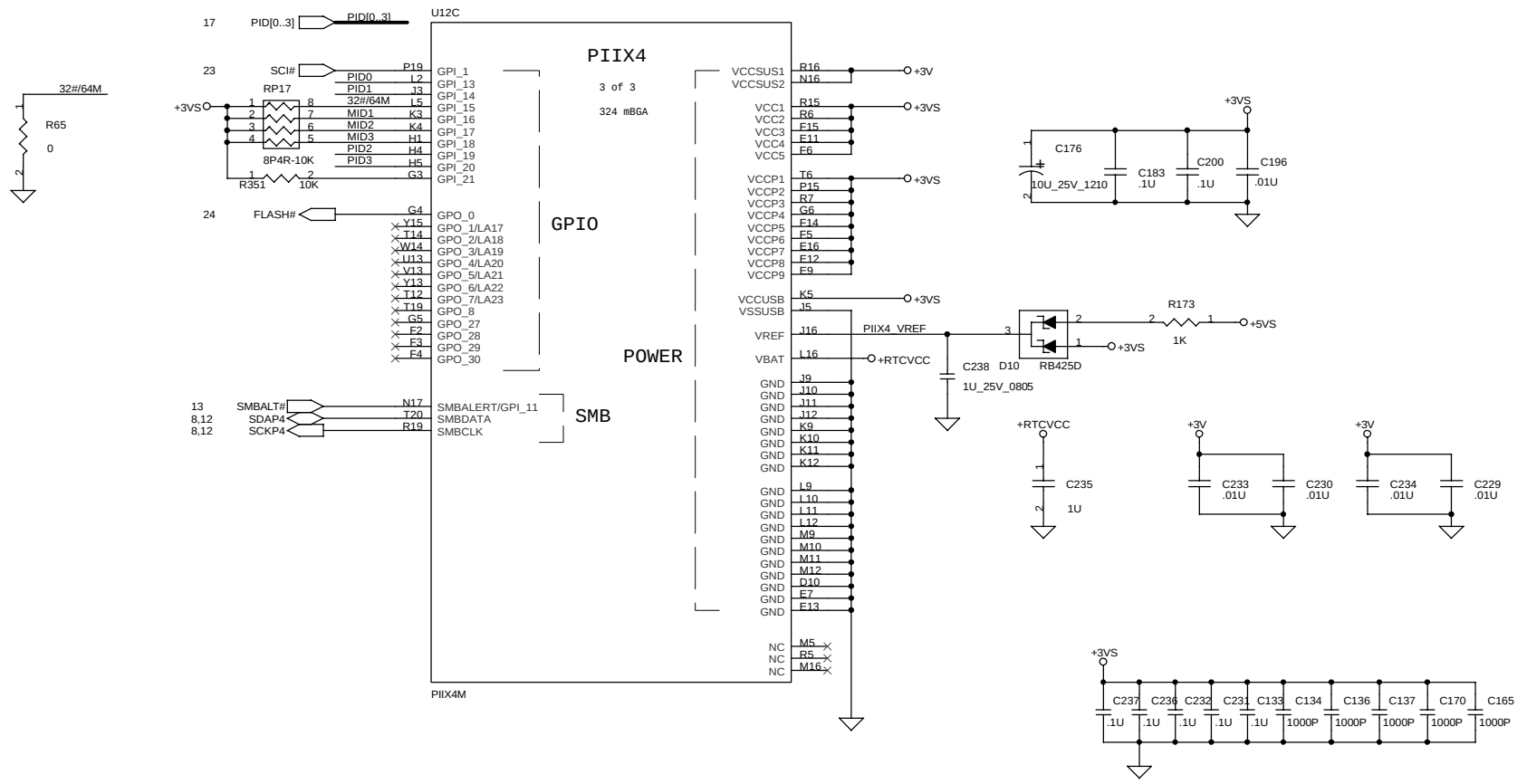
PDD[0..15] PDD[0..15] 20
SDD[0..15] SDD[0..15] 20



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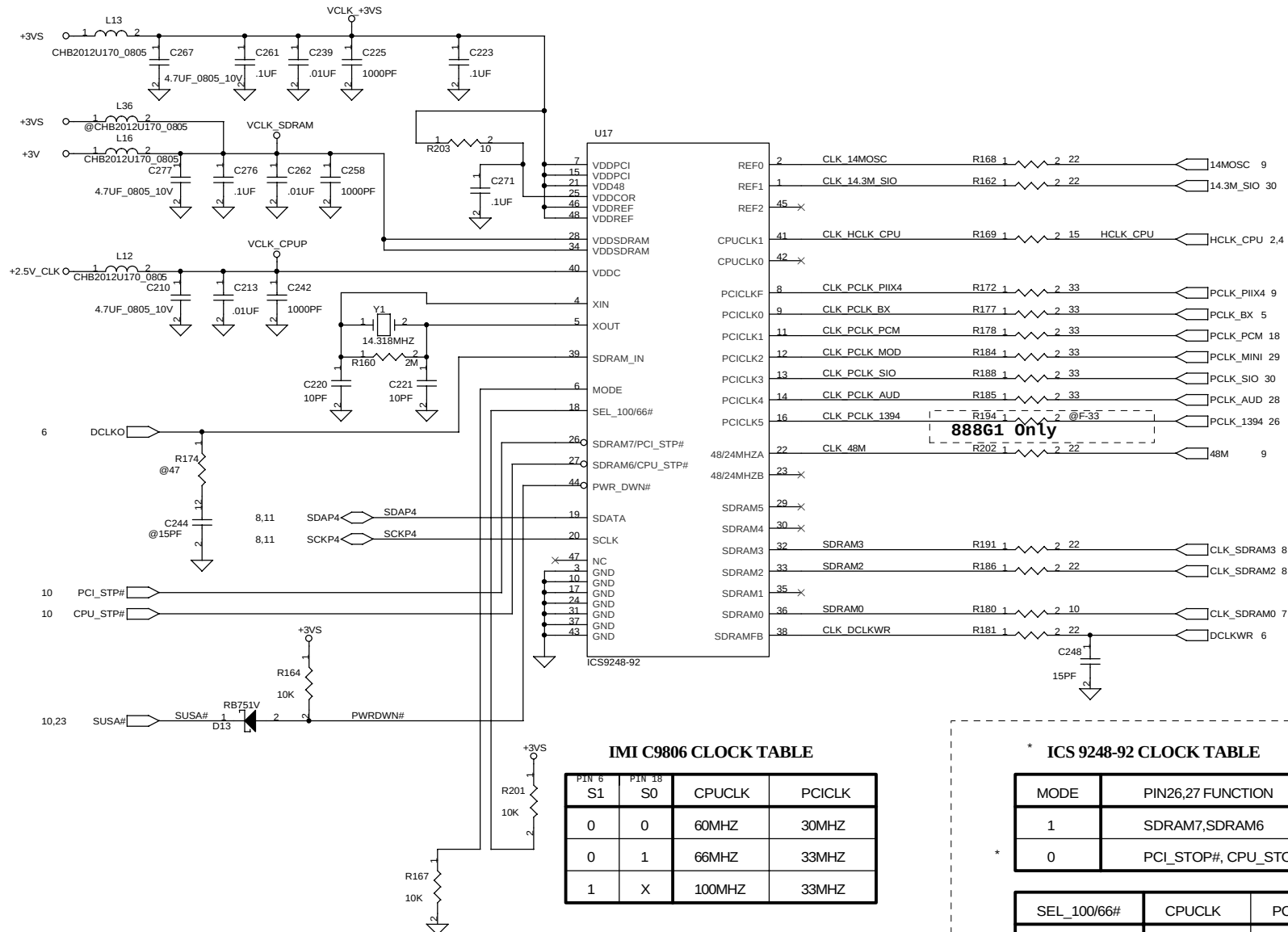
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CLOCK GENERATOR & BUFFER



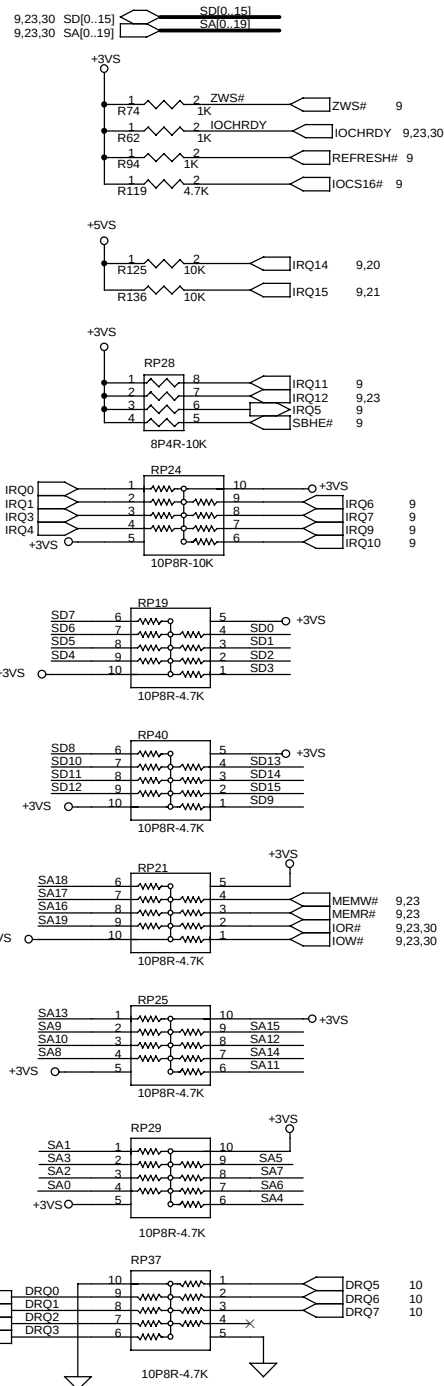
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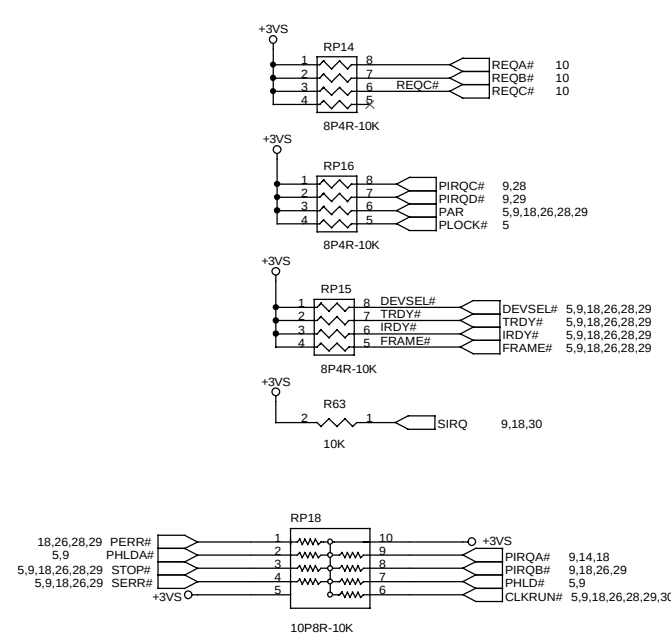
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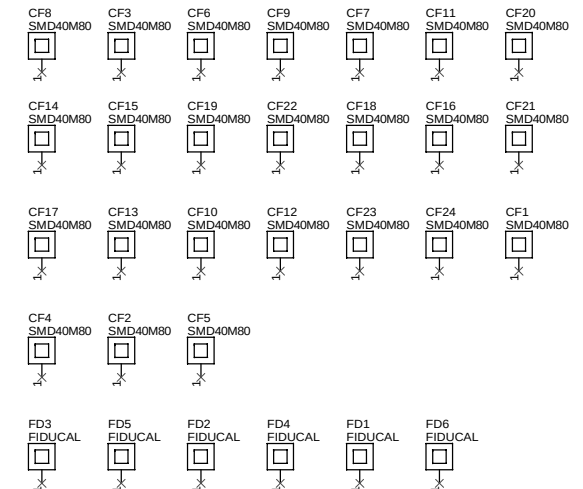
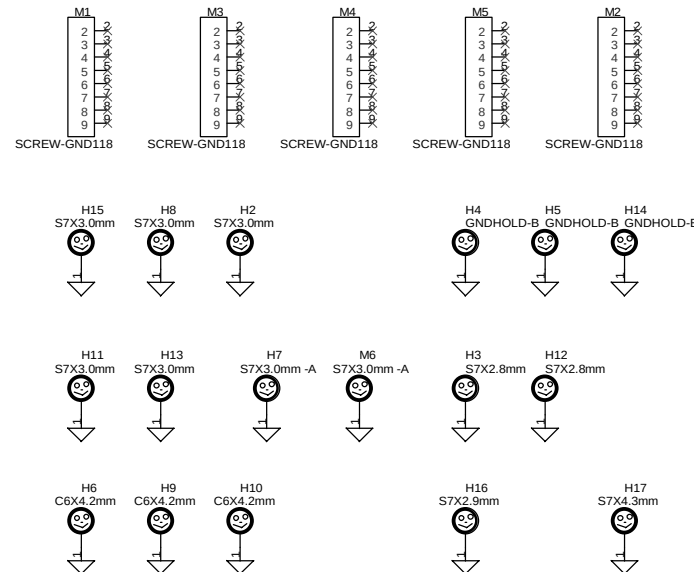
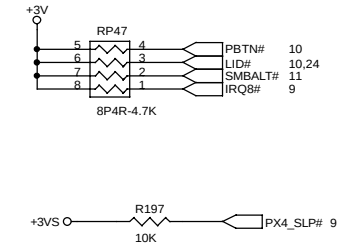
ISA BUS Pullup



PCI BUS Pullup



Other PIIX4E Signals Pullup



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5 GAD[0..31] GAD[0..31]
5 ST[0..2] ST[0..2]
5 SBA[0..7] SBA[0..7]
5 GC/BE#[0..3] GC/BE#[0..3]

U10A

GAD0 B11
GAD1 A11
GAD2 C11
GAD3 D12
GAD4 C12
GAD5 A12
GAD6 B12
GAD7 D13
GAD8 A13
GAD9 E14
GAD10 C14
GAD11 D14
GAD12 A14
GAD13 A18
GAD14 D15
GAD15 B14
GAD16 D18
GAD17 C19
GAD18 E17
GAD19 E18
GAD20 D20
GAD21 B20
GAD22 F18
GAD23 F19
GAD24 G18
GAD25 G19
GAD26 F20
GAD27 D17
GAD28 J20
GAD29 G16
GAD30 G20
GAD31 F16

CPUCLK

RESET#

5 GIRDY#
5 GFRAME#
5 PIPE#
5 GTRDY#
5 GDEVSEL#
5 GSTOP#
5 GPAR

GC/BE#0 B13
GC/BE#1 E15
GC/BE#2 E20
GC/BE#3 C18

9,13,18 PIRQA#
5 GREQ#
5 GGNT#
5 SBSTB

ST0 H16
ST1 H17
ST2 H20
SBA0 A19
SBA1 B18
SBA2 A17
SBA3 C17
SBA4 E16
SBA5 B16
SBA6 C16
SBA7 A15

H18 RBF#
C13 AD_STBA
F17 AD_STB1

H19 CLKRUN#
B15 AGP_BUSY#
C15 STP_AGPF#
V4 PCI33EN

F13 VPP
F14 VPP
G15 VPP
J16 VPP

E11 AGPCLAMP
R2 GIOCLAMP
W2 TESTEN

ATI MOBILITY

ZV PORT

PCI/AGP BUS INTERFACE

CORE I/O MEMORY POWER

D11 ZVPORT0
D10 ZVPORT1
C10 ZVPORT2
B10 ZVPORT3
A10 ZVPORT4
D9 ZVPORT5
C9 ZVPORT6
B9 ZVPORT7
A9 ZVPORT8
D8 ZVPORT9
C8 ZVPORT10
B8 ZVPORT11
A8 ZVPORT12
E7 ZVPORT13
D7 ZVPORT14
C7 ZVPORT15

R1 SSIN
T1 SSOUT
T2 X

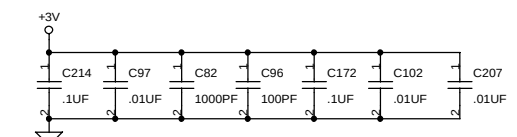
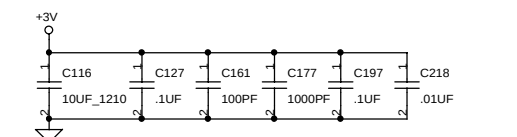
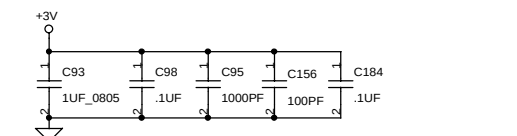
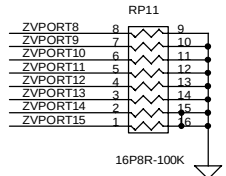
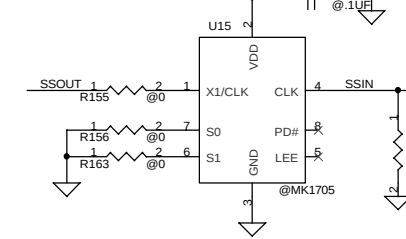
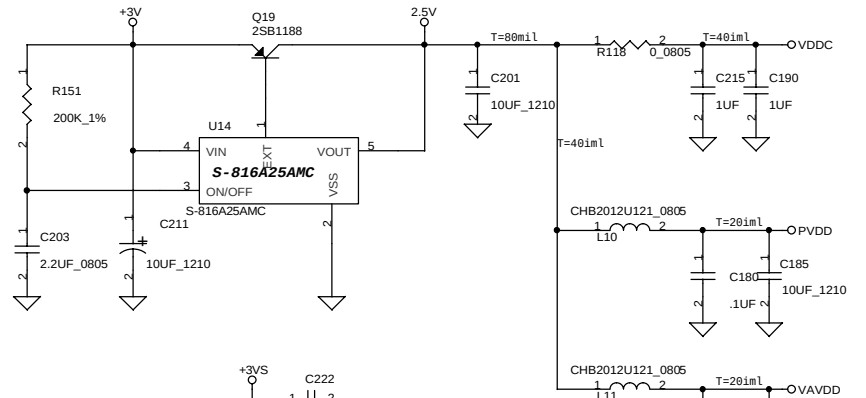
T10 VDDC
H6 VDDC
E10 VDDC
K5 VDDC
L16 VDDC
U11 VDDC

T12 VDDR
T14 VDDR
P15 VDDR
E8 VDDR
F6 VDDR
H5 VDDR
N5 VDDR
P6 VDDR
T7 VDDR
T9 VDDR

VREF M16
R106 10K +3V

VDDQ T13
T16 VDDQ
T8 VDDQ
R5 VDDQ
P16 VDDM
P5 VDDM
R6 VDDM
R15 VDDM

VSS R4
J10 VSS
J11 VSS
K9 VSS
J12 VSS
K10 VSS
K11 VSS
K12 VSS
L9 VSS
L10 VSS
L11 VSS
L12 VSS
M9 VSS
M10 VSS
M11 VSS
M12 VSS
G6 VSS
E7 VSS
F15 VSS
R7 VSS
T15 VSS
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J9 VSS
P3 VSS
P3 VSS
T11 VSS

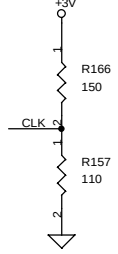
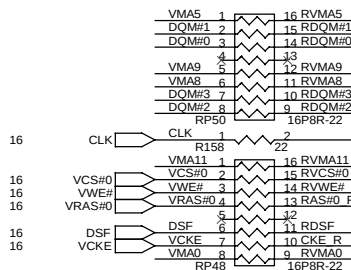
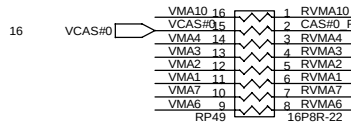
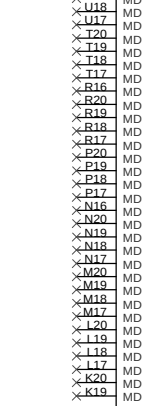
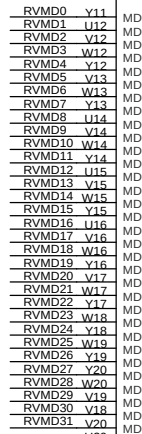
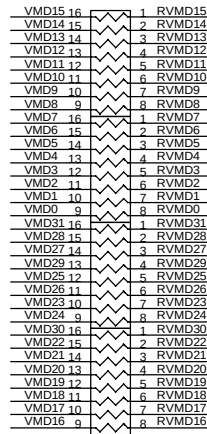


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U10B

FLAT PANEL INTERFACE

MEMORY INTERFACE

GPIO

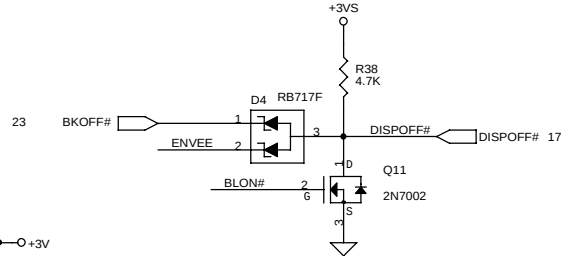
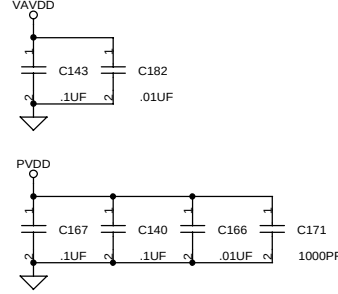
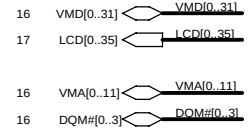
DAC1

DAC2

PLL

ATI MOBILITY

** Do not populate RP23, R114, R117, R128, R124, and C192 when use TFT panel.



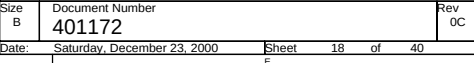
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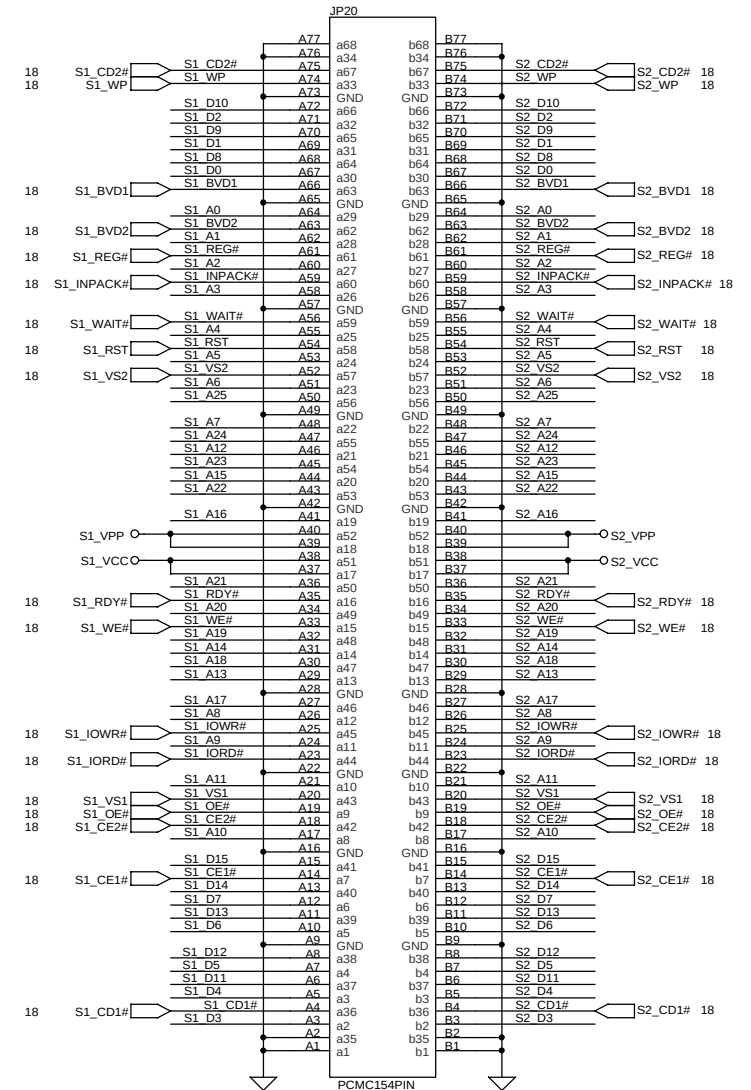
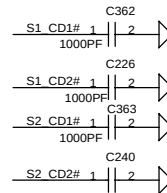
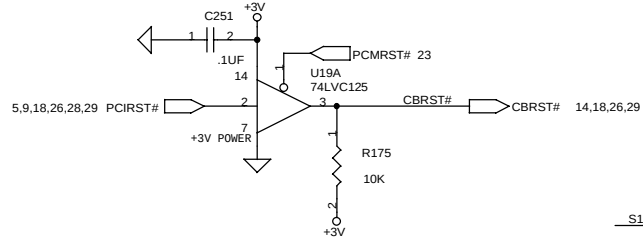
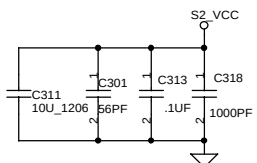
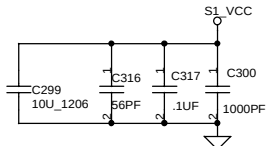
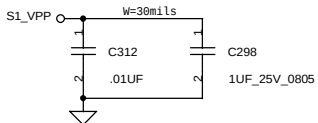
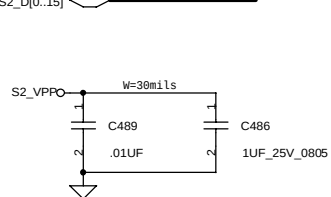
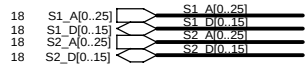
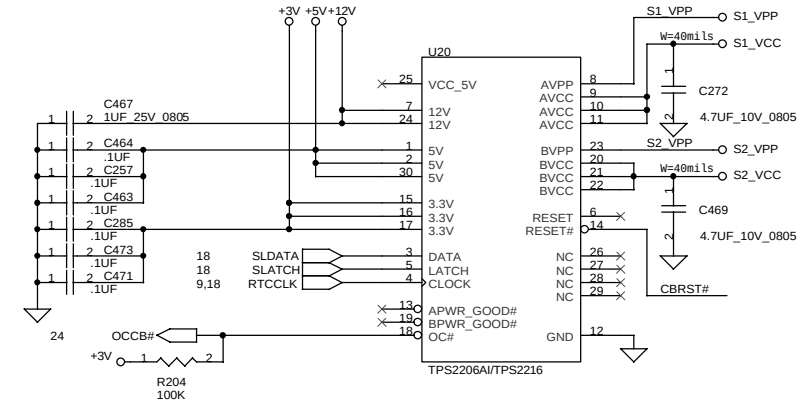
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PCMCIA POWER CTRL. Wire ZV PORT to Slot A

CARDBUS SOCKET

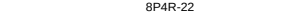
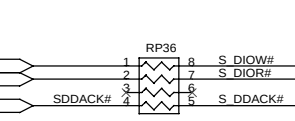
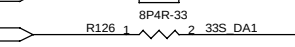
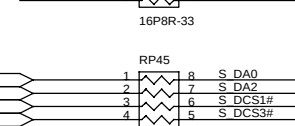
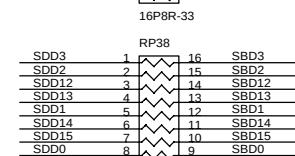
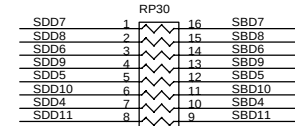
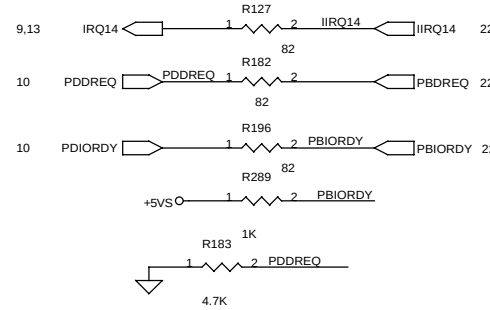
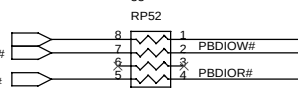
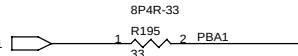
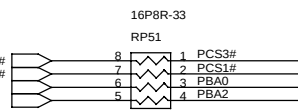
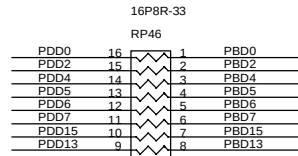
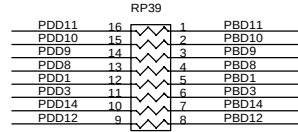
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IDE Series Resistor Place them close SB

10 SDD[0..15] SDD[0..15]
10 PDD[0..15] PDD[0..15]
21 SBD[0..15] SBD[0..15]
22 PBD[0..15] PBD[0..15]



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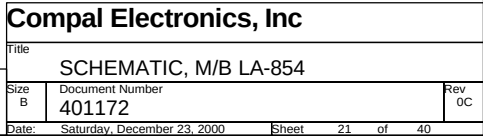
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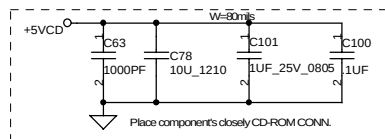
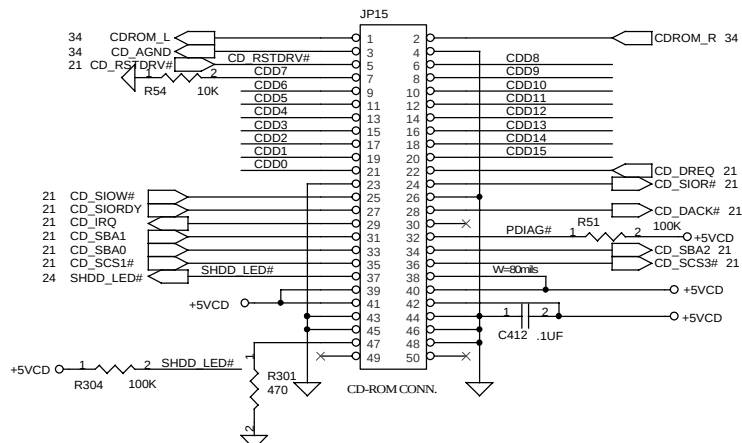
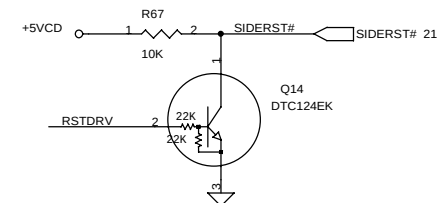
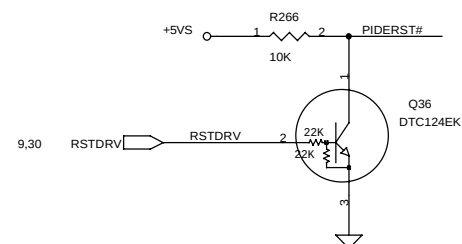
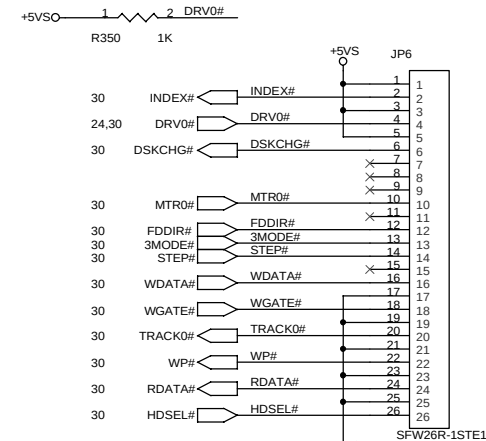
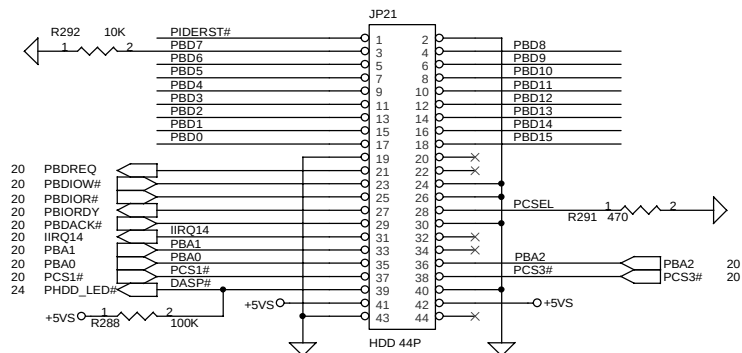
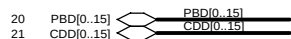
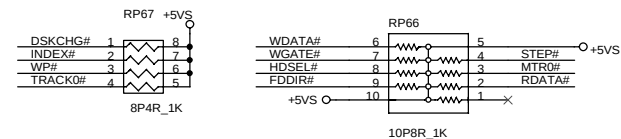
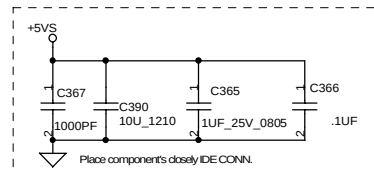
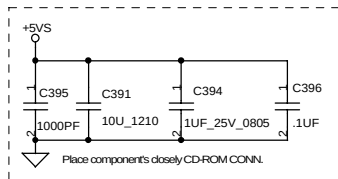
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IDE,CD-ROM & FDD Module CONN.



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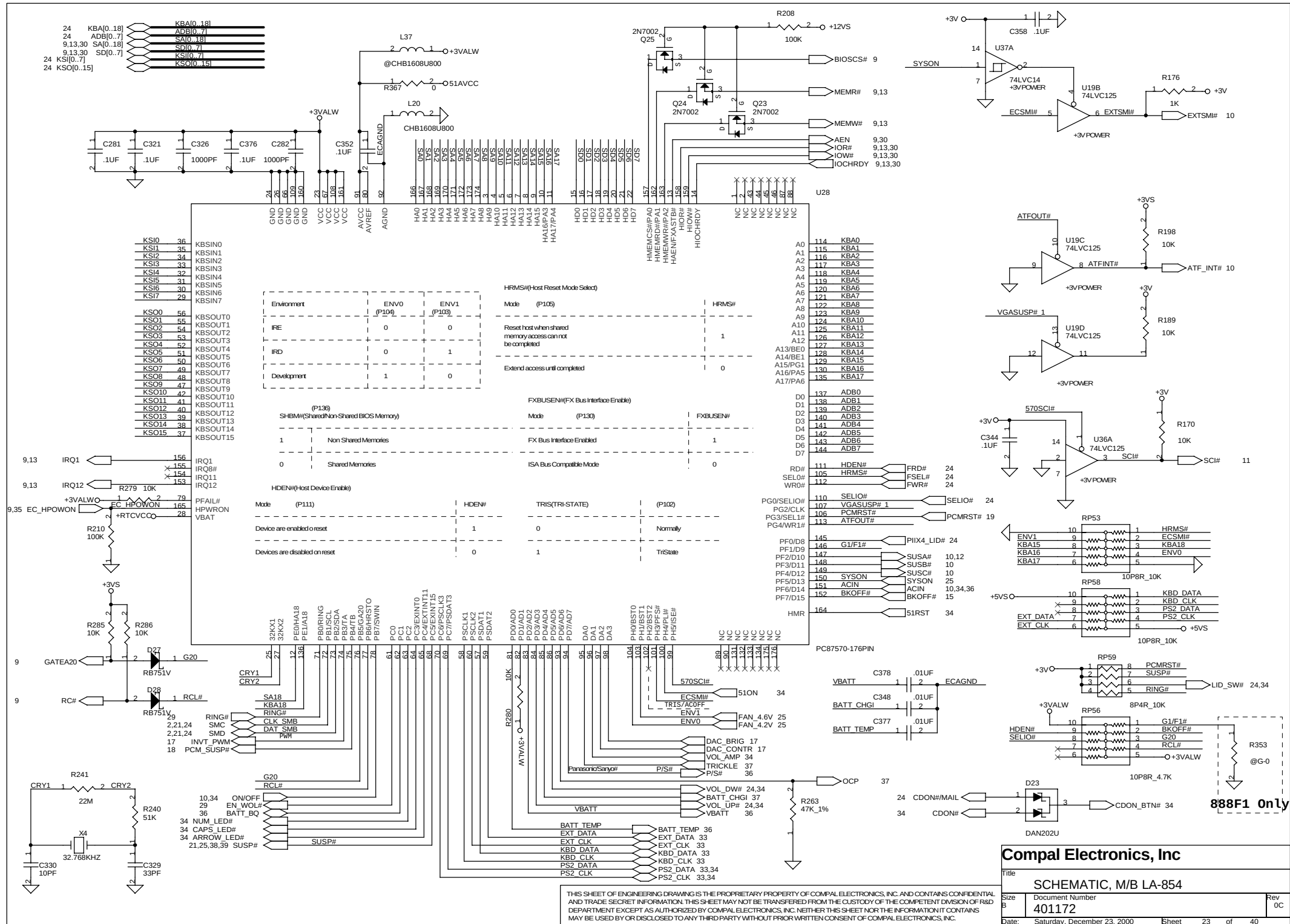
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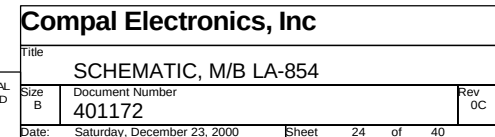
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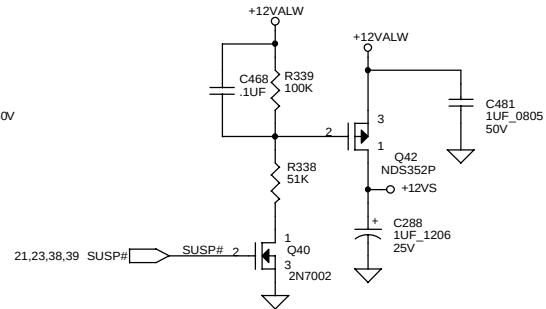
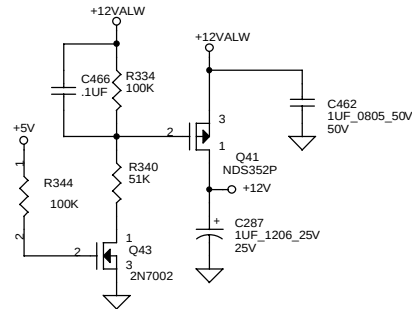
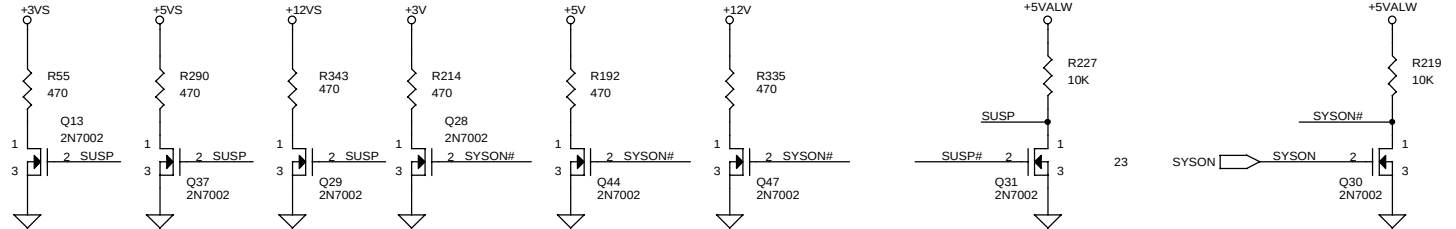
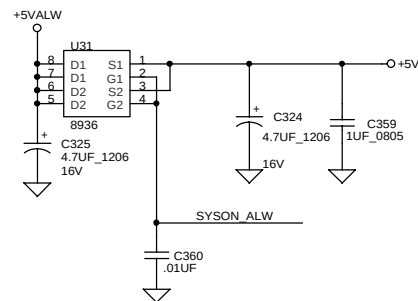
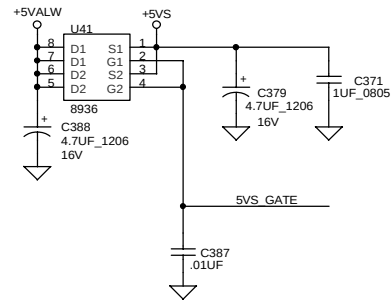
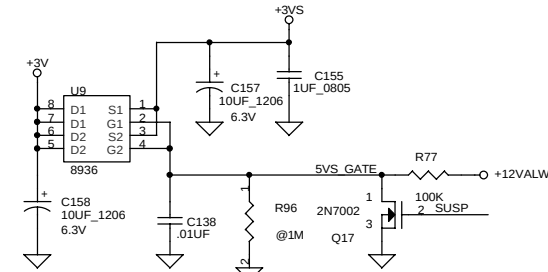
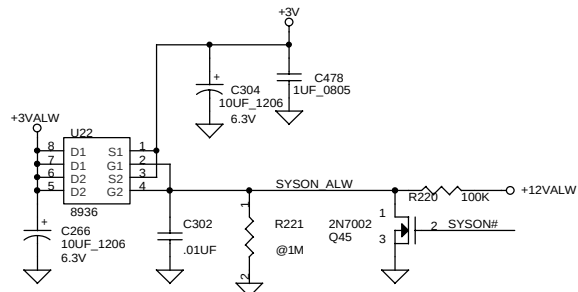
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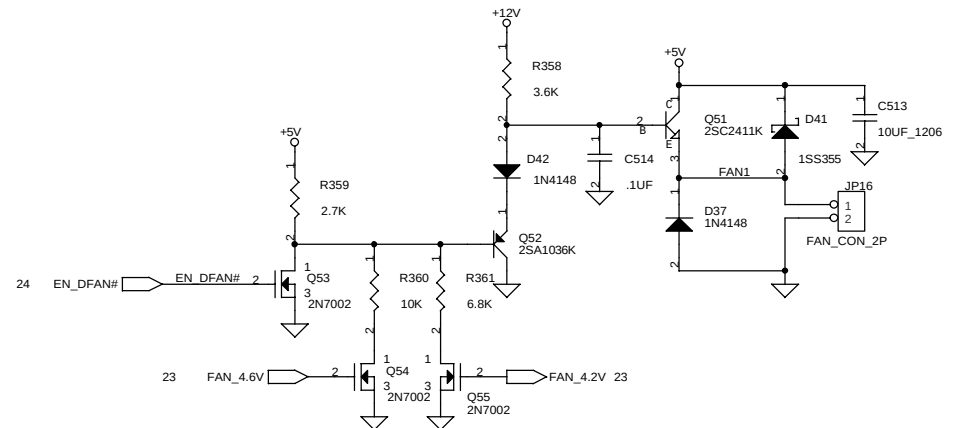
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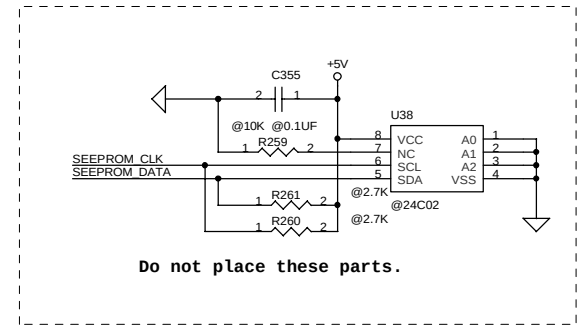
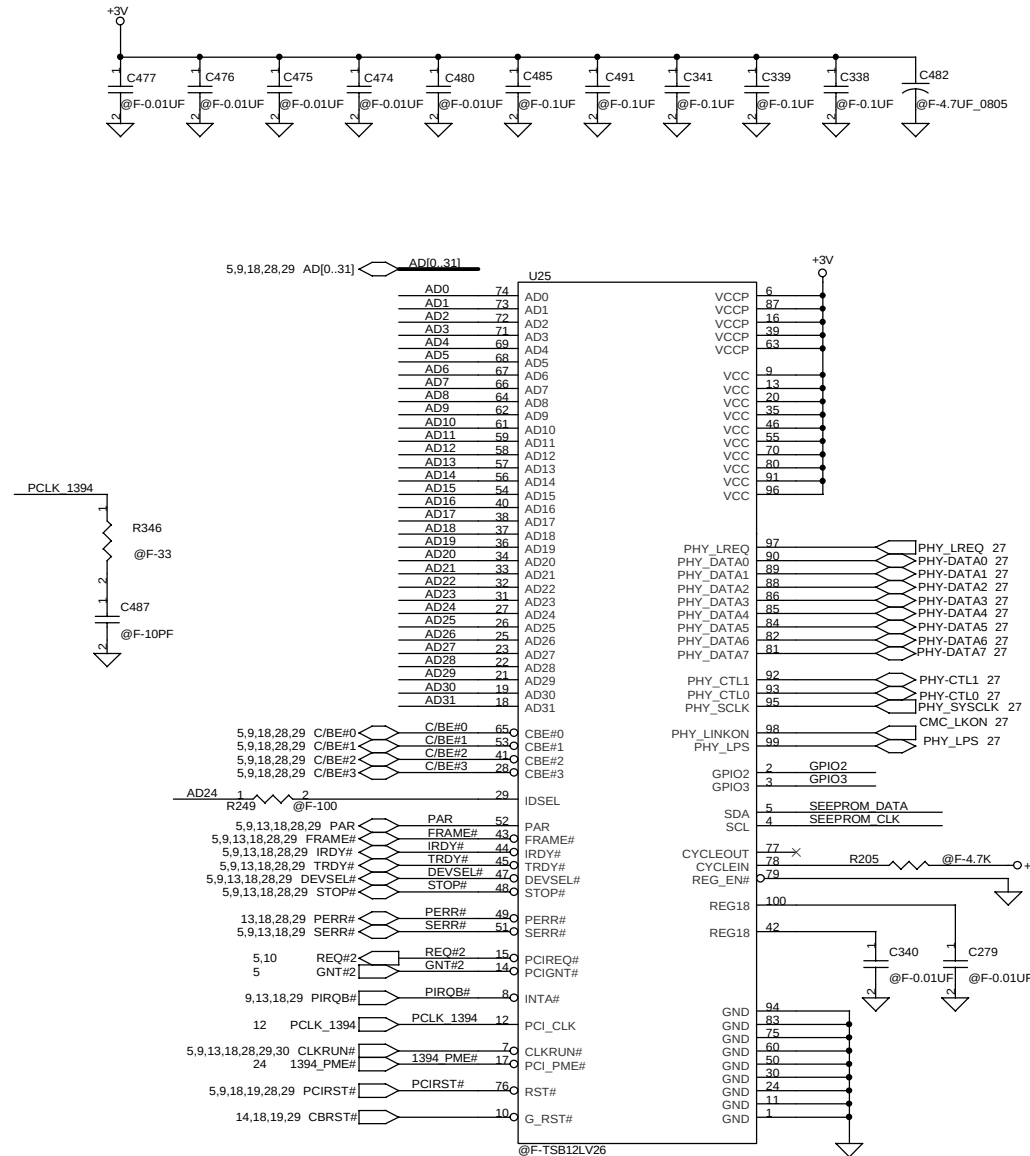


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1394 Host controller



Do not place these parts.

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5,9,18,26,29 AD[0..31]

AD[0..31]

U30

C528

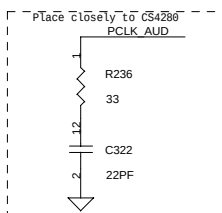
R216

100K

C335

10UF_6.3V_120V

Place component's to CS-4281



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5,9,18,26,29 C/BE#2
5,9,18,26,29 C/BE#3

9,13 PIRQ#
5,9,18,19,26,29 PCIRST#
12 PCLK_AUD
5 GNT#4
5,10 REQ#4

5,9,13,18,26,29,30 CLKRUN#
5,9,18,26,29 AD19
5,9,13,18,26,29 FRAME#
5,9,13,18,26,29 IRDY#
5,9,13,18,26,29 TRDY#

5,9,13,18,26,29 DEVSEL#
5,9,13,18,26,29 STOP#
13,18,26,29 PERR#
5,9,13,18,26,29 PAR
24,29 AUD_PME#

24 EC_VOLUP#
24 EC_VOLDW#

+3VSO

CS4281-CQ

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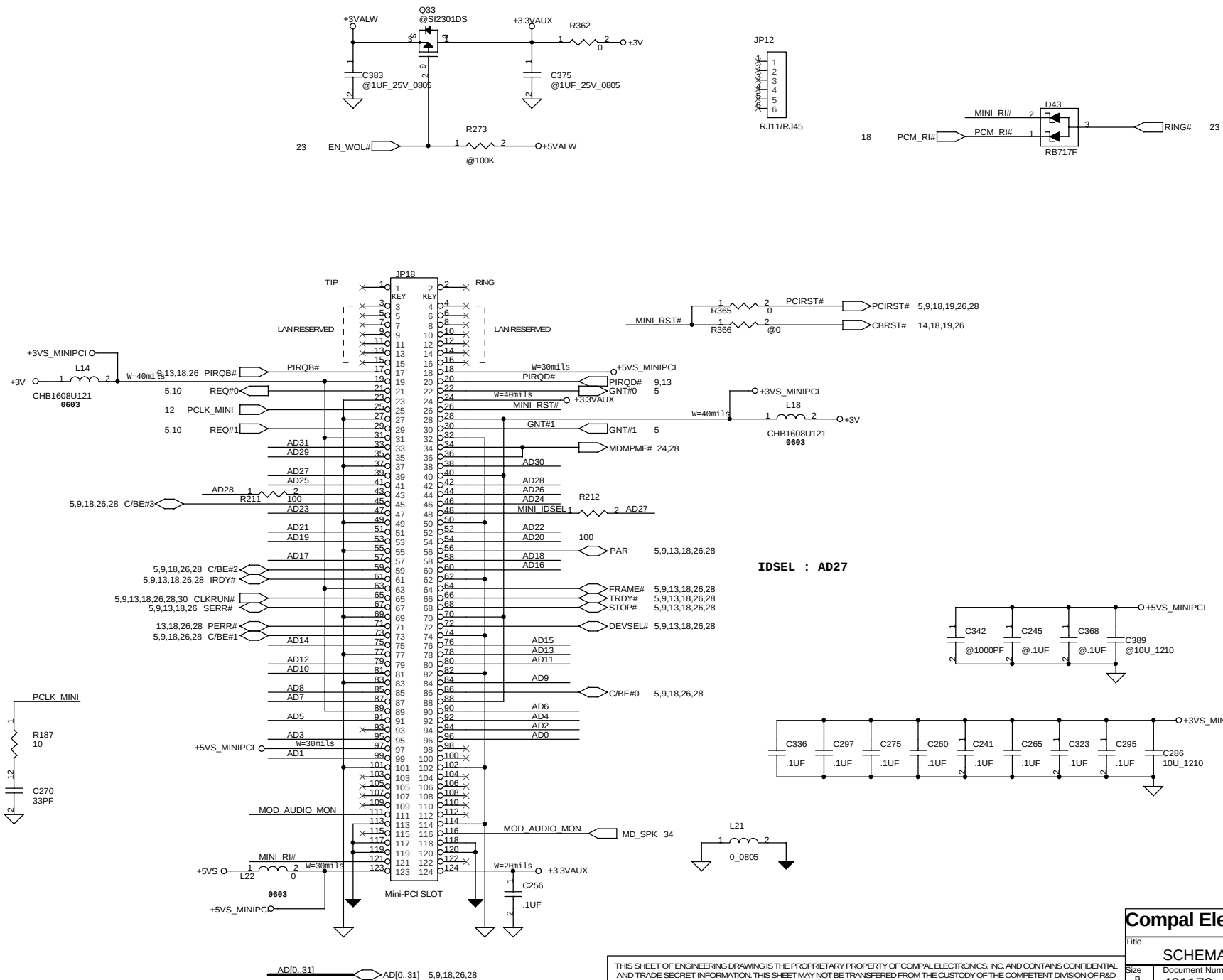
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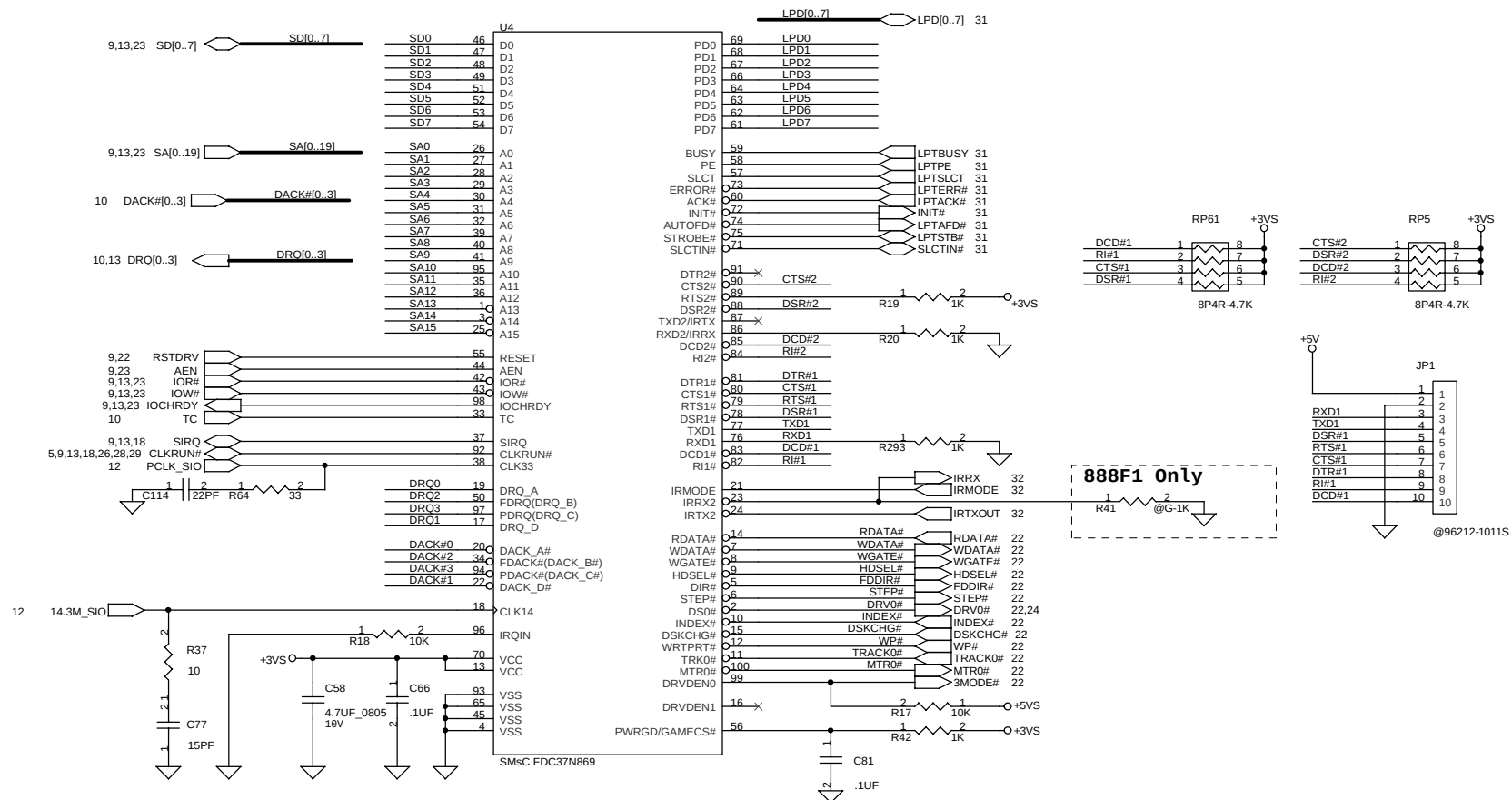
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SUPER I/O SMsC FDC37N869

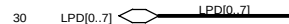
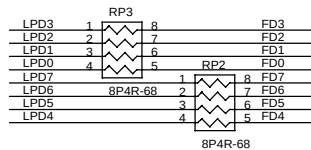
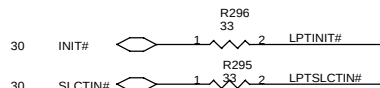
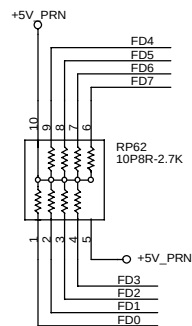
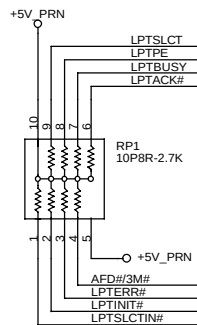
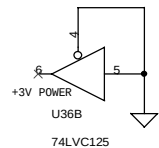
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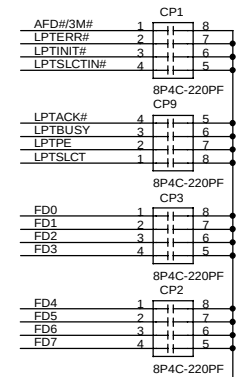
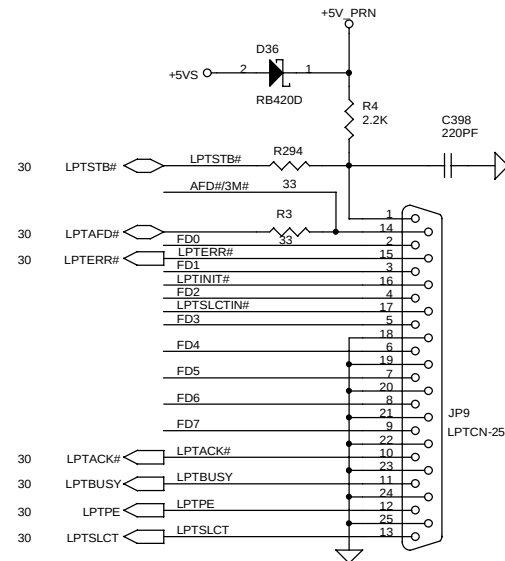
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PARALLEL PORT



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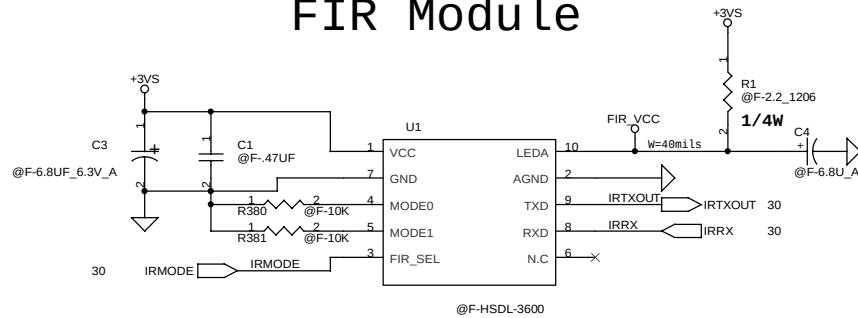
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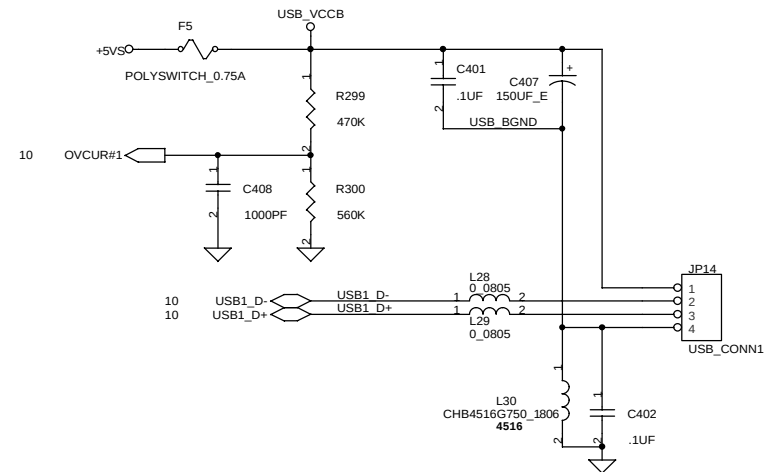
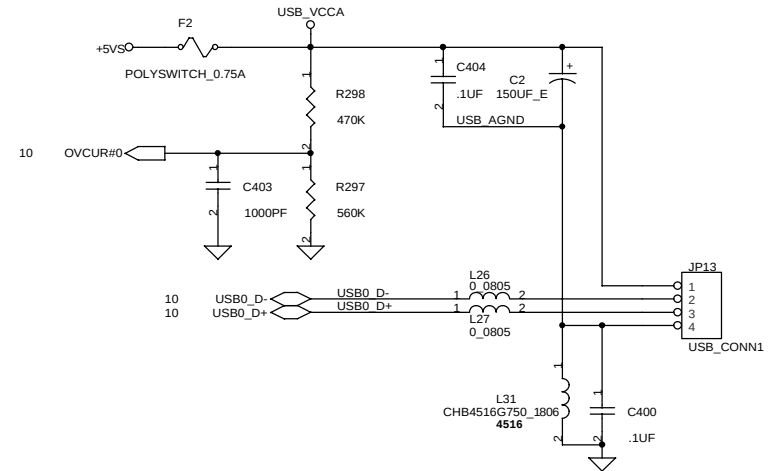
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FIR Module



The component's most place
cloely IRDA MODULE.

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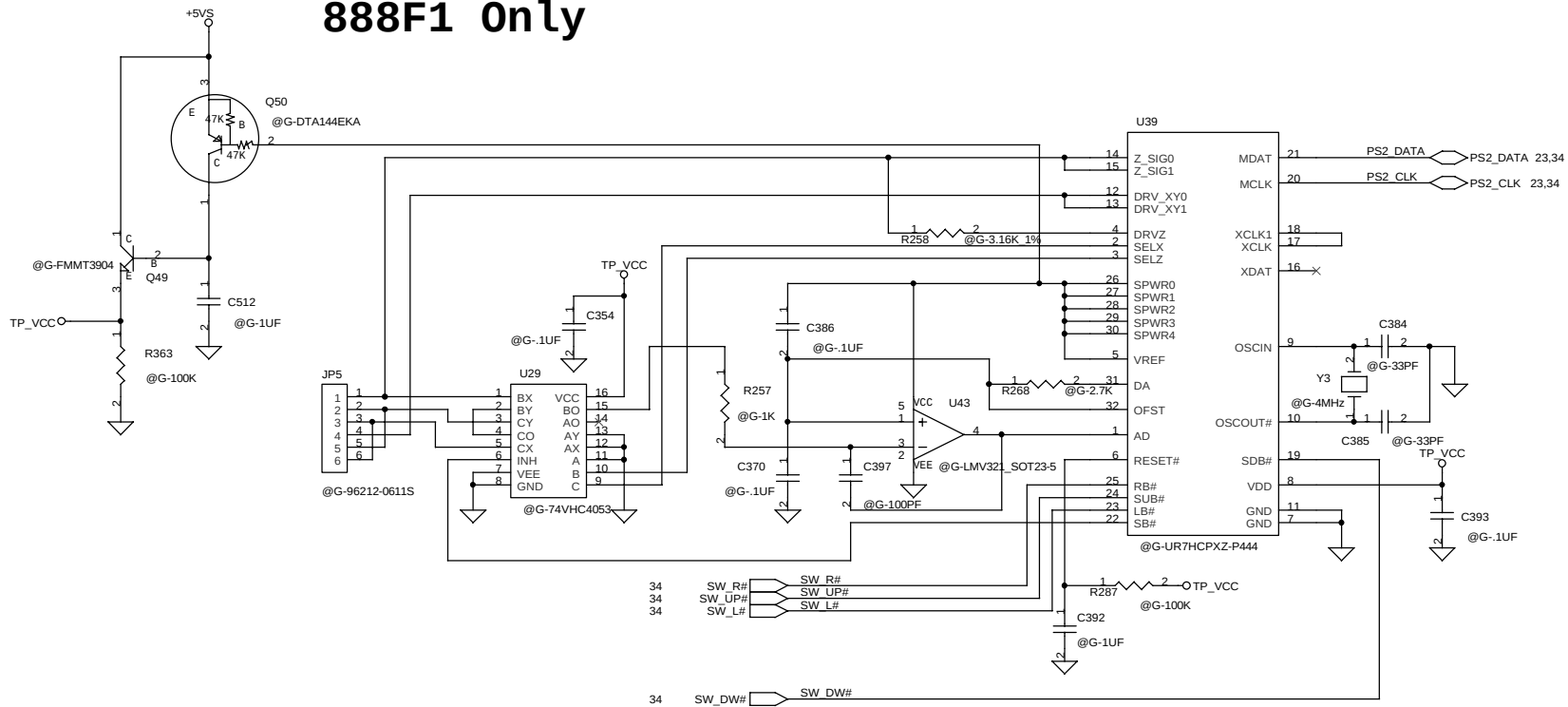
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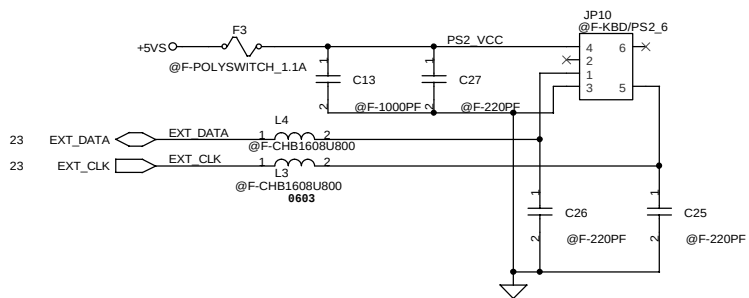
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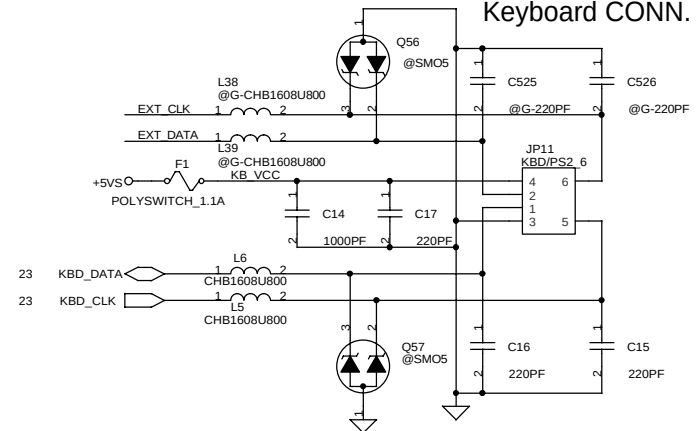


PS2 CONN.



888G1 Only

Keyboard CONN.

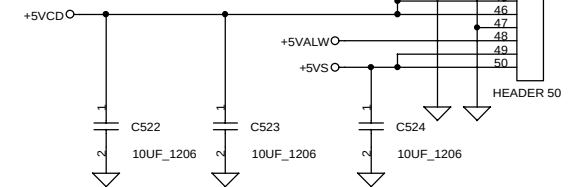
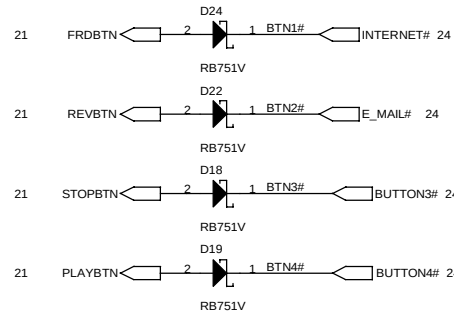
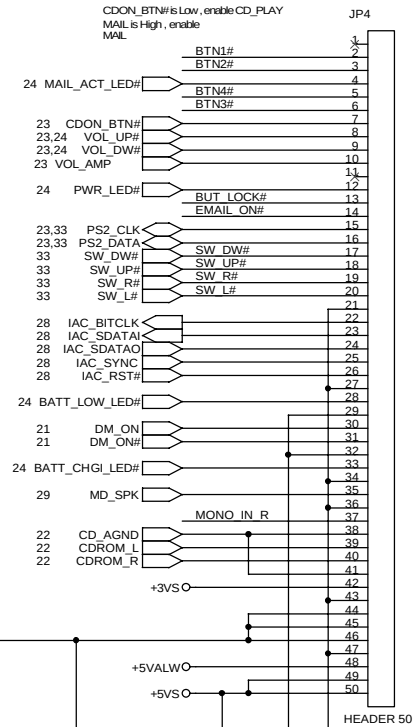
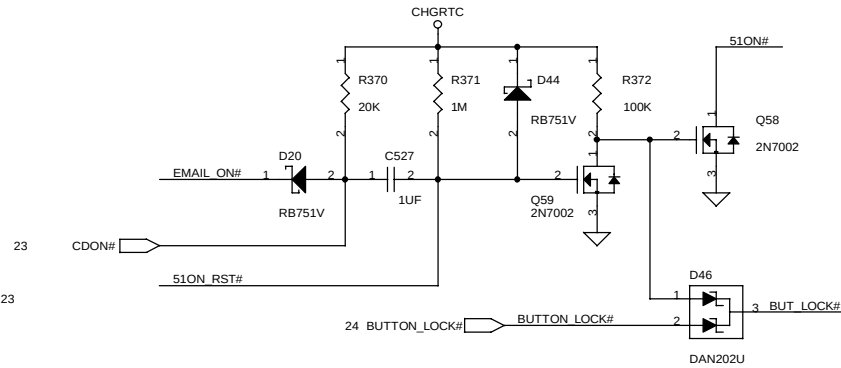
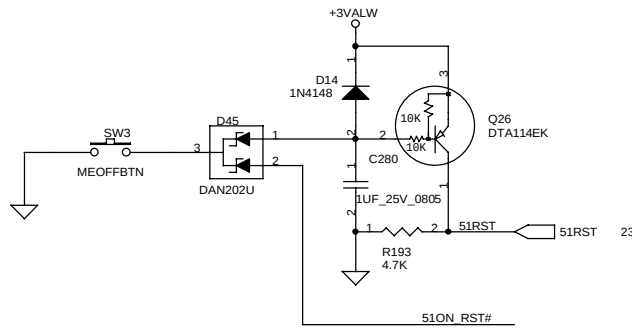
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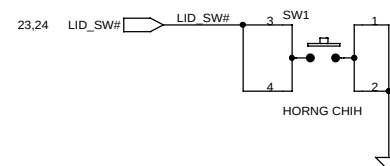
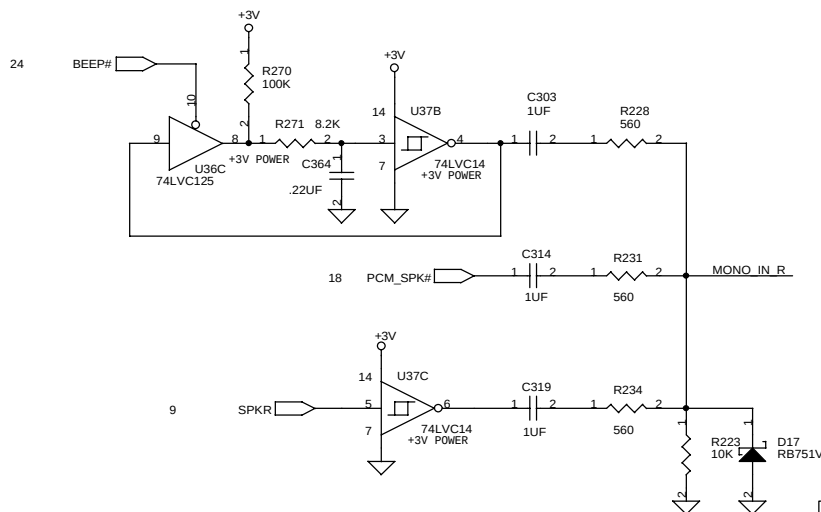
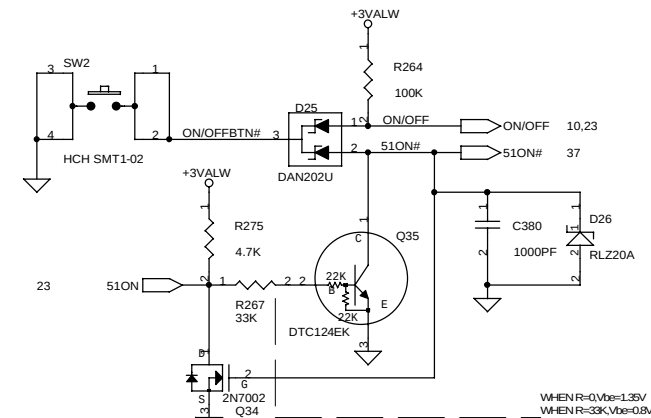
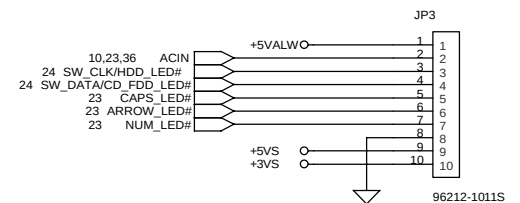
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Reset Button



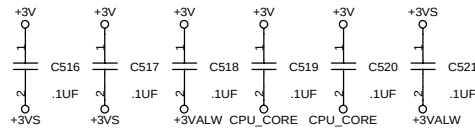
System Window Connector



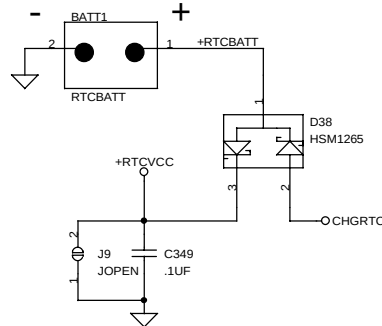
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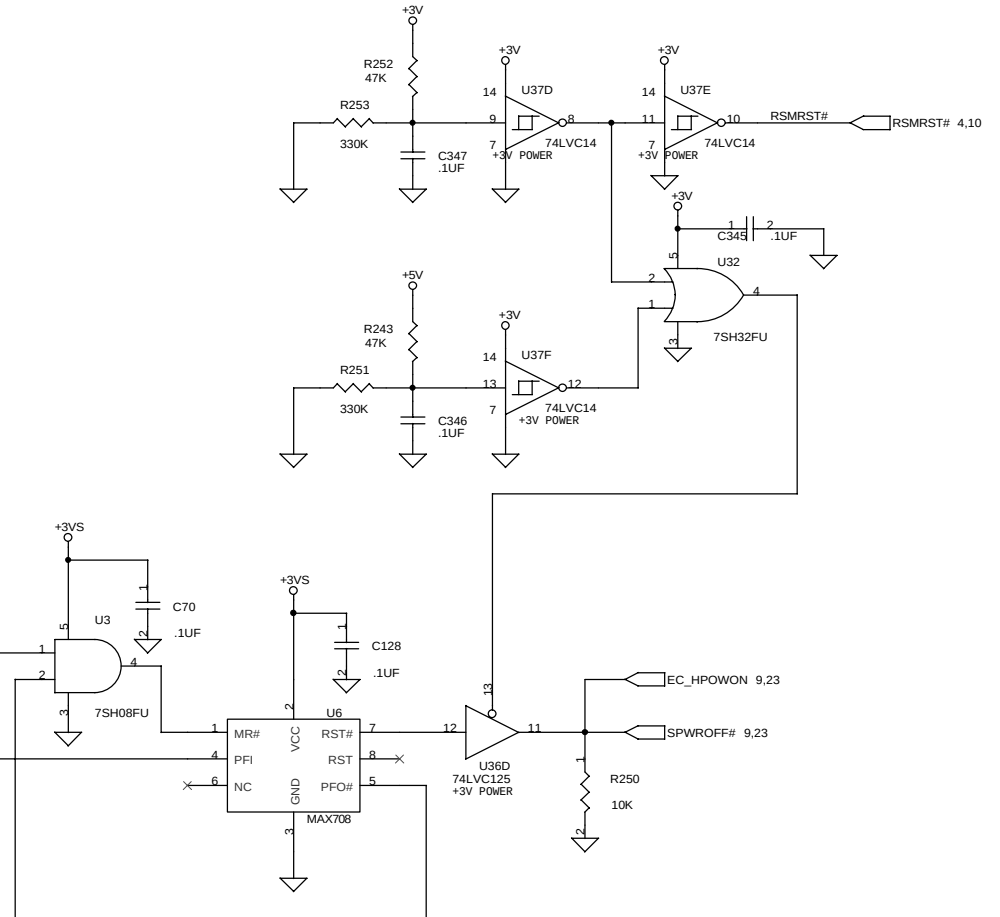
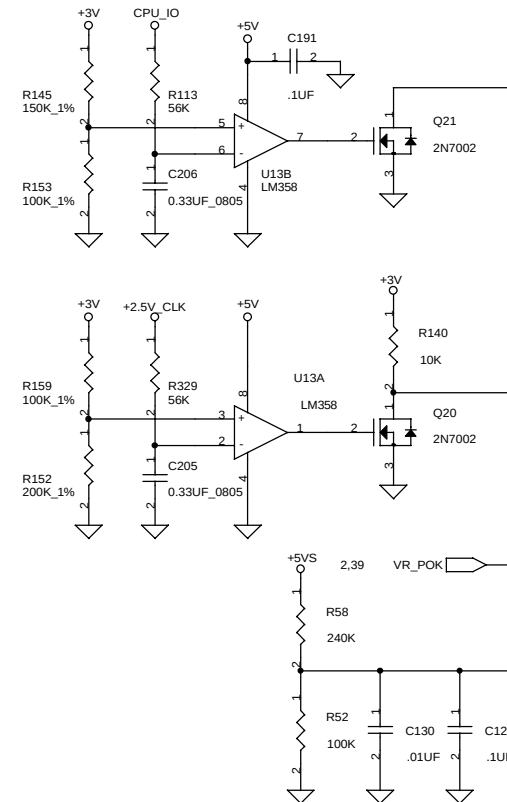
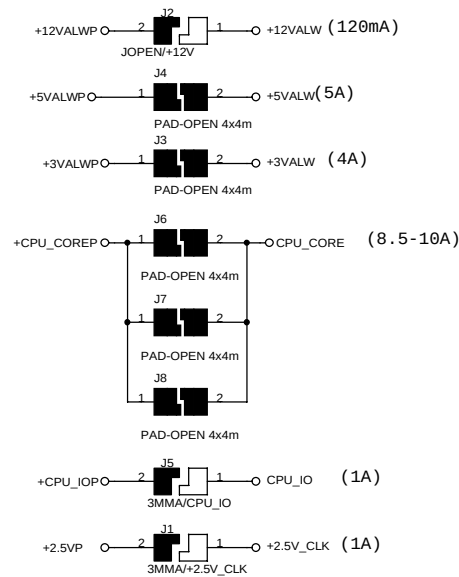
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RTC BATT

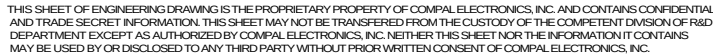


DC/DC INTERFACE

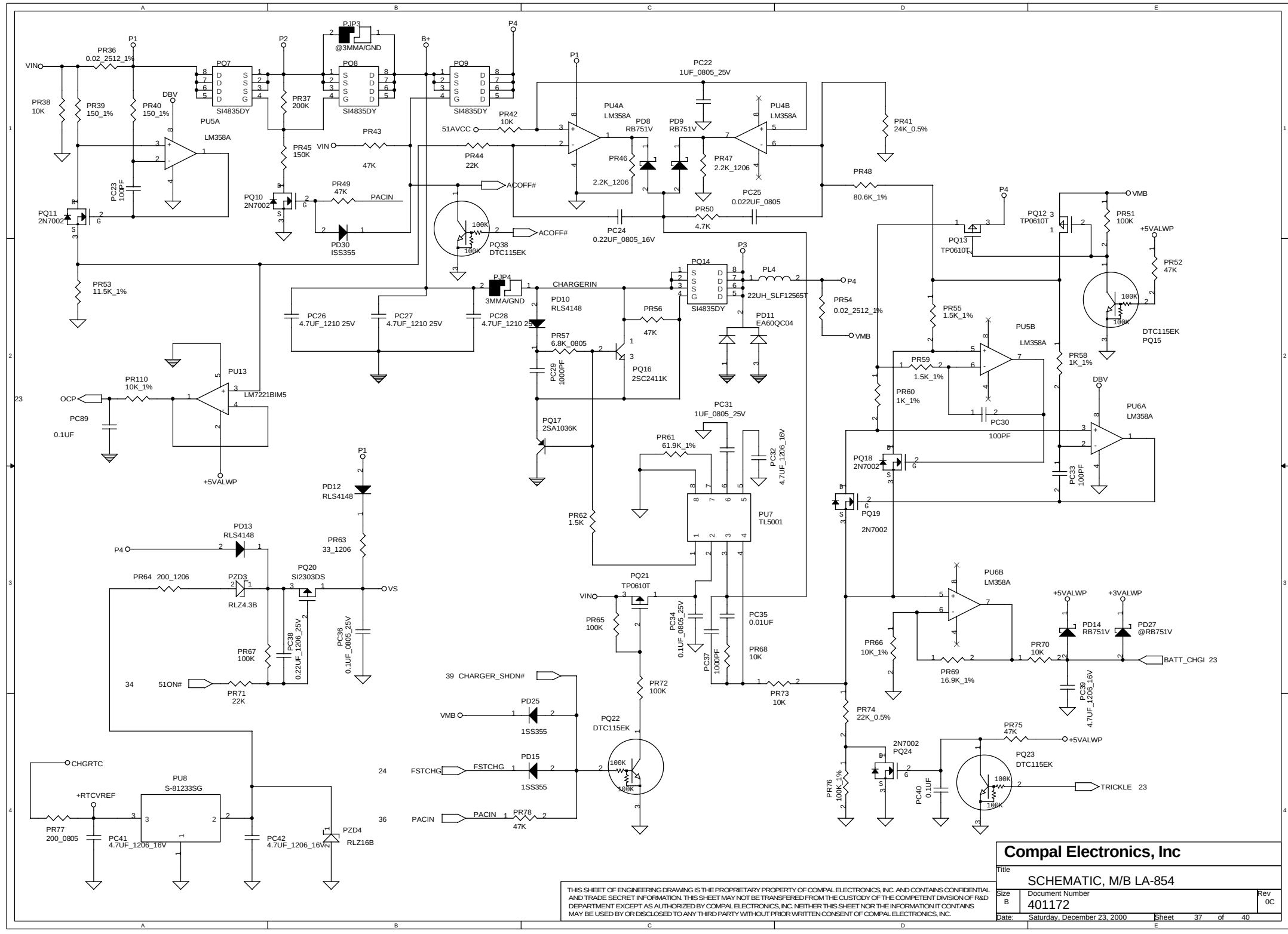


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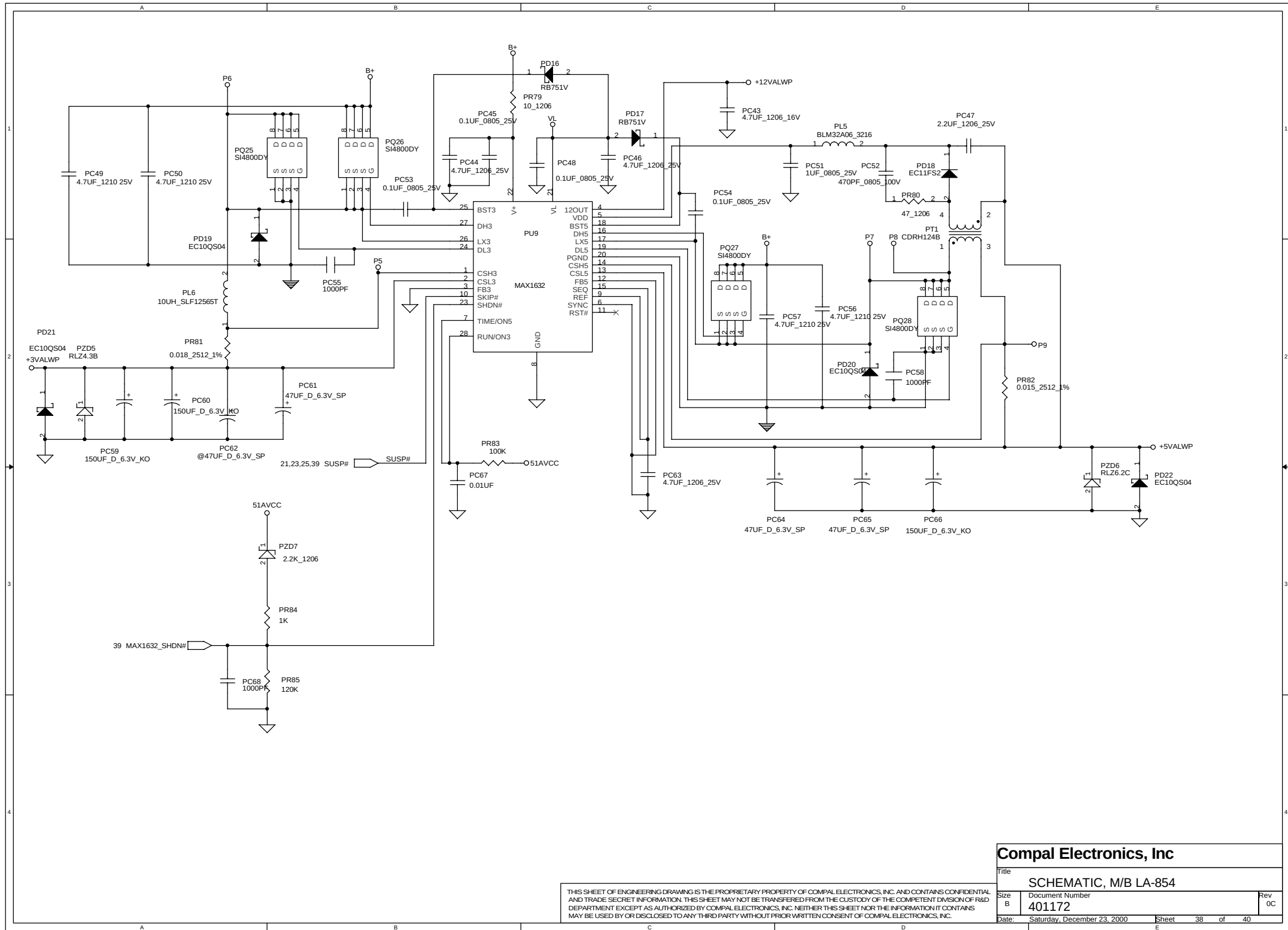


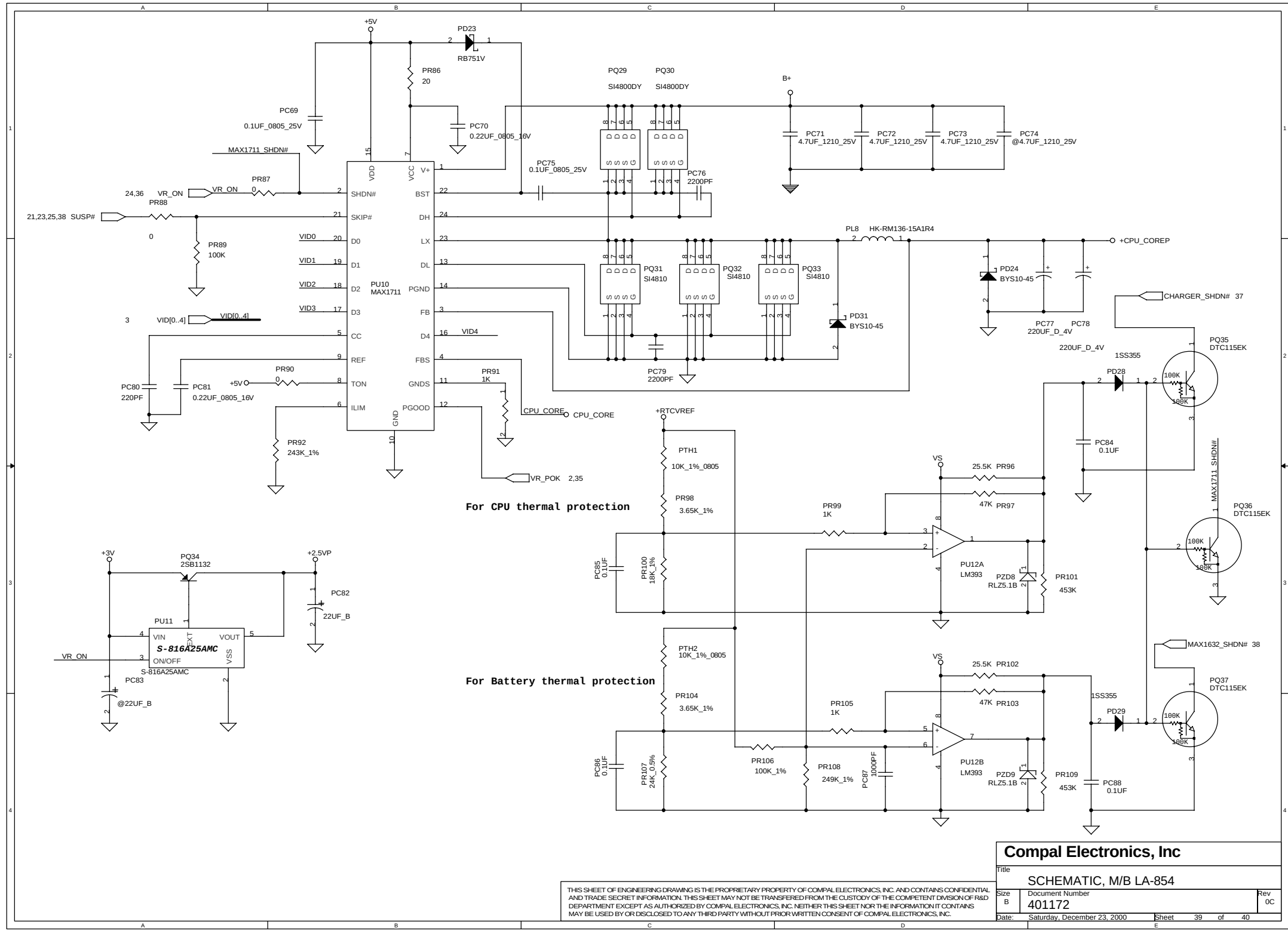
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For CPU thermal protection

For Battery thermal protection

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888F1 PIR LIST

03/13/00 Written by Jerry

P3, 4, 5: Change value of U8 to 4432X-100M
P16: Change value of U24 to 512KX2K2
P24: Delete R247 and add net DD on pin 8 of U40
P30: Change net GND_SIGNALr to GND on pin2 of JP1
P33: Change value of U39 to UR7HCPXZ-P444
P35: Change net CPI_IO to CPU_IO on pin1 of R113

03/15/00 Written by Jerry

P14: Delete net +3V on pin T13, T16, T8, R5, P16,
P5, R6, R15 of U10, These pins should be NC.
P38: Change PT1 to CDRH124B
P39: Add PL8 and reserve it

03/17/00 Written by Jerry

P3: Delete C175
P14: Swap SSIN and SSOUT on U15
P16: Swap VMA8 and VMA10 on U24

03/20/00 Written by Jerry

P14: Add R352 on net SSIN for pull down
Change value of R151 to 200K
P34: Change tolerance of R270 and R271 to +/- 5%
P29: Short pin34 and pin36 of JP18 for PME#
Short pin 111 and pin 116 of JP18 for MD_SPK
P34: Delete net MOD_AUDIO_MON on pin 33 of JP4

03/21/00 Written by Jerry

P23: Add R353 for G1/F1# select
***** Rev0.2 PIR List *****

04/20/00 Written by Jerry

P07: Add @ on R213 and C289
P10: Add Correct library D5 and D8
Add module port PX4_RI# on net PX4_RI#
P12: Change value of R180 to 15 ohm
Change VCC of R164 and R201 to +3VS
P14: Change VCC to +3V on pin R2 of U10
Change value of R151 to 200K_1%
P15: Do not populate RP23, R114, R117, R128,
R124, and C192 when use TFT panel
P17: Do not populate CP6 and CP7 and populate 0 ohm on
CP4 and CP5 when use TFT panel
Populate 0 ohm on R21, and C55 when use TFT panel.
Populate 0 ohm on C18, C23, and C24 when use TFT panel
Change net B+ to +3VALW on pin1 and pin3 of JP2
P18: Add net PCM_RI# on pin E14 of U33
P20: Delete D32
P21: Delete L15, L17, C291, C333, C274, C273, C327,
R237, R209, R215, R246, R238, R222, R229 and U26
P22: Change value of L32 to 0.0805
P23: Add net NUM_LED# on pin 63 of U28
Add net CAPS_LED# on pin 64 of U28
Add net ARROW_LED# on pin 65 of U28
Add net FAN_4.6V on pin 103 of U28
Add net FAN_4.2V on pin 104 of U28
Add Module port RING# on net RING#
P24: Delete R349 and Q48 when 888F1 configuration
Add net S1RING# on pin 6 of U34
Change net EN_DFAN to EN_DFAN# on pins of U34
Add net BATT_LOW_LED# on pin 9 of U34
Add net BATT_CHGI_LED# on pin 16 of U34
Change net SW_CLK to SW_CLK/HDD_LED# on pin 16 of U35
Change net SW_DATA to SW_DATA/CD_FDD_LED# on pin 19 of U35
Add D39 and net PX4_RI# on pin 2 of D39
Change value of U42 to NM24C16
Add D40 and R357 for OR net PCM1_LED and PCM2_LED to net PCM_LED
Delete net PCM1_LED on pin8 of U21
Delete net PCM2_LED on pin11 of U21
Delete net HDD_LED# on pin13 of U21
Add net PCM_LED on pin8 of U21
Add net SHDD_LED# on pin11 of U21
Add net PHDD_LED# on pin13 of U21
Add R356 on net CD_INTA# for pull high +5VCD
Delete RP54
Pin3 of RP55 tie to net CDONW/MAIL
Pin4 of RP55 tie to net BUTTON_LOCK#
P25: Delete Q39 and C449 for FAN
Add Q55, Q54, Q53, Q52, Q51, D42, D41, R359, R360, R361,
R358, C514, and C513 for FAN
P26: Add R354 and R355 for signals of SEEPROM_CLK
and SEEPROM_DATA pull down
P29: Add R362 between +3_VALUX and +3V
Change VCC +3VS to +3V on L14 and L18 for MINI PCI
Delete net MD_MIC on pin115 of JP18
Add parts D43, R364, C515, and U49 for net RING#
Add net MINI_RI# on pin 121 of JP18
Add R365 between pin 26 of JP18 and net PCIRST#
Add R366 between pin 26 of JP18 and net CBRST#
Add @ on R366
P30: Change DRQ and DACK# signals on U4
P33: Delete C513 and add R363 on TP_VCC
Add @ on F3, C13, C27, C26, C25, L4, L3, and JP10 when 888F1

P34: Delete net MD_MIC on pin28 of JP4
Delete net LEFT on pin30 of JP4
Delete net RIGHT on pin31 of JP4
Delete net AGND on pin29 and pin32 of JP4
Add net BATT_LOW_LED# on pin28 of JP4
Add net BATT_CHGI_LED# on pin33 of JP4
Add net DM_ON on pin30 of JP4
Add net DM_ON# on pin31 of JP4
Add net GND on pin29 and pin32 of JP4
Change JP3 to 10pin connector and re-assign pin out
Change value of SW2 to HCH SMT1-02
P36: Change value of PC16 to 47UF_D_6.3V
P37: Add PD25 between net VMB and pin2 of PD15

04/28/00 Written by Jerry

P03: Delete C117
P12: Add L36 for SDRAM Clock VCC
P35: Add C516, C517, C518, C519, C520, and C521for EMI

04/29/00 Written by Jerry

P22: Delete L32
P34: Add C522, C523, and C524 for SW/B

05/02/00 Written by Jerry

P15: Add J10 between VGA_GND and GND
Add net VGA_GND on pin J4, L5, G5, E4, D4, M5, M4,
F5, and K18 of U10

05/04/00 Written by Jerry

P29: Add @ on L22, C342, C245, C368, and C389
P34: Short pin49 and 50 of JP4

05/05/00 Written by Jerry

P23: RP56, U28, R279, R280 change VCC to +3VALW
RP59 change VCC to +3V
Change value of RP56 to 2.7K
P24: U21, U27, U40, U18, U23, RP55, RP60, R206,
and R233 change VCC to +3VALW
Add @ on D18
Add SST39VF040 on U49
Change value of U21 and U27 to 74LVC244
Change value of U40 to 74LVC32
P29: Delete U49, C151, and R364
P34: D14, Q26, R264, and R275 chnage VCC to +3VALW
Pin18 of JP3 change VCC to +3VS
P35: Delete U5 and C83
Net EC_HPOMON tie to net SPWROFF#
C518 and C521 change VCC to +3VALW

05/08/00 Written by Jerry

P23: Add L37 and R367, add @ on L37
P36: Change value of PD22 to RL23.6B

***** Rev0.3 PIR List *****

05/16/00 Written by Jerry

Change package of C1206, C1210, TAN_A, and TAN_B to
CAP-A_B_1206_1210_TAN

05/18/00 Written by Jerry

P19: Change value of C486 and C298 to 1UF_25V_0805
P27: Change value of C455 to 4.7UF_0805

06/13/00 Written by Jerry

P23: Change value of C352 to 0.1UF
Change value of RP56 to 10P8R_4.7K
Delete net CLK_SMB and DAT_SMB on RP56
P24: Add R368 and R369 for SMB pull high
P29: Add L22 for Mini PCI +5VS
P33: Add L38, L39, C525, and C526 for PS/2 Ycabale

06/15/00 Written by Jerry

P03: Delete C37 and C47
P15: Delete J10
P23: Change net S10N# to CDONW
P33: Add Q56, and Q57 for ESD
P34: Add D45, R378, R371, D44, R372, C527, Q59,
Q58, and D46 for S10N# signal
Change value of D20 to R8751V
P36: PJP2 pin2 and Pin3 short
P39: Delete PL7

06/16/00 Written by Jerry

P13: Add H17
P17: Delete CP6, CP7, CP4, and CP5 for EMI
ADD LP1, LP2, LP3, and LP4 for EMI
P28: Add R373 and C528 for EMI

06/20/00 Written by Jerry

P08: Remove @ on R348, C483, R341, and C472
P12: Change value of R180 to 10 ohm
Change package of Y1 to 2 pin package
P13: Add M6
P15: Change value of RP23 to 16P8R-56 Ohm
P16: Remove @ on R230 and C320
P17: Remove @ on R21 and C55
Change value of C45, C18, C23, and C24 to 220PF
P18: Add R374 and R375 for 1420RST#

06/23/00 Written by Jerry

P02: Change value of R132 and R134 to 1K

06/26/00 Written by Jerry

P14: Change value of C215 and C190 to 1UF
P24: Remove @ on CP10, CP11, CP12, CP13, CP14, and CP15

07/05/00 Written by Jerry

P17: Change value of L23, L24, and L25 to FCM2012C80

***** Rev0.4 PIR List *****

07/08/00 Written by Jerry

P23: Change value of R263 to 47K
Add net OCP
P32: Add R380 and R381 for 888G1
P36, 27, 38, 39: Update DC/DC circuit

07/10/00 Written by Jerry

P24: Remove @ on D15
Add @ on D16

07/13/00 Written by Jerry

P37: Swap VCC and GND pin on PU13

***** Rev1.0 PIR List *****

07/29/00 Written by Jerry

P15: Change value of R158 to 22 Ohm

0/31/00 Written by Jerry

P34: Change vaule of R371 to 1M

888F2 PIR LIST

***** Rev0.1 PIR List *****

10/12/00 written by Allen

P23: Add BATT_BQ from 87570
P38: Add PD30,PR111,PR112 for Smart Battery
P39: Add PD31 at CPU_COREP

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