

**DC/DC POWER**

**+1.5V POWER**

**+1.8V POWER**

**+2.5V POWER**

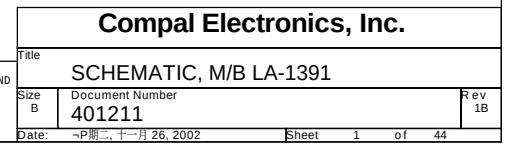
**+3VALW POWER**

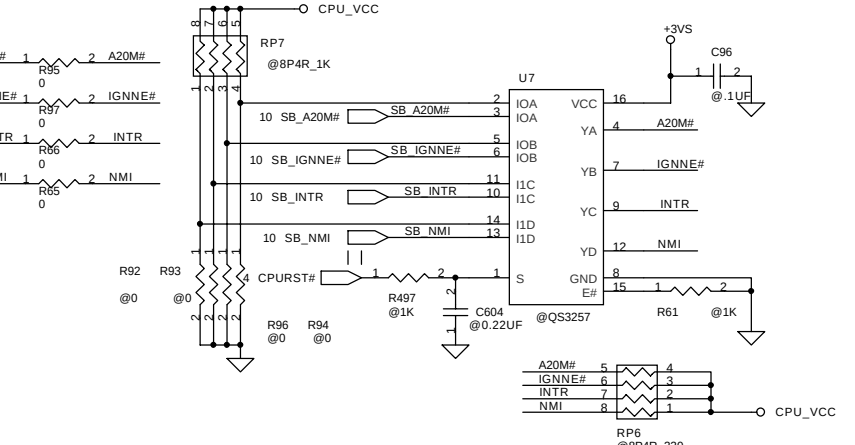
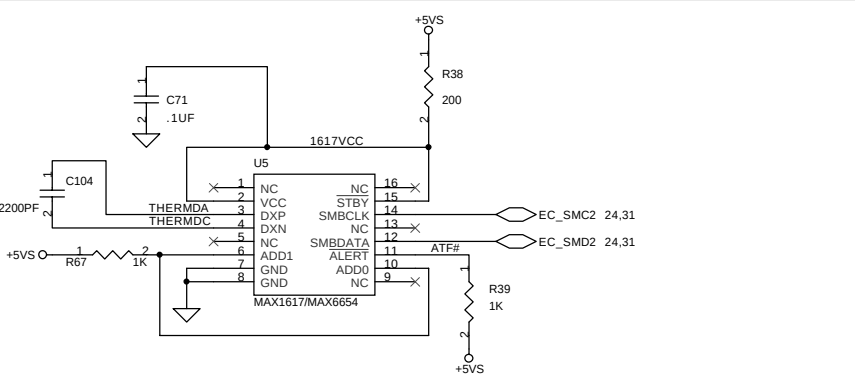
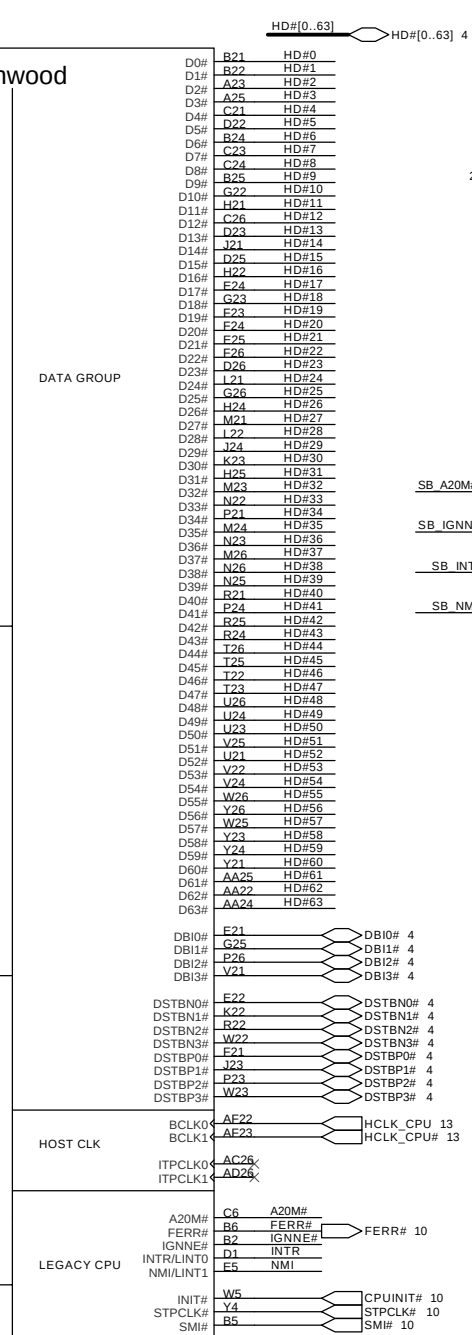
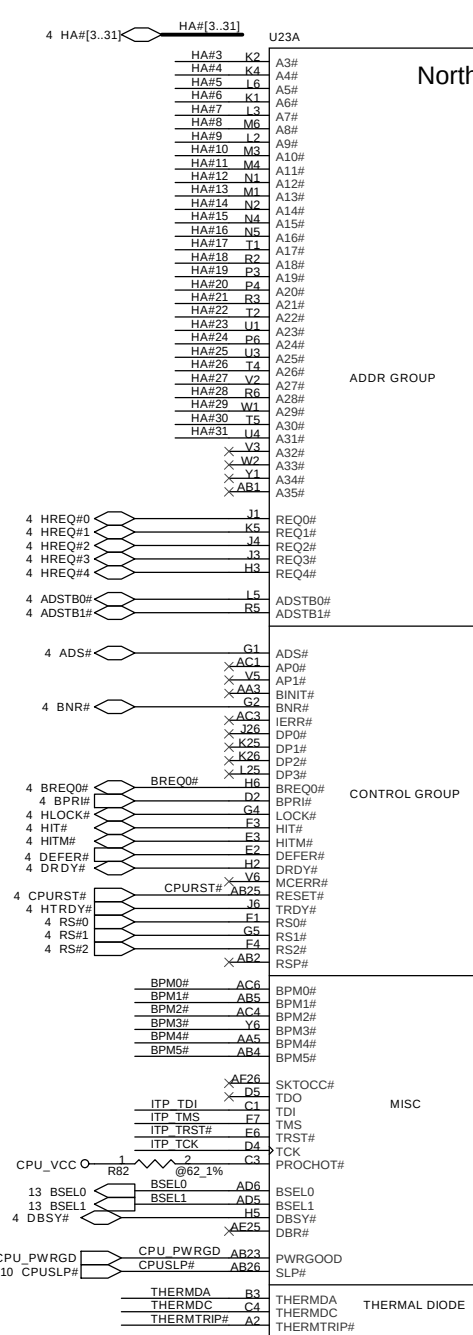
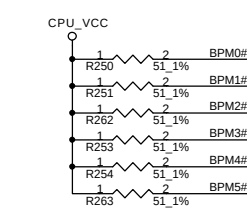
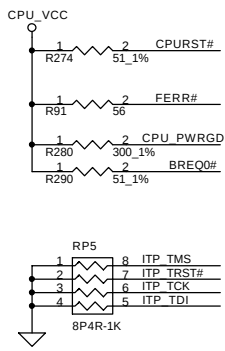
**+5VALW POWER**

**+12VALW POWER**

**CPU\_VCC POWER**

**PAGE 36,37,38,39,40,41**



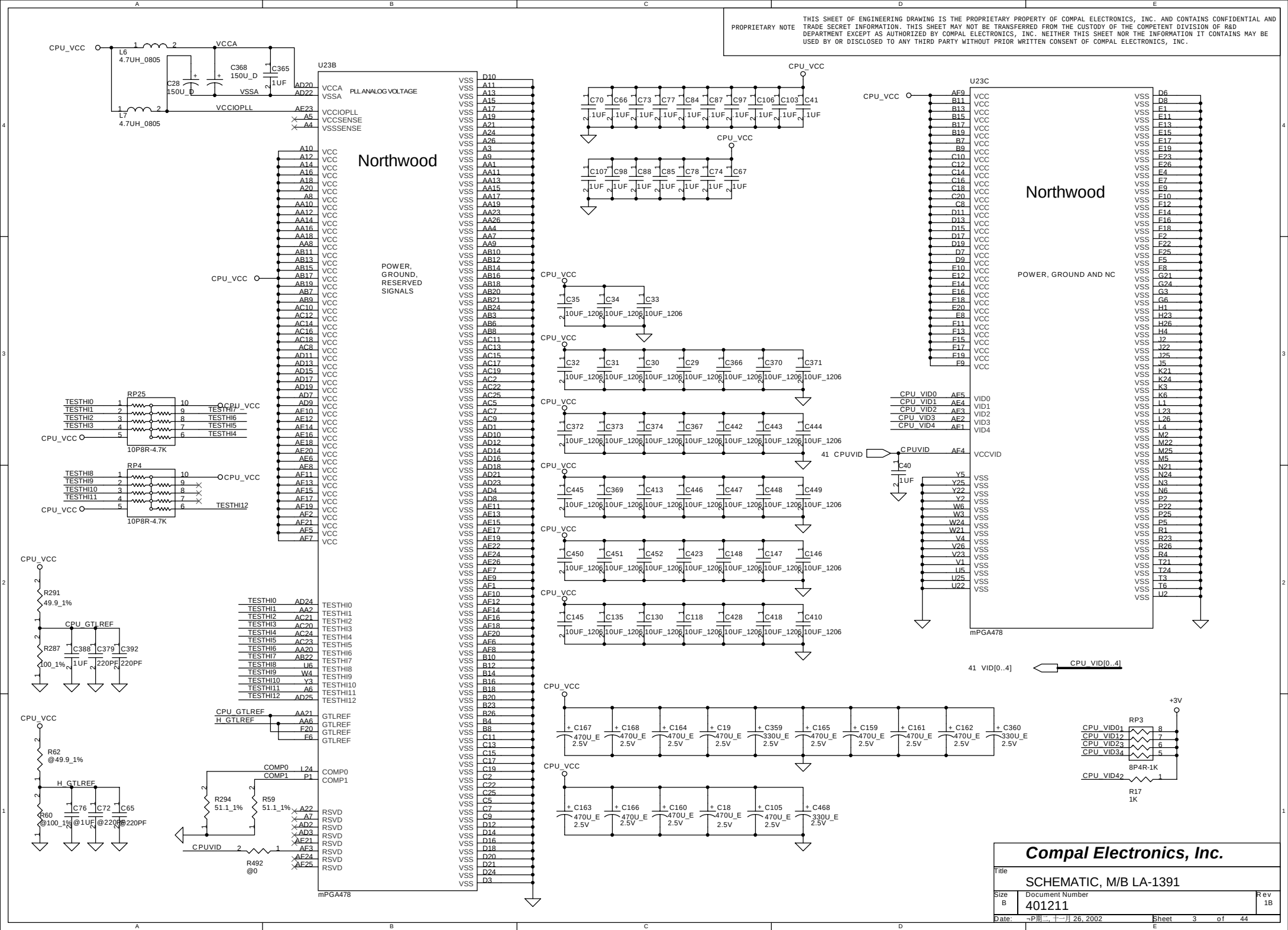


| SW1 RATIO SELECT |     |       |        |      |
|------------------|-----|-------|--------|------|
| RATIO            | NMI | A20M# | IGNNE# | INTR |
| 15X              | H   | L     | L      | L    |
| 16X              | L   | H     | H      | H    |
| 17X              | L   | H     | H      | L    |
| 18X              | L   | H     | L      | H    |
| 19X              | L   | H     | L      | L    |
| 20X              | L   | L     | H      | H    |
| 21X              | L   | L     | H      | L    |
| 22X              | L   | L     | L      | H    |
| 23X              | H   | H     | H      | L    |
| 24X              | L   | L     | L      | L    |

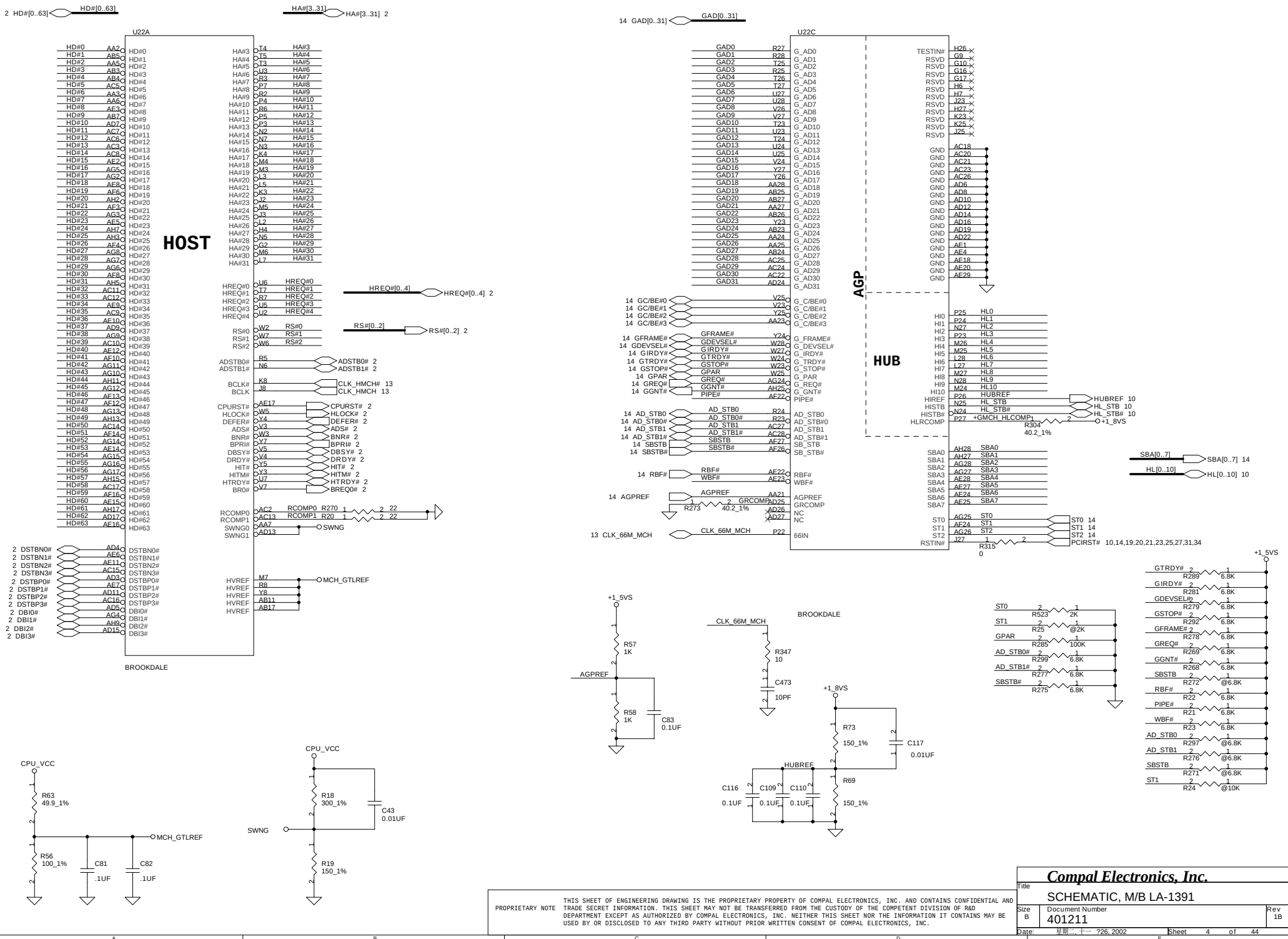
| SELPSB[1:0] | STSEM BUS FREQUENCY |
|-------------|---------------------|
| 00          | 100MHZ              |
| 01          | RESERVED            |
| 10          | RESERVED            |
| 11          | RESERVED            |

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

|                              |                        |        |
|------------------------------|------------------------|--------|
| Compal Electronics, Inc.     |                        |        |
| Title SCHEMATIC, M/B LA-1391 |                        |        |
| Size B                       | Document Number 401211 | Rev 18 |
| Date: ~P期二, 十一月 26, 2002     | Sheet 2                | of 44  |

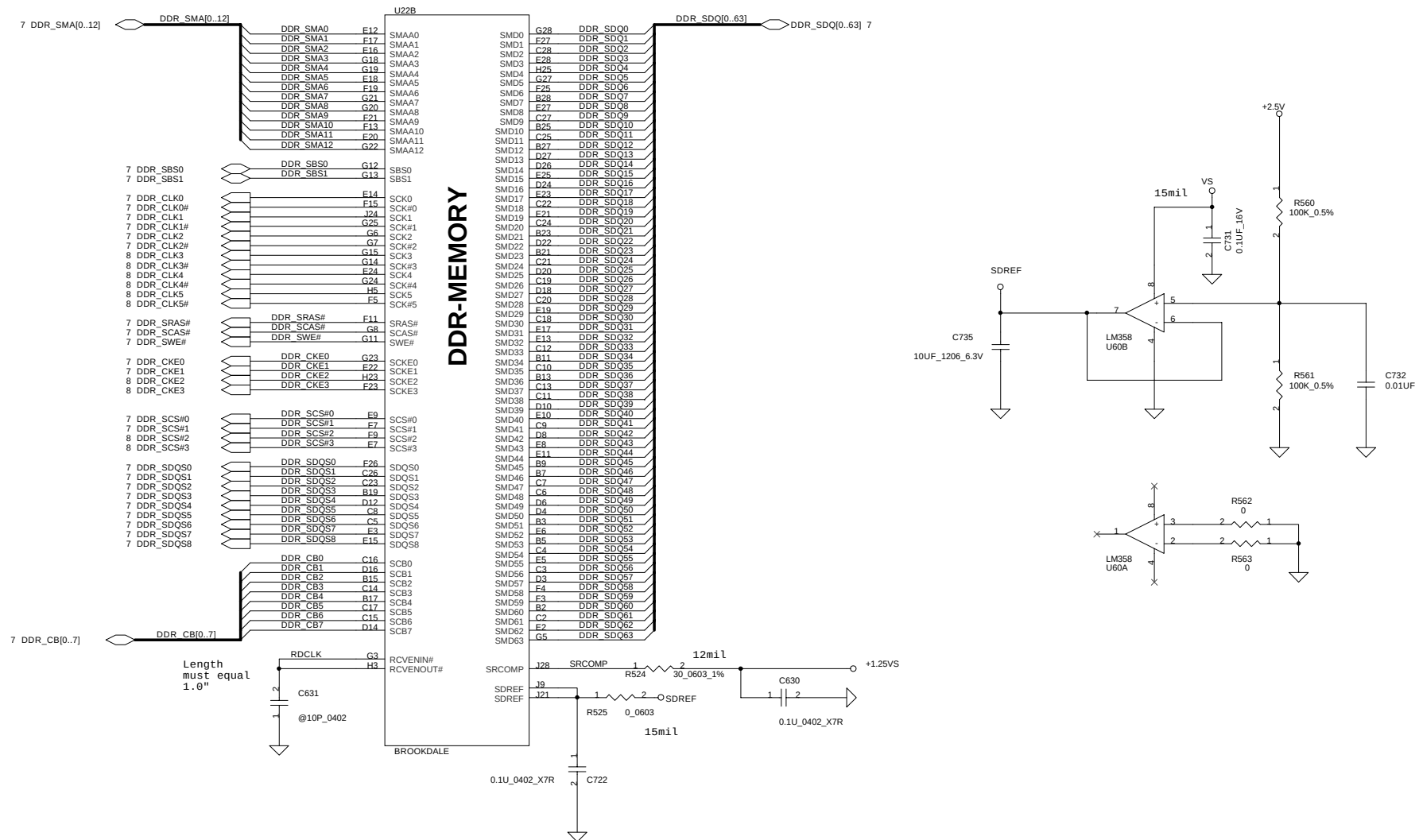


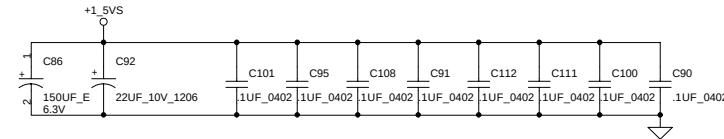
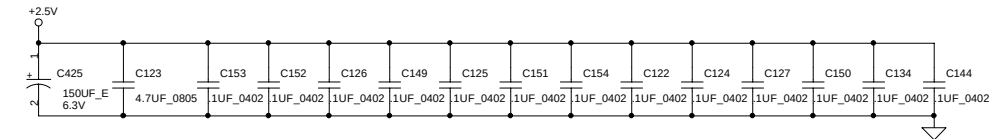
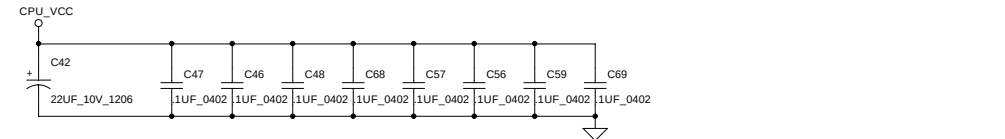
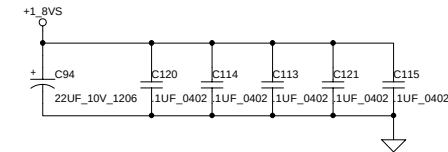
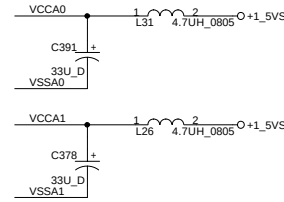
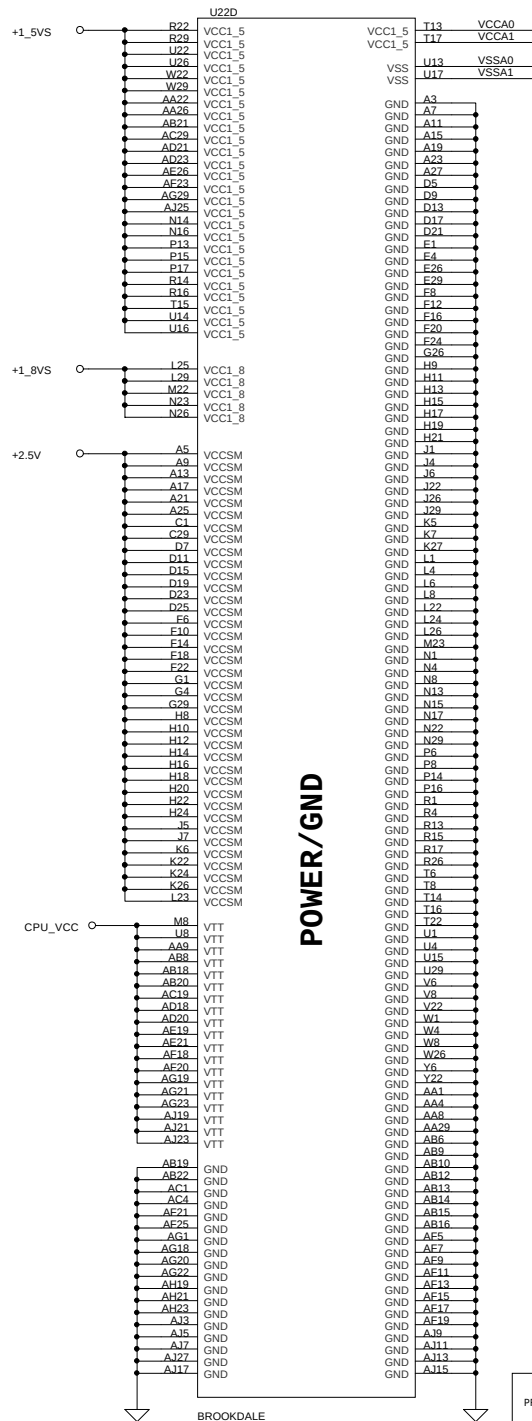
# MC-1/3(GTL+,AGP,HUB)



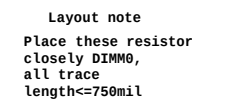
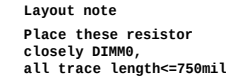
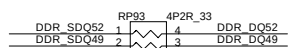
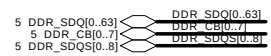
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

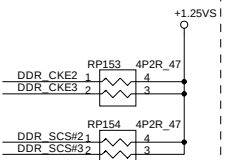
|                        |                  |               |
|------------------------|------------------|---------------|
| Title                  |                  |               |
| SCHEMATIC, M/B LA-1391 |                  |               |
| Size                   | Document Number  | Rev           |
| B                      | 401211           | 1B            |
| Date:                  | 日期: 11-226, 2002 | Sheet 4 of 44 |





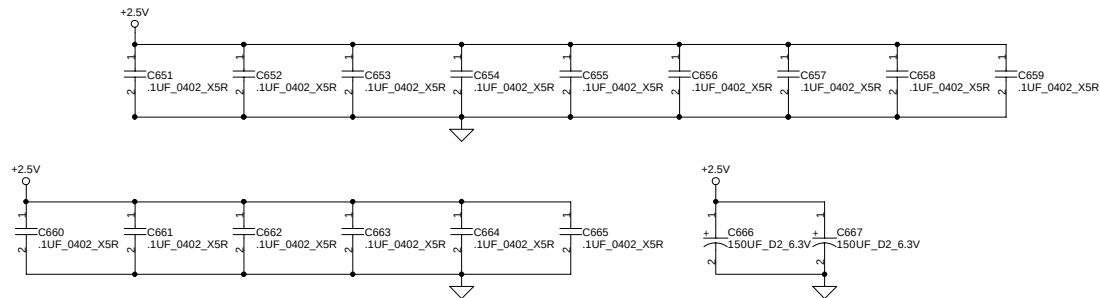
Place these resistor  
closely DIMM0,  
all trace length<750mil



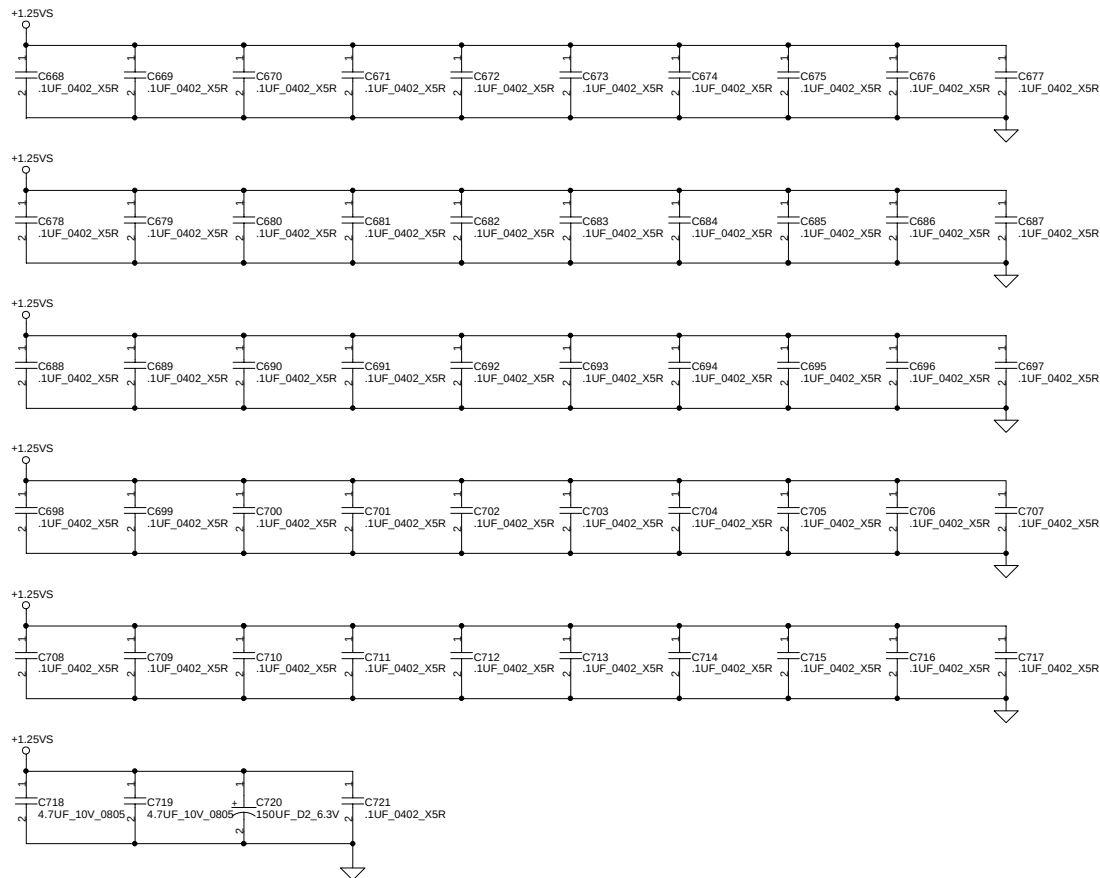


Layout note  
Place these resistor  
closely DIMM1,  
all trace length  
Max=1.3"

Layout note :  
Distribute as close as possible  
to DDR-SODIMM.



Layout note :  
Place one cap close to every 2 pull up resistors termination to  
+1.25V



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND  
TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D  
DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE  
USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

| Compal Electronics, Inc. |                    |  |               |
|--------------------------|--------------------|--|---------------|
| Title                    |                    |  |               |
| SCHEMATIC, M/B LA-1391   |                    |  |               |
| Size                     | Document Number    |  | Rev           |
|                          | 401211             |  | 1B            |
| Date:                    | 星期二, 十一月 226, 2002 |  | Sheet 9 of 44 |

(FW82801BA)

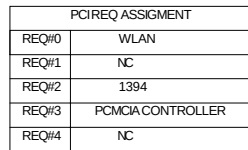
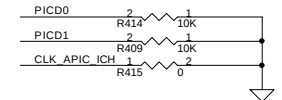
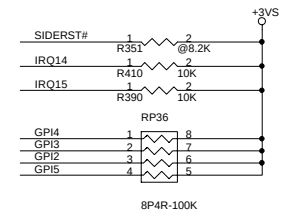
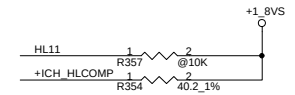
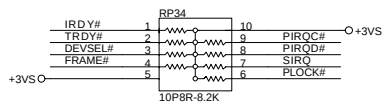


Figure 1: RP35 Pin Connections. The diagram shows a component labeled RP35 with pins 1 through 10. The connections are as follows:

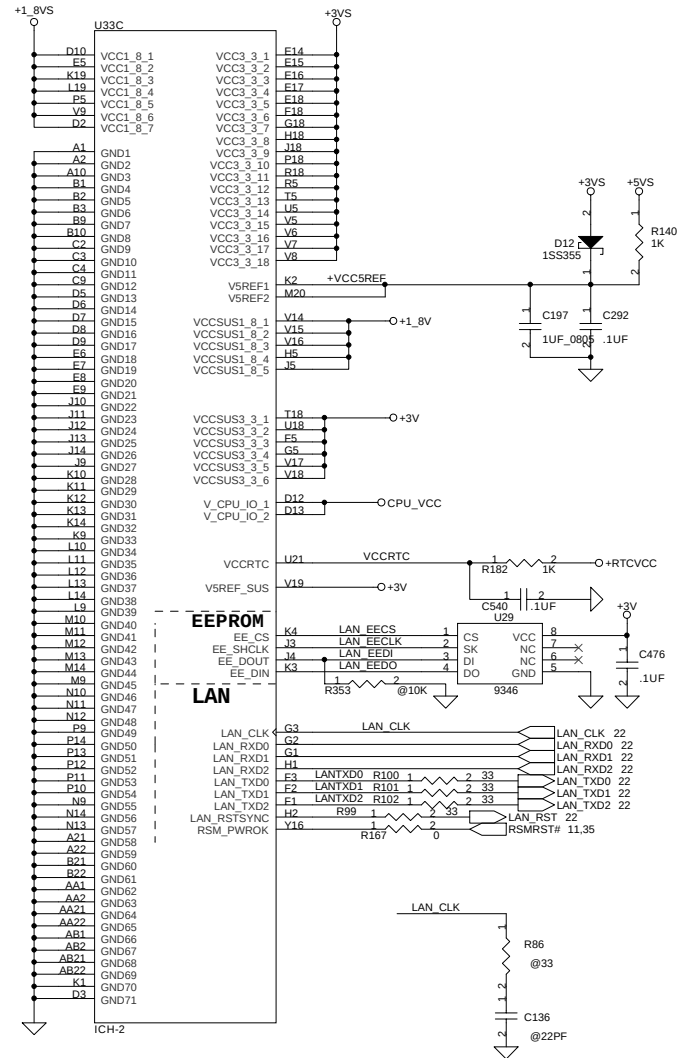
- Pin 1: PERR#
- Pin 2: REQA#
- Pin 3: STOP#
- Pin 4: SERR#
- Pin 5: +3VSO
- Pin 6: Ground (X)
- Pin 7: REQ#4
- Pin 8: PIROB#
- Pin 9: PIRQA#
- Pin 10: +3VS

The component is also labeled 10P8R-8.2K.

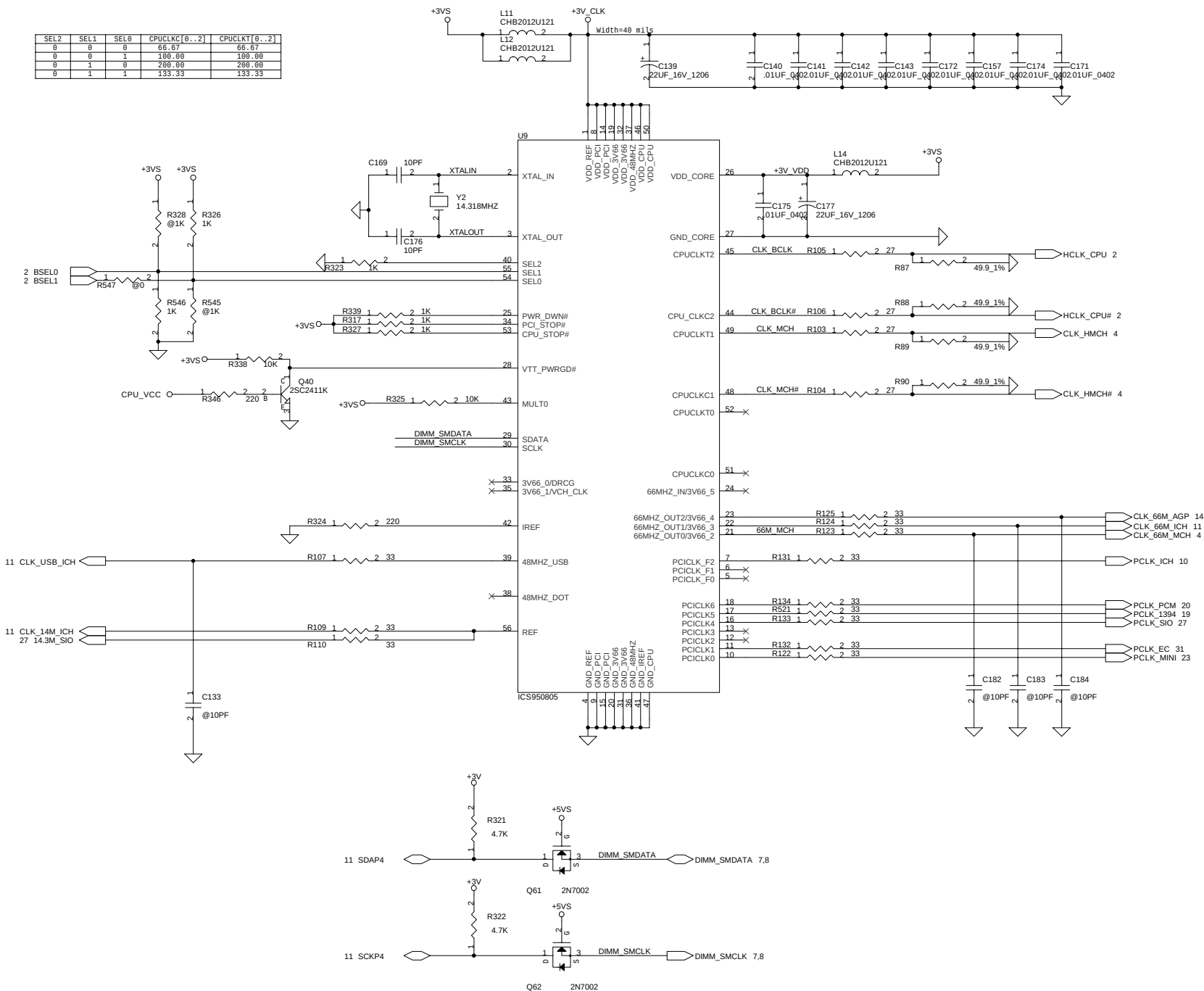


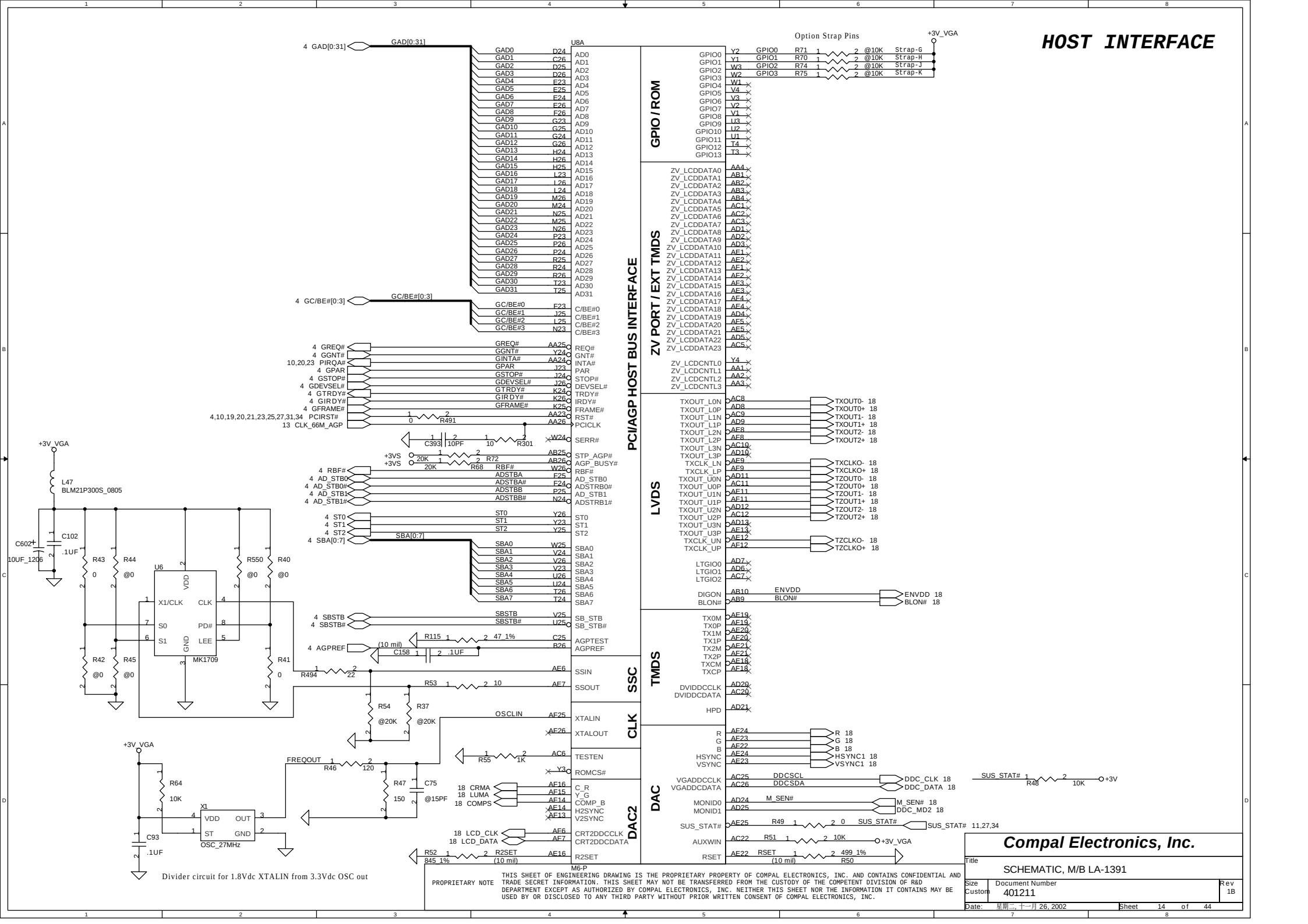


**Compal Electronics, Inc.**

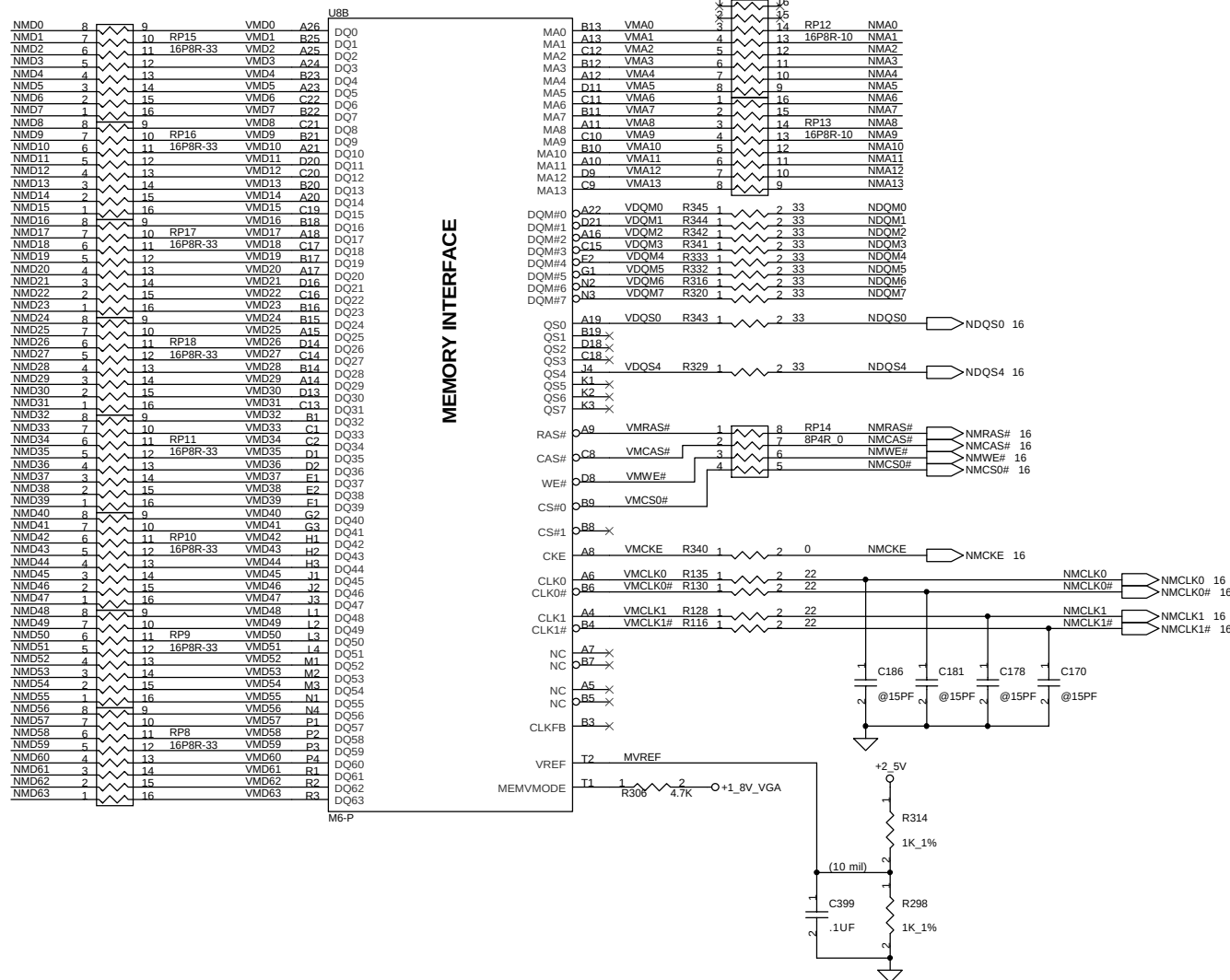


| SEL2 | SEL1 | SEL0 | CPUCLK[0..2] | CPUCLKT[0..2] |
|------|------|------|--------------|---------------|
| 0    | 0    | 0    | 66.67        | 66.67         |
| 0    | 0    | 1    | 100.00       | 100.00        |
| 0    | 1    | 0    | 200.00       | 200.00        |
| 0    | 1    | 1    | 133.33       | 133.33        |





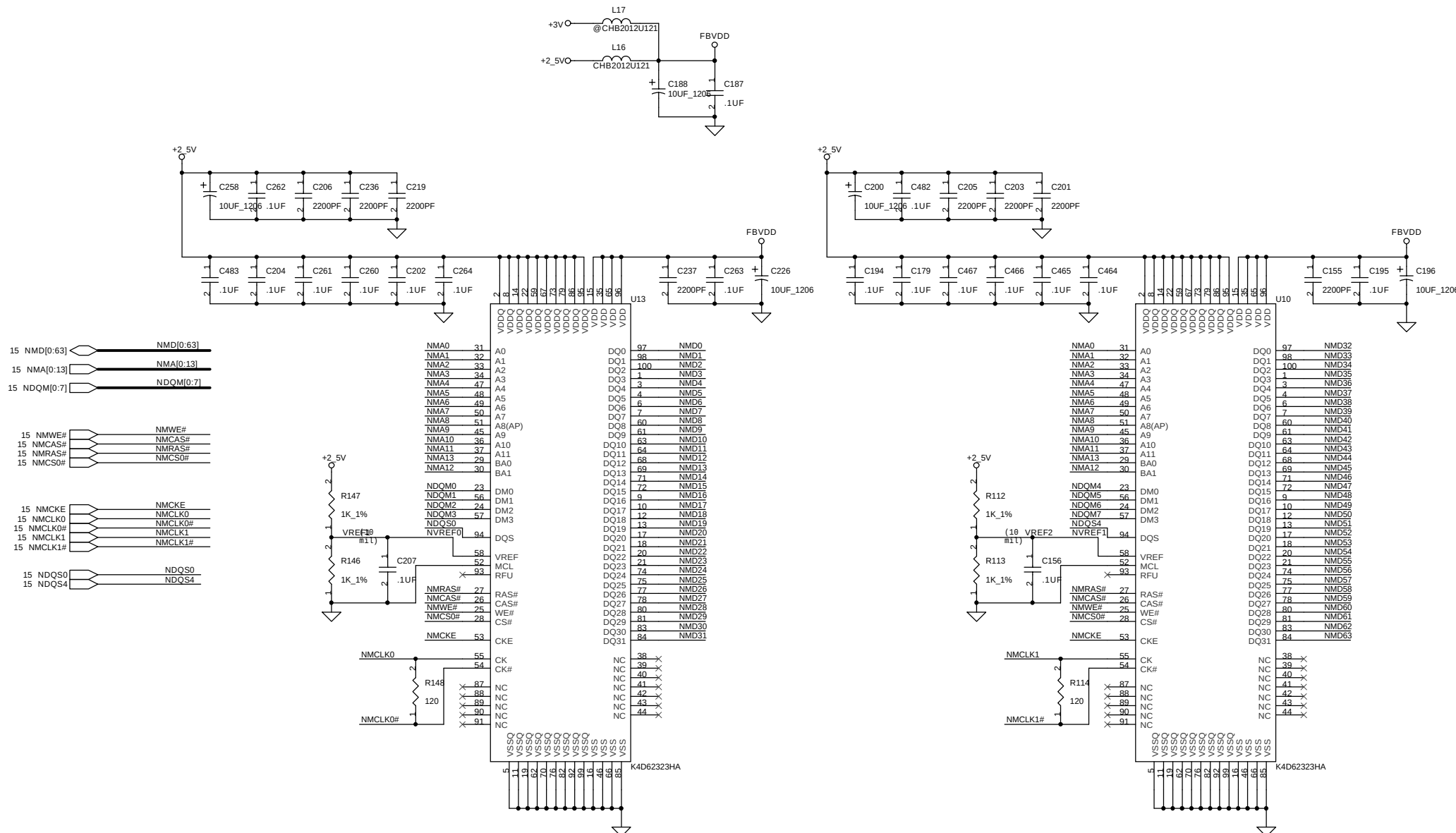
# MEMORY INTERFACE



PROPRIETARY NOTE THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

|                                 |                        |        |
|---------------------------------|------------------------|--------|
| <b>Compal Electronics, Inc.</b> |                        |        |
| Title SCHEMATIC, M/B LA-1391    |                        |        |
| Size Custom                     | Document Number 401211 | Rev 18 |
| Date: 星期二, 十一月 26, 2002         | Sheet 15               | of 44  |

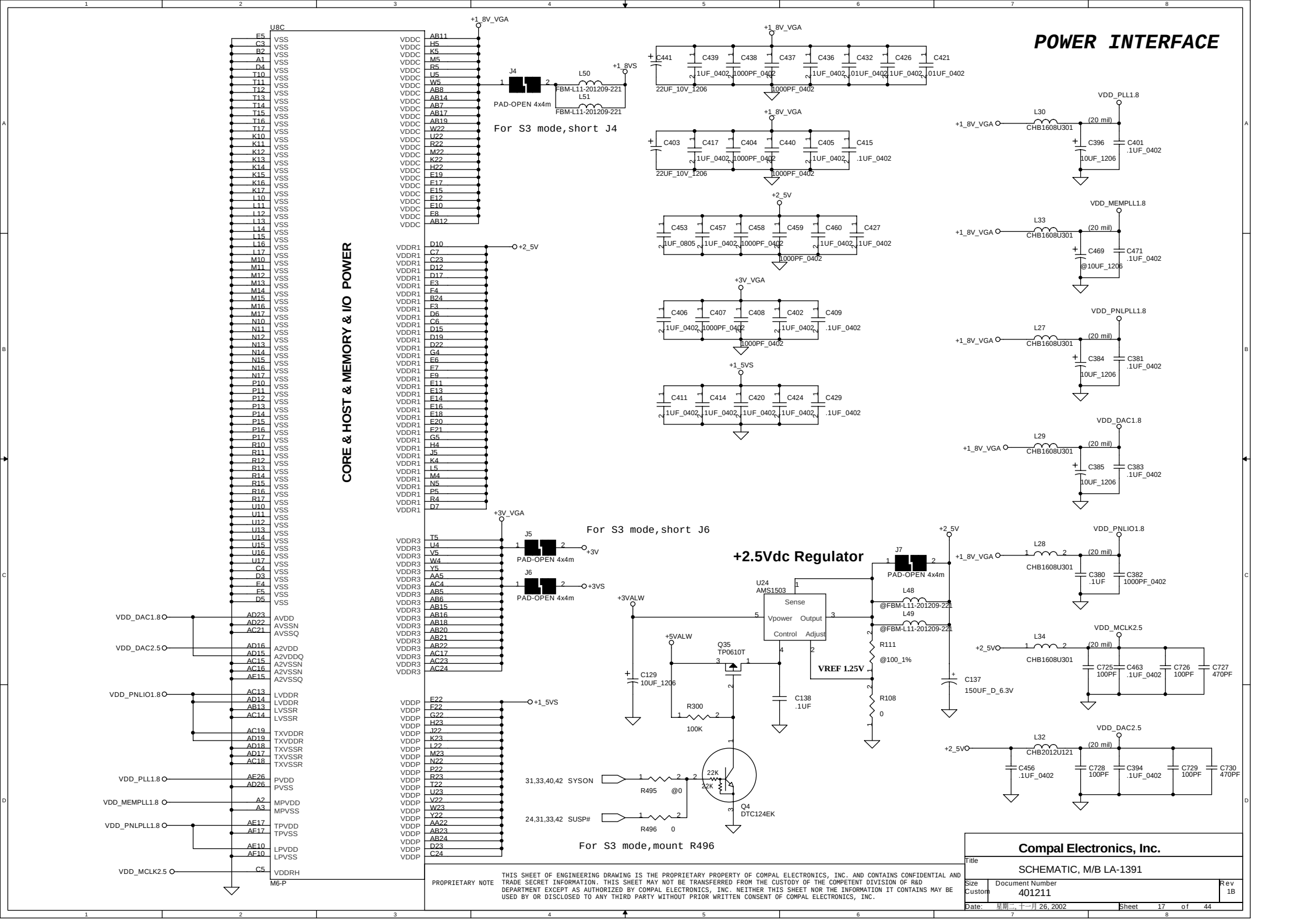
# DDR SDRAM

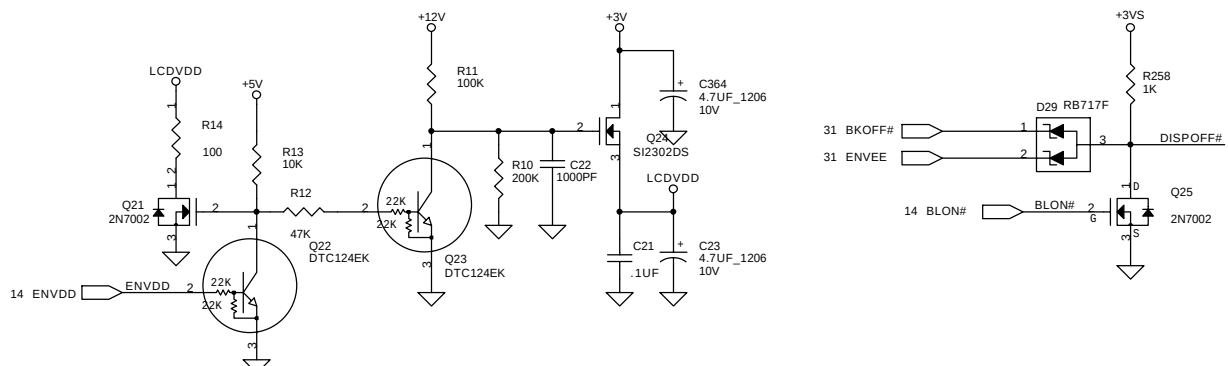
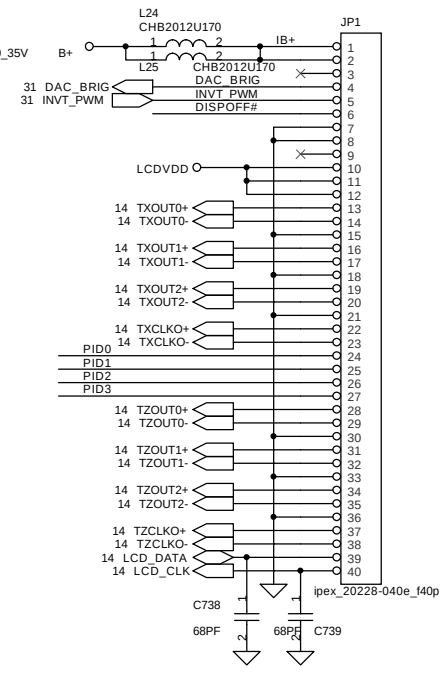
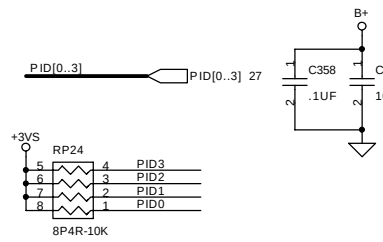
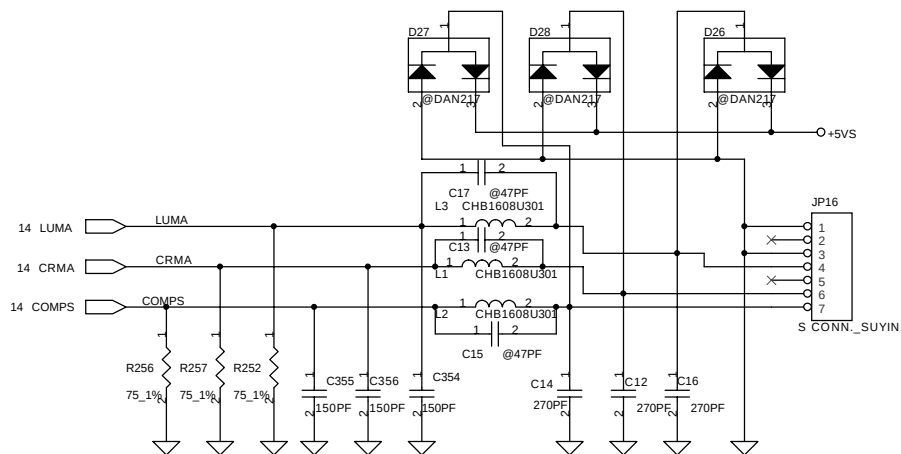


**Compal Electronics, Inc.**

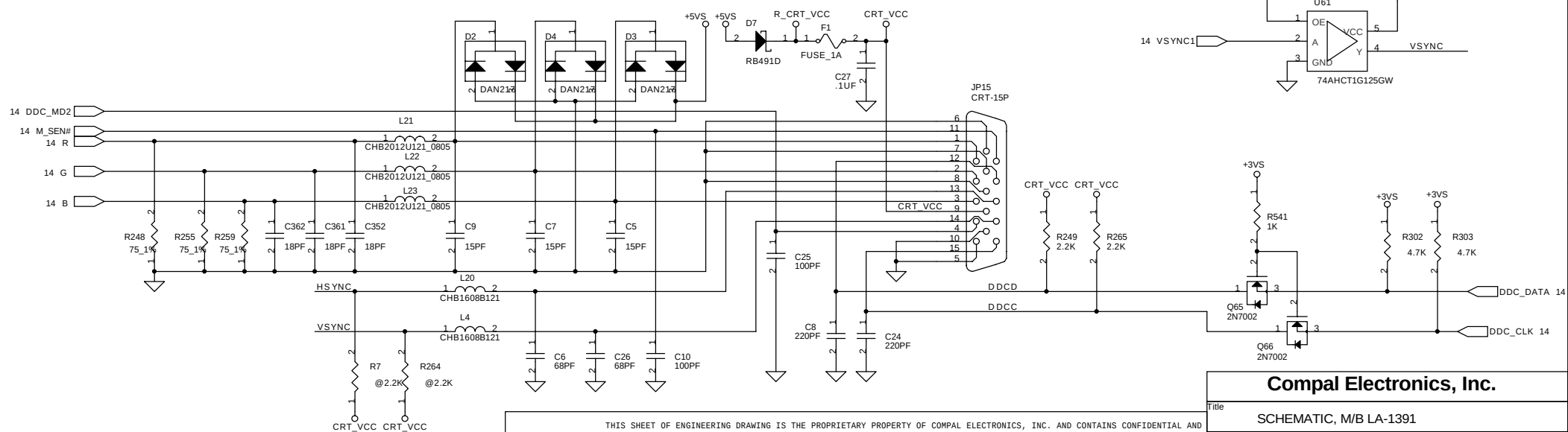
|        |                   |       |                        |       |
|--------|-------------------|-------|------------------------|-------|
| Title  |                   |       | SCHEMATIC, M/B LA-1391 |       |
| Size   | Document Number   |       |                        | Rev   |
| Custom | 401211            |       |                        | 18    |
| Date:  | 星期二, 十一月 26, 2002 | Sheet | 16                     | of 44 |

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.





## CRT Connector



**Compal Electronics, Inc.**

SCHEMATIC, M/B LA-1391

|       |                 |                |
|-------|-----------------|----------------|
| Size  | Document Number | Rev            |
| B     | 401211          | 18             |
| Date: | 2002.12.26      | Sheet 18 of 44 |

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

10,20,23 AD[0..31] AD[0..31]

U57  
TSB43AB22

TSB43AB22

PCI BUS INTERFACE

PHY PORT 2

BIAS CURRENT

OSCILLATOR

FILTER

EEPROM 2 WIRE BUS

POWER CLASS

PHY PORT 1

AD31 22 PCI\_AD31  
AD30 24 PCI\_AD30  
AD29 25 PCI\_AD29  
AD28 26 PCI\_AD28  
AD27 28 PCI\_AD27  
AD26 29 PCI\_AD26  
AD25 31 PCI\_AD25  
AD24 32 PCI\_AD24  
AD23 37 PCI\_AD23  
AD22 38 PCI\_AD22  
AD21 40 PCI\_AD21  
AD20 41 PCI\_AD20  
AD19 42 PCI\_AD19  
AD18 43 PCI\_AD18  
AD17 45 PCI\_AD17  
AD16 46 PCI\_AD16  
AD15 61 PCI\_AD15  
AD14 63 PCI\_AD14  
AD13 65 PCI\_AD13  
AD12 66 PCI\_AD12  
AD11 67 PCI\_AD11  
AD10 69 PCI\_AD10  
AD9 70 PCI\_AD9  
AD8 71 PCI\_AD8  
AD7 74 PCI\_AD7  
AD6 76 PCI\_AD6  
AD5 77 PCI\_AD5  
AD4 79 PCI\_AD4  
AD3 80 PCI\_AD3  
AD2 81 PCI\_AD2  
AD1 82 PCI\_AD1  
AD0 84 PCI\_AD0  
C/BE#3 34 PCI\_C/BE3  
C/BE#2 47 PCI\_C/BE2  
C/BE#1 60 PCI\_C/BE1  
C/BE#0 73 PCI\_C/BE0  
PCLK 1394 16 PCI\_CLK  
GNT#2 18 PCI\_GNT  
REQ#2 19 PCI\_REQ  
1394 IDSEL 36 PCI\_IDSEL  
FRAME# 49 PCI\_FRAME  
IRDY# 50 PCI\_IRDY  
TRDY# 52 PCI\_TRDY  
DEVSEL# 53 PCI\_DEVSEL  
STOP# 54 PCI\_STOP  
PERR# 56 PCI\_PERR  
PIRQC# 13 PCI\_INTA/CINT  
31 1394 PME# 21 PCI\_PME/CSTSCHG  
SERR# 57 PCI\_SERR  
PAR 58 PCI\_PAR  
11,20,23,27,31 CLKRUN# 12 PCI\_CLKRUN  
4,10,14,20,21,23,25,27,31,34 PCIRST# 85 PCI\_RST

20,21,23 CBRST# 14 G\_RST  
GPIO3 89  
GPIO2 90  
PCLK 1394  
R518 220  
R519 220  
R520 @22  
C629 @10PF

For TSB43AA22  
C627,C628  
change to 0  
ohm to short  
to GND

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

+3V  
C606 .01UF  
C607 .01UF  
C608 .01UF  
C609 .01UF  
C610 .01UF  
C611 .1UF  
C612 .1UF  
C613 .1UF  
C614 .1UF  
+3V  
C615 .1UF  
C616 .1UF  
C617 .1UF  
C618 .1UF

L52 0.0805  
C619 .01UF  
C620 4.7UF\_0805

Y4 24.576 MHz  
C622 15PF  
C623 15PF

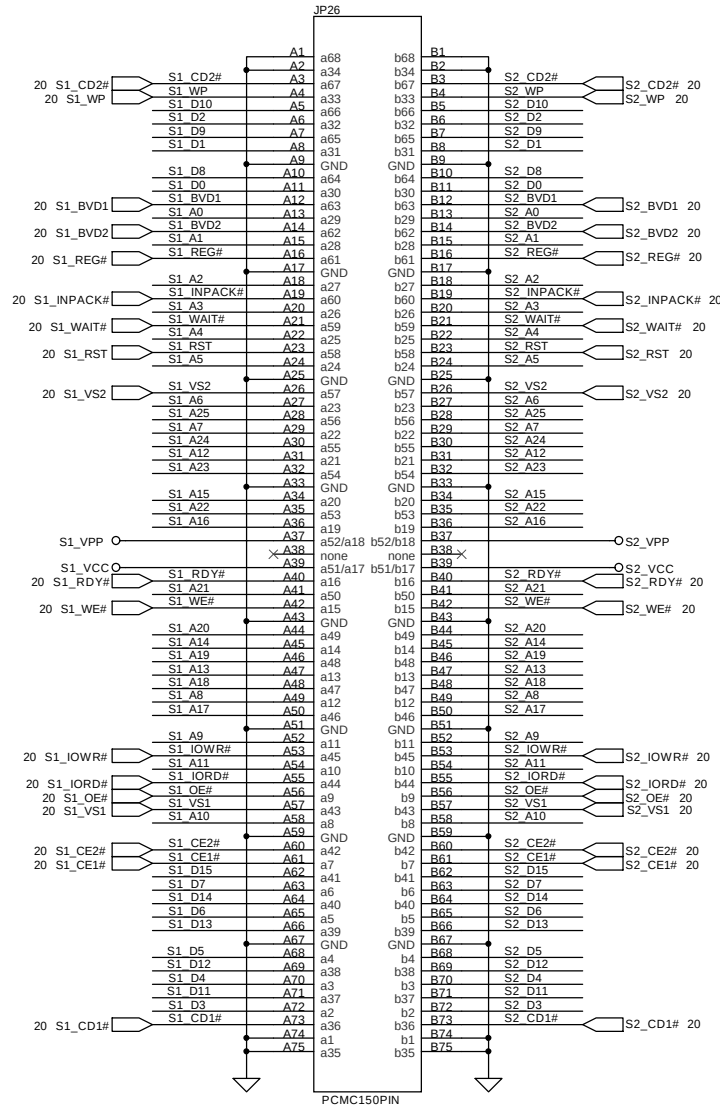
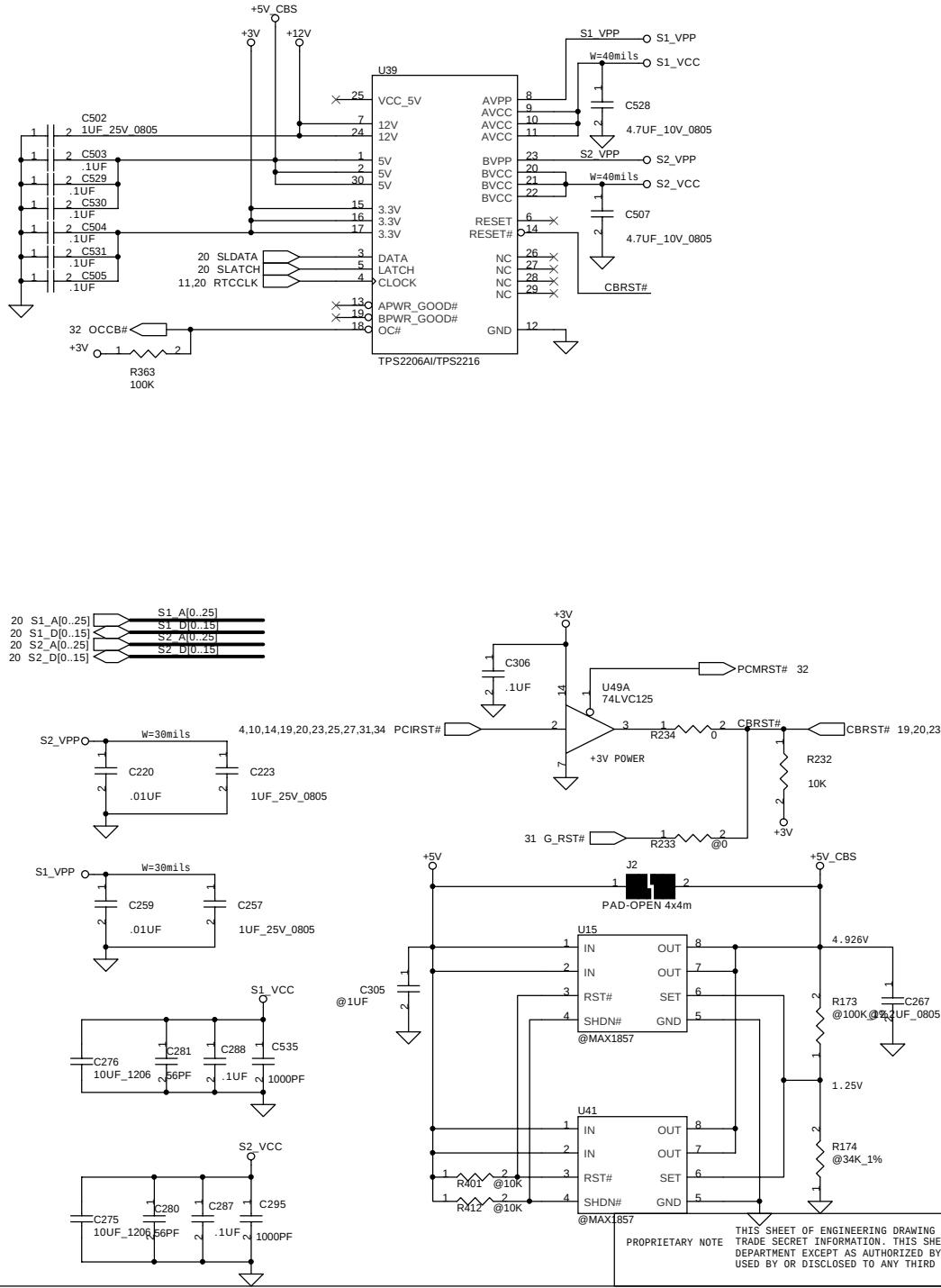
R507 56.2 1%\_0603  
R508 56.2 1%\_0603  
C625 0.33UF\_0603  
R510 56.2 1%\_0603  
R511 5.11K 1%\_0603  
R515 56.2 1%\_0603  
C626 220PF

JP28  
TPB0- 1  
TPB0+ 2  
TPA0- 3  
TPA0+ 4  
Molex SD-54030-0411



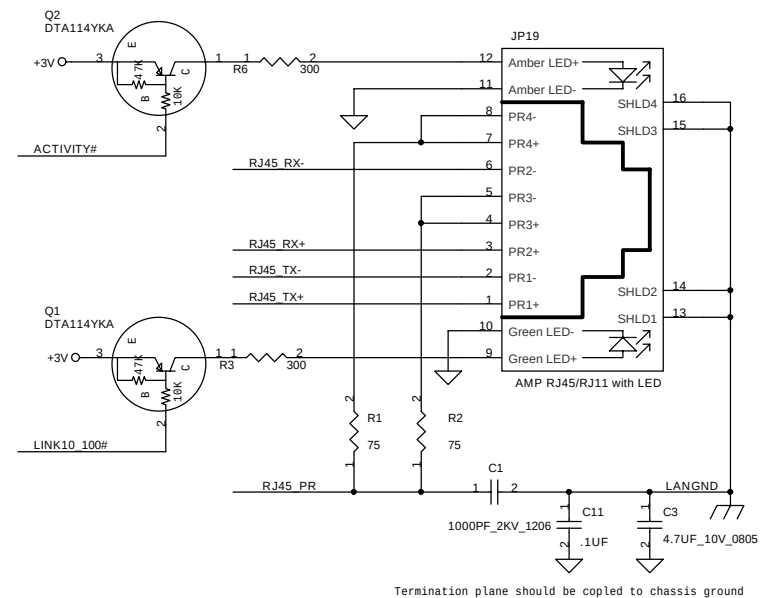
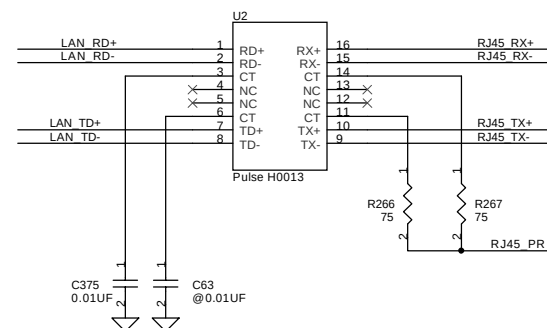
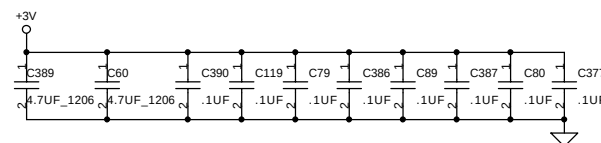
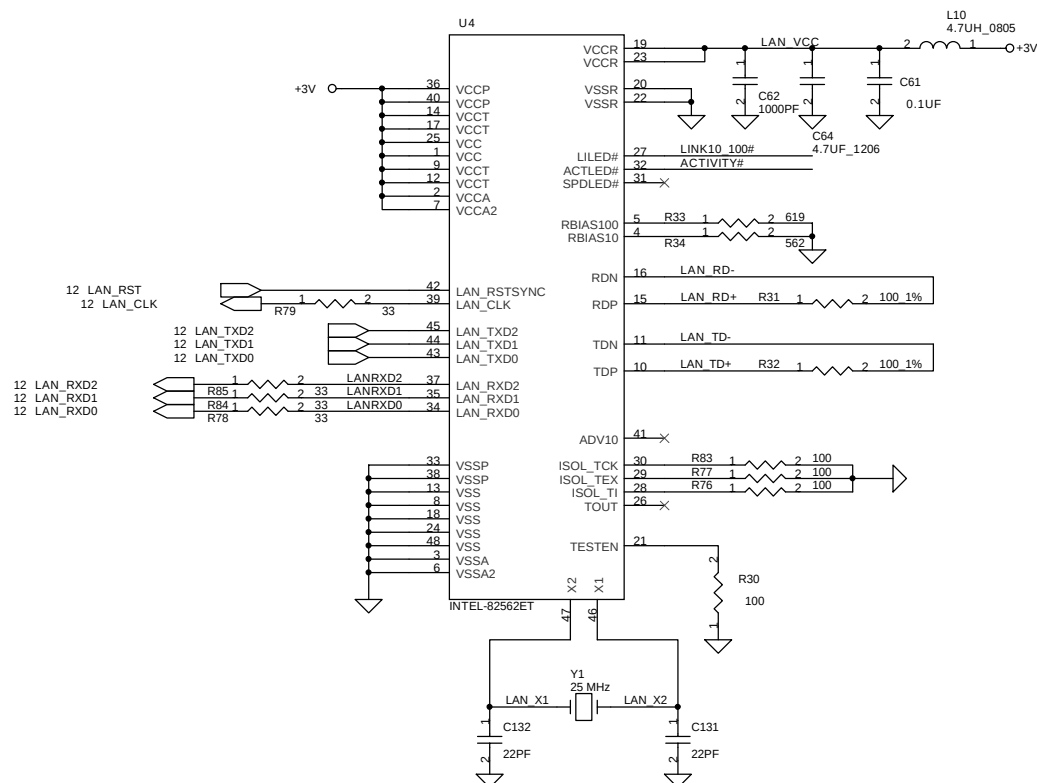
PCMCIA POWER CTRL.

CARDBUS  
SOCKET



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

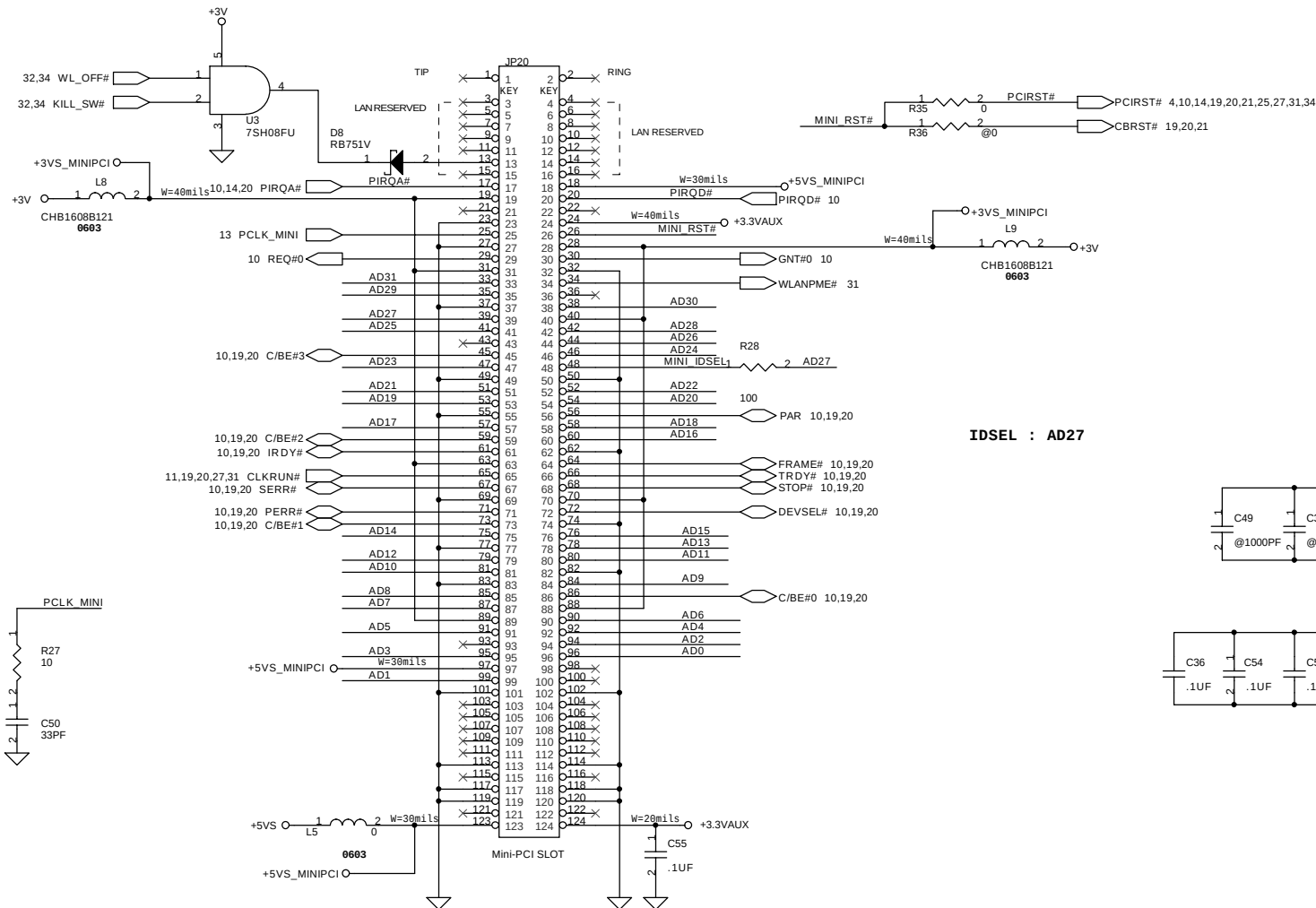
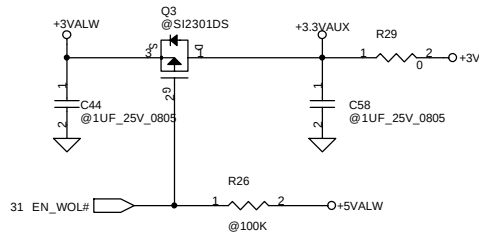
|                          |                   |        |          |
|--------------------------|-------------------|--------|----------|
| Compal Electronics, Inc. |                   |        |          |
| Title                    |                   |        |          |
| SCHEMATIC, M/B LA-1391   |                   |        |          |
| Size B                   | Document Number   | Rev 1B |          |
| 401211                   |                   |        |          |
| Date:                    | -F期二、十一月 26, 2002 | Sheet  | 21 of 44 |



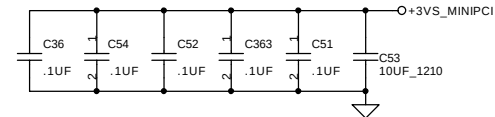
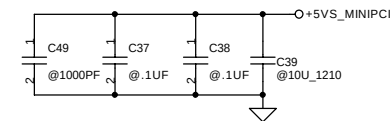
**Compal Electronics, Inc.**

|       |                    |        |                        |       |
|-------|--------------------|--------|------------------------|-------|
| Title |                    |        | SCHEMATIC, M/B LA-1391 |       |
| Size  | Document Number    | 401211 |                        | Rev   |
| B     |                    |        |                        | 1B    |
| Date: | ~P期二, 十一月 26, 2002 | Sheet  | 22                     | of 44 |

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



IDSEL : AD27

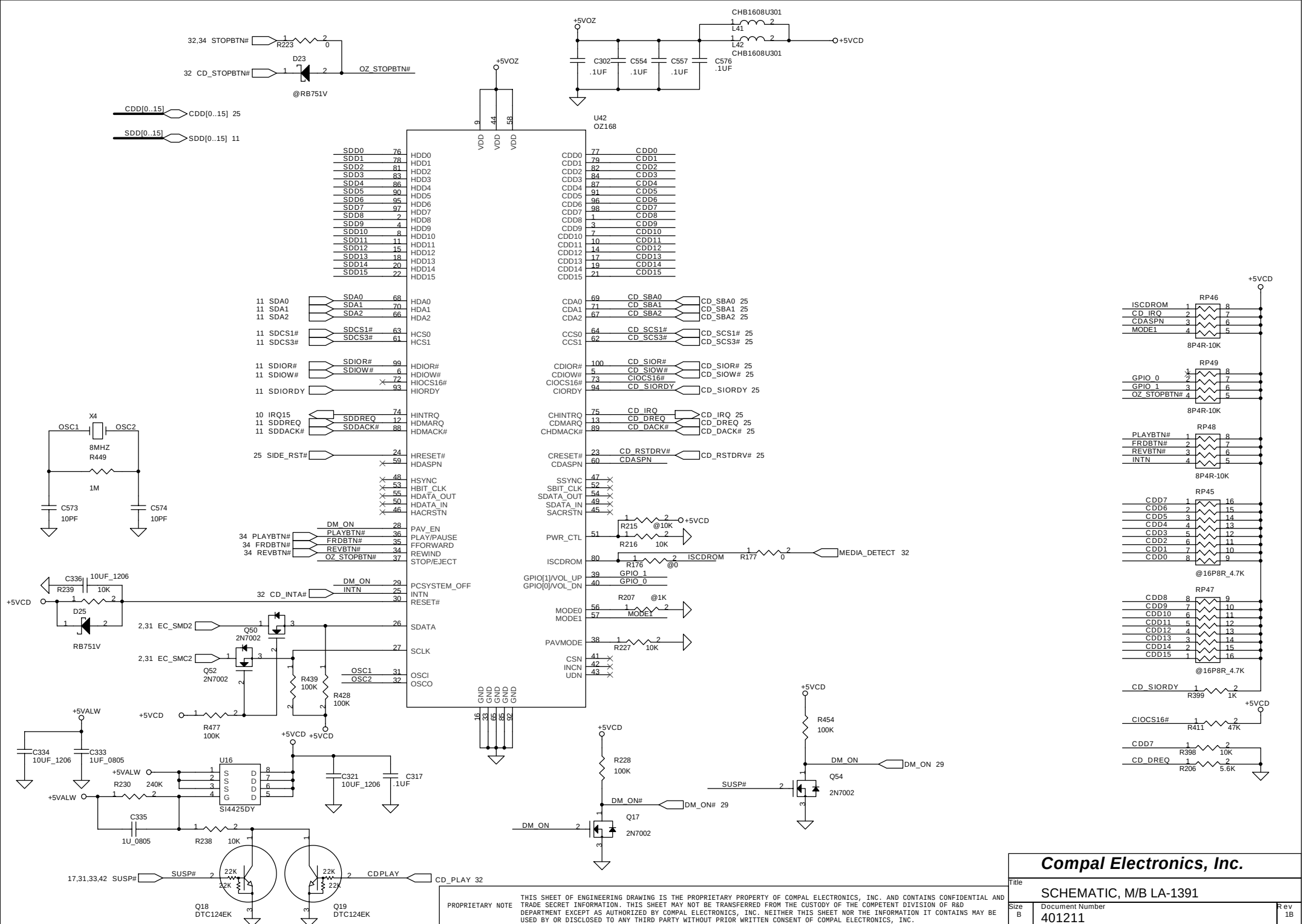


AD[0..31] AD[0..31] 10,19,20

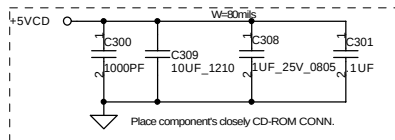
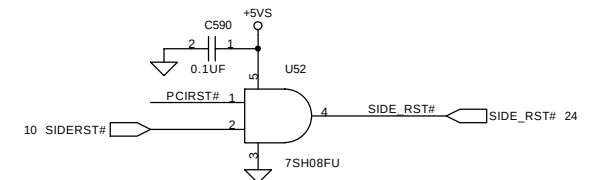
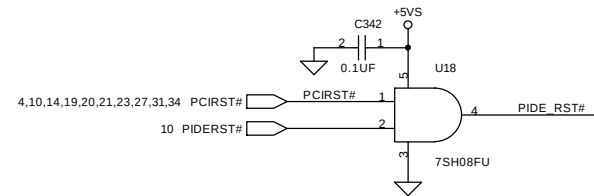
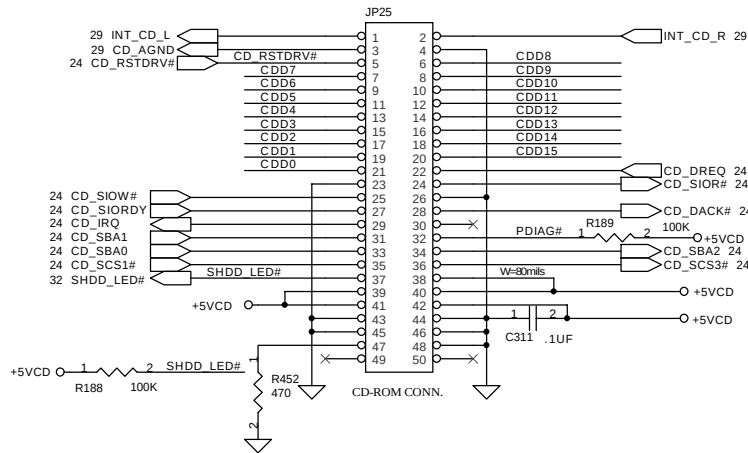
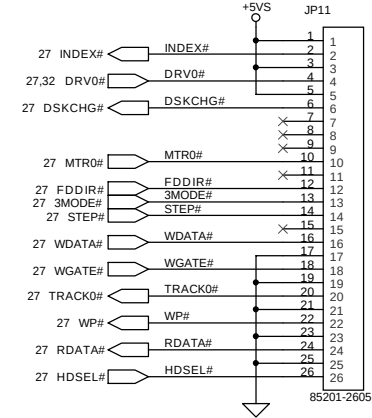
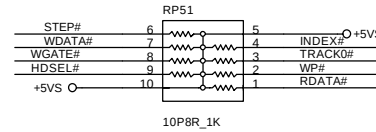
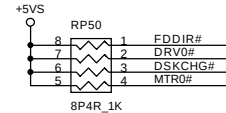
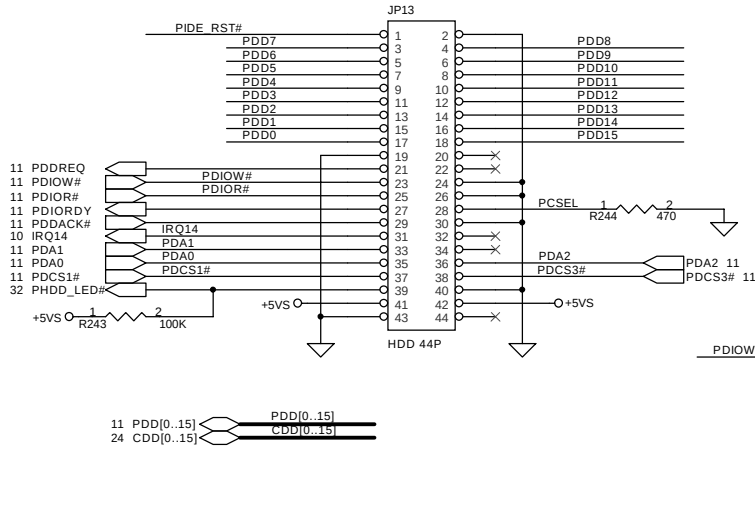
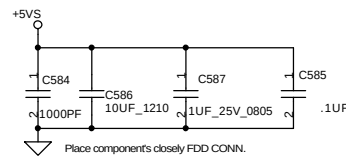
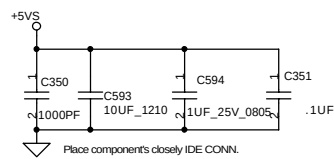
PROPRIETARY NOTE THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

**Compal Electronics, Inc.**

|        |                   |                        |          |
|--------|-------------------|------------------------|----------|
| Title  |                   | SCHEMATIC, M/B LA-1391 |          |
| Size B | Document Number   | 401211                 |          |
| Date:  | ~P期二,十一月 26, 2002 | Sheet                  | 23 of 44 |
|        |                   | Rev                    | 1B       |



# IDE,CD-ROM Module CONN.



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

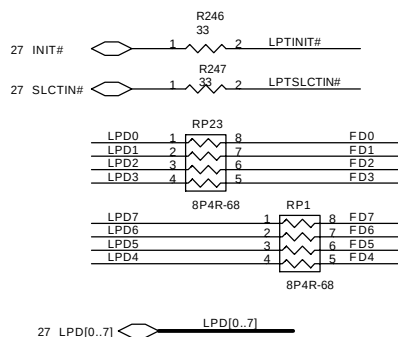
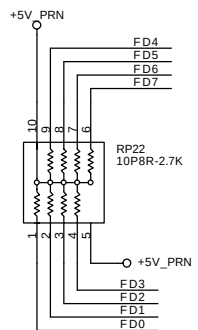
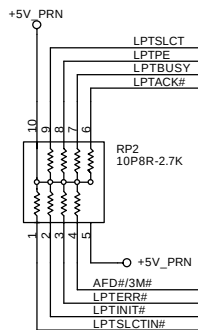
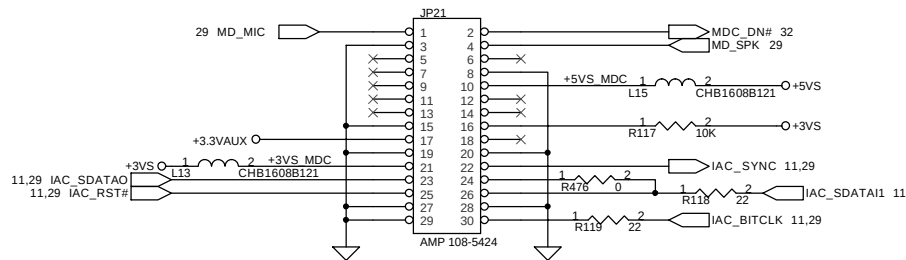
Compal Electronics, Inc.

SCHEMATIC, M/B LA-1391

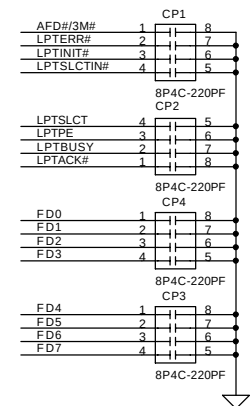
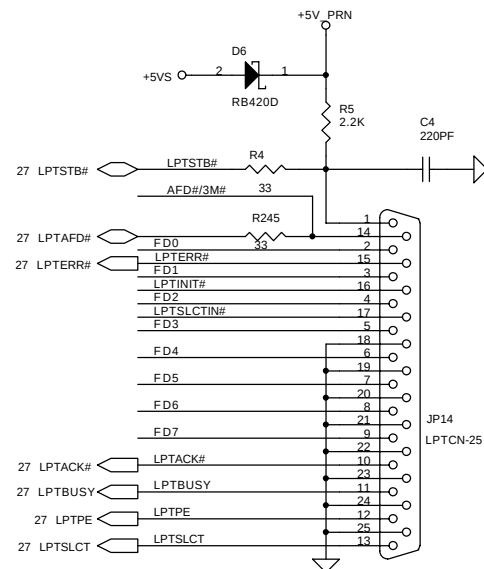
Document Number  
401211

Rev  
1B

Date: 2002.11.26 Sheet 25 of 44



## PARALLEL PORT



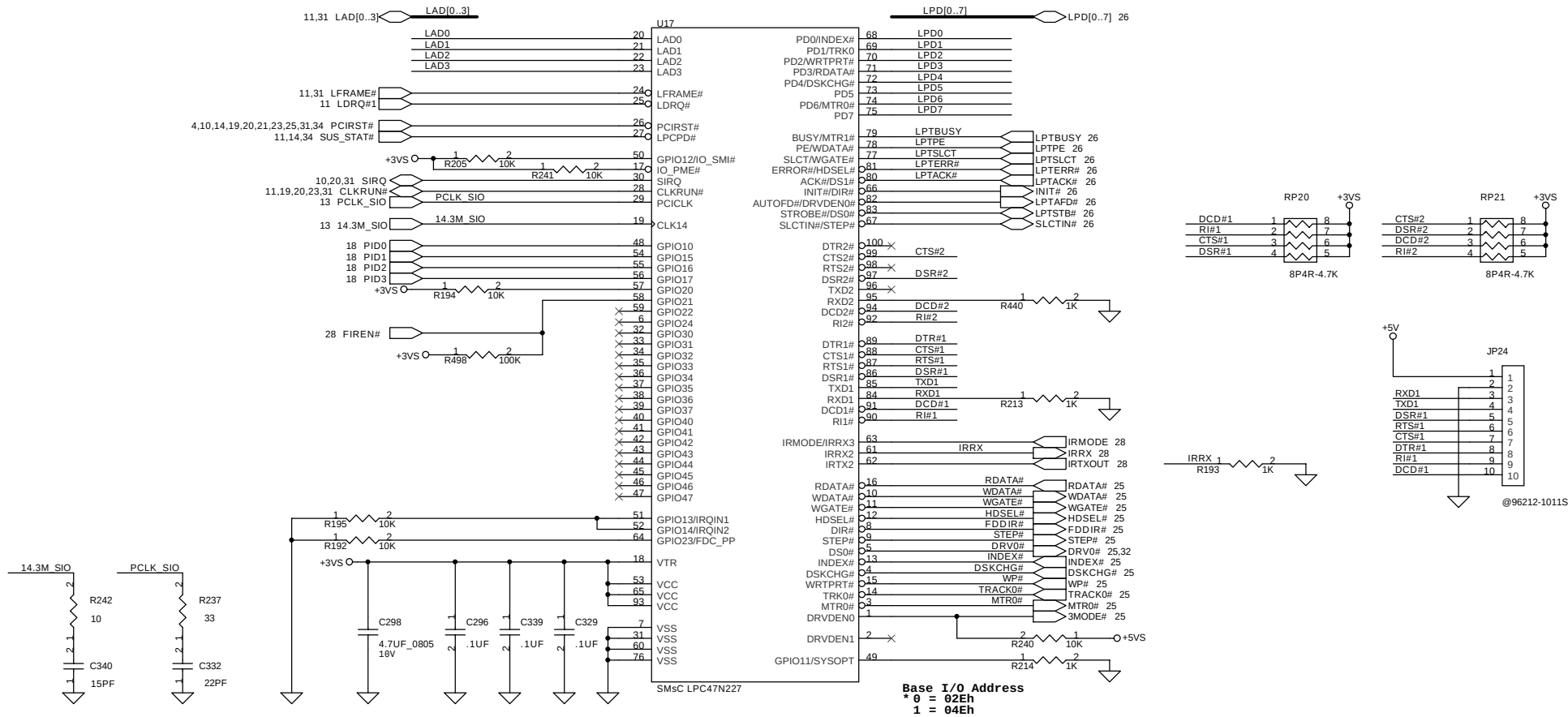
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

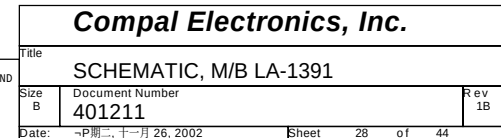
**Compal Electronics, Inc.**

SCHEMATIC, M/B LA-1391

|        |                 |                |
|--------|-----------------|----------------|
| Size   | Document Number | Rev            |
| 401211 |                 | 1B             |
| Date:  | ~P期二,十一月26,2002 | Sheet 26 of 44 |

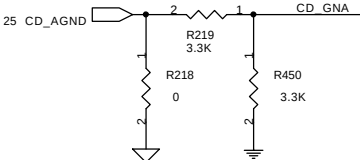
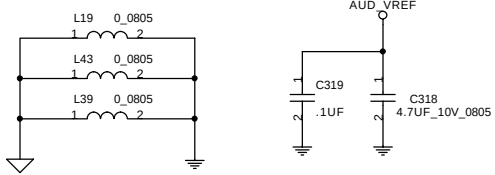
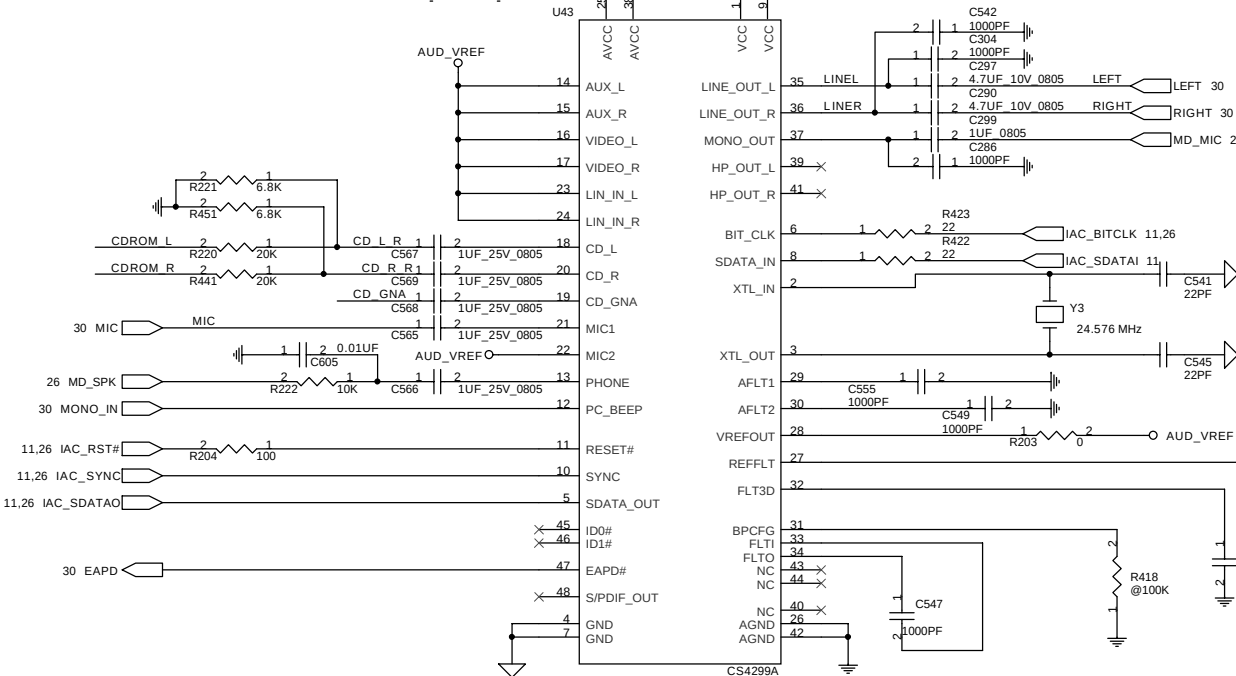
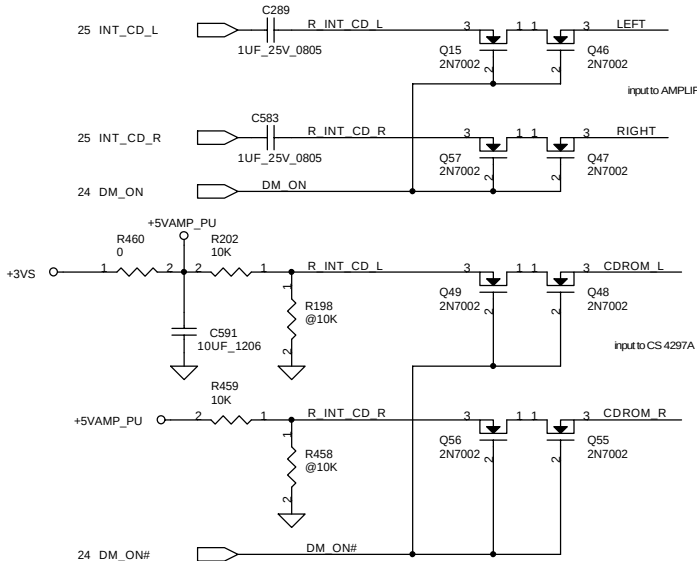
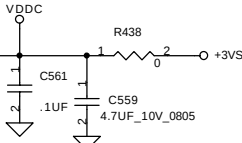
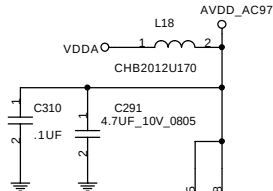
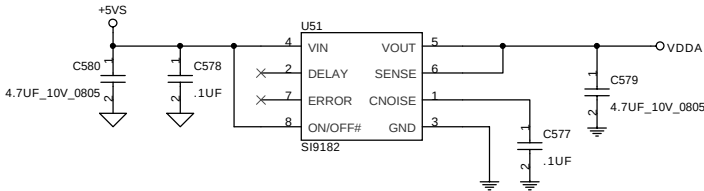
SUPER I/O SMsC FDC47N227





PROPRIETARY NOTE THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

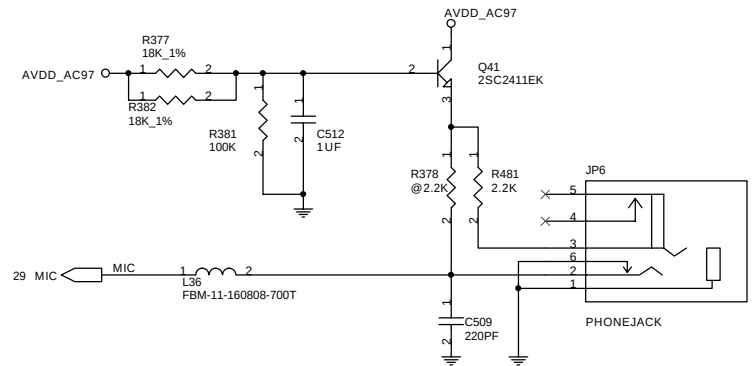
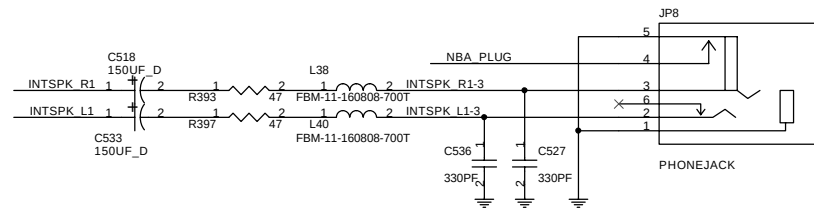
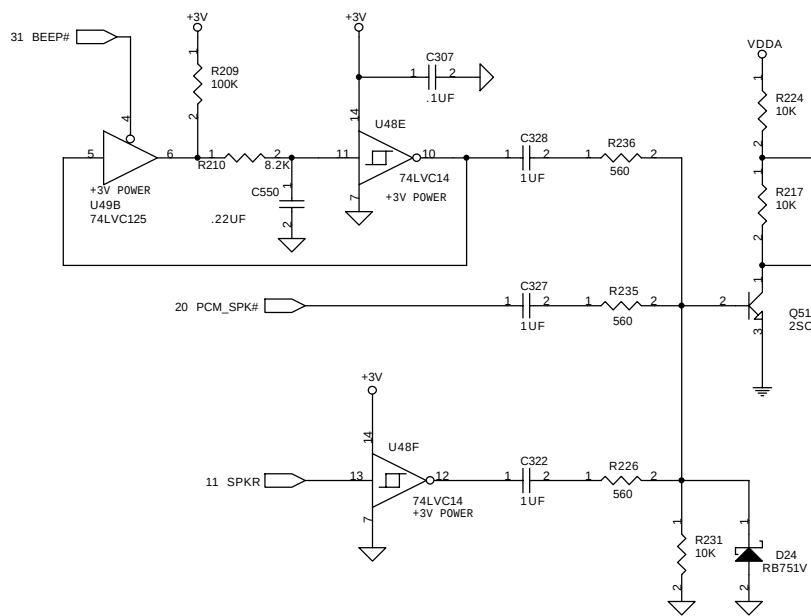
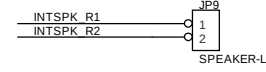
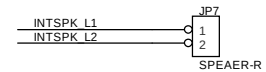
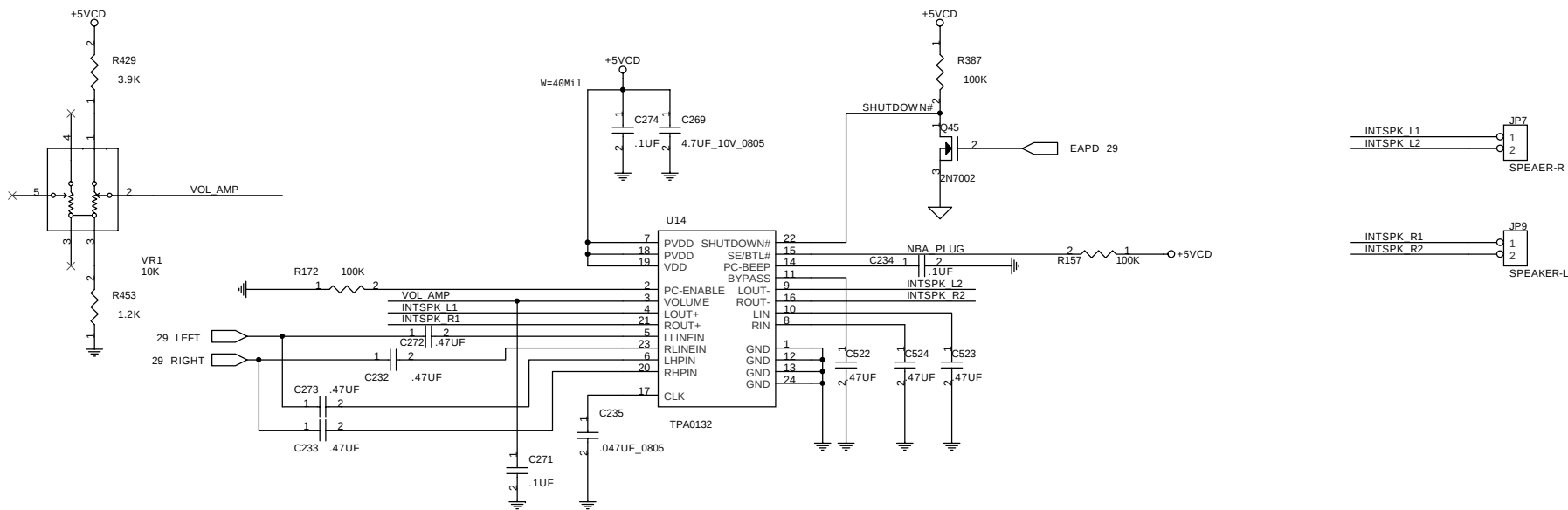
AC97 Codec



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

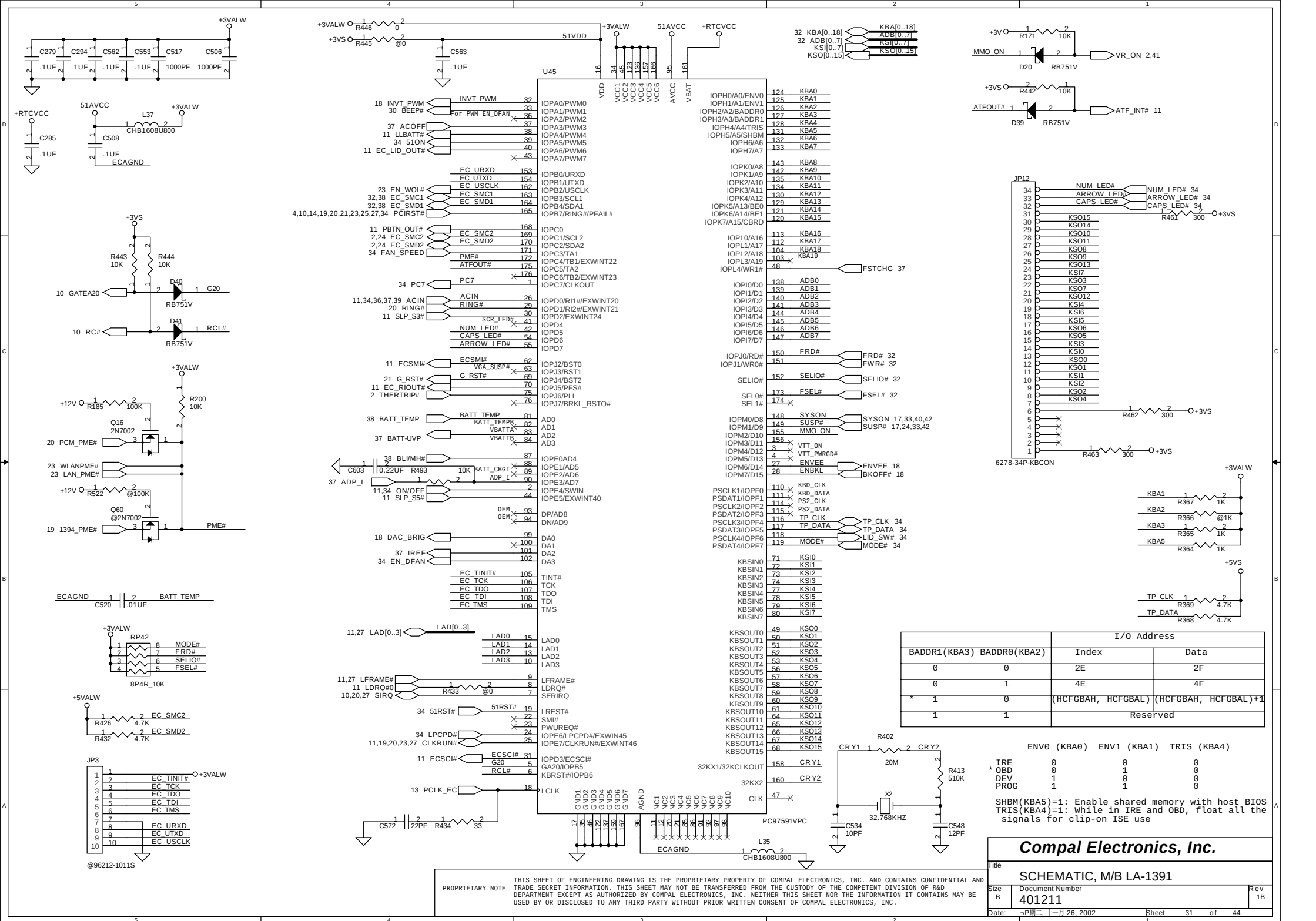
| Compal Electronics, Inc. |                 |                 |                |
|--------------------------|-----------------|-----------------|----------------|
| Title                    |                 |                 |                |
| SCHEMATIC, M/BLA-1391    |                 |                 |                |
| Size                     | Document Number | Rev             |                |
| B                        | 401211          | 18              |                |
| Date:                    |                 | ~P期二,十一月26,2002 | Sheet 29 of 44 |

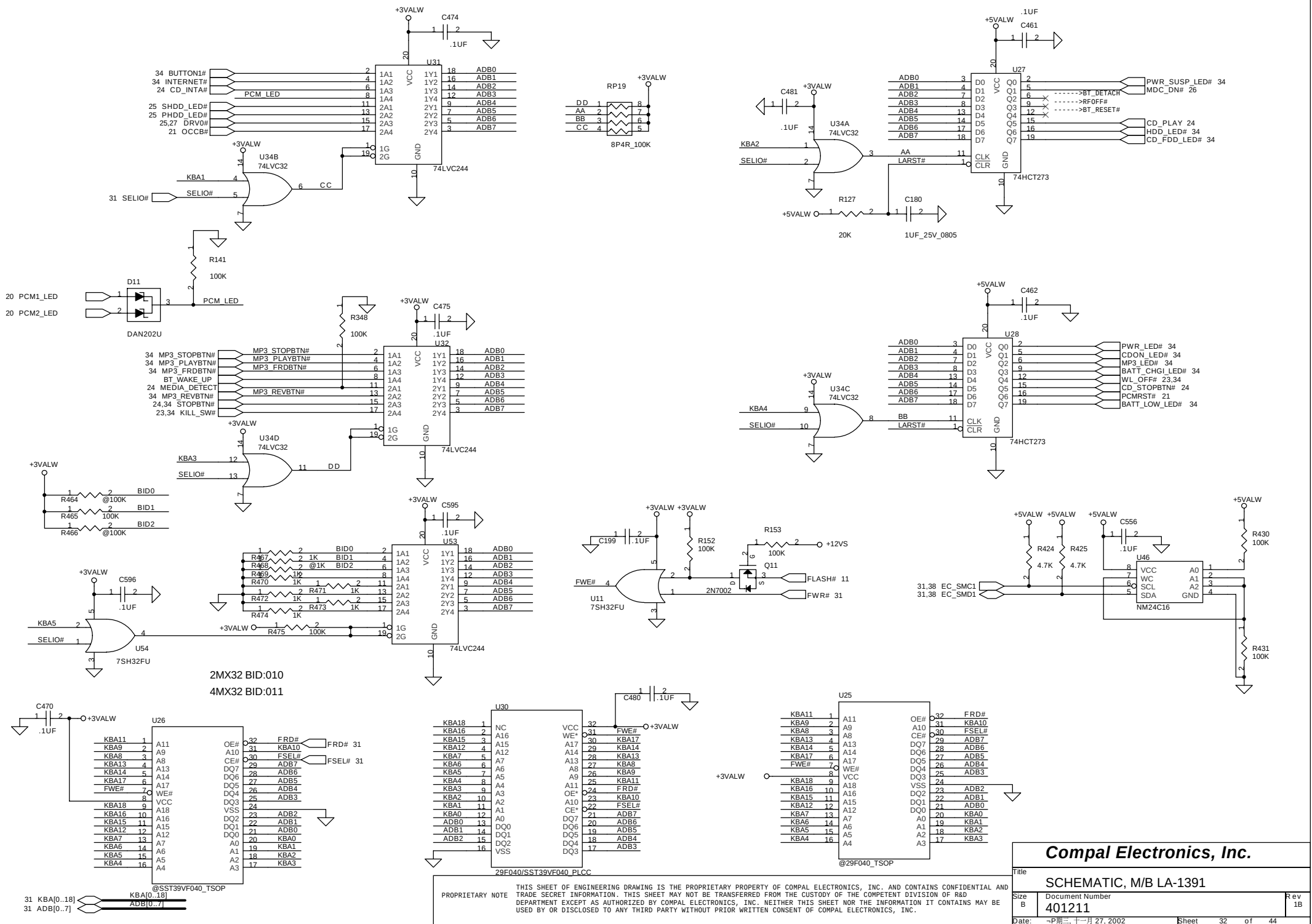
# AMP & Audio Jack

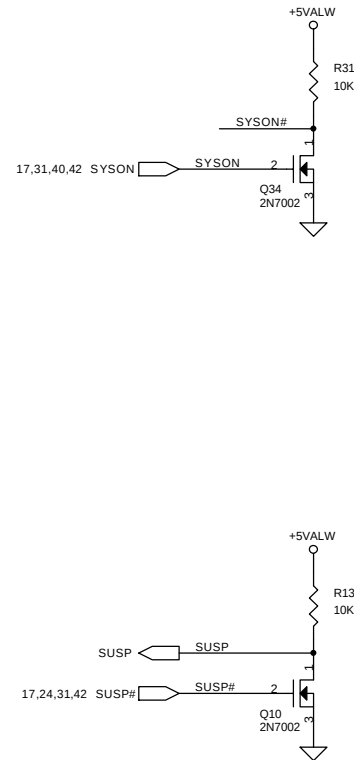
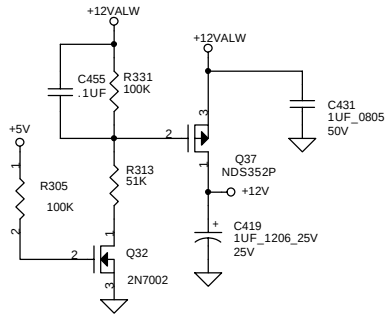
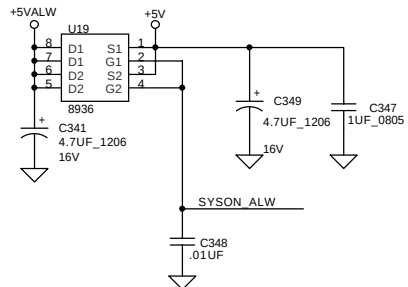
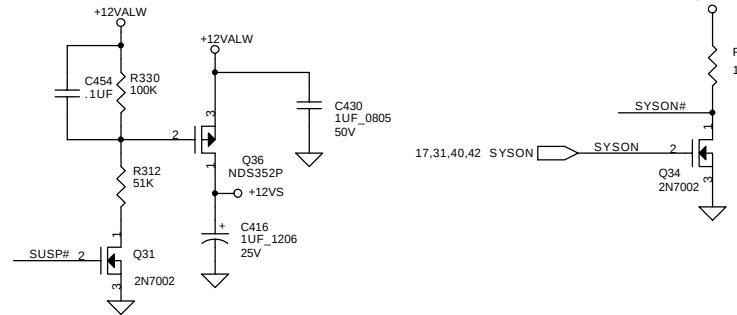
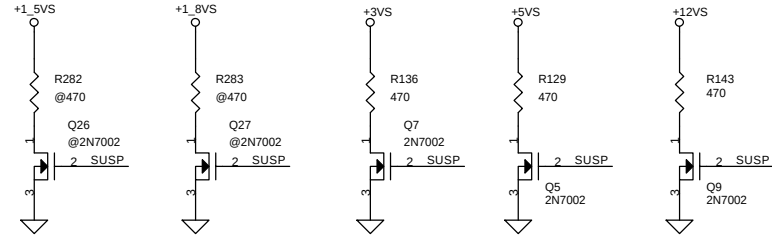
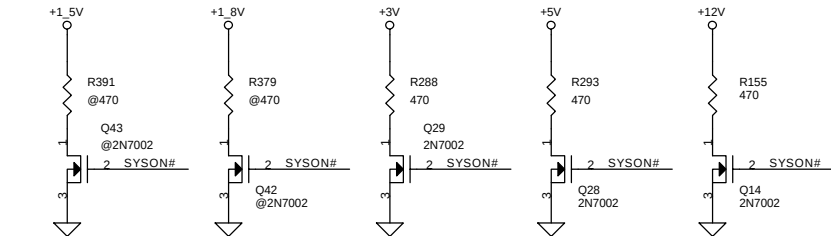
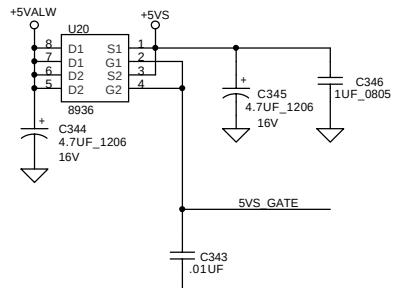
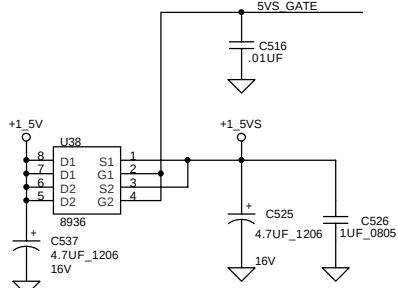
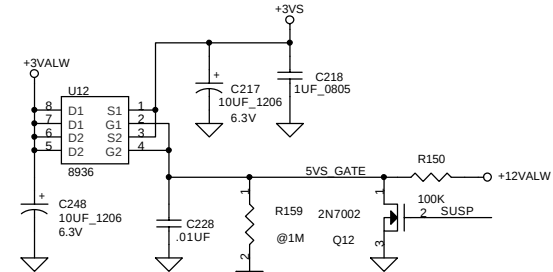
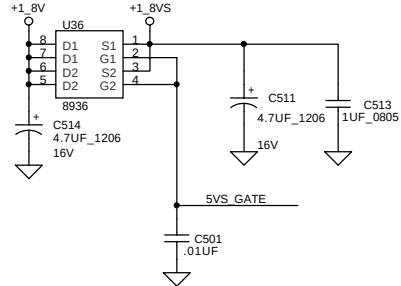
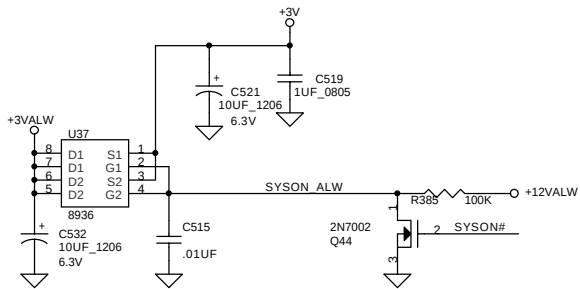


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

|                           |                        |        |
|---------------------------|------------------------|--------|
| Compal Electronics, Inc.  |                        |        |
| SCHEMATIC, M/B LA-1391    |                        |        |
| Size B                    | Document Number 401211 | Rev 1B |
| Date: ~P 周二, 十二月 26, 2002 | Sheet 30               | of 44  |



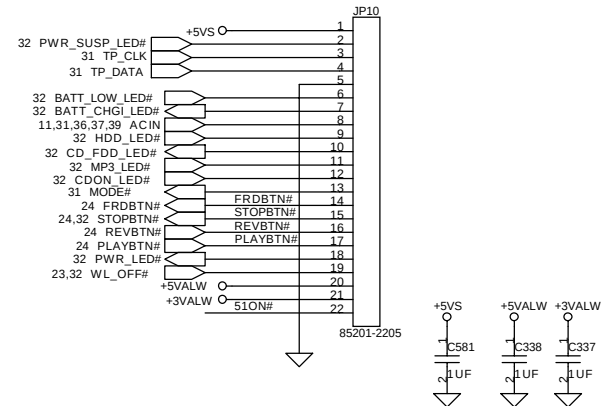
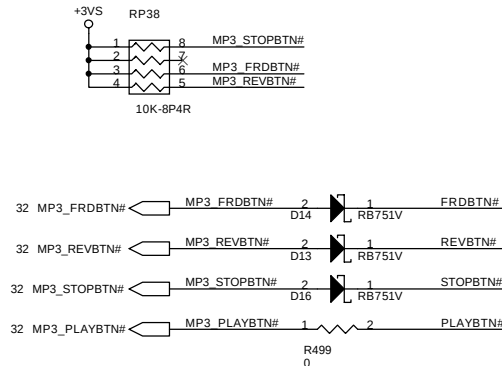
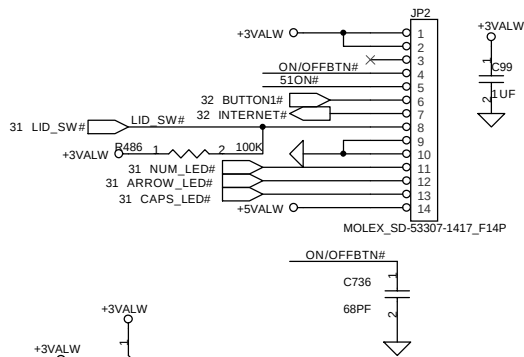




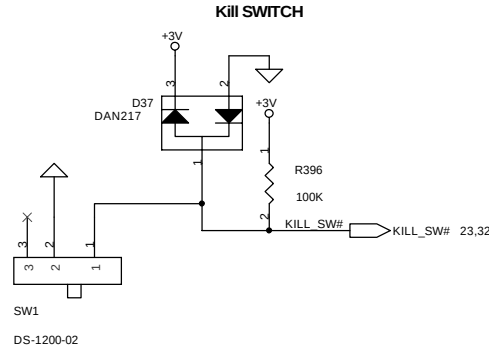
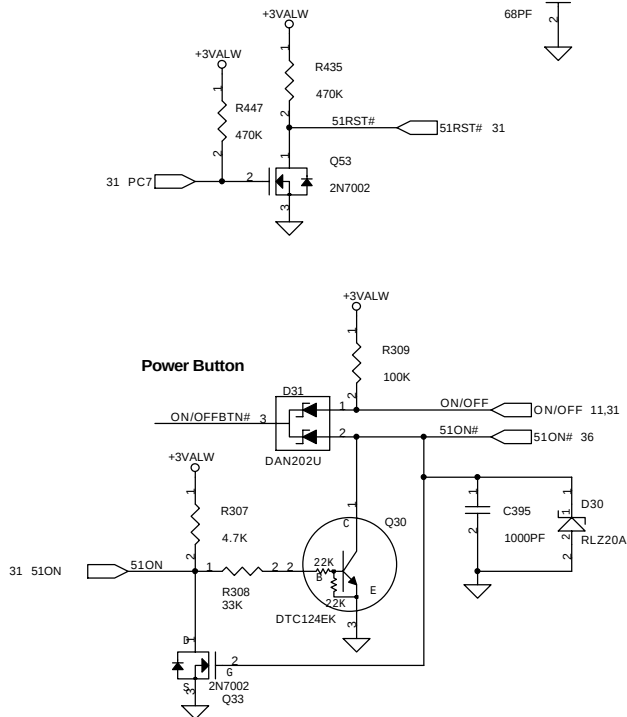
**Compal Electronics, Inc.**

|                        |                 |  |                |
|------------------------|-----------------|--|----------------|
| Title                  |                 |  |                |
| SCHEMATIC, M/B LA-1391 |                 |  |                |
| Size                   | Document Number |  | Rev            |
| B                      | 401211          |  | 1B             |
| Date:                  | ~P期二,十一月26,2002 |  | Sheet 33 of 44 |

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



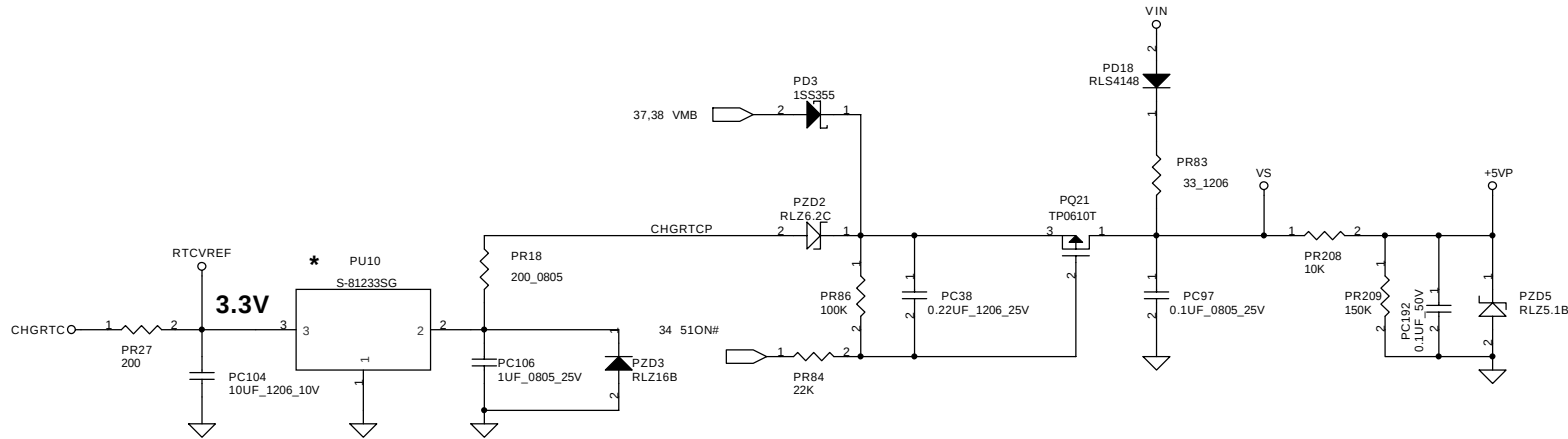
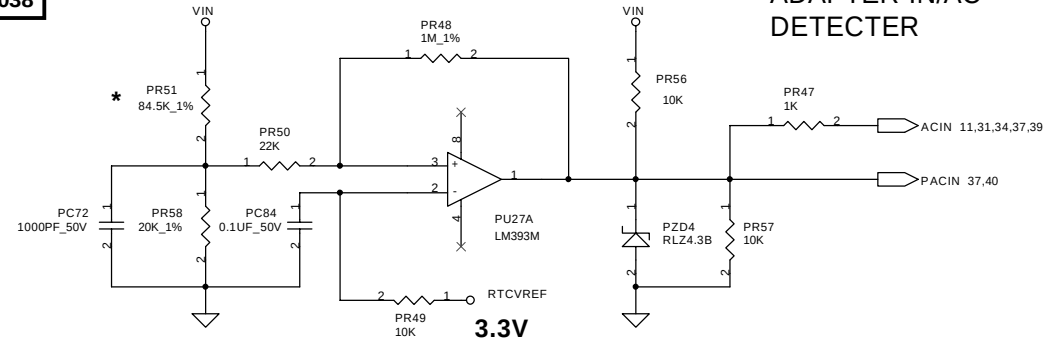
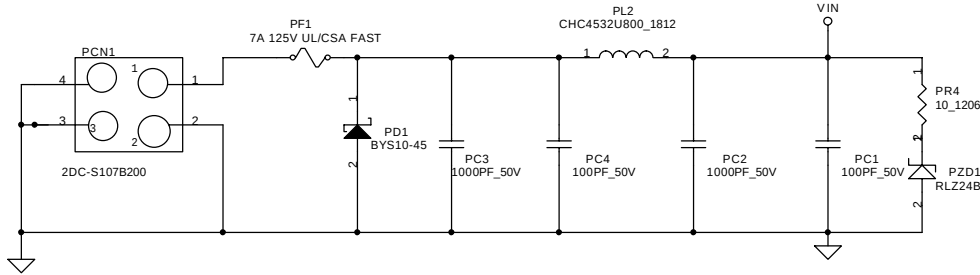
## FAN CONN.





## Vin Detector

High 18.764 17.901 17.063  
Low 17.745 16.903 16.038



+1.8VP (3A,120mils ,Via NO.= 6)  
PJP4 3MMA

+12VALWP (120mA,20mils ,Via NO.= 1)  
PJP3 120mA,20mils ,Via NO.= 1  
JOPEN/+12V

+5VALWP (5A,200mils ,Via NO.= 10)  
PJP2 5A,200mils ,Via NO.= 10  
PAD-OPEN 4x4m

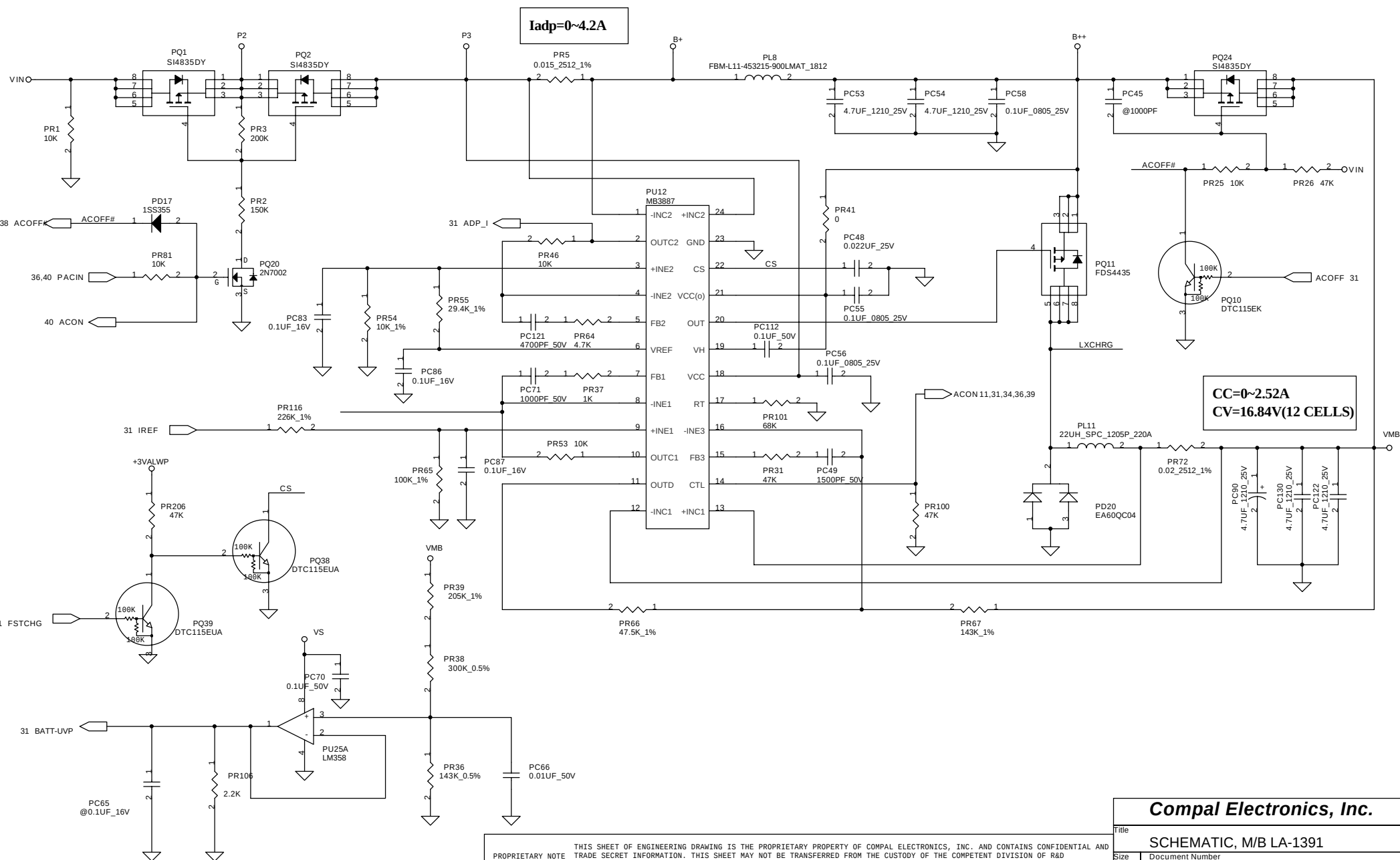
+3VALWP (5A,200mils ,Via NO.= 10)  
PJP5 5A,200mils ,Via NO.= 10  
PAD-OPEN 4x4m

+1.5VP (3A,120mils ,Via NO.= 6)  
PJP6 3MMA/CPU\_IO

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND PROPRIETARY INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

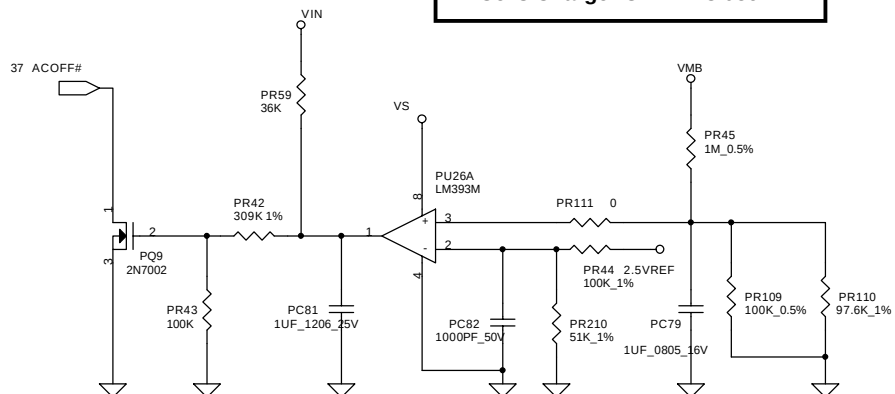
Compal Electronics, Inc.

|       |                 |                        |          |
|-------|-----------------|------------------------|----------|
| Title |                 | SCHEMATIC, M/B LA-1391 |          |
| Size  | Document Number | Rev                    |          |
| B     | 401211          | 1B                     |          |
| Date: | 2002年11月26日     | Sheet                  | 36 of 44 |



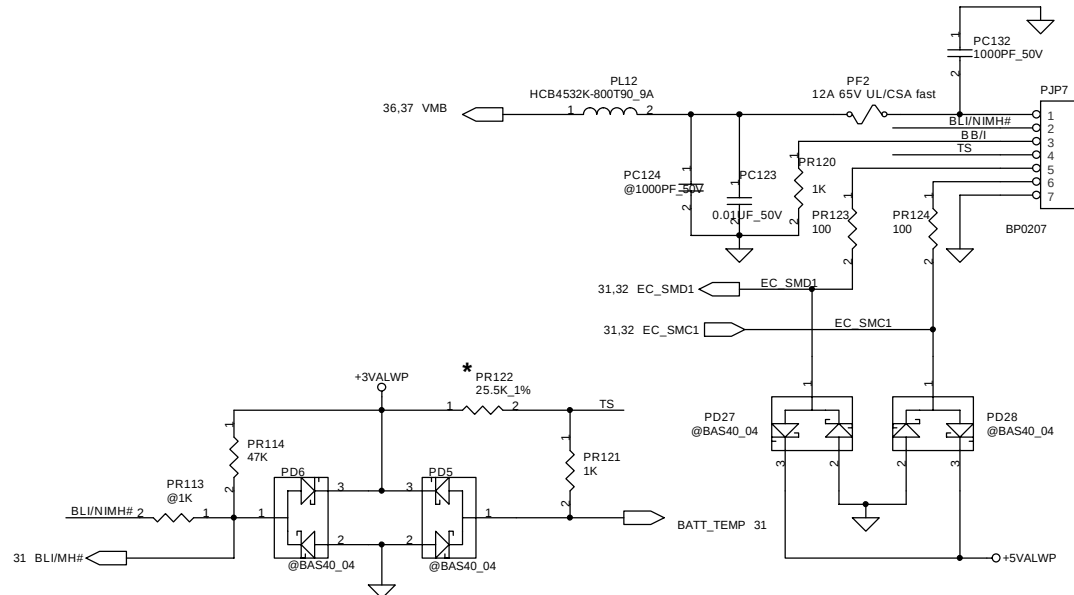
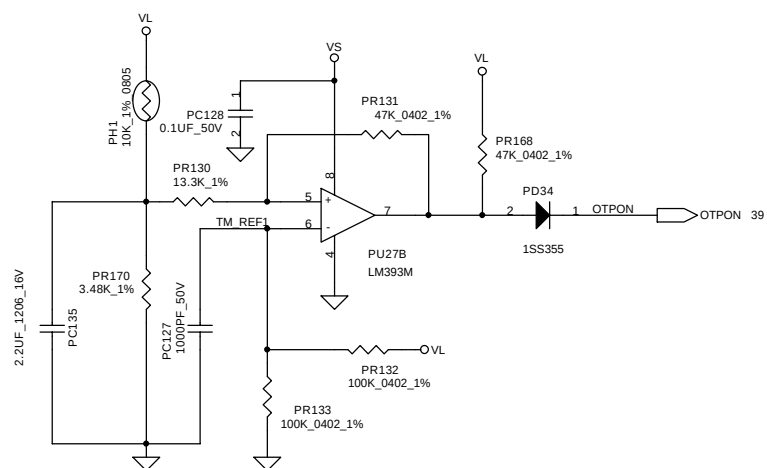
PROPRIETARY NOTE THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

# 12 Cells Charger OVP : 18.059V



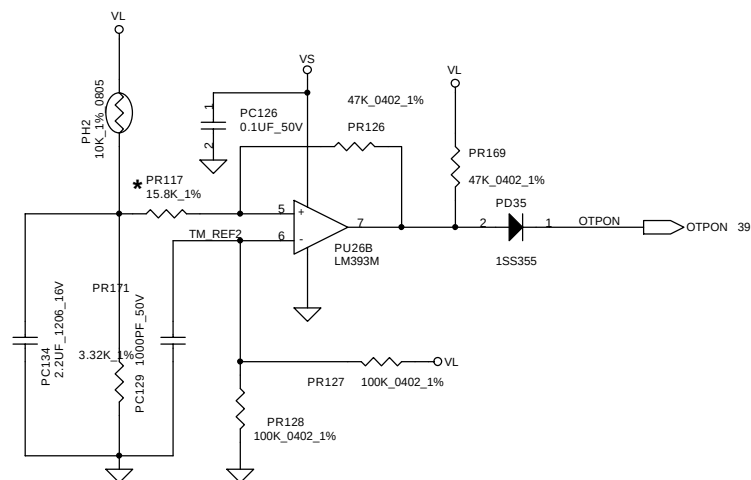
## PH1 under CPU botten side :

CPU thermal protection at 76 degree C  
Recovery at 45 degree C



## PH2 near main Battery CONN :

BAT. thermal protection at 82 degree C  
Recovery at 45 degree C



Compal Electronics, Inc.

Title  
SCHEMATIC, M/B LA-1391

Size  
B  
Document Number  
401211

Rev  
1B

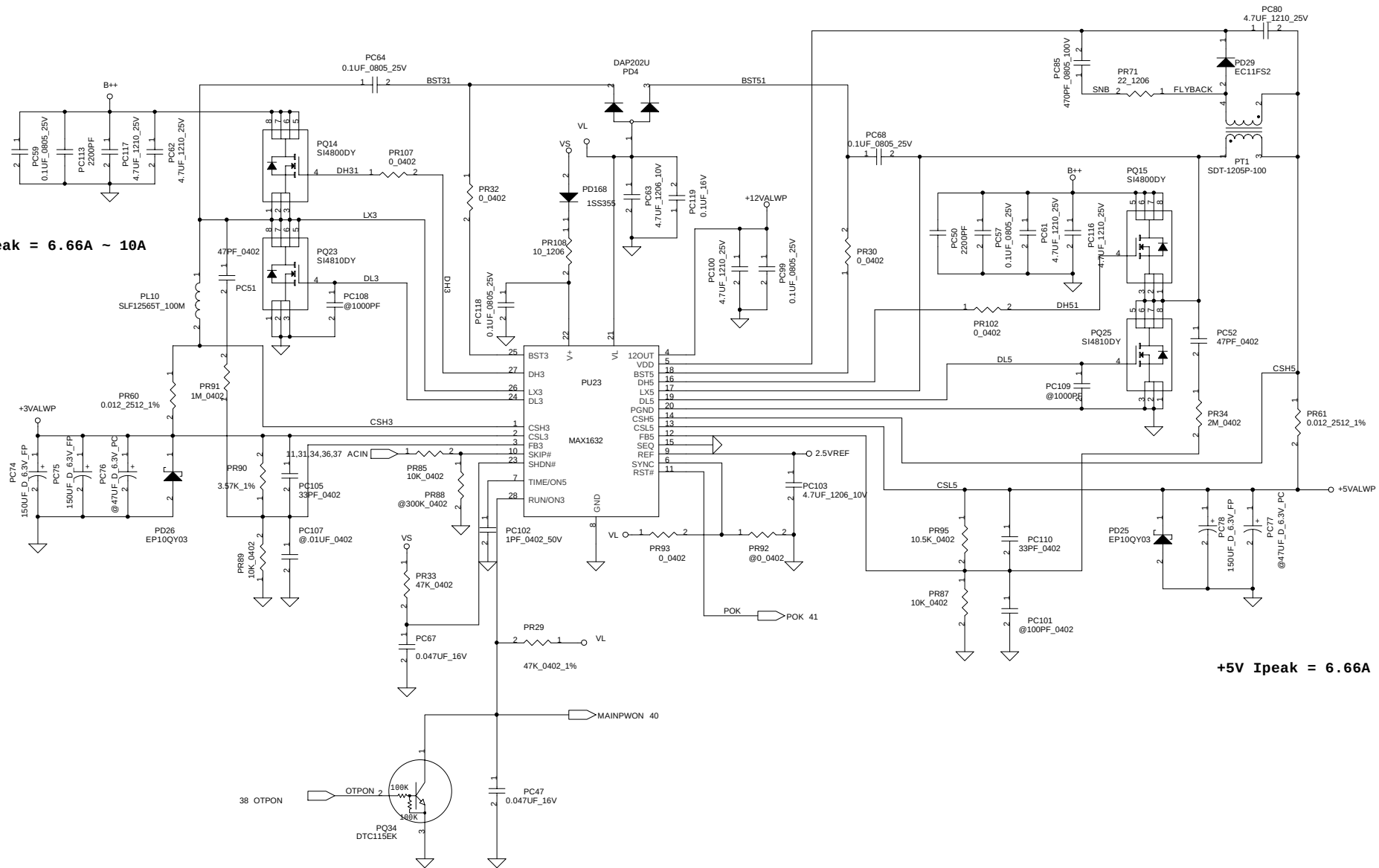
Date: 2002.11.26

Sheet 38 of 44

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND PROPRIETARY INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

+3.3V Ipeak = 6.66A ~ 10A

+5V Ipeak = 6.66A ~ 10A



Compal Electronics, Inc.

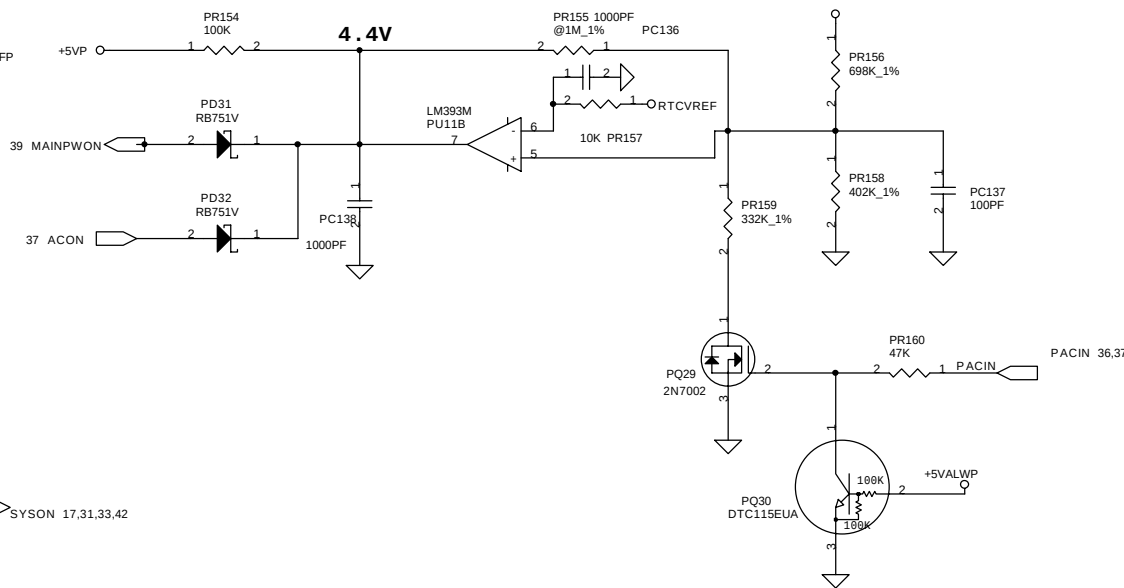
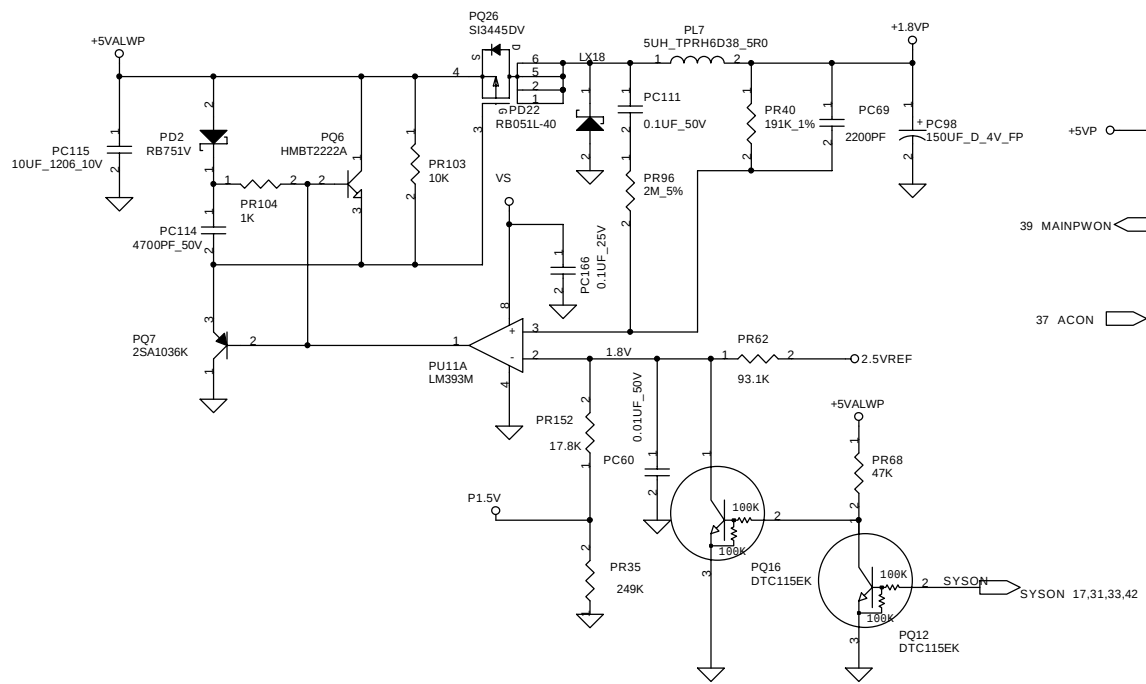
SCHEMATIC, M/B LA-1391

|       |                    |                |
|-------|--------------------|----------------|
| Size  | Document Number    | Rev            |
|       | 401211             | 1B             |
| Date: | 星期二, 11月 226, 2002 | Sheet 39 of 44 |

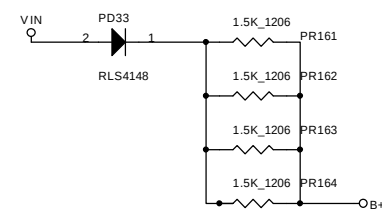
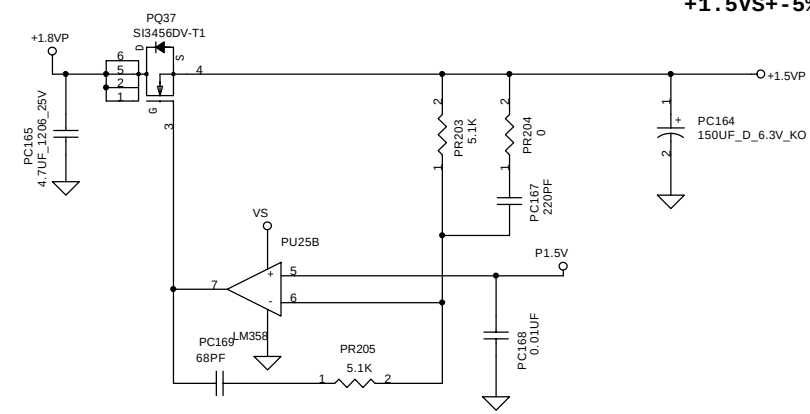
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

1.8V/1.5V

+1.8VP+-5%

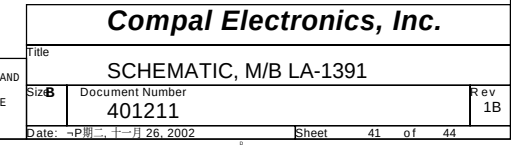


+1.5VS+-5%

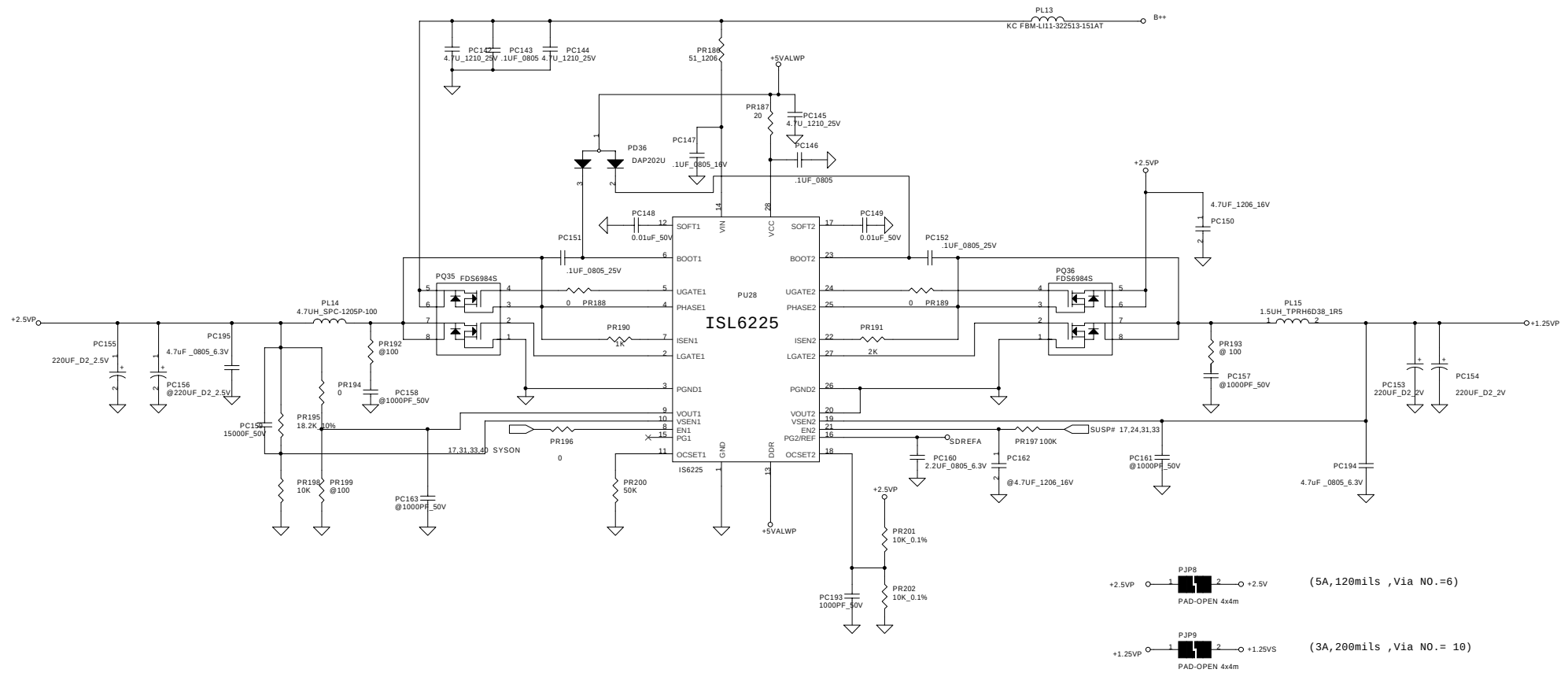


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND PROPRIETARY NOTE TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

|                                 |                 |  |     |
|---------------------------------|-----------------|--|-----|
| <b>Compal Electronics, Inc.</b> |                 |  |     |
| Title                           |                 |  |     |
| SCHEMATIC, M/B LA-1391          |                 |  |     |
| Size                            | Document Number |  | Rev |
| B                               | 401211          |  | 1B  |
| Date: 2002.11.26                |                 |  |     |
| Sheet 40 of 44                  |                 |  |     |



+2.5V/+1.25V



**Compal Electronics, Inc.**

SCHEMATIC, M/B LA-1391

Document Number  
401211

Rev  
1B

|                           |                |
|---------------------------|----------------|
| Date: 星期四 G, 十一月 26, 2002 | Sheet 42 of 44 |
|---------------------------|----------------|

PROPRIETARY NOTE THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

# ATR11 PIR LIST

# HW PIR LIST

# PWR PIR LIST

| ATEST |                                             | 9/6~                                                                                                                                                             |     |
|-------|---------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|
| 1     | +2.5V LEVEL ERROR                           | Remove R111 180ohm<br>Change R108 from 100 to 0 ohm                                                                                                              | P15 |
| 2     | SDRAM BANK select error                     | Change CLK_SDRAM3 from U22.F3 to U22.E3<br>Change CLK_SDRAM5 from U22.H5 to U22.G5<br>Change RRAS#3 from U22.H7 to U22.F7<br>Change RRAS#5 from U22.G6 to U22.H6 | P5  |
| 3     | Add CPU VCCVID pin                          | Add R492 connect to U23.AF3                                                                                                                                      | P3  |
| 4     | TV OUT output error                         | Change CRMA from U8.AF15 to U8.AF16<br>Change LUMA from U8.AF16 to U8.AF15                                                                                       | P12 |
| 5     | Add MK1707 output terminate                 | Add R494 22 ohm                                                                                                                                                  | P12 |
| 6     | Add low pass filter for<br>MK1707 power pin | Add L47                                                                                                                                                          | P12 |
| 7     | Change VGA option resistor<br>to jumper     | Change R284 to J3<br>Change R286 to J4<br>Change R295 to J5<br>Change R296 to J6                                                                                 | P15 |
| 8     | Reserve +2.5V option control                | Add R495 for SYS0N control<br>Add R496 for SUSP control                                                                                                          | P15 |
| 9     | Change LCD connector type                   | Change J31                                                                                                                                                       | P16 |
| 10    | Add TI TPS2042 for USB<br>protect circuit   | Add U56(TPS2042)                                                                                                                                                 | P25 |

## BTEST

|  |  |  |  |
|--|--|--|--|
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |

|                                 |                        |       |        |
|---------------------------------|------------------------|-------|--------|
| <b>Compal Electronics, Inc.</b> |                        |       |        |
| Title SCHEMATIC, M/B LA-1391    |                        |       |        |
| Size Custom                     | Document Number 401211 |       | Rev 1B |
| Date: ~P期二..十一月 26, 2002        | Sheet 43               | of 44 |        |

PROPRIETARY NOTE      THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

**Page 1 of 1**  
**for PWR**

|                                 |                           |           |       |
|---------------------------------|---------------------------|-----------|-------|
| <b>Compal Electronics, Inc.</b> |                           |           |       |
| Title<br>SCHEMATIC, M/B LA-1391 |                           |           |       |
| Size                            | Document Number<br>401211 | Rev<br>1B |       |
| Date: 星期二, 十二月 26, 2002         | Sheet                     | 44        | of 44 |

PROPRIETARY NOTE THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.