

Compal Confidential

NYU00 LA-5301P R02 Schematics Document

Menlow-Silverthorne with Poulsbo

Monday, April 06, 2009

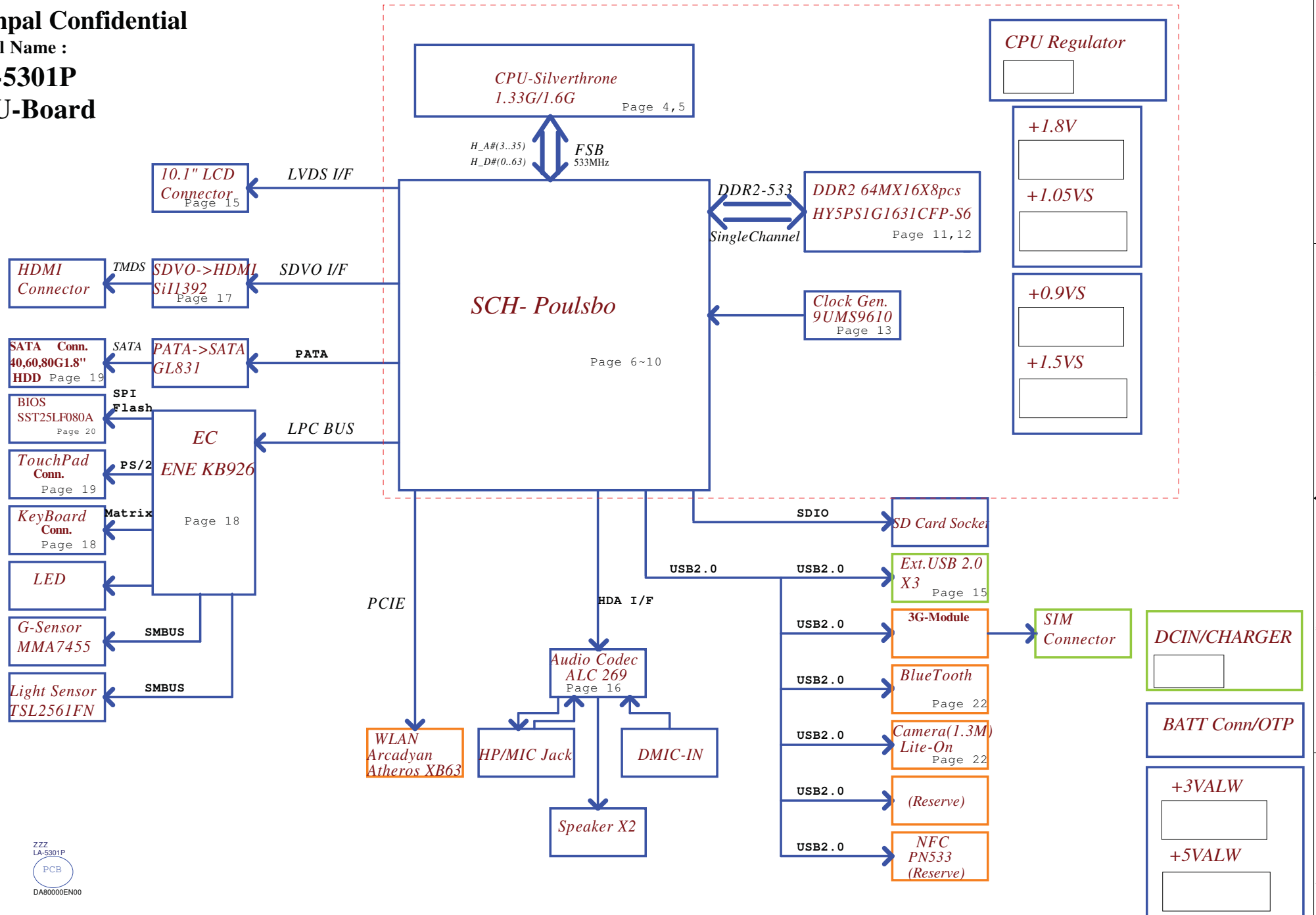
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				Cover Sheet	
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Compal Confidential

Model Name :

LA-5301P

CPU-Board



Security Classification		Compal Secret Data		Title	
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Voltage Rails O MEANS ON X MEANS OFF

power plane State	+VBAT	+5VALW +3VALW	+1.8V +0.9V	+5VS +3VS +1.8VS +1.5VS +CPU_CORE +VCCP	CLOCK
S0	O	O	O	O	O
S3	O	O	O	X	X
S5 S4/AC	O	O	X	X	X
S5 S4/ Battery only	O	X	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X	X

O MEANS ON
X MEANS OFF

S3 : STR
S4 : STD
S5 : SOFT OFF

BOM Structure

MARK	FUNCTION
@	NC FOR ALL

Address

USB PORT LIST

PORT	DEVICE
0	HOST LEFT1
1	HOST LEFT2
2	HOST RIGHT1
3	3G
4	BT
5	CAM
6	(RESERVE)
7	(RESERVE)

EC SM Bus1 address

Device	Address
GSENSOR MMA7455	0001_1101

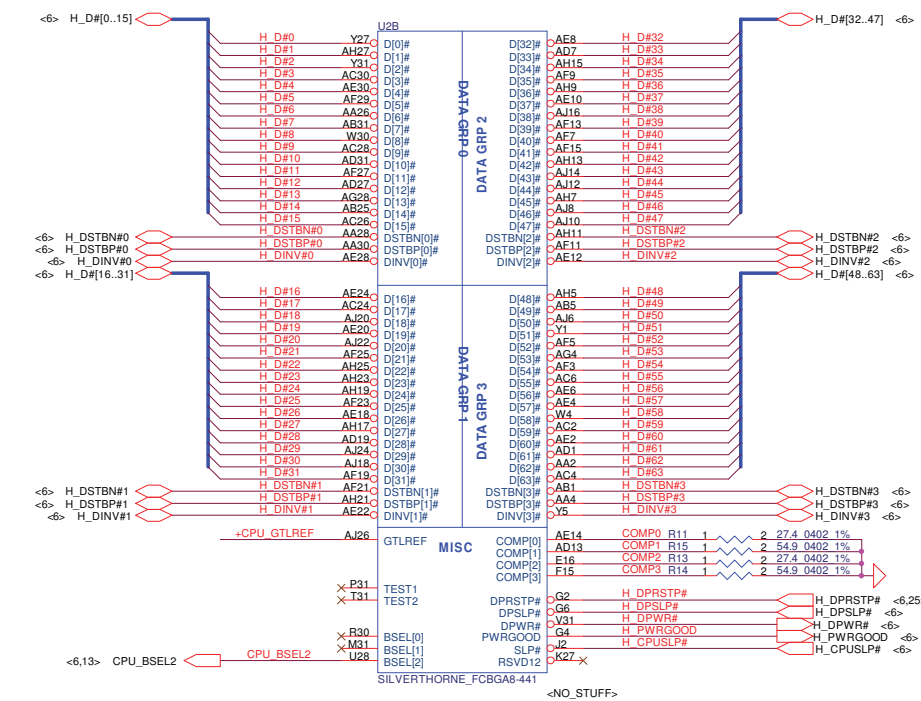
EC SM Bus2 address

Device	Address
THERMAL SENSOR 1 EMC1402-1-ACZL-TR	100_1100
THERMAL SENSOR 2 EMC1402-2-ACZL-TR	100_1101
LIGHT SENSOR TSL2561FN	ADDRSEL GND 0101001

Poulsbo SM Bus address

Device	Address
Clock Generator (ICS90UMS9610BKLF-T)	1101 001Xb

Silverthorne Host Data Interface

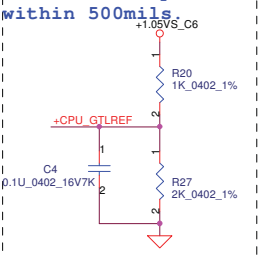


Layout note: Route TEST3 & TEST5 traces on ground referenced layer to the TPs

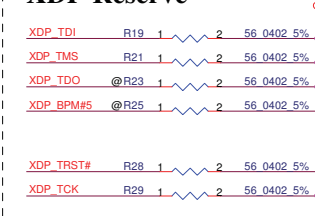
CPU_BSEL	CPU_BSEL2	CPU_BSEL1	CPU_BSEL0
100	1	0	1
133	0	0	1

Resistor placed within 0.5" of CPU pin.Trace should be at least 25 mils away from any other toggling signal. COMP[0,2] trace width is 18 mils. COMP[1,3] trace width is 4 mils.

Close to CPU pin AD26 within 500mils

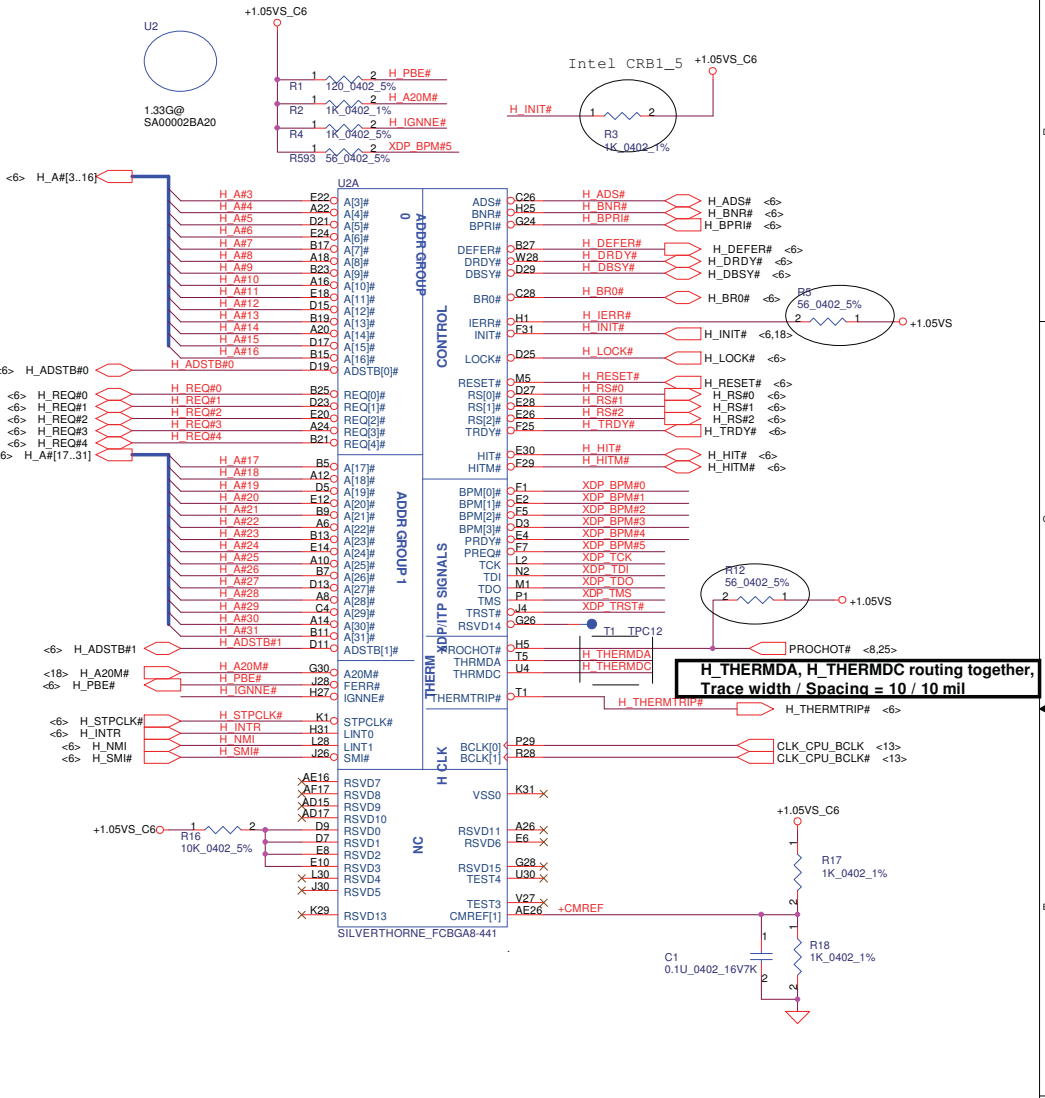


XDP Reserve



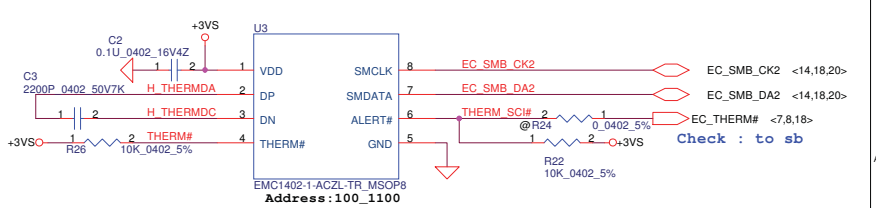
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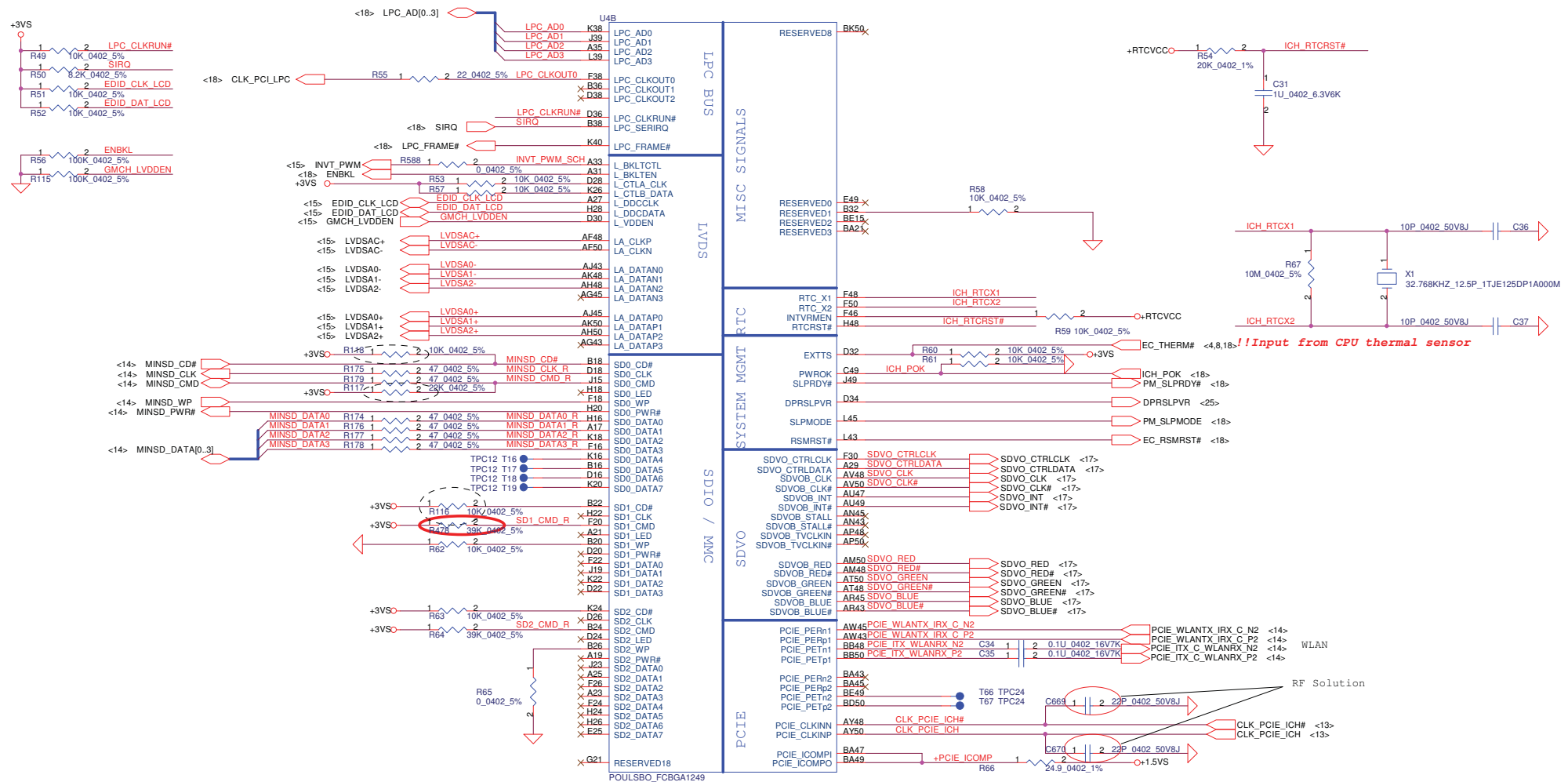
Silverthorne Host Data Interface



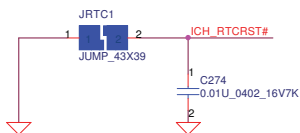
H_THERMDA, H_THERMDC routing together, Trace width / Spacing = 10 / 10 mil

Thermal Sensor





RTC Reset SW (Place under door)



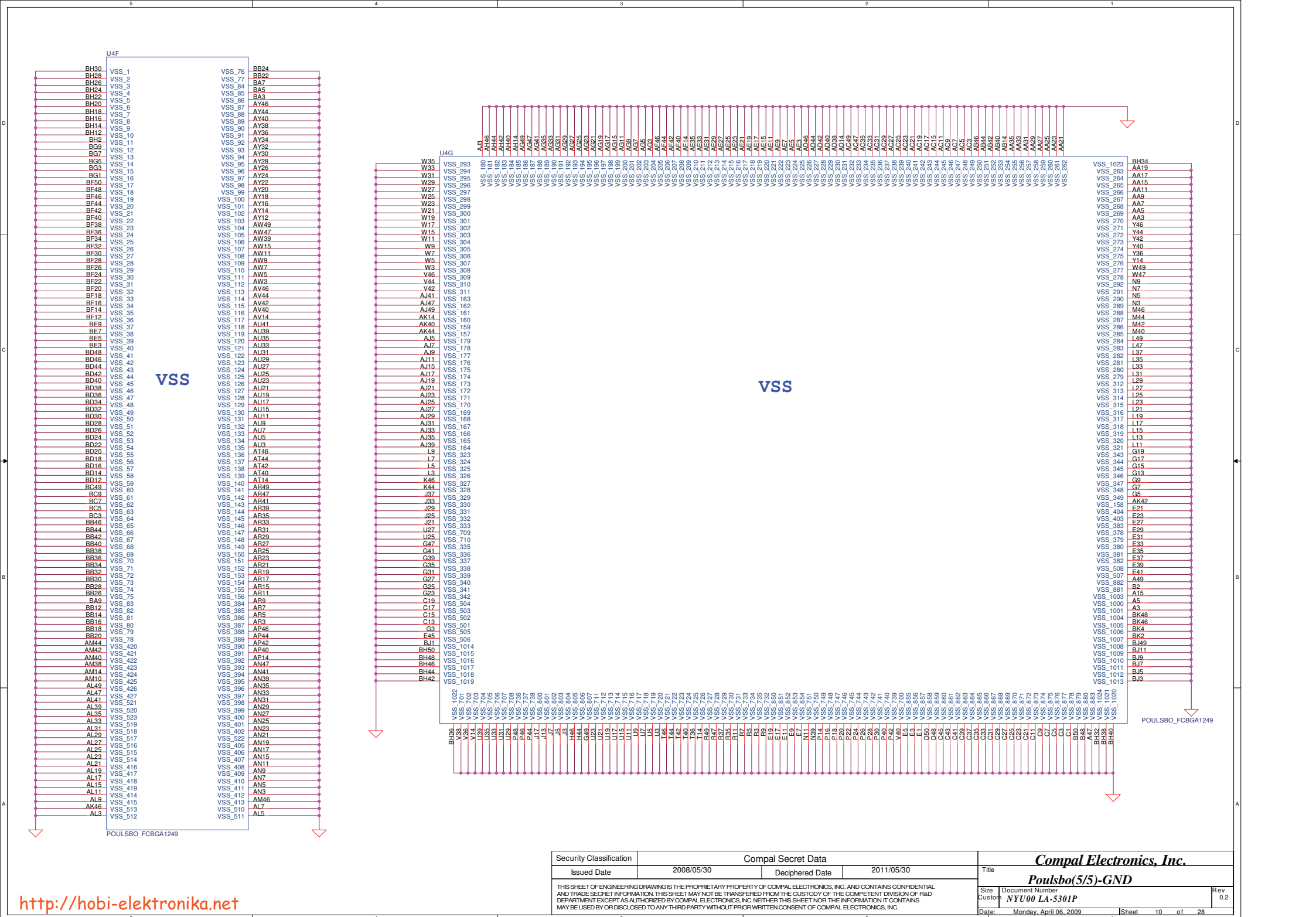
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GP I00: Internal pull-down 22k
GP I03: Internal Pull-high 22k

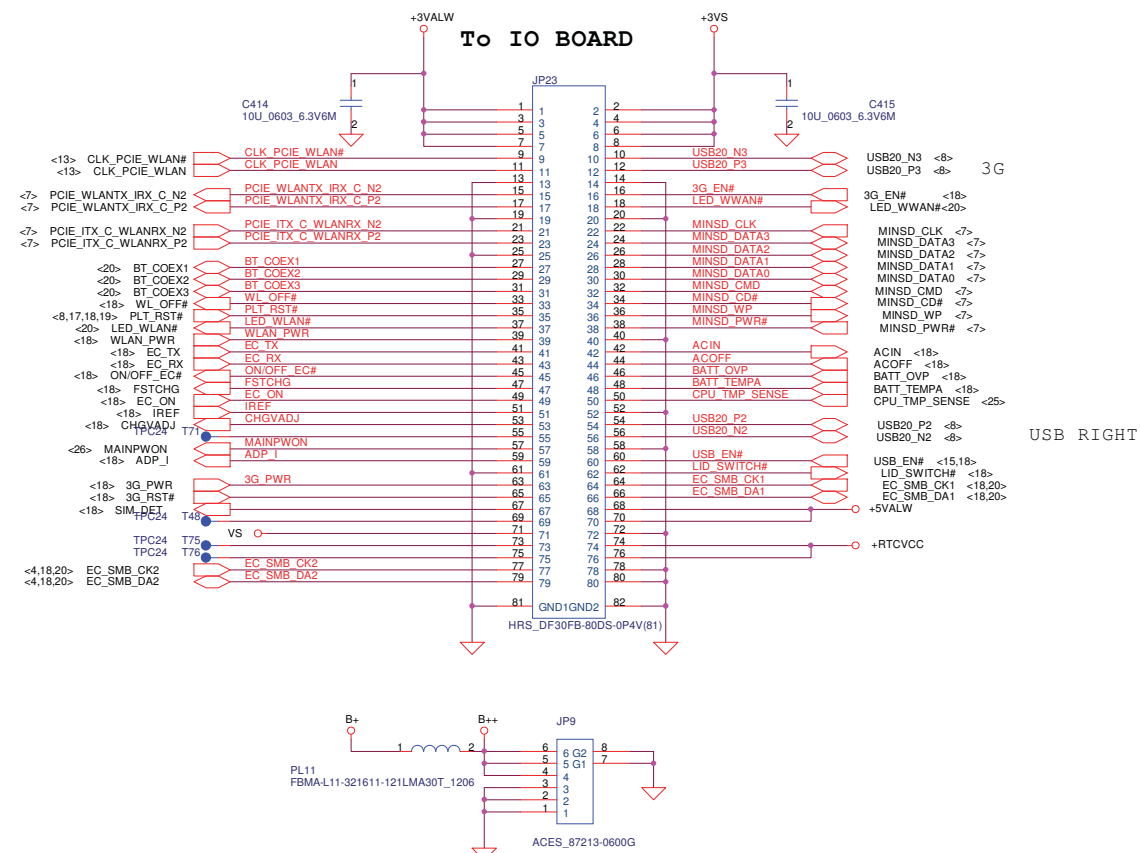


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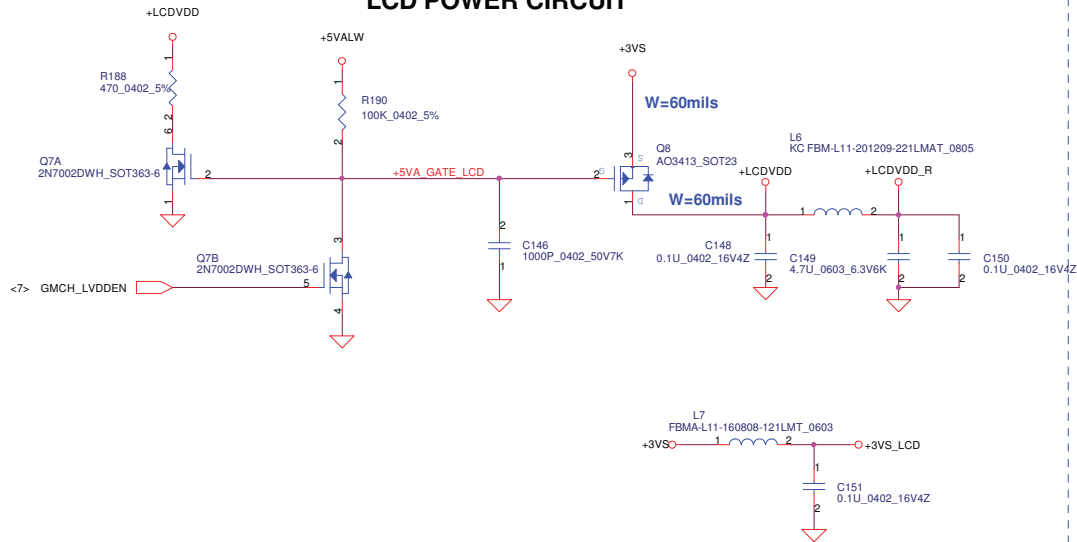
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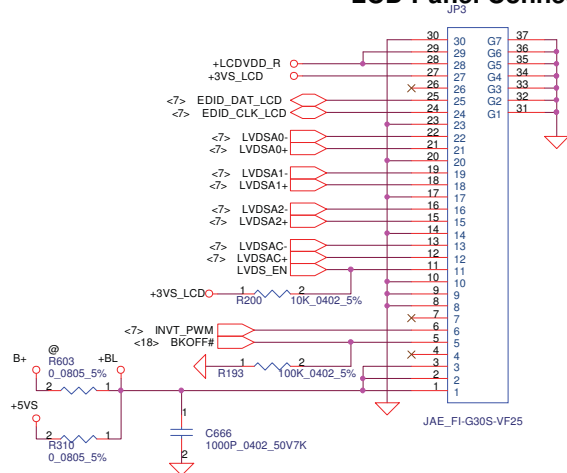


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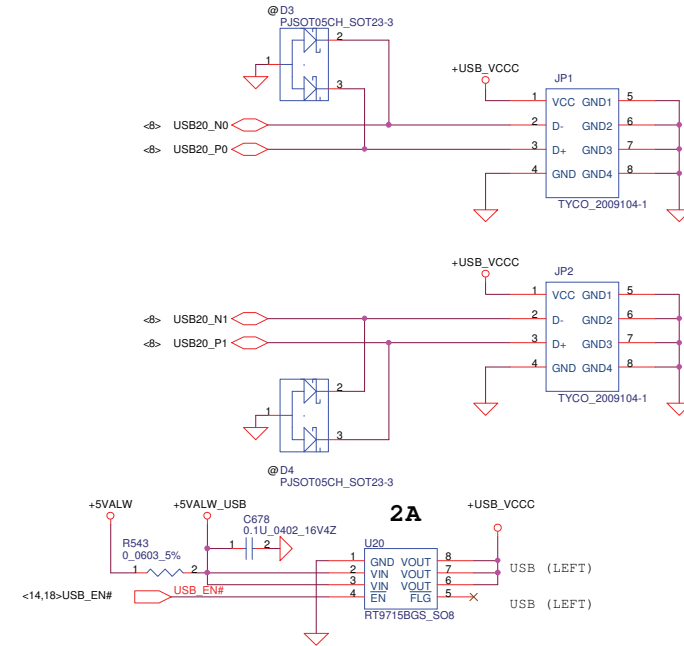
LCD POWER CIRCUIT



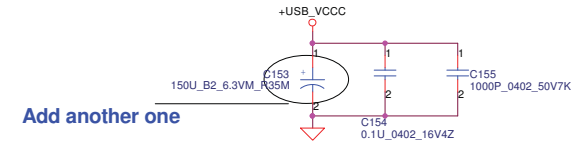
LCD Panel Connector



USB/B Connector (Left)



Close to JP5



Codec Regulator

HeadPhone

HeadPhone

Sense Pin	Impedance	Codec Signals
SENSE A	39.2K	PORT-A (PIN 32, 33)
	20K	PORT-B (PIN 21, 22)
	10K	PORT-C (PIN 23, 24)
	5.1K	PORT-D (PIN 48)
SENSE B	39.2K	PORT-E (PIN 14, 15)
	20K	PORT-F (PIN 16, 17)
	10K	PORT-G (PIN 20)
	5.1K	PORT-H (PIN 47)

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Beep Circuit

EC Beep

 BEEP#

 SPKR

+5V_VDDA_HD

+3VS

R225 10K_0402_5%

R227 560_0402_5%

R229 560_0402_5%

R231 10K_0402_5%

R230 2.4K_0402_5%

C211 1U_0402_6.3V6K

C214 1U_0402_6.3V6K

C213 1U_0402_6.3V6K

C230 2.4K_0402_5%

C237 0.1U_0402_16V4Z

C238 0.1U_0402_16V4Z

D13 CH751H-40PT_SOD323-2

D14 PJSOT05CH_SOT23-3

D15 PJSOT05CH_SOT23-3

C9 MMBT3904WH_SOT323-3

Speaker Conn.

SPKL+ L28 1 2 0 0603 5%

SPKL- L29 1 2 0 0603 5%

SPKR+ L30 1 2 0 0603 5%

SPKR- L31 1 2 0 0603 5%

SPK L1 1 2 0 0603 5%

SPK L2 1 2 0 0603 5%

SPK R1 1 2 0 0603 5%

SPK R2 1 2 0 0603 5%

D14 PJSOT05CH_SOT23-3

D15 PJSOT05CH_SOT23-3

C237 0.1U_0402_16V4Z

C238 0.1U_0402_16V4Z

HP/MIC JACK

+5V_VDDA_HD

R468 100K_0402_5%

HP_MIC_SENSE_C0

MIC_1

HPR

HPL

JACK-AGND

C240 10P_0402_50V8J

C239 10P_0402_50V8J

C237 0.1U_0402_16V4Z

C238 0.1U_0402_16V4Z

D16 PJSOT05CH_SOT23-3

D17 PJSOT05CH_SOT23-3

J1 JUMP_43X39

J2 JUMP_43X39

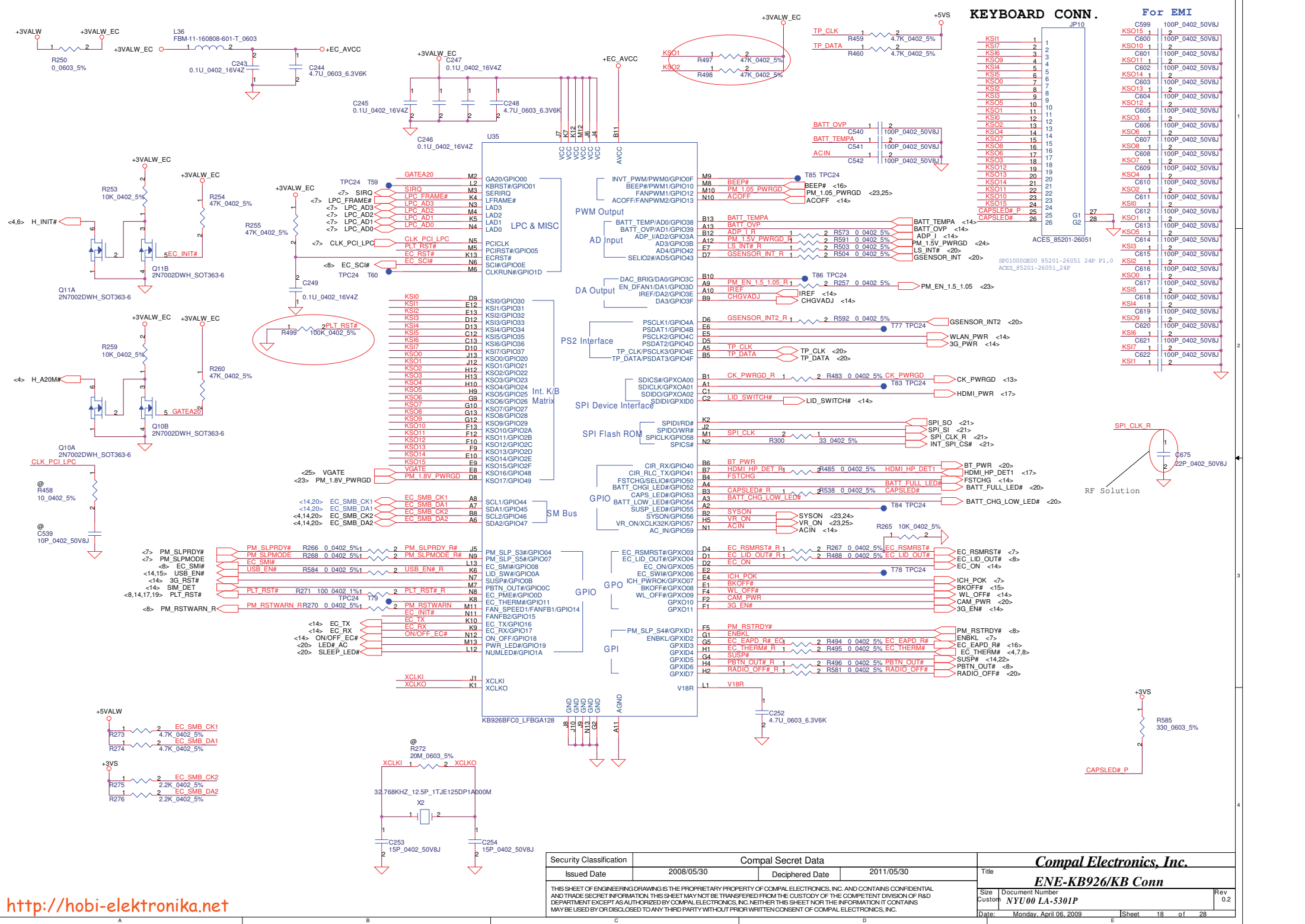
LINGY_CX-31930-Q

2011/05/30		Title		Compal Electronics, Inc.	
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Sense Pin	Impedance	Codec Signals
SENSE A	39.2K	PORT-A (PIN 32, 33)
	20K	PORT-B (PIN 21, 22)
	10K	PORT-C (PIN 23, 24)
	5.1K	PORT-D (PIN 48)
SENSE B	39.2K	PORT-E (PIN 14, 15)
	20K	PORT-F (PIN 16, 17)
	10K	PORT-G (PIN 20)
	5.1K	PORT-H (PIN 47)

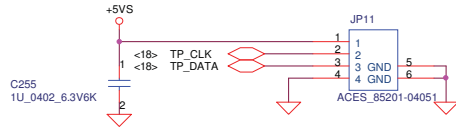
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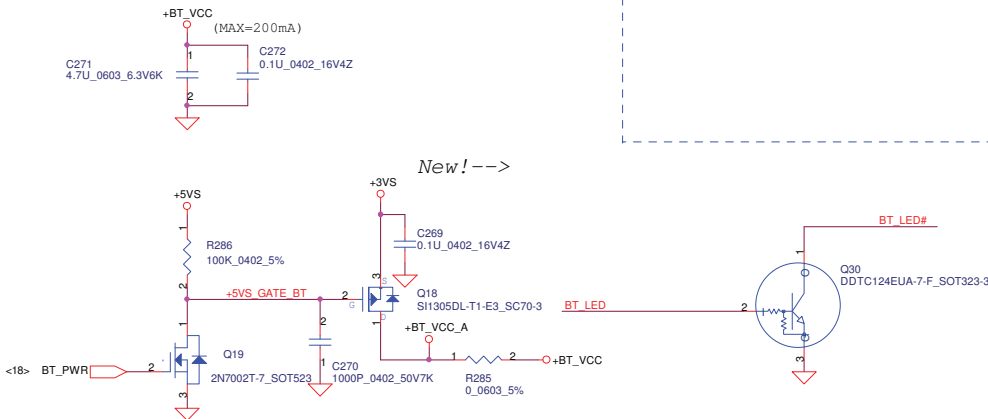
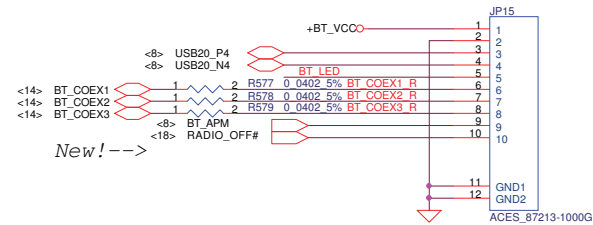


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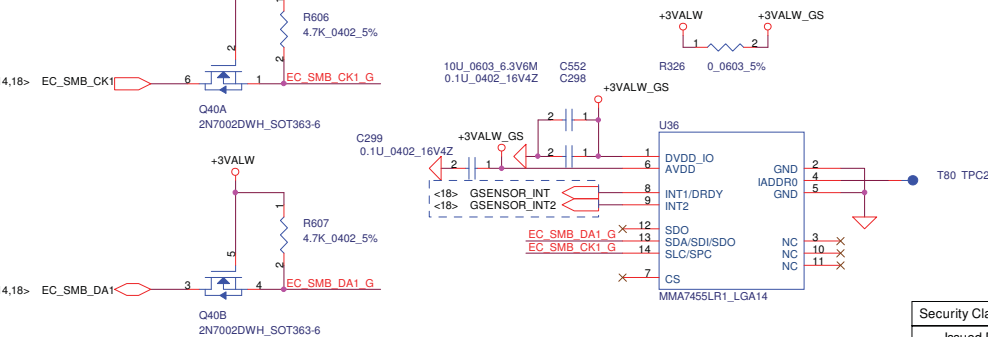
Touch/B Connector



BlueTooth Connector



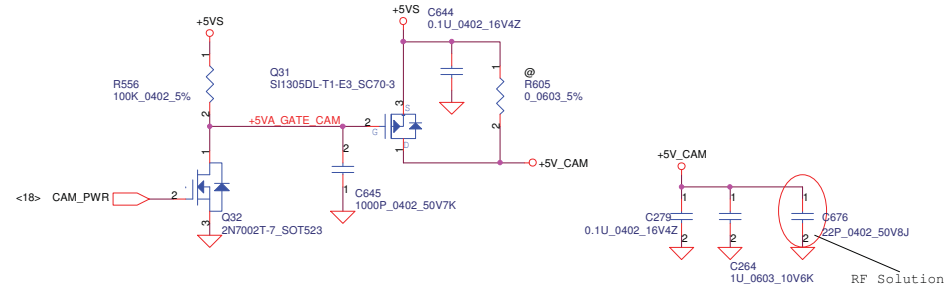
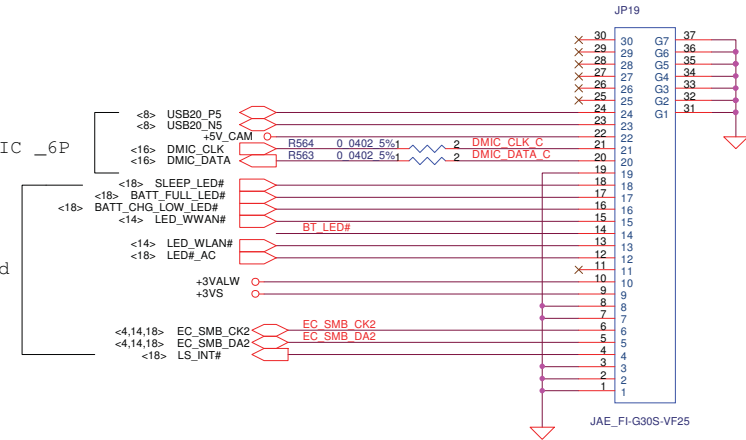
G-Sensor



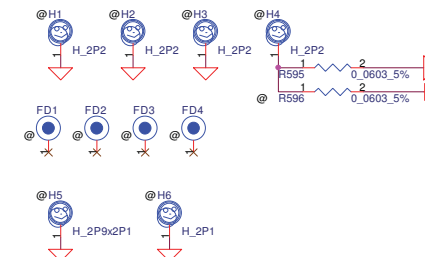
GPS + CMOS Camera/MIC + LED Board Connector

CMOS & MIC _6P

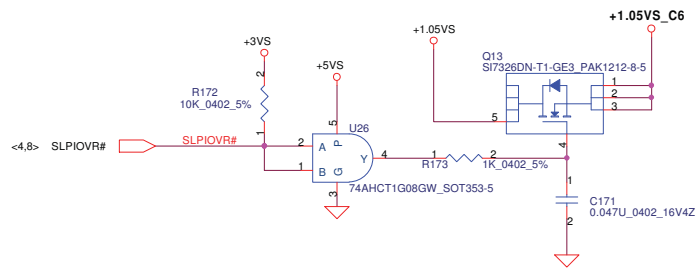
LED & ALS Board



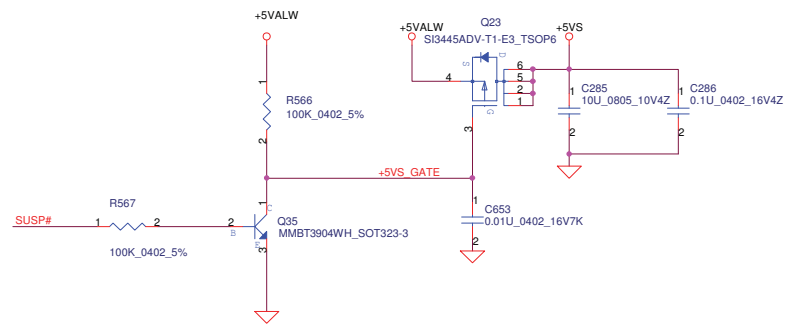
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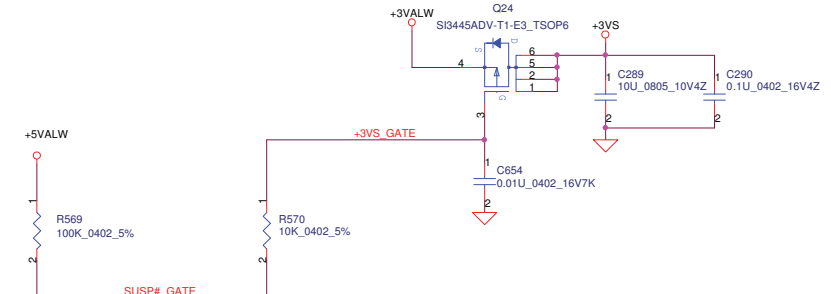
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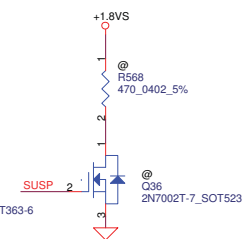
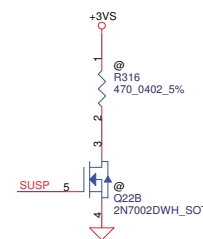
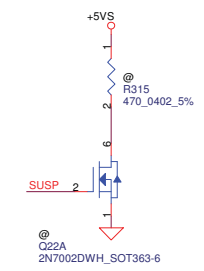
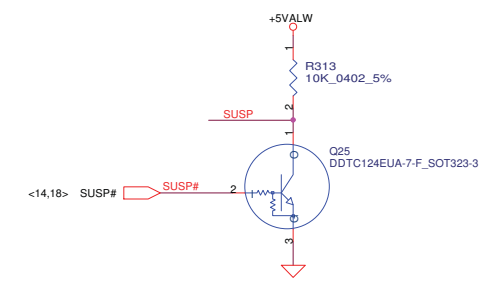
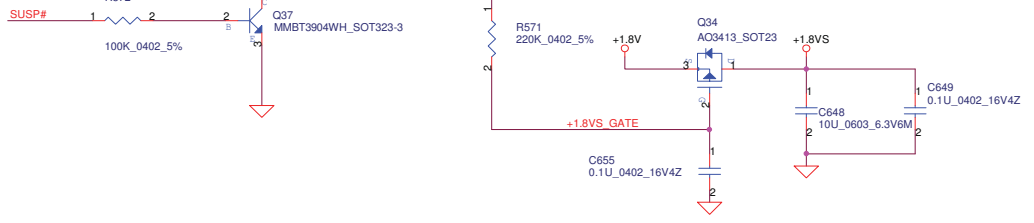
+5VALW to +5VS Transfer



+3VALW to +3VS Transfer

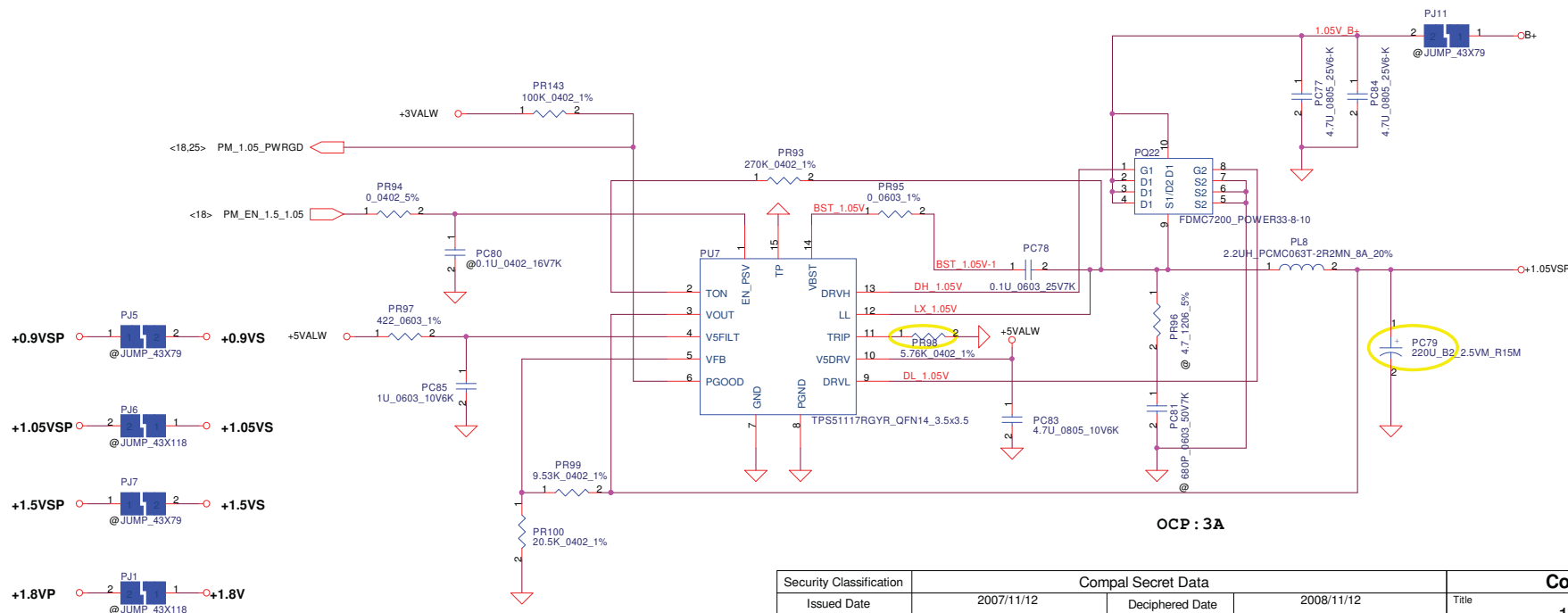
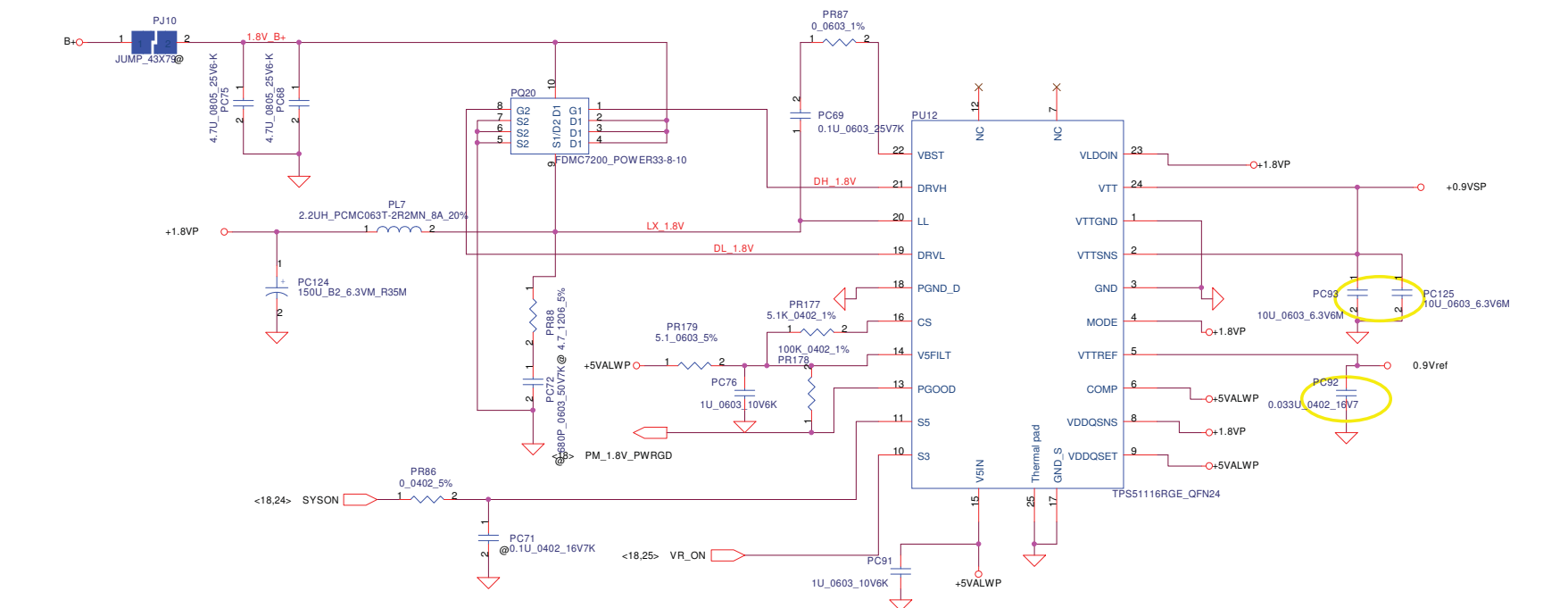


+1.8V to +1.8VS Transfer



PRODUCT SUMMARY			
V _{gs}	r _{DS(on)}	ID (A)	
-8	0.042 @ VGS = .4,5 V	+-5.6	
	0.060 @ VGS = .2,5 V	+-4.7	
	0.080 @ VGS = .1,8 V	+-2.9	

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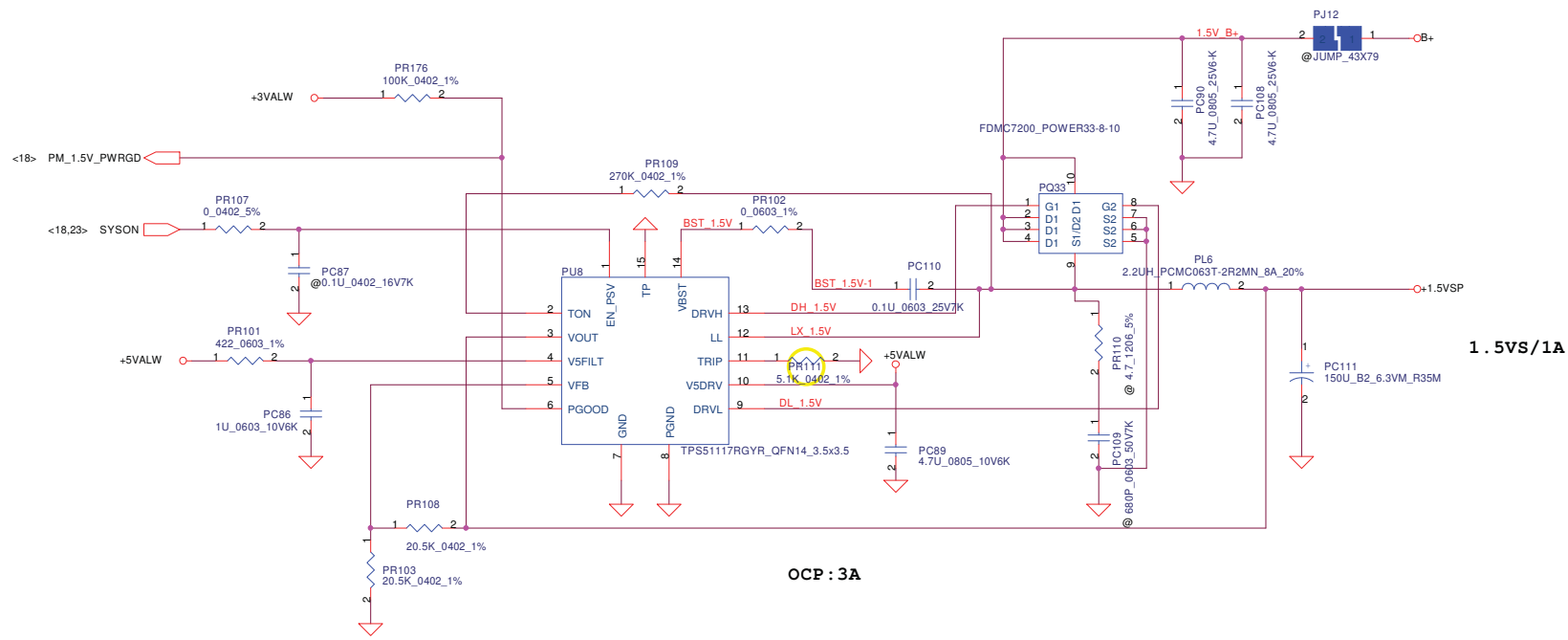
1.05V/1.5A

OCP : 3A

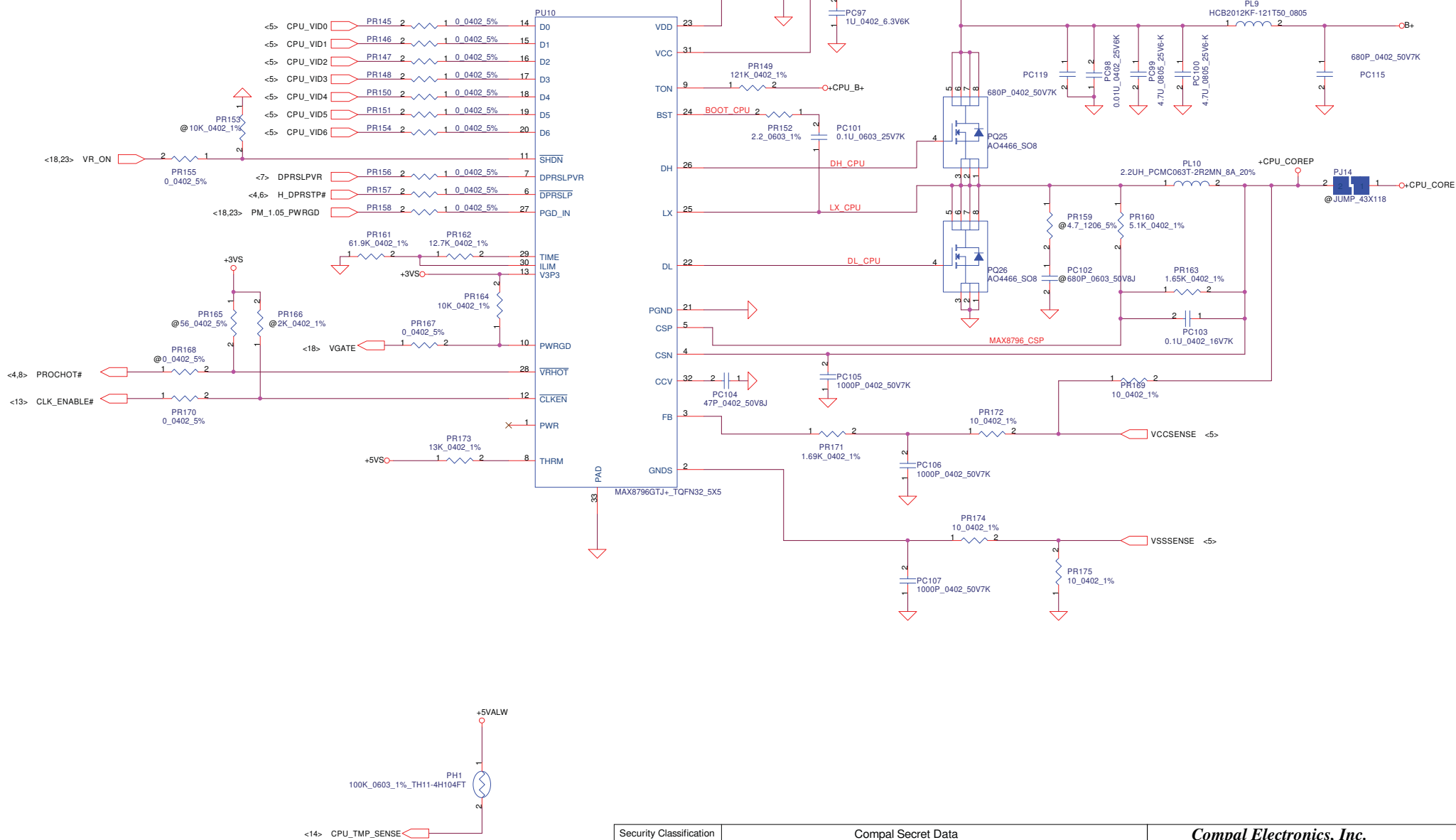
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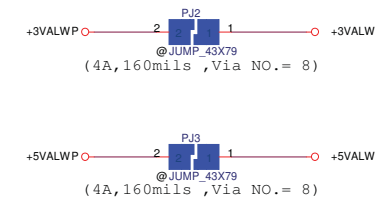
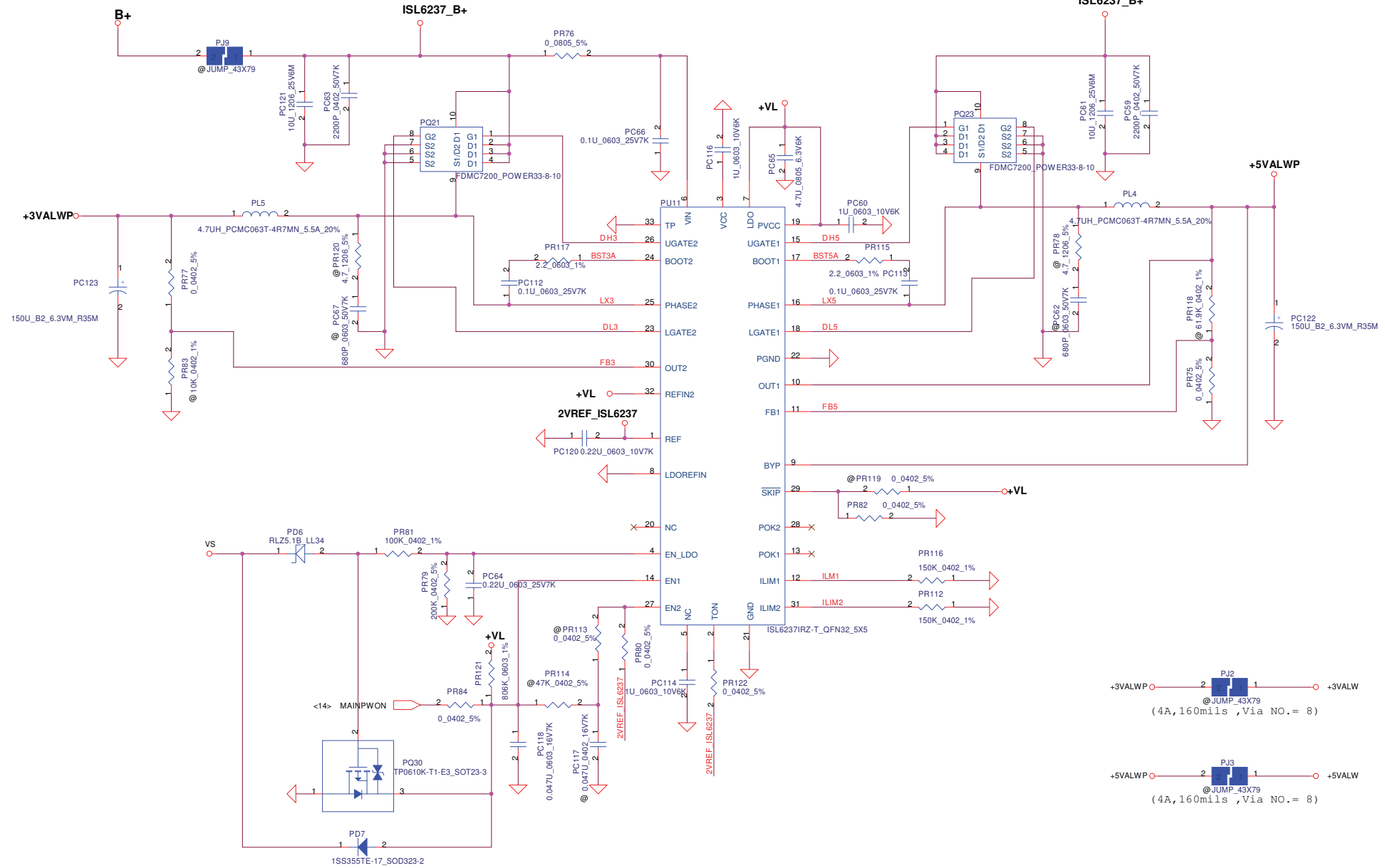
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ITEM	Fixed Issue/Reason for change	REV	PAGE	CHANGE LIST	
1	1.DC/DC switch Capacity voltage tolerance 2.B+ is over Q23, Q24, Q34 Gate Voltage and cause the leakage on +1.8VS & +3VS.	0.2	PAGE 22	1.Change C653, C654, C655 to 50V tolerance 2.Q23, Q24, Q34 Gate pull high voltage change to +5VALW	090310
2	1.Change JP19 to I-PEX 30Pin.	0.2	PAGE 20	1.Change JP19 to I-PEX 30Pin(I-PEX_20439-R30E-02).	090310
3	1.Del reserve DB port Conn. of EC	0.2	PAGE 21	1.Delete JP26. 2.Delete LPC DB port.	090310
4	1.Follow the LCD SPEC to add Pin 20 LVDS_EN	0.2	PAGE 15 PAGE 18	1.Add LVDS_EN on JP3 Pin20 and U35 B10 2.Add R200 100K pull high.	090310
5	1.BT is USB 2.0 Full Speed. Change USB port to Port 4	0.2	PAGE 14 PAGE 20	1.BT USB change to USB20 port 4. 2.3G USB change to USB20 port 7.	090310
6	1.C5, C8, C550 change to lower cost and lower ESR.	0.2	PAGE 05	1.C5, C8, C550 change from 220U_B15G_70m ohm to 220U_B2_15m ohm	090311
7	1.C52, C55, C63, C153 change to lower cost and lower ESR.	0.2	PAGE 09	1.C52, C55, C63, C153 change from 150U_B15G_70m ohm to 150U_B2_35m ohm	090311
8	1.Add HP, HP/MIC Auto Detect circuit.	0.2	PAGE 16	1.Add U38 Comparator circuit.	090312
9	1.USB port change to on board. 1.SATA FPC connector change to TOP conduction.	0.2	PAGE 15	1.Change JP1 and add JP2 TYCO_2009104. 2.Change JP20 from 87152 type to 87151type.	090320
10	1.Panel Backlight voltage change for efficient.	0.2	PAGE 15	1.Add B+ for LED backlight supply.	090326
11	1.GL831 +1.8V pin input output separate for decrease the power consumption	0.2	PAGE 19	1.U37 Pin8, 24, 35(CVDD1, 2, 3) use internal 3.3V to 1.8V regulator 2.Other 1.8V pins use system +1.8VS power.	090326
12	1.G-Sensor application need always power.	0.2	PAGE 20	1.Change Interface to EC SMBUS1, change to +3VALW power 2.Add level shift for SMBUS I/F (+5VALW to +3VALW.) 3.Change G-Sensor from MMA7660 to MMA7455	090326
13					090327
14					

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