

ZZZ1



PCB
DA2@

ZZZ2



PCB
DA2@
LA-4011P

ZZZ3



PCB
DA2@
LS-4011P

ZZZ4



PCB
DA2@
LS-4012P

ZZZ5



PCB
DA2@
LS-4013P

Compal Confidential

JAW91 Schematics Document

AMD S1/ ATI RS690M(C) / SB600

2008/11/20

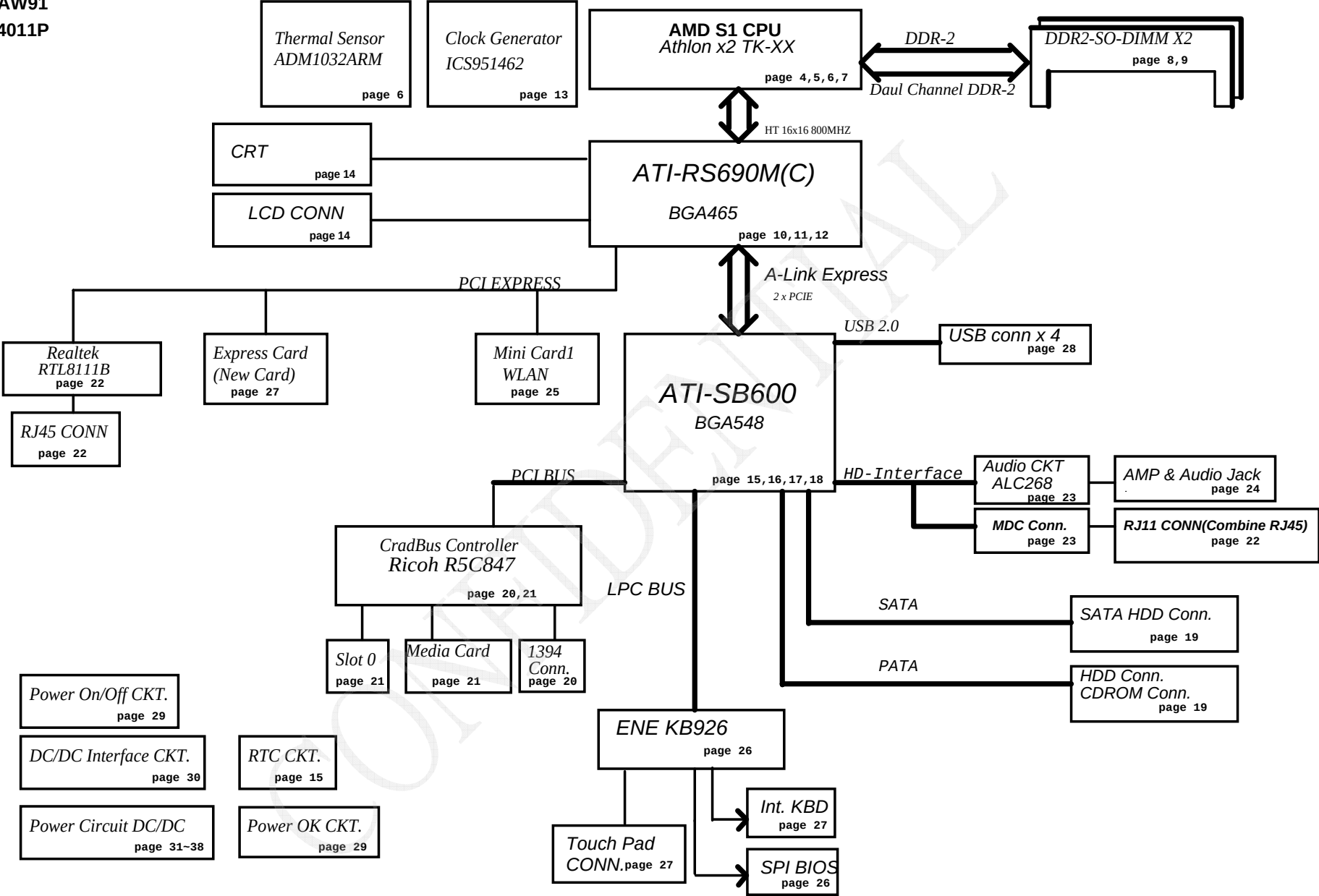
Rev:1.0

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		2008/7/6		Date	
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Project Code: JAW91

File Name : LA-4011P



Power Plane	Description	S1	S3	S4/ S5
VIN	Adapter power supply (19V)	ON	ON	ON
B+	AC or battery power rail for power circuit.	ON	ON	ON
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+1.2V_HT	1.2V switched power rail	ON	OFF	OFF
+0.9V	0.9V switched power rail for DDR terminator	ON	ON	OFF
+1.2VALW	1.2V always on power rail	ON	ON	ON
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+1.8V	1.8V power rail for DDR	ON	ON	OFF
+3VALW	3.3V always on power rail	ON	ON	ON
+3V	3.3V power rail	ON	ON	OFF
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON
+5VS	5V switched power rail	ON	OFF	OFF
+RTCVCC	RTC power	ON	ON	ON

SIGNAL STATE	SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1(Power On Suspend)	LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

Vcc	3.3V +/- 5%			
Ra / Rc	100K +/- 5%			
Board ID	Rb / Rd	VAD_BID min	VAD_BID typ	VAD_BID max
0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

Device	IDSEL#	REQ#/GNT#	Interrupts
CardBus	AD21	0	PIRQE/PIRQF/PIRQG

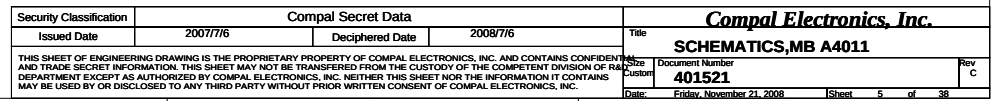
Device	Address	Device	Address
Smart Battery	0001 011X b?	ADM1032	1001 110X b?
EEPROM(24C16/02)	1010 000X b?		
(24C04)	1011 000Xb?		

Device	Address
Clock Generator (ICS 951462AGT)	1101 001Xb?
DDRII DIMM0	1001 000Xb?
DDRII DIMM2	1001 010Xb?

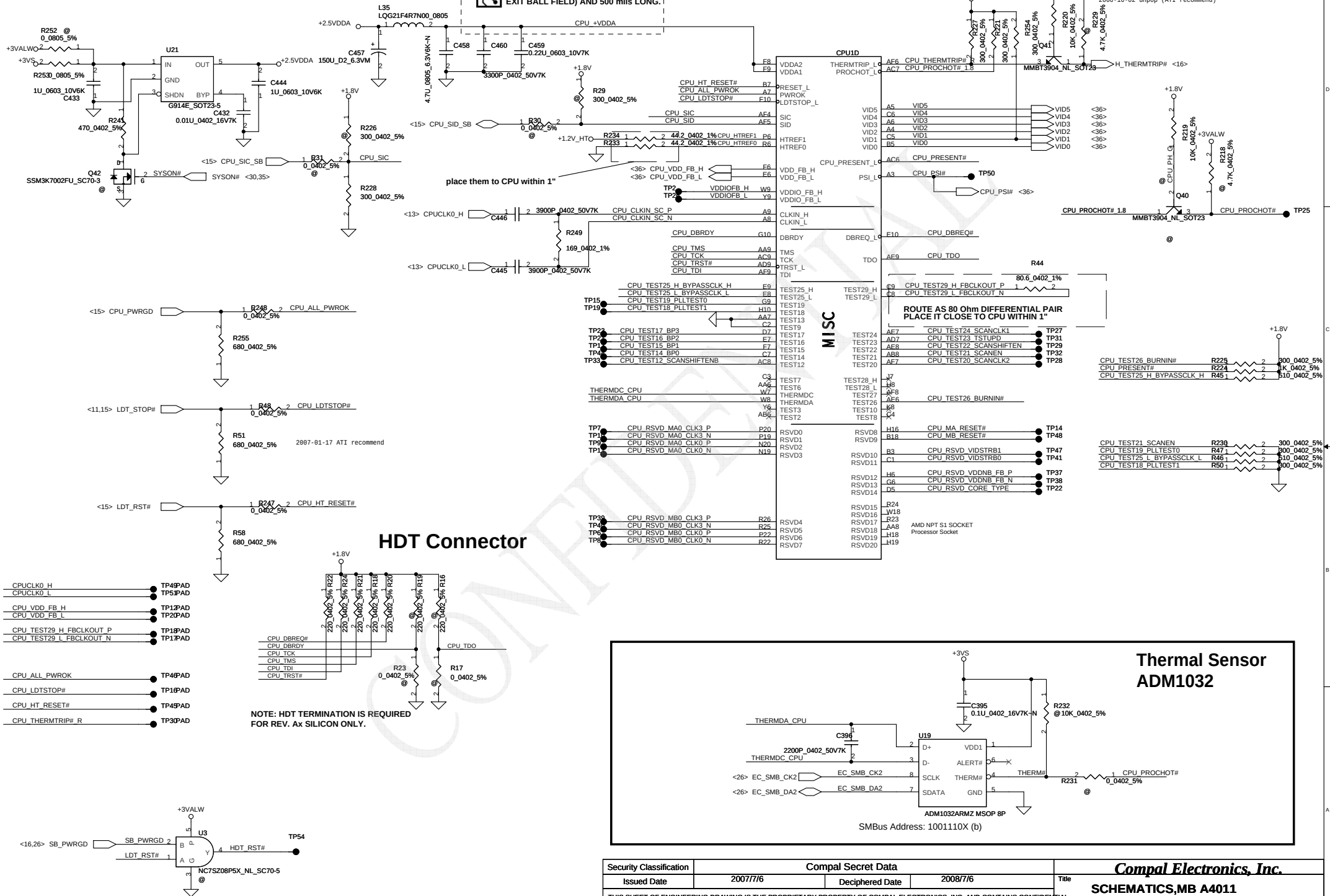
Board ID	PCB Revision
0	0.2 (KH4 ES1)
1	0.3 (KH4 ES2)
2	0.4 (KH4 PP)
3	0.5 (KH4 PPR)
4	1.0 (KH4 IRT)
5	
6	
7	

[illegible]

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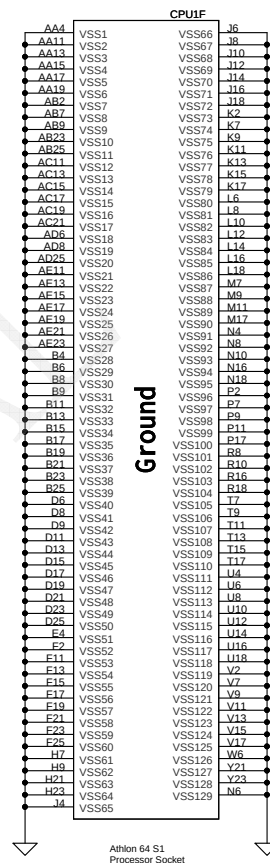
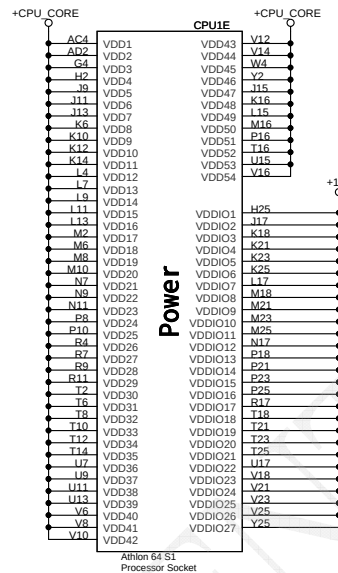
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ATHLON Control and Debug

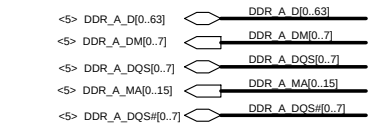
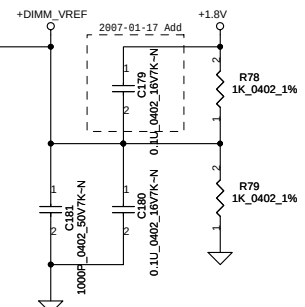
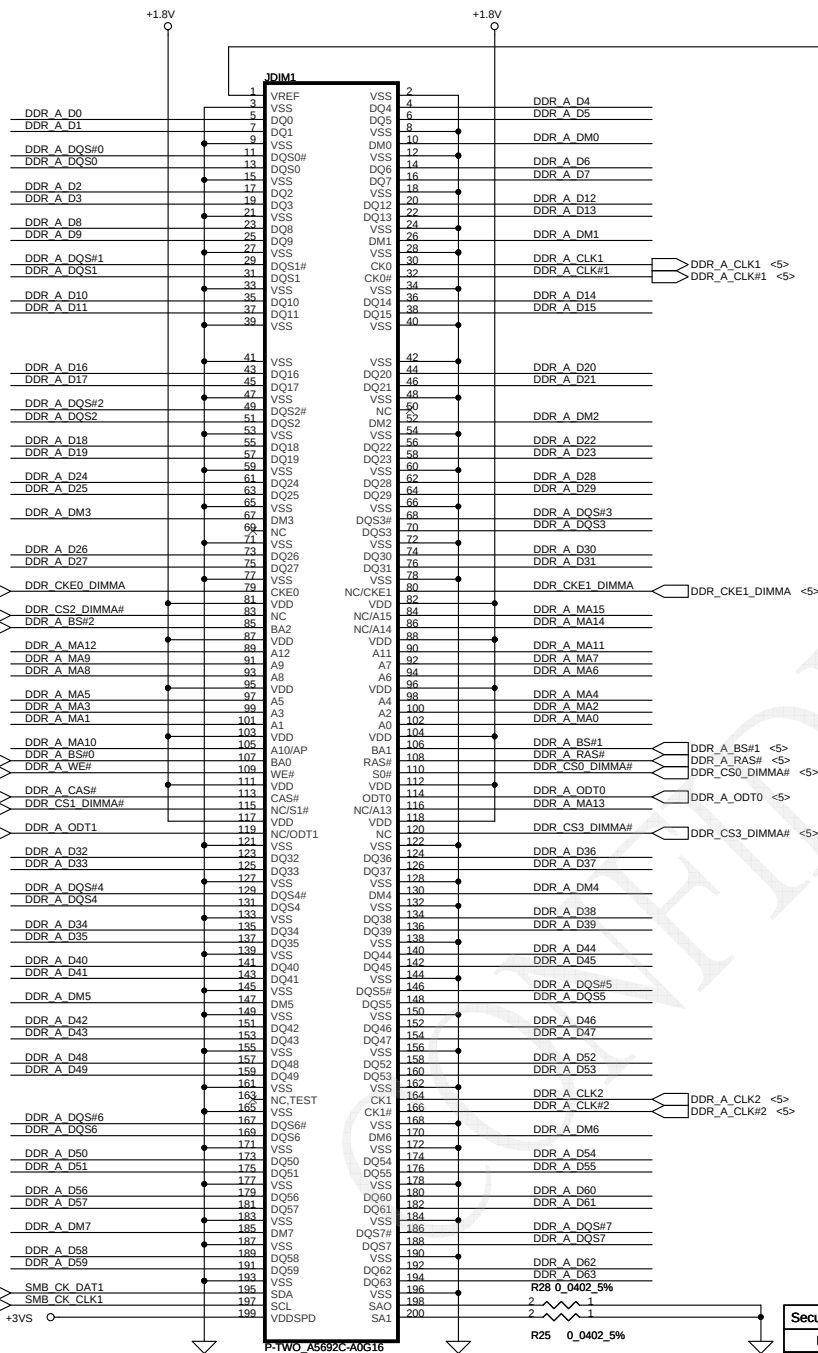


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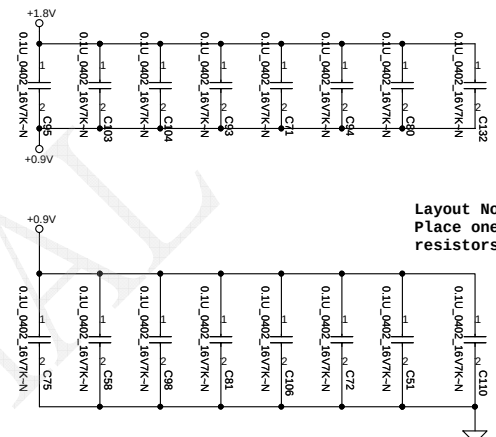
PLACE CLOSE TO SO-DIMM1 AS POSSIBLE



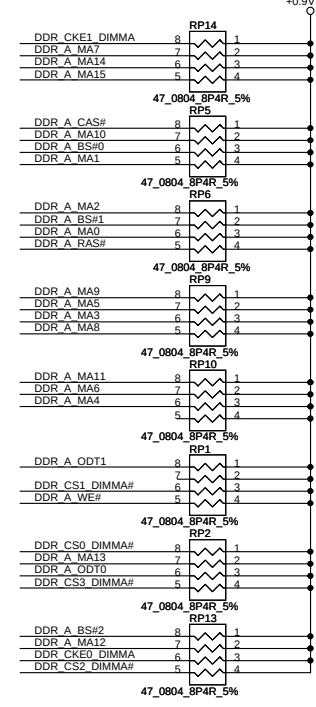
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Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9V



Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9V



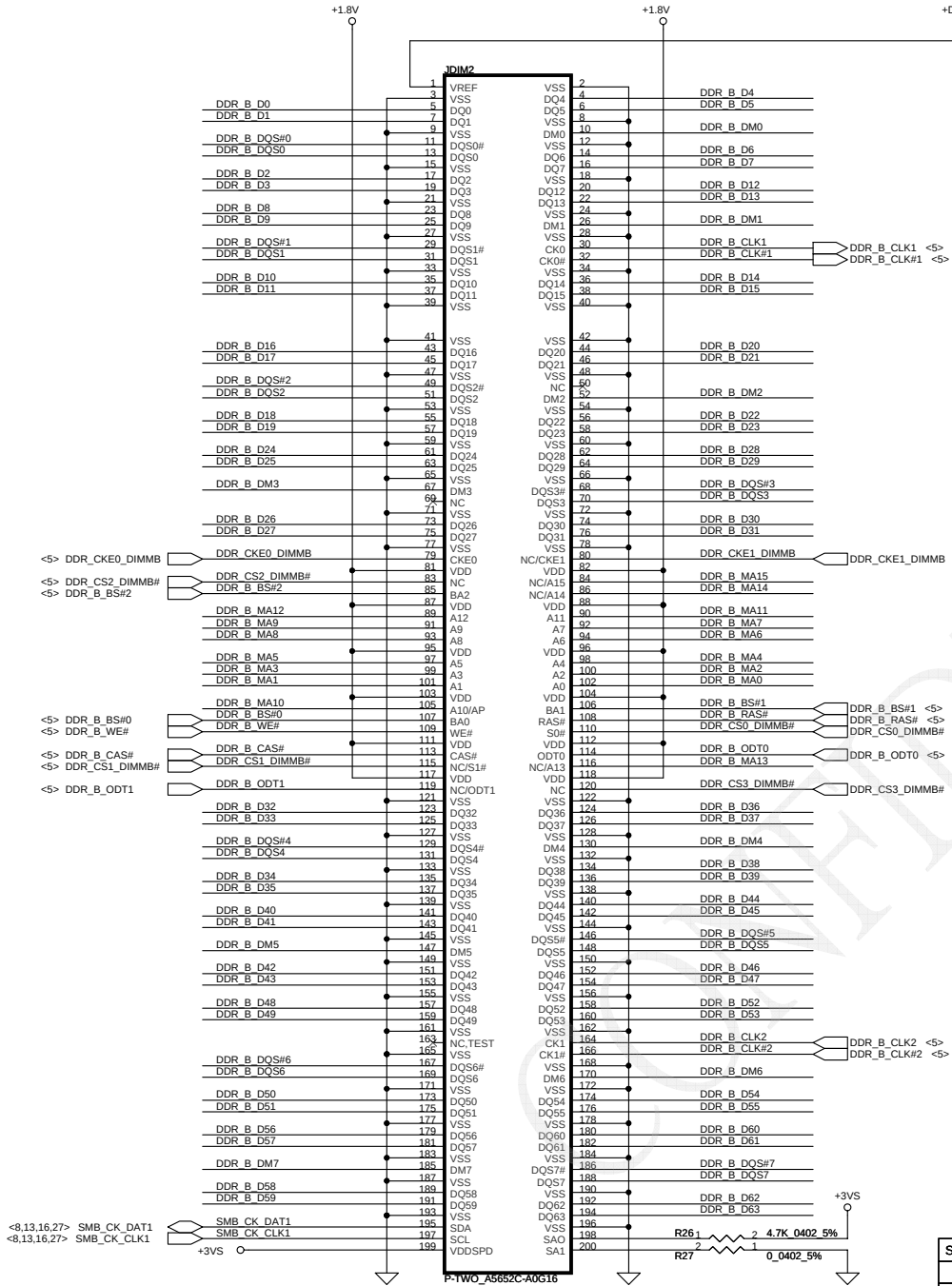
<9,13,16,27> SMB_CK_DAT1
<9,13,16,27> SMB_CK_CLK1



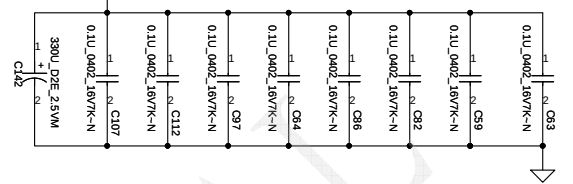
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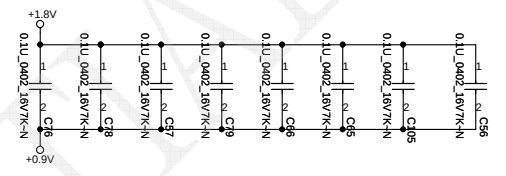
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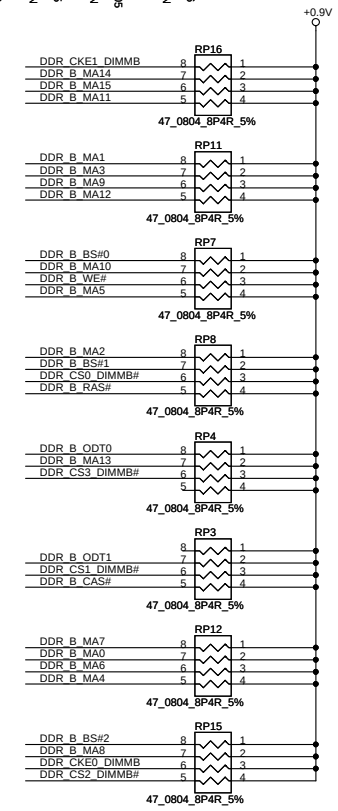
- <5> DDR_B_D[0..63] DDR B D[0..63]
- <5> DDR_B_DM[0..7] DDR B DM[0..7]
- <5> DDR_B_DQS[0..7] DDR B DQS[0..7]
- <5> DDR_B_MA[0..15] DDR B MA[0..15]
- <5> DDR_B_DQS# [0..7] DDR B DQS# [0..7]



Layout Note:
Place one cap close to every 2 pullup
resistors terminated to +0.9V



Layout Note:
Place one cap close to every 2 pullup
resistors terminated to +0.9V



<8,13,16,27> SMB_CK_DAT1
<8,13,16,27> SMB_CK_CLK1



P-TWO_A5652C-A0G16

CONN@

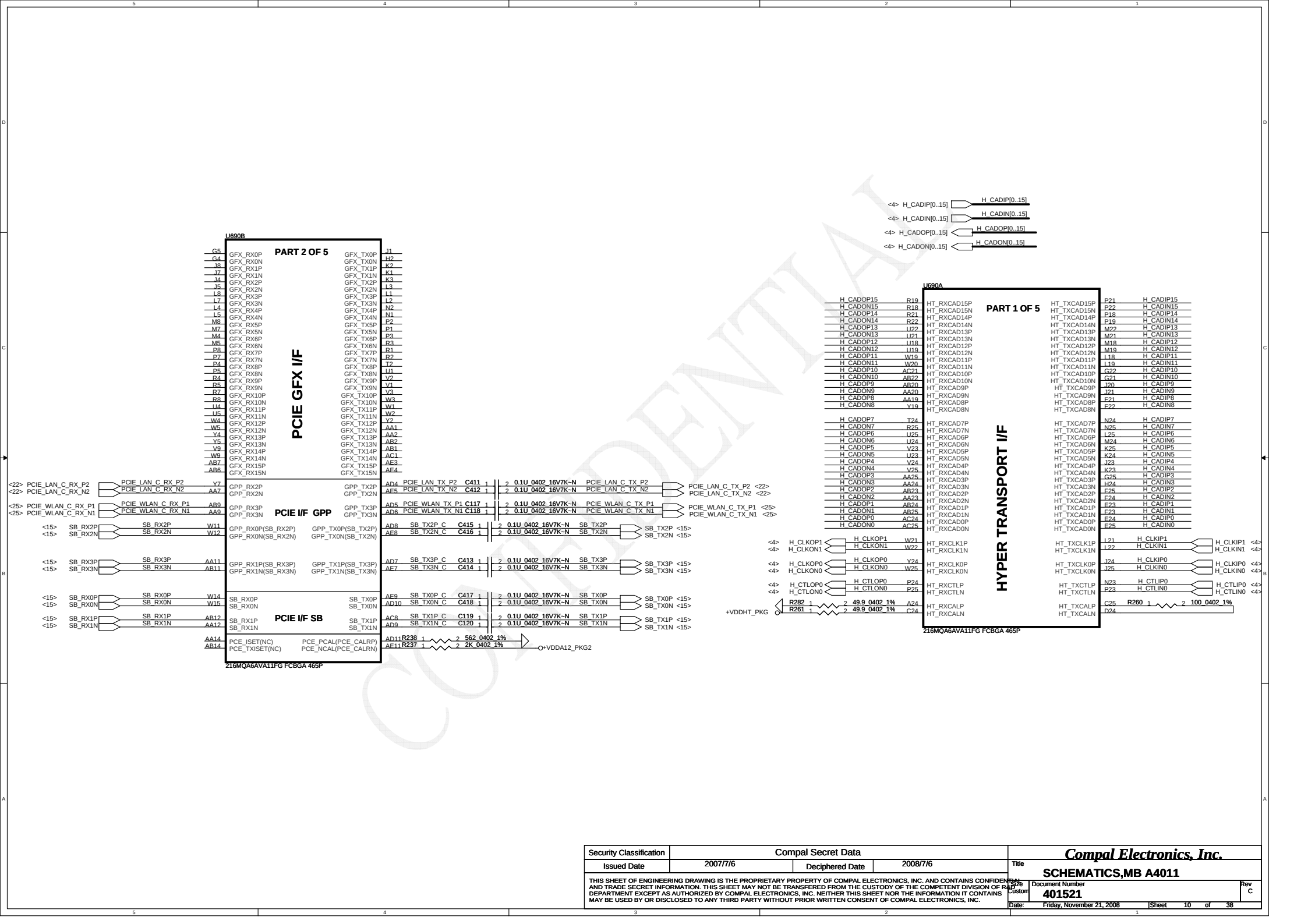
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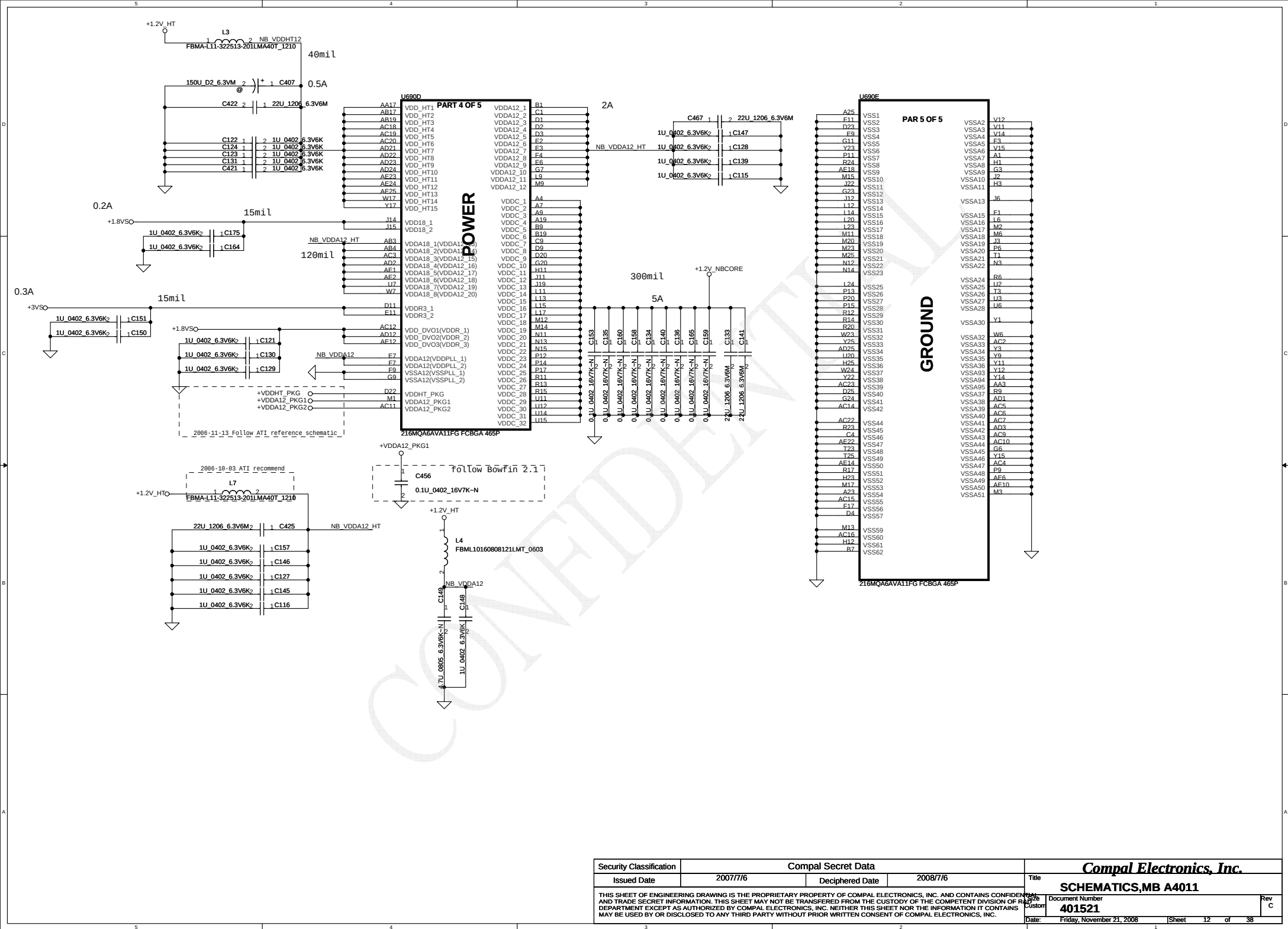
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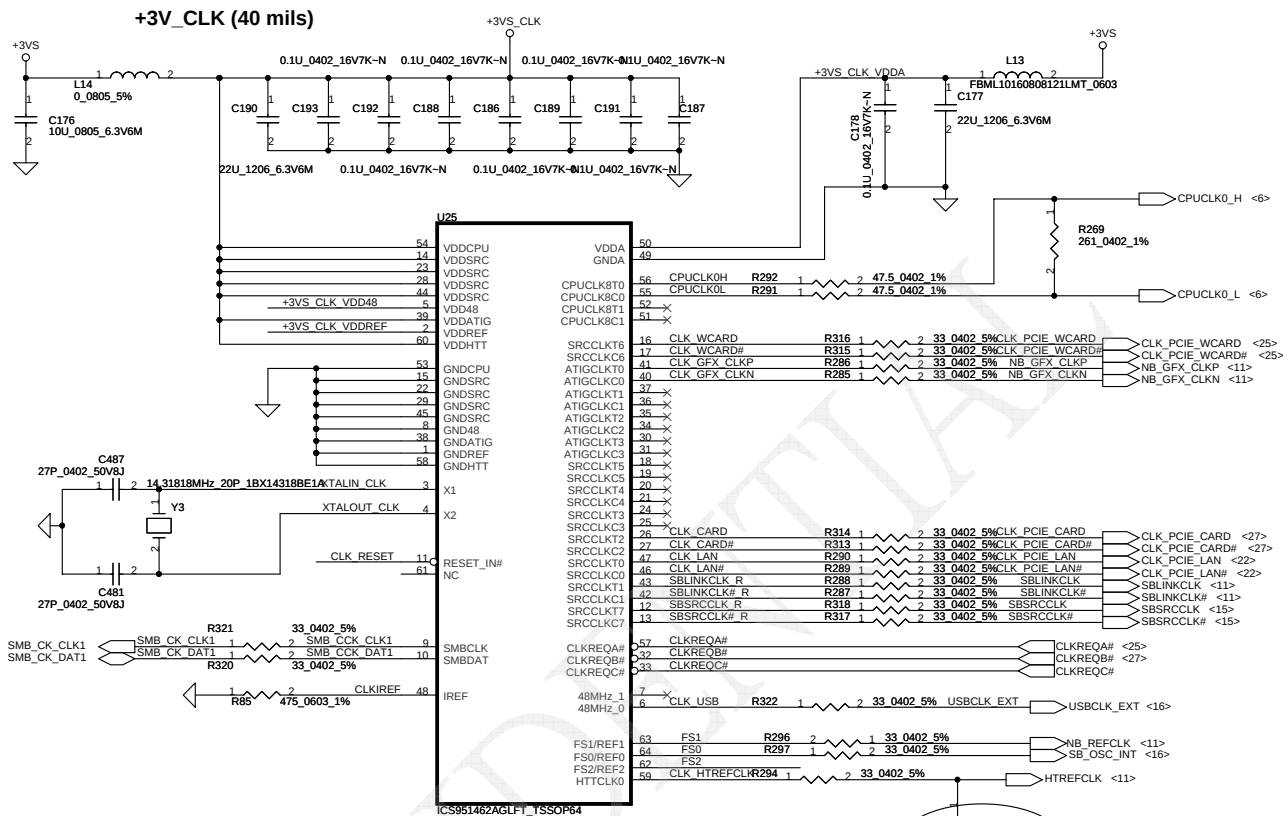
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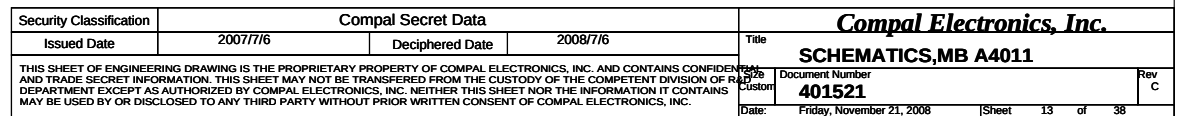


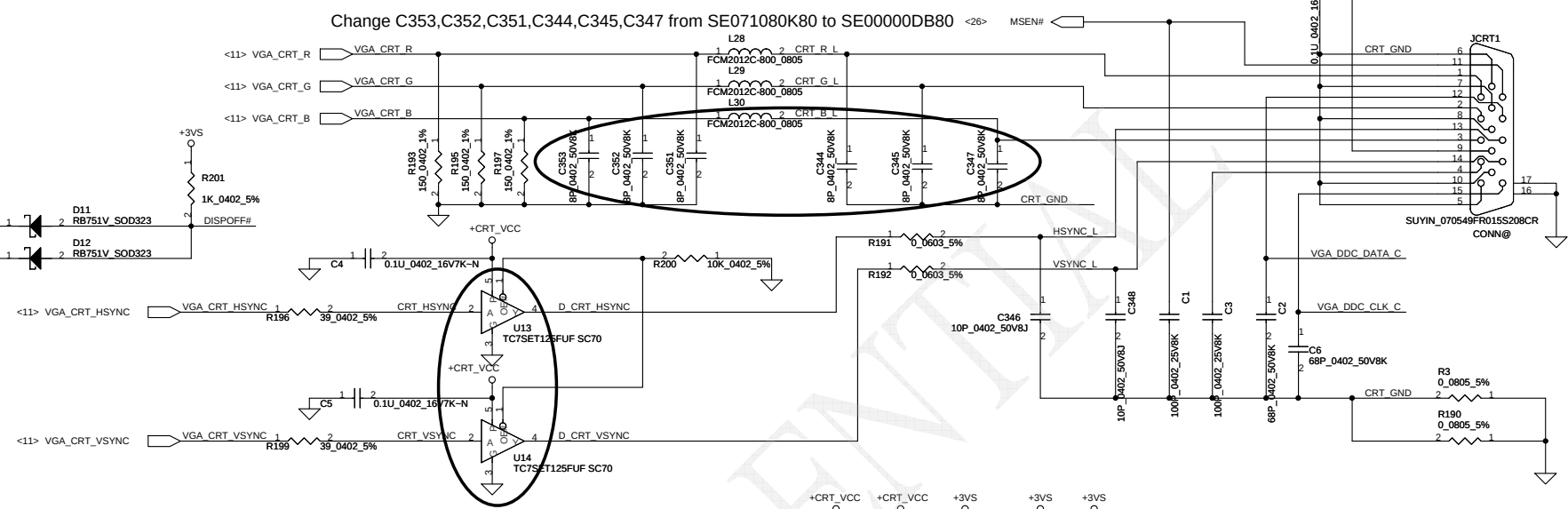


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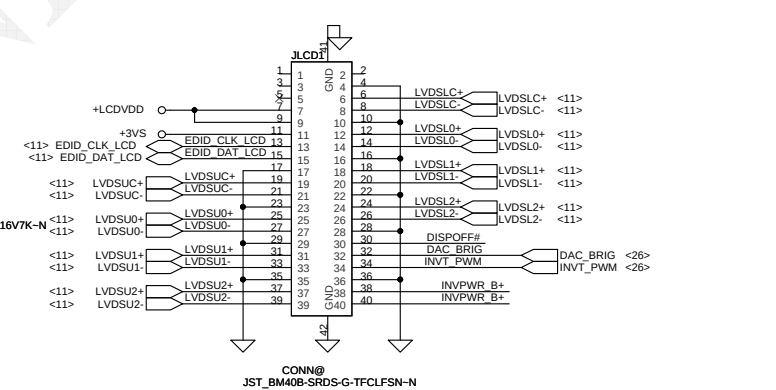
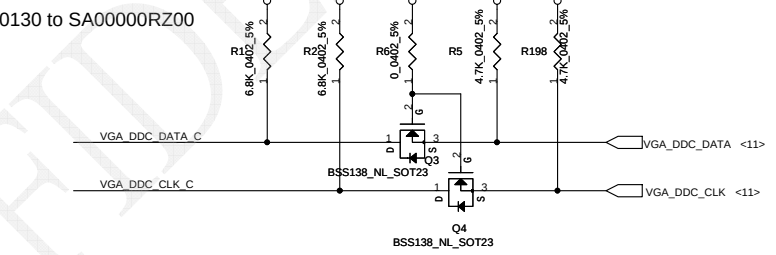
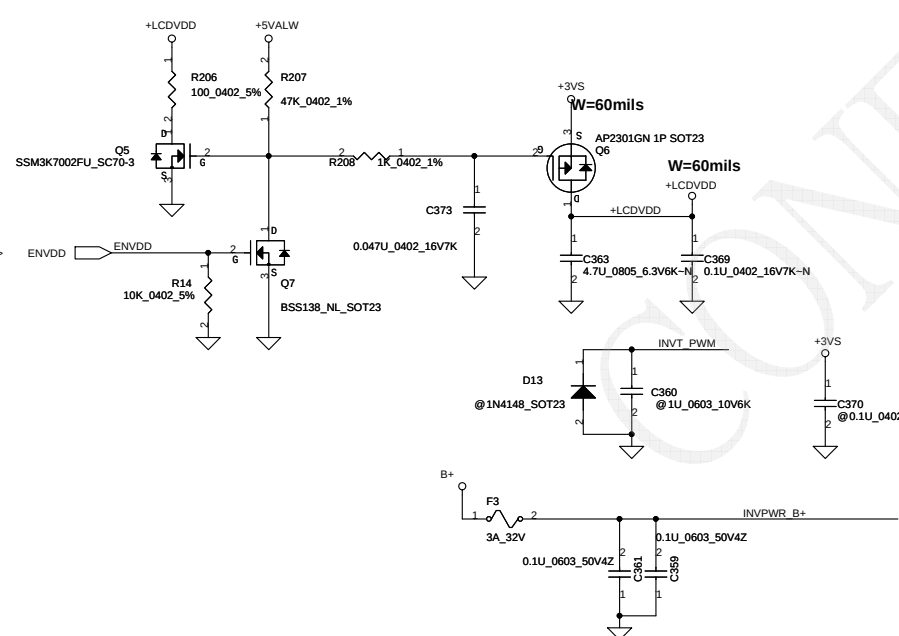


FS2	FS1	FS0	CPU	SRCCLK [2:1]	HTT	PCI	USB	COMMENT
0	0	0	Hi-Z	100.00	Hi-Z	Hi-Z	48.00	Reserved
0	0	1	X	100.00	X/3	X/6	48.00	Reserved
0	1	0	180.00	100.00	60.00	30.00	48.00	Reserved
0	1	1	220.00	100.00	36.56	73.12	48.00	Reserved
1	0	0	100.00	100.00	66.66	33.33	48.00	Reserved
1	0	1	133.33	100.00	66.66	33.33	48.00	Reserved
1	1	1	200.00	100.00	66.66	33.33	48.00	Normal ATHLON64 operation

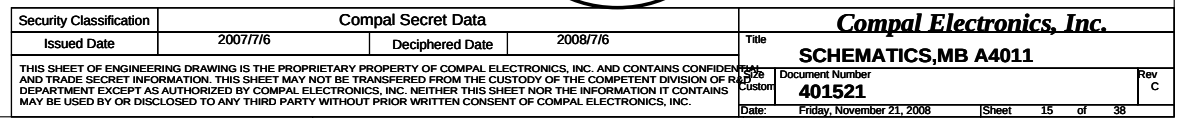


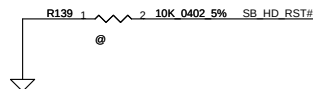
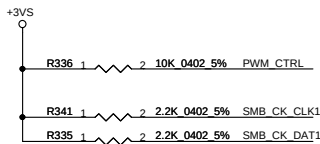
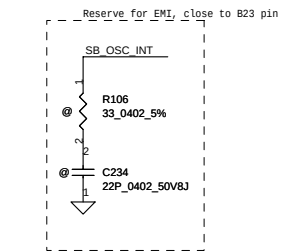


LCD POWER CIRCUIT

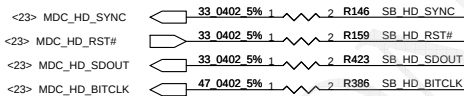


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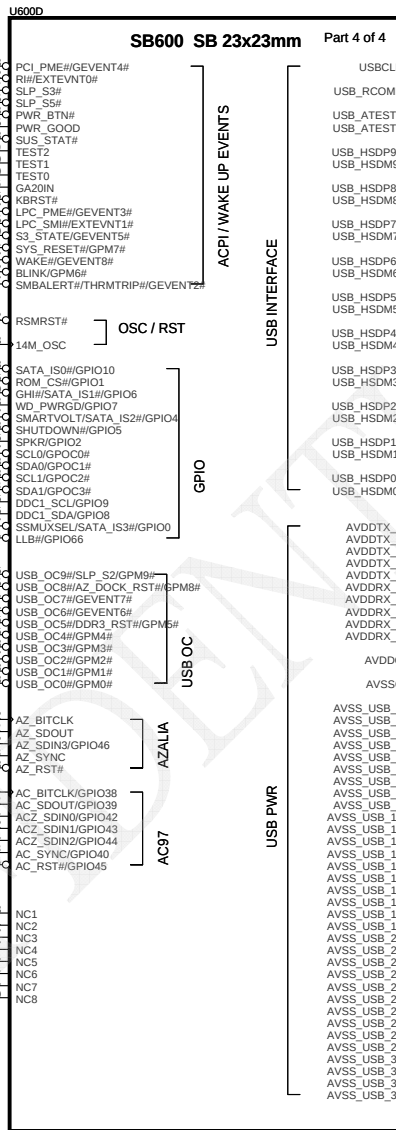
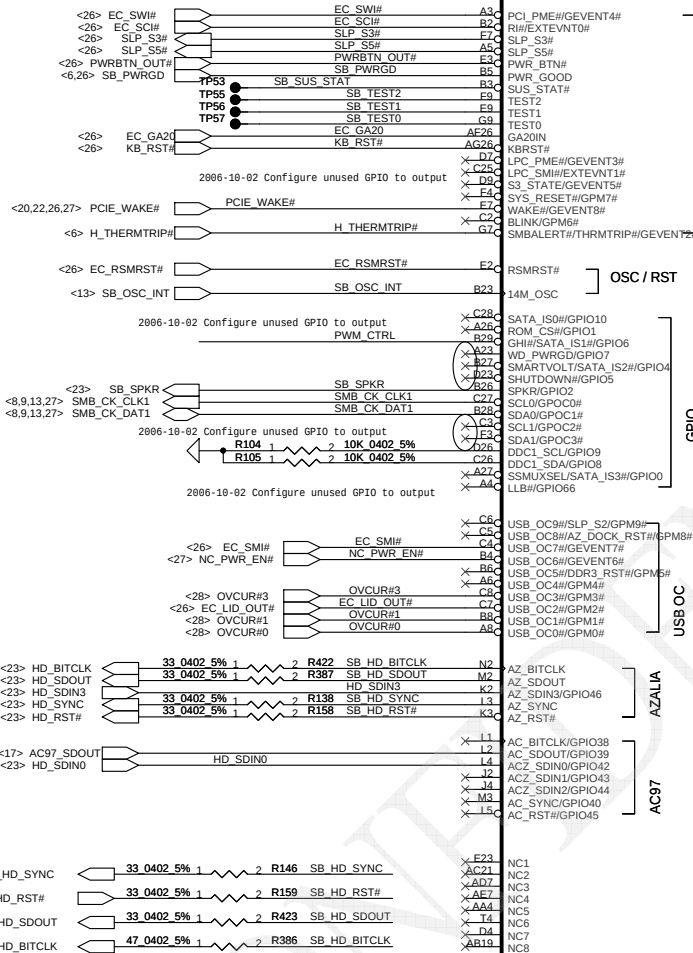




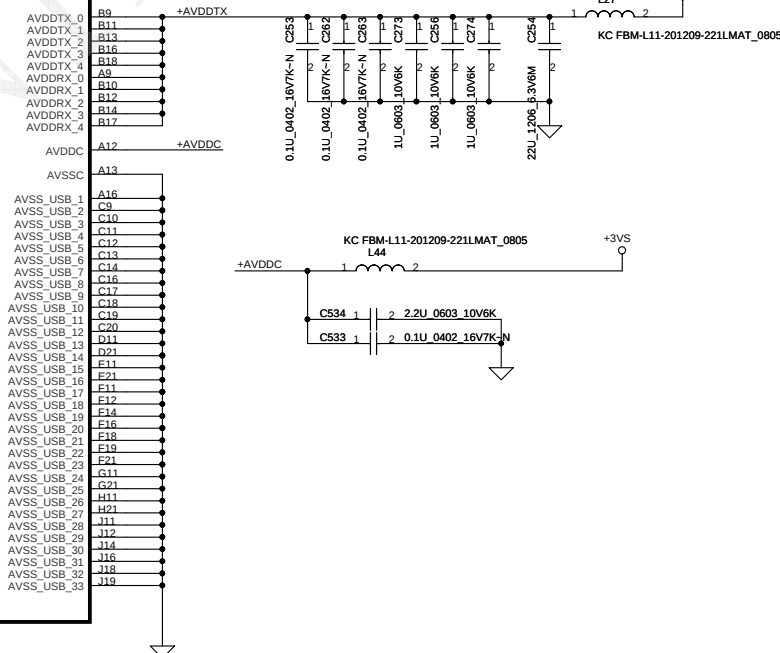
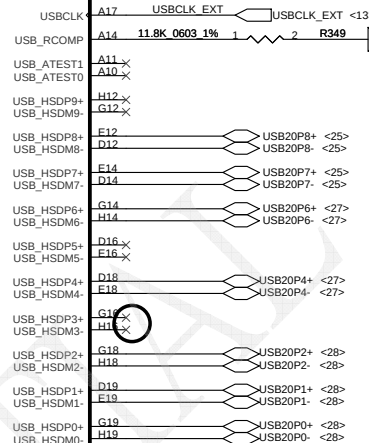
2007-01-15
Change SDIN to AZ_SDIN3



Change R386 from 33ohm to 47ohm (For EMI)



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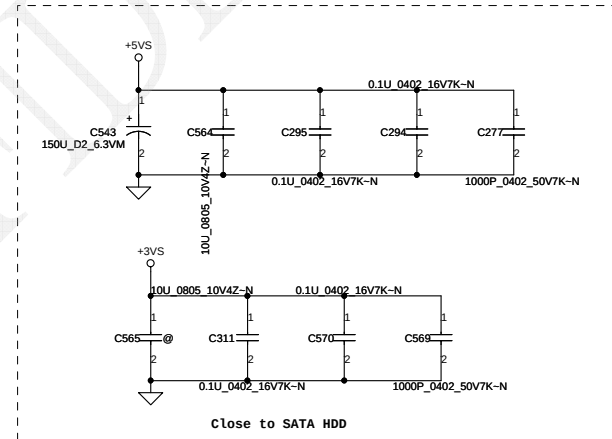
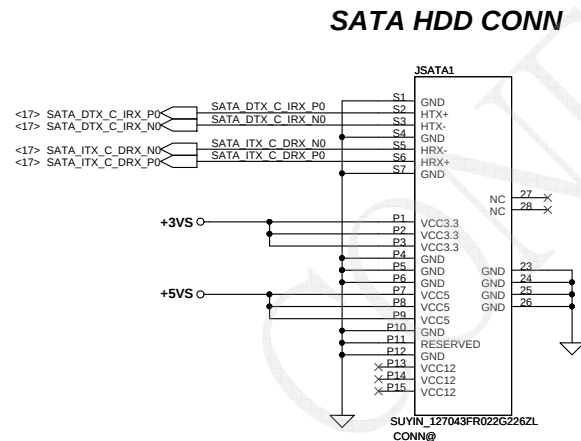
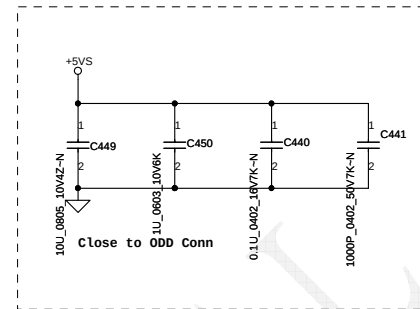
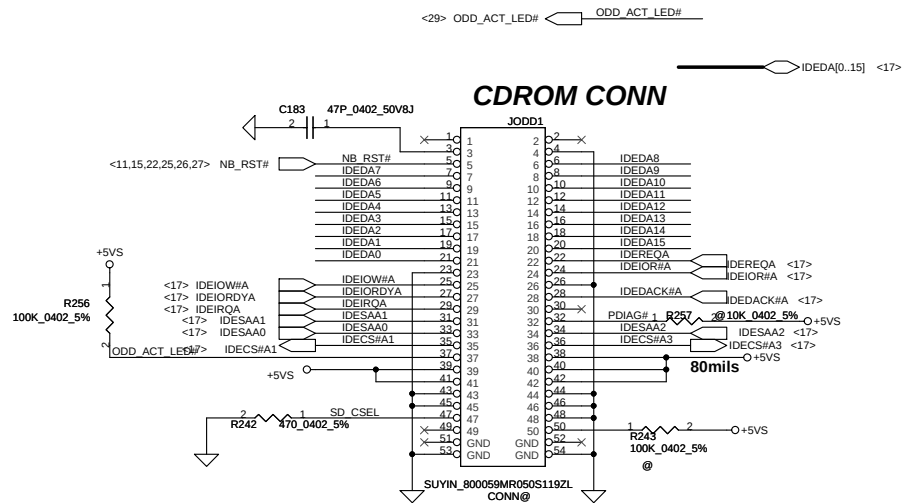
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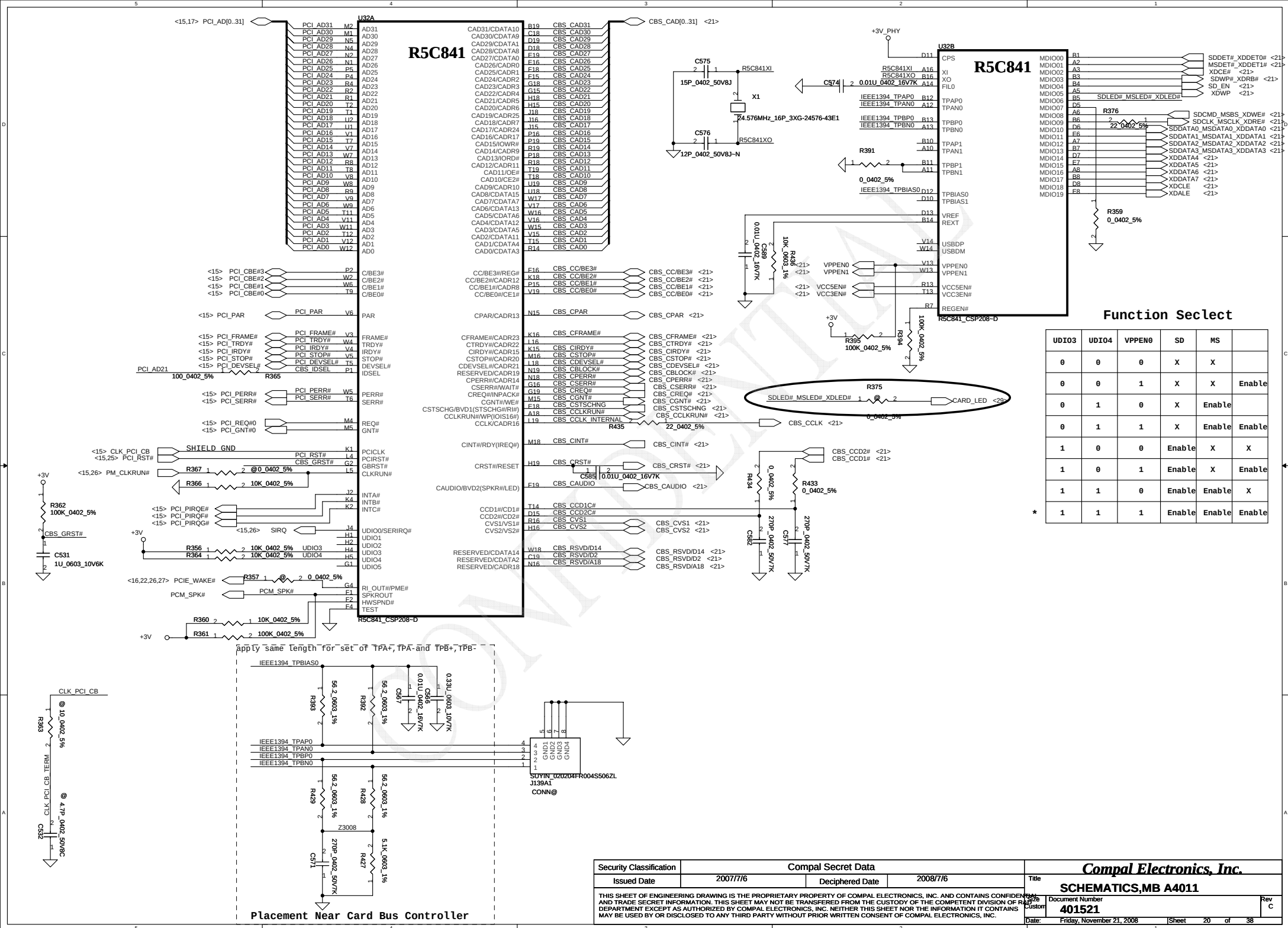
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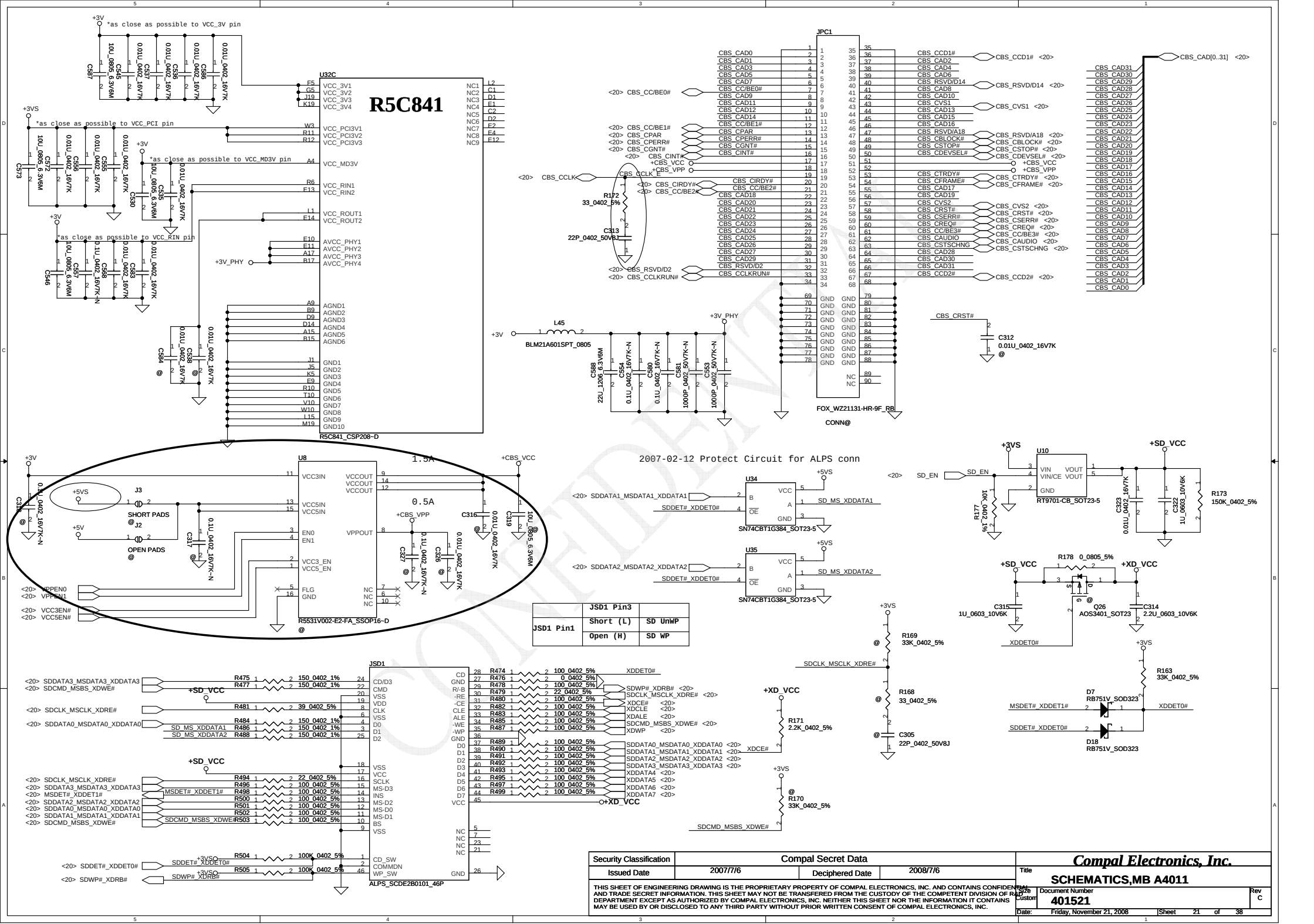
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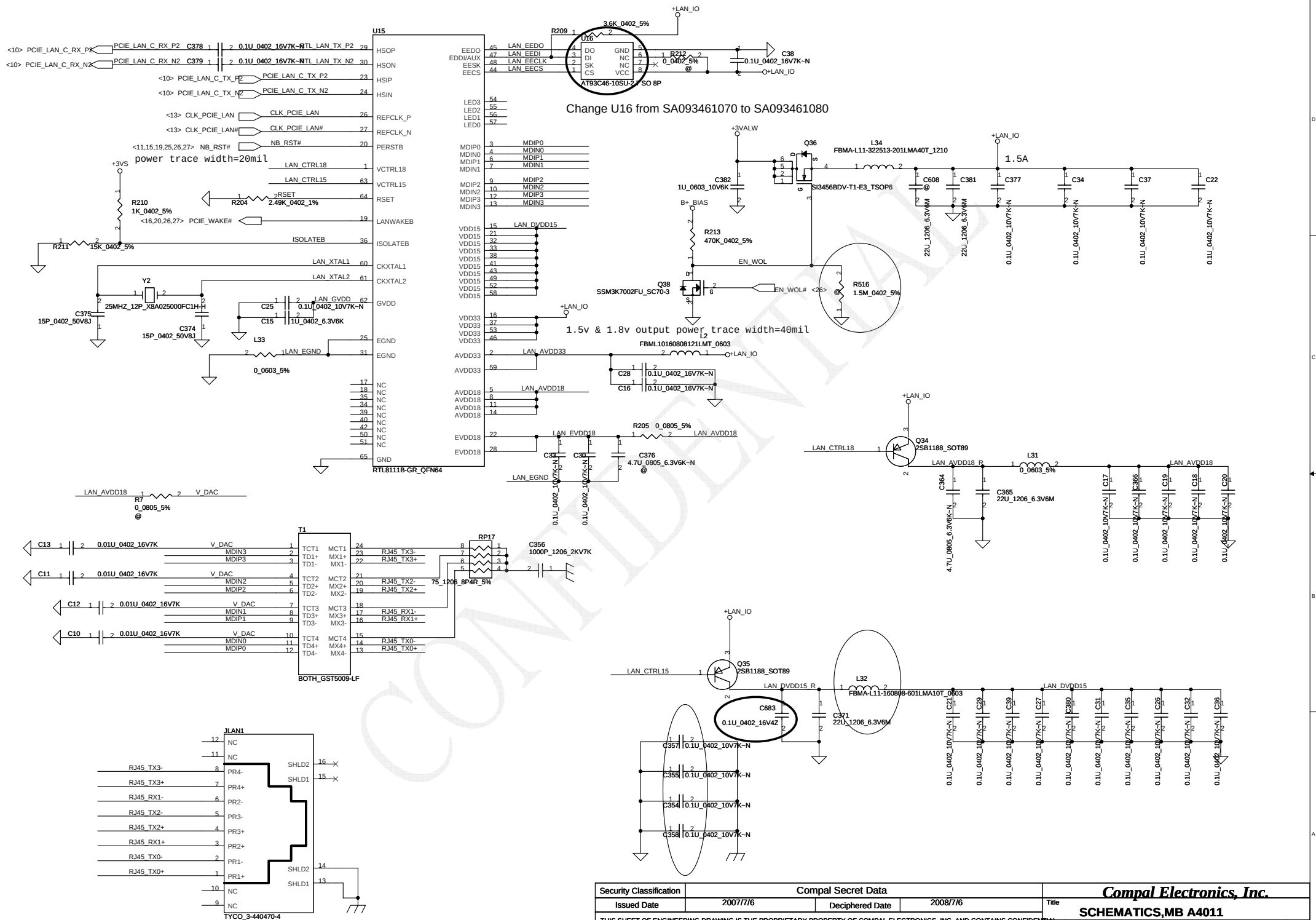
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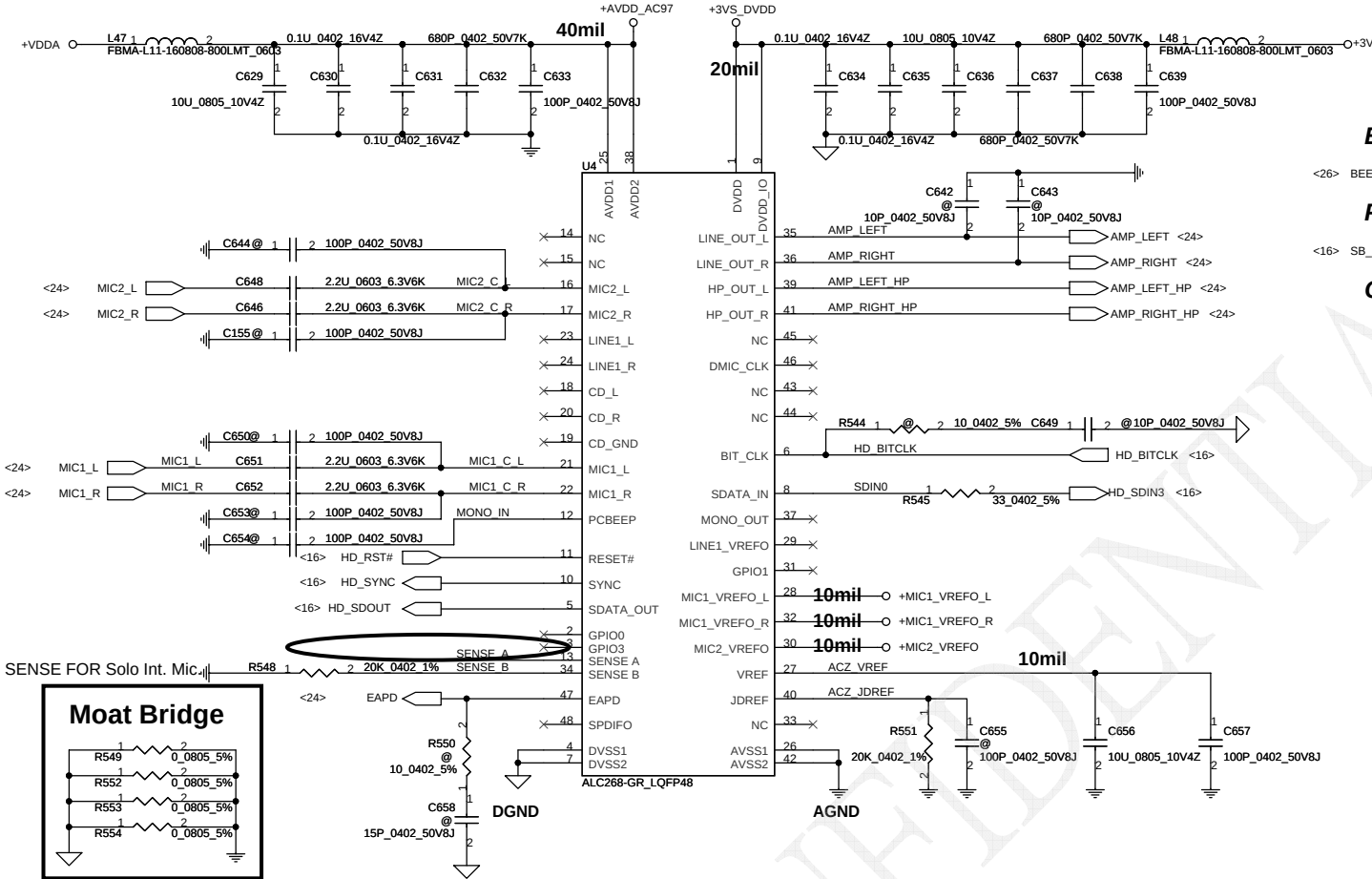






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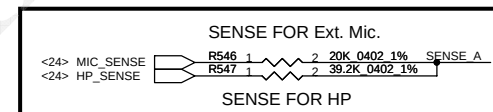
HD Audio Codec



EC Beep

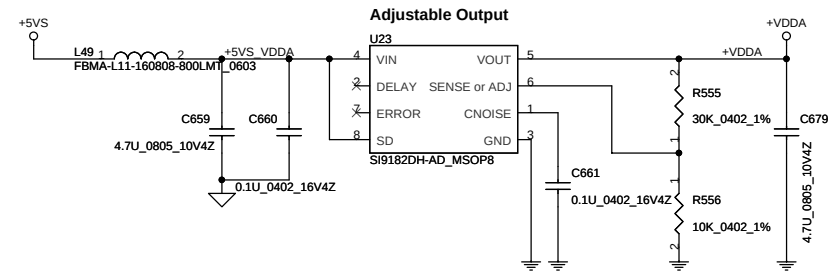
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CardBus Beep

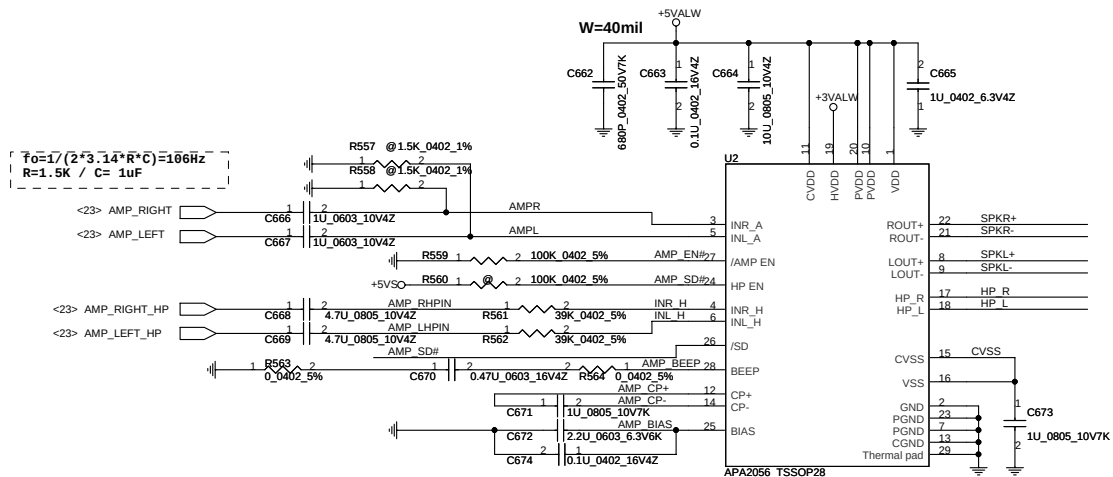


Sense Pin	Impedance	Codec Signals	Funnnction
SENSE A / B	39.2K	PORT-A (PIN 39, 41)	HP
	20K	PORT-B (PIN 21, 22)	MIC
	10K	PORT-C (PIN 23, 24)	LINE IN
	5.1K	PORT-D (PIN 35, 36)	LINE Out
SENSE B	39.2K	PORT-E (PIN 14, 15)	HP
	20K	PORT-F (PIN 16, 17)	MIC
	10K	PORT-G (PIN 43, 44)	LINE IN
	5.1K	PORT-H (PIN 45, 46)	LINE Out

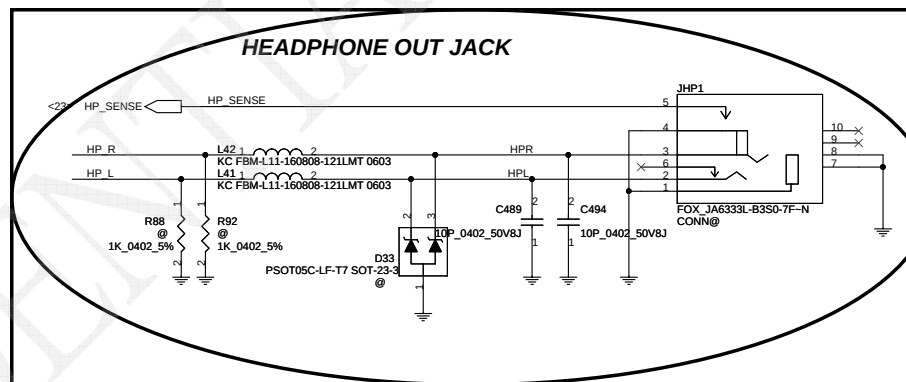
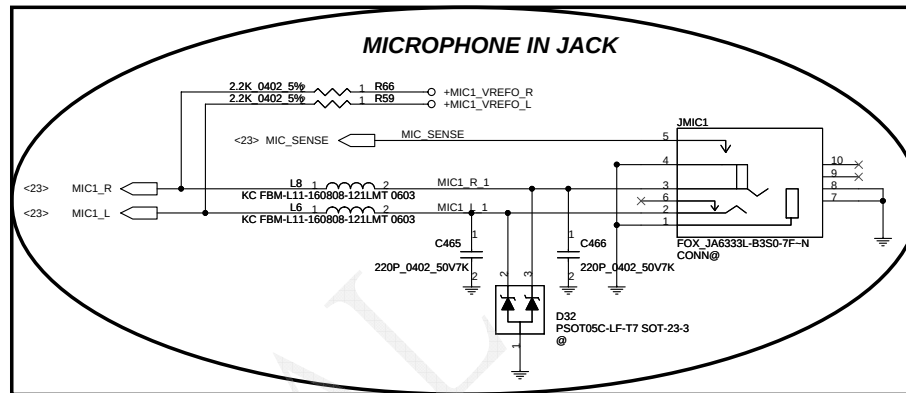
Regulator for CODEC



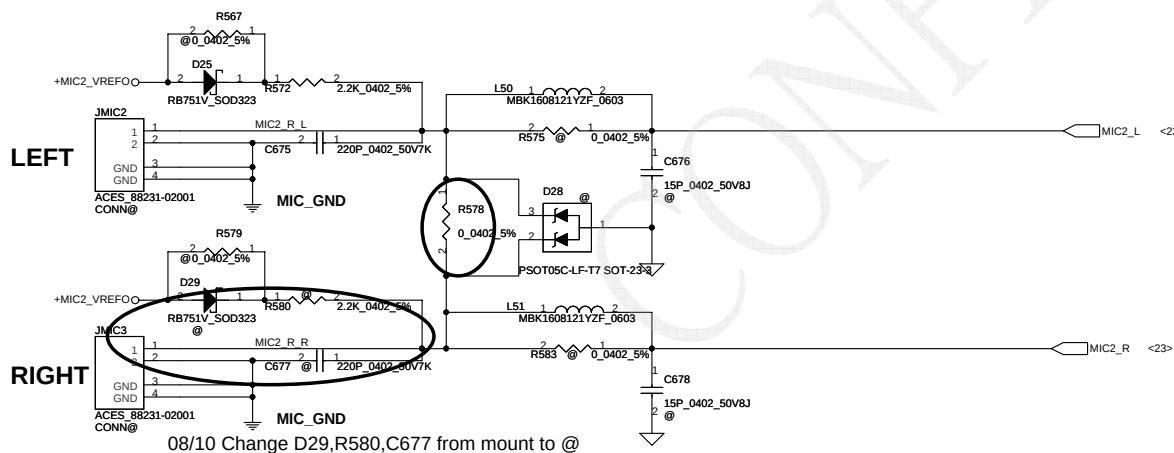
APA2056 SPK/HP Amplifier



IN_A Gain = 10dB (Internal Speaker)
 IN_H Gain = 0dB (Headphone)

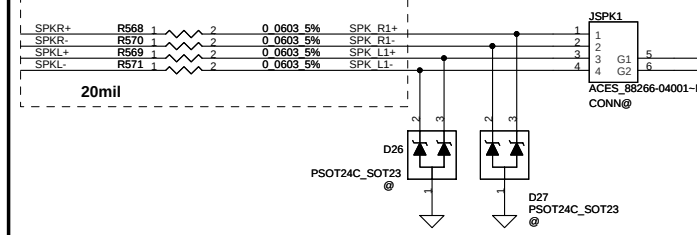


MIC

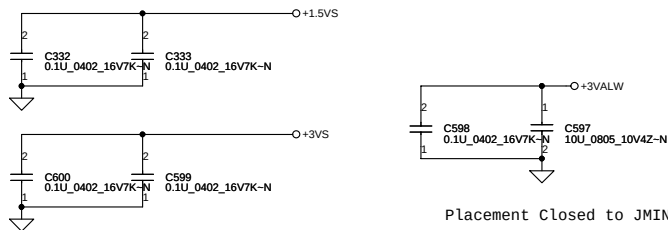


08/10 Change D29, R580, C677 from mount to @

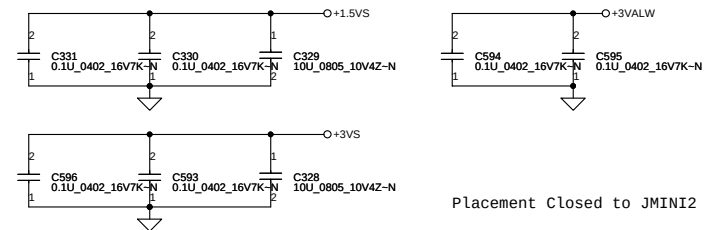
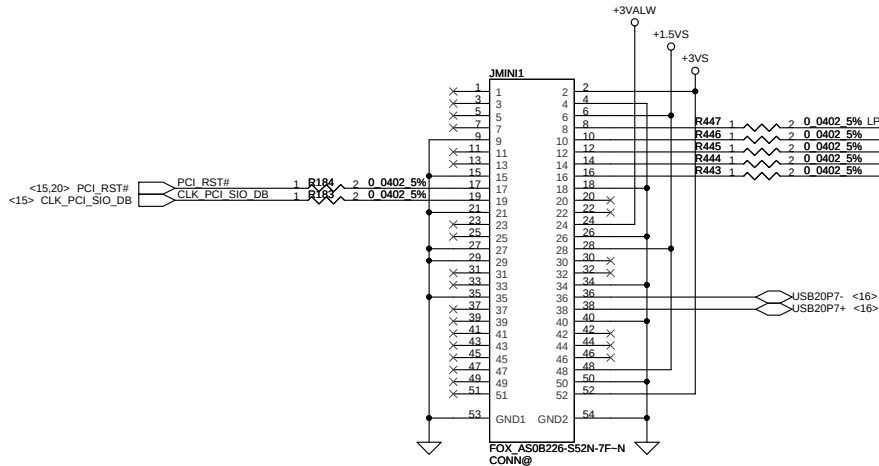
Speaker Conn.



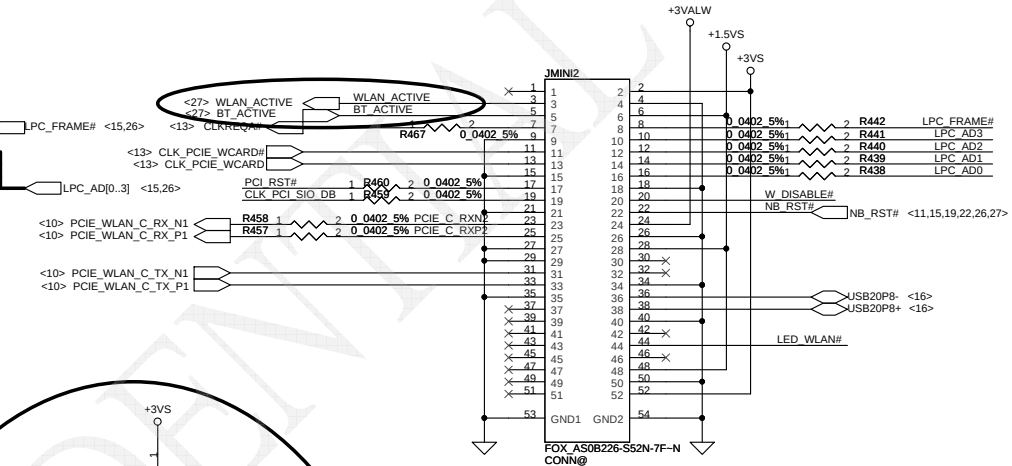
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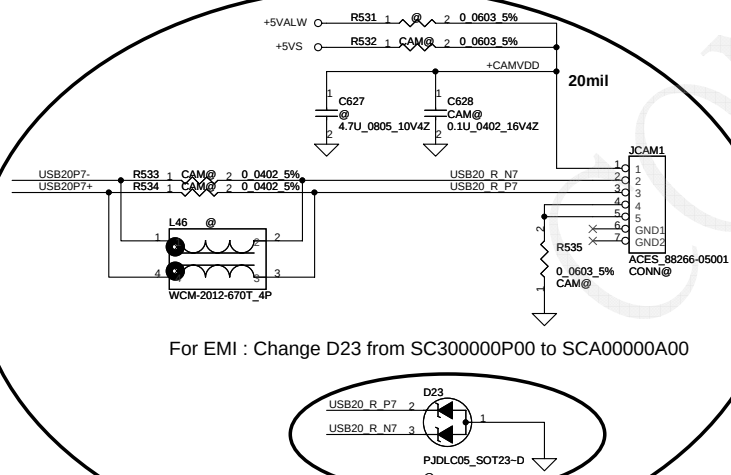
Mini-Express Card (Reserve for debug or TV)



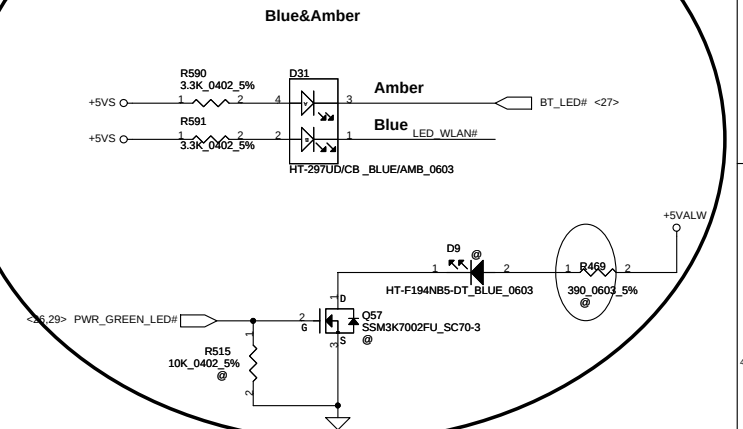
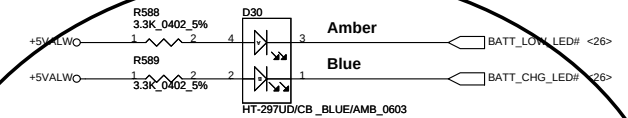
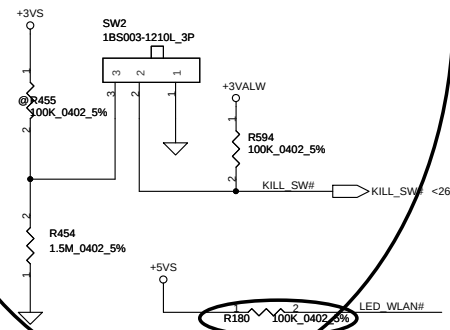
Mini-Express Card



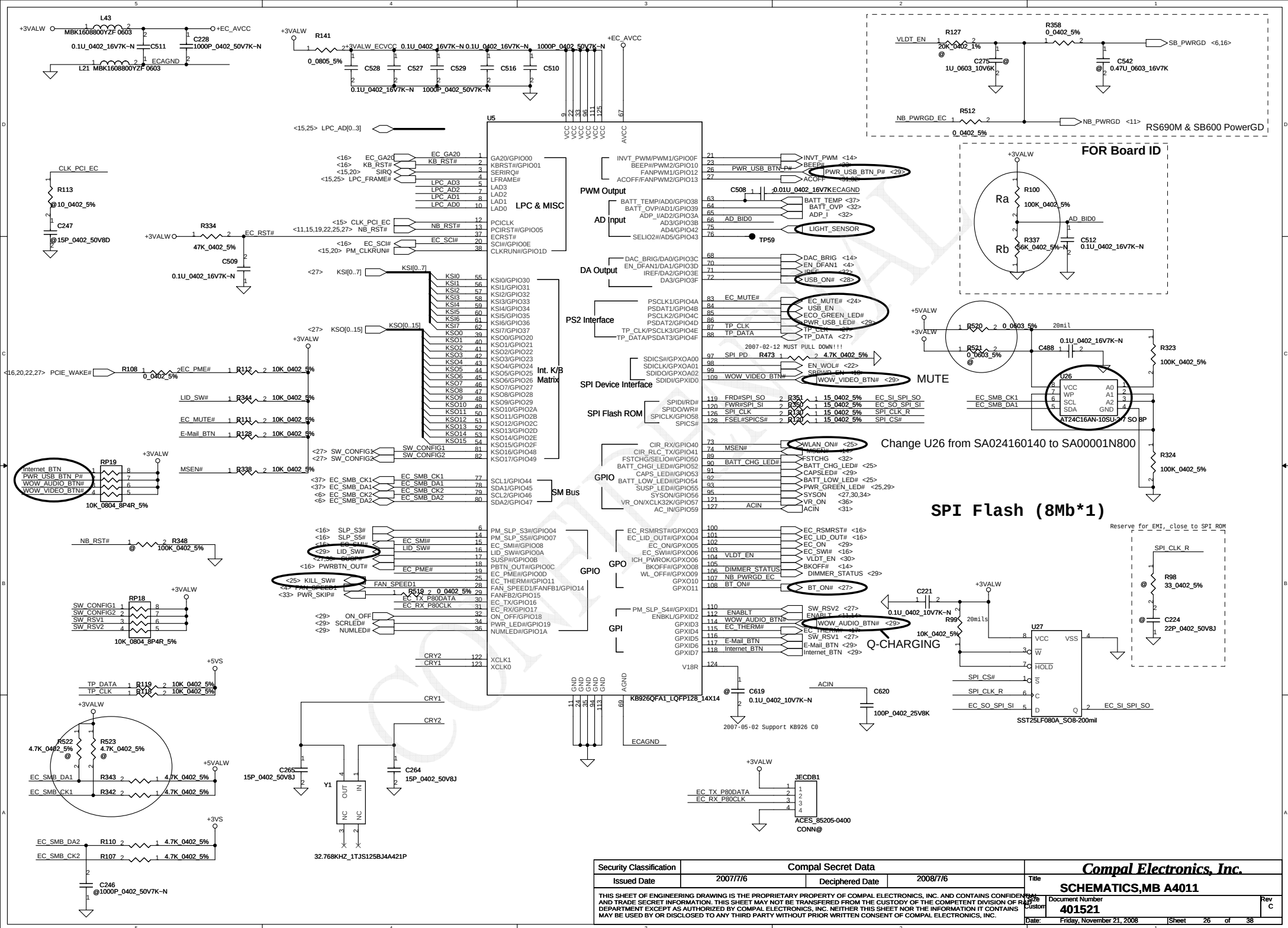
CAMERA



Wireless BTN Killer switch



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BT

KSO8	C209	@100P_0402_25V8K
KSI3	C198	@100P_0402_25V8K
KSO9	C208	@100P_0402_25V8K
KSI2	C199	@100P_0402_25V8K
KSI1	C200	@100P_0402_25V8K
KSO10	C207	@100P_0402_25V8K
KSO11	C206	@100P_0402_25V8K
KSI0	C201	@100P_0402_25V8K
KSO12	C205	@100P_0402_25V8K
KSO13	C204	@100P_0402_25V8K
KSO14	C203	@100P_0402_25V8K
KSO15	C202	@100P_0402_25V8K

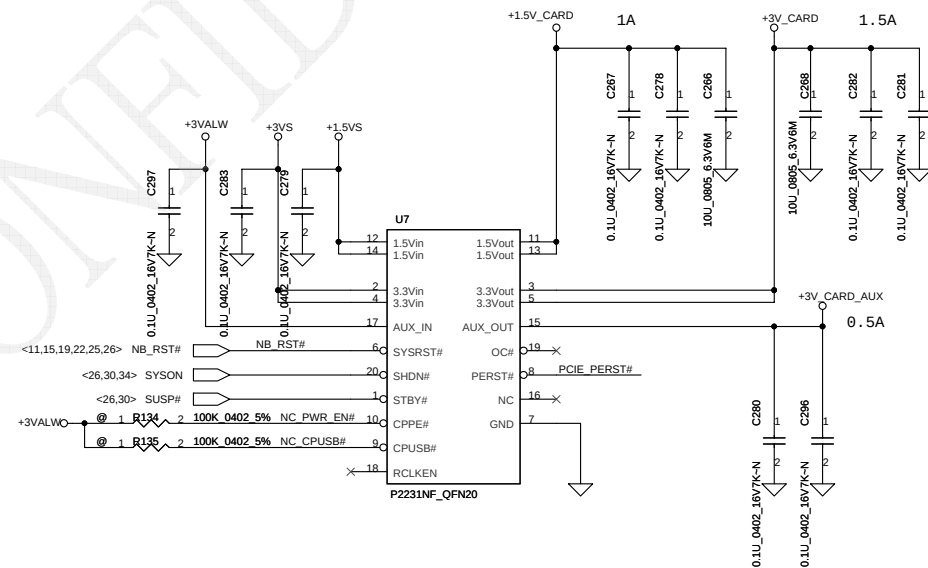
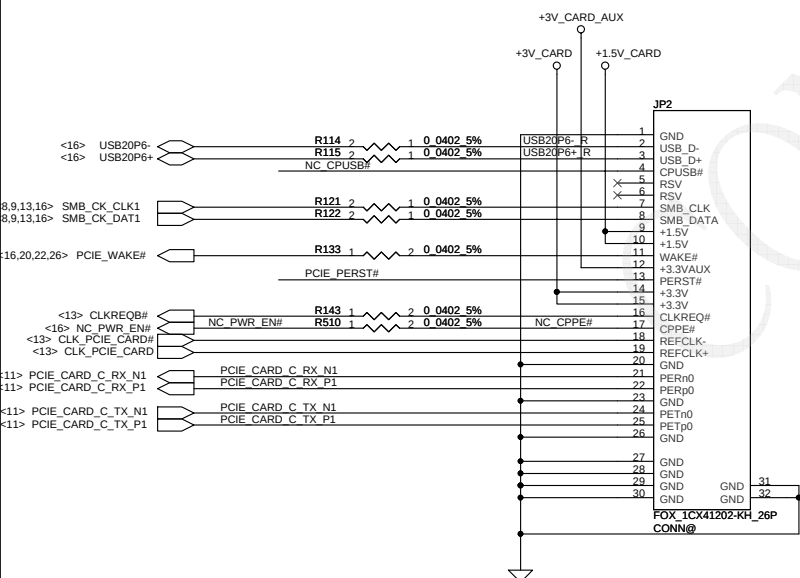
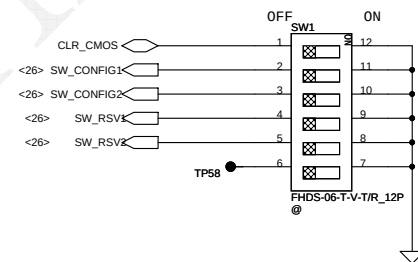
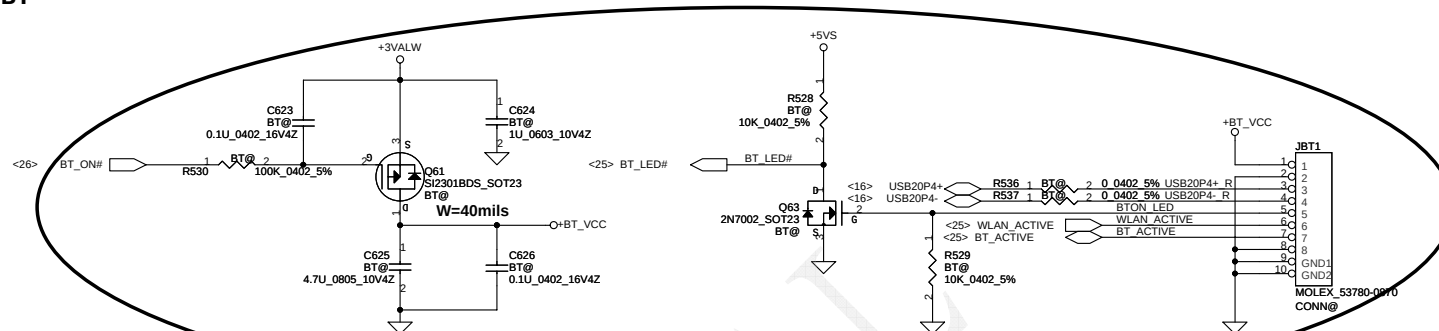
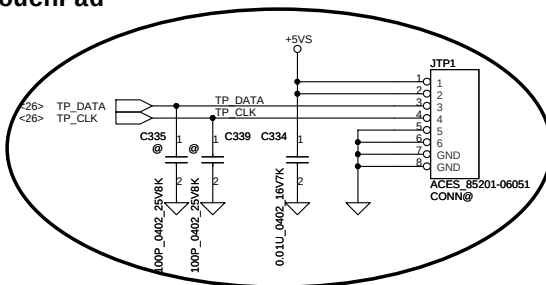
KSI7	C194	@100P_0402_25V8K
KSI6	C195	@100P_0402_25V8K
KSI5	C196	@100P_0402_25V8K
KSO0	C217	@100P_0402_25V8K
KSO1	C216	@100P_0402_25V8K
KSO2	C215	@100P_0402_25V8K
KSI4	C197	@100P_0402_25V8K
KSO3	C214	@100P_0402_25V8K
KSO4	C213	@100P_0402_25V8K
KSO5	C212	@100P_0402_25V8K
KSO6	C211	@100P_0402_25V8K
KSO7	C210	@100P_0402_25V8K

INT_KBD CONN.

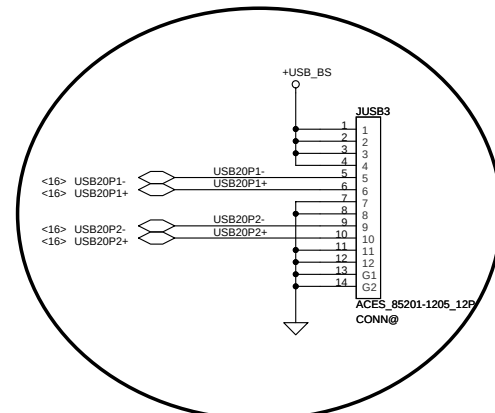
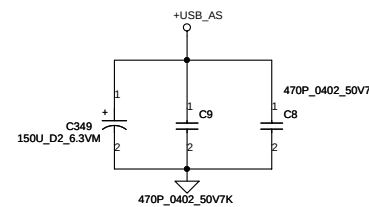
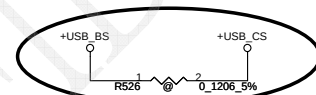
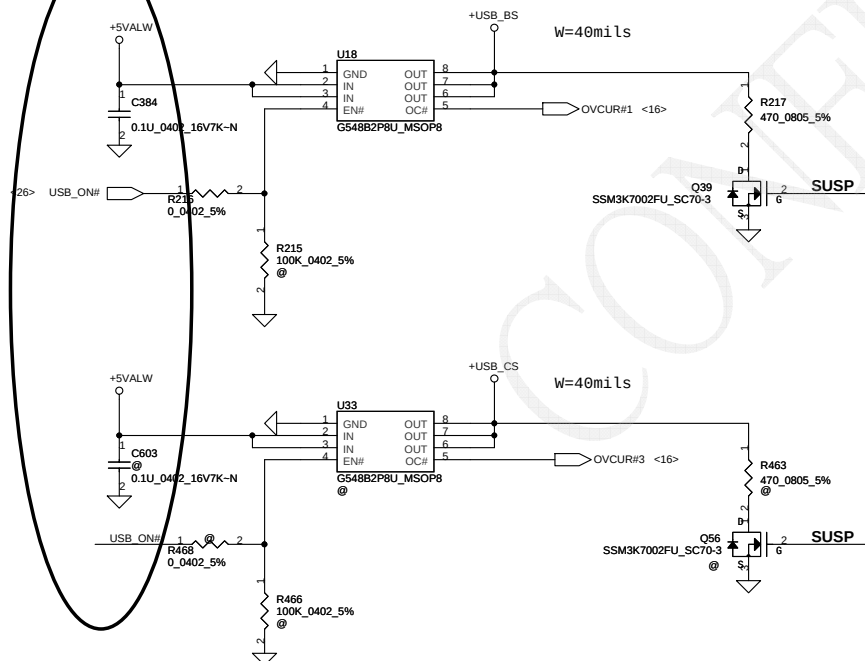
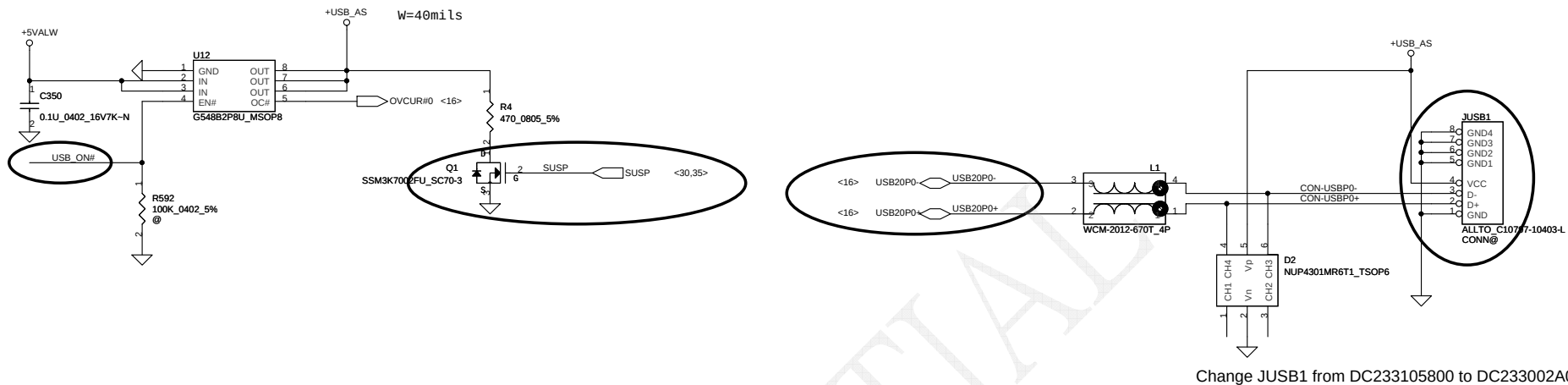
KSI1	25	25
KSI7	24	24
KSI6	23	23
KSO9	22	22
KSI4	21	21
KSI5	20	20
KSI2	19	19
KSO0	18	18
KSI3	17	17
KSO5	16	16
KSO1	15	15
KSI0	14	14
KSO2	13	13
KSO4	12	12
KSO7	11	11
KSO8	10	10
KSO3	9	9
KSO12	8	8
KSO13	7	7
KSO14	6	6
KSO11	5	5
KSO10	4	4
KSO15	3	3
	2	2
	1	1

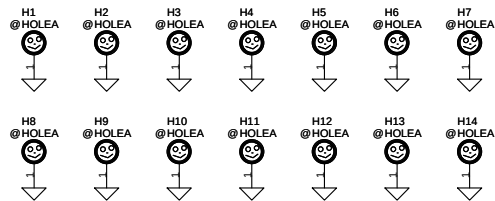
KSI[0..7]	KSI[0..7]	<26>
KSO[0..15]	KSO[0..15]	<26>

TouchPad

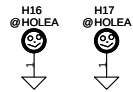


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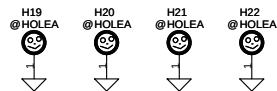
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H_C216D144



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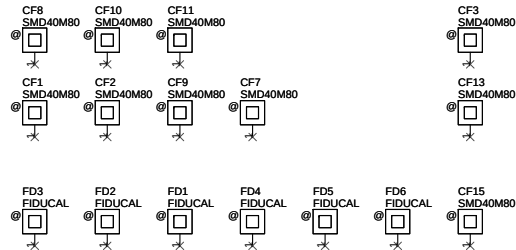
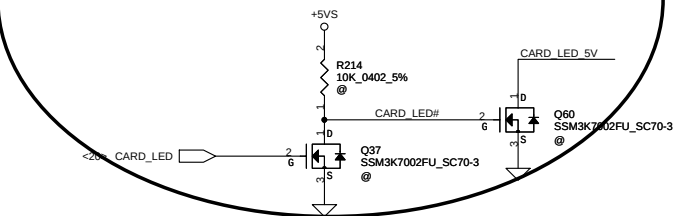
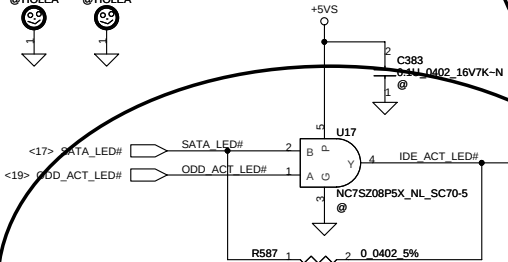
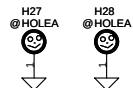
H_C276D165 (CPU)



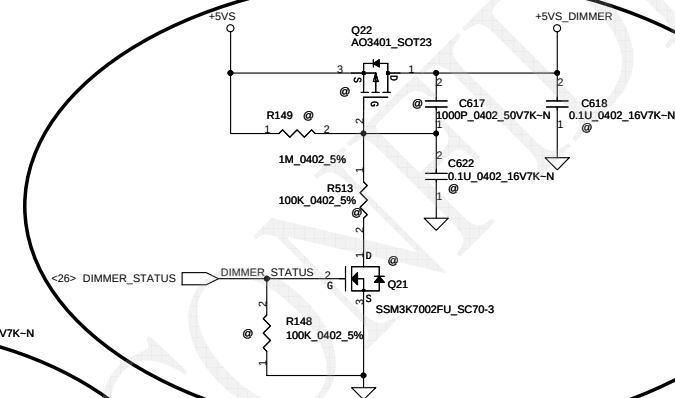
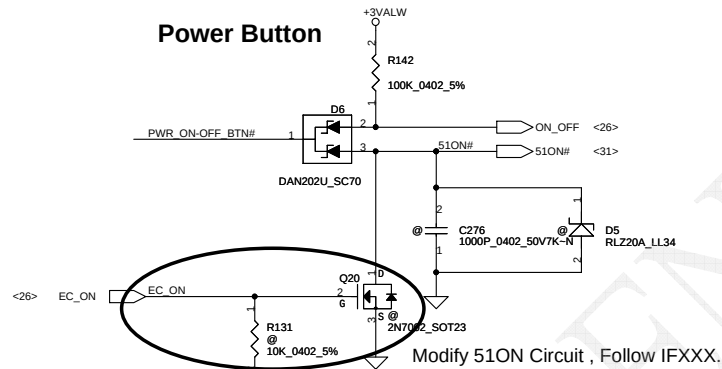
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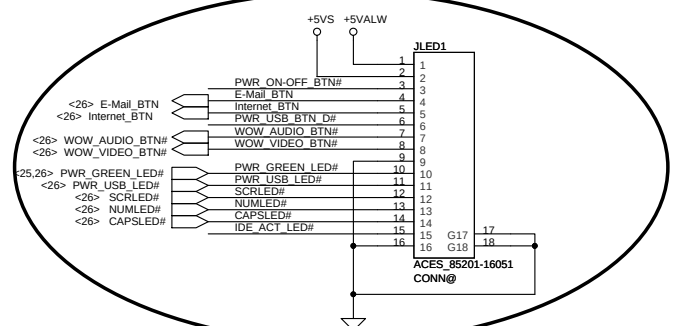
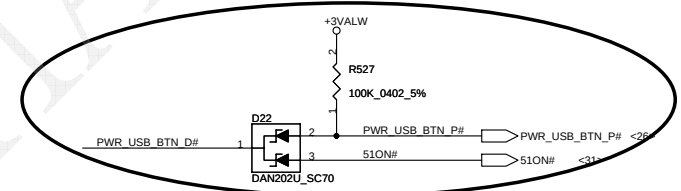
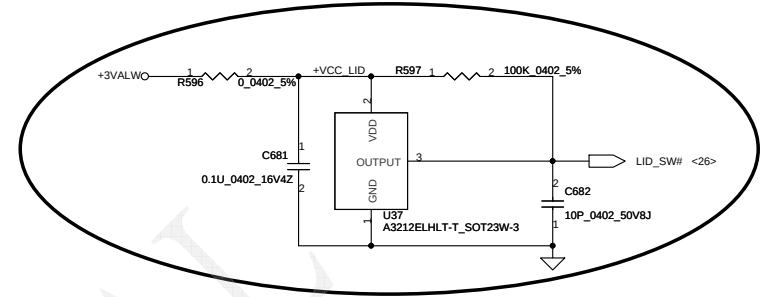
H_C114D114N



Power Button



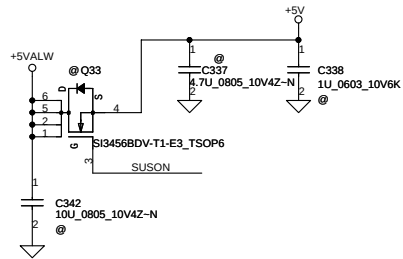
LID Switch



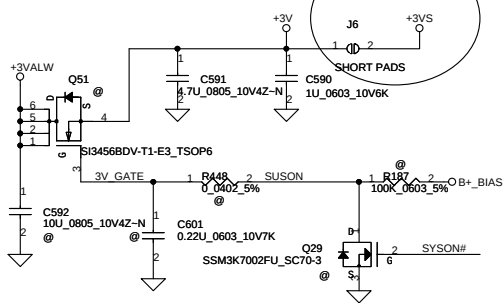
PWR_USB_BTN_D#	@C302	100P_0402_25V8K	E-Mail_BTN	@C292	100P_0402_25V8K
WOW_AUDIO_BTN#	@C303	100P_0402_25V8K	Internet_BTN	@C293	100P_0402_25V8K
WOW_VIDEO_BTN#	@C304	100P_0402_25V8K	SCRLED#	@C42	100P_0402_25V8K
PWR_GREEN_LED#	@C309	100P_0402_25V8K	NUMLED#	@C43	100P_0402_25V8K
PWR_USB_LED#	@C310	100P_0402_25V8K	CAPSLED#	@C44	100P_0402_25V8K
			IDE_ACT_LED#	@C46	100P_0402_25V8K
			PWR_ON-OFF_BTN#	@C291	100P_0402_25V8K

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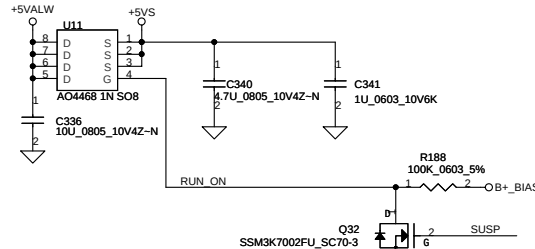
+5VALW TO +5V



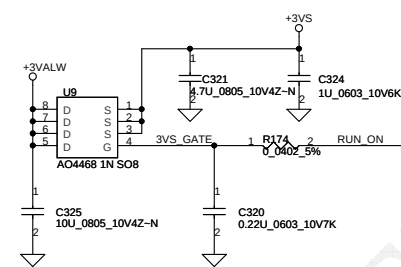
+3VALW TO +3V



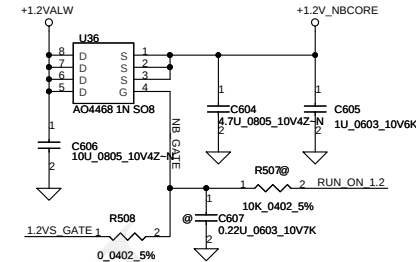
+5VALW TO +5VS



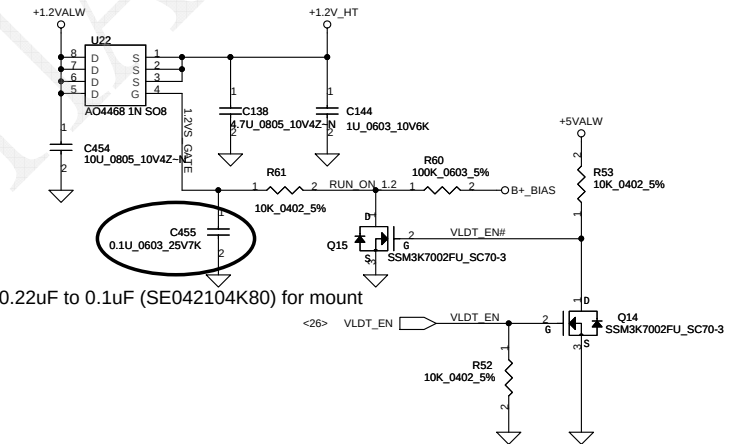
+3VALW TO +3VS



+1.2VALW TO +1.2V_NBCORE

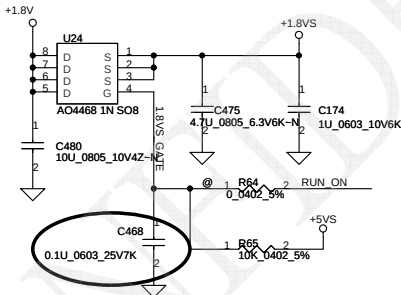


+1.2VALW TO +1.2V_HT

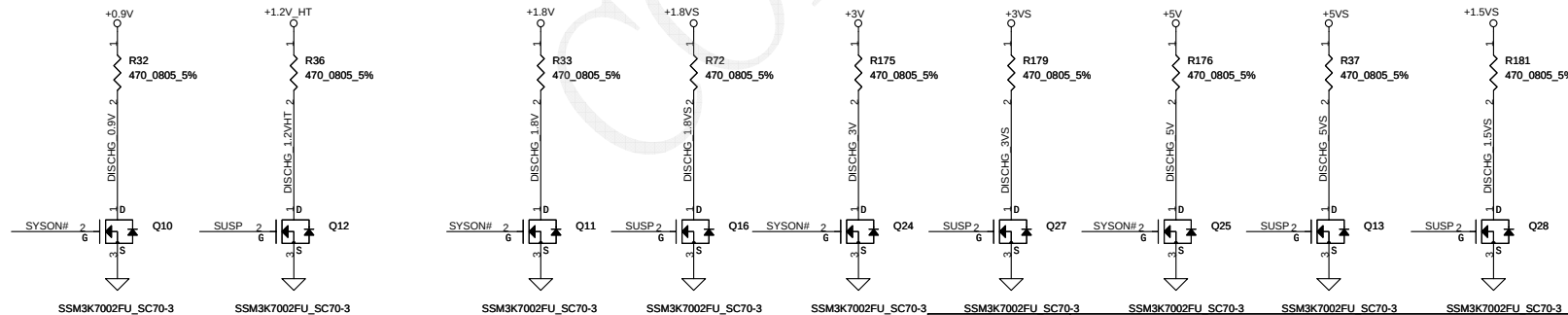
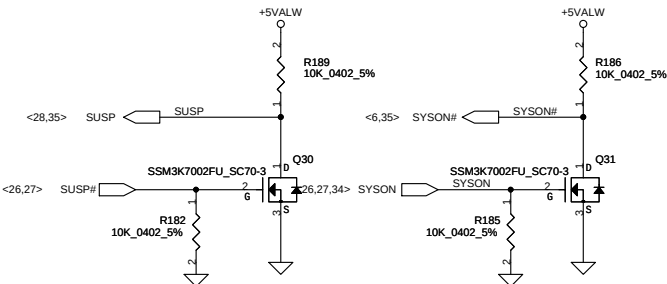


Change C455 from 0.22uF to 0.1uF (SE042104K80) for mount

+1.8V TO +1.8VS



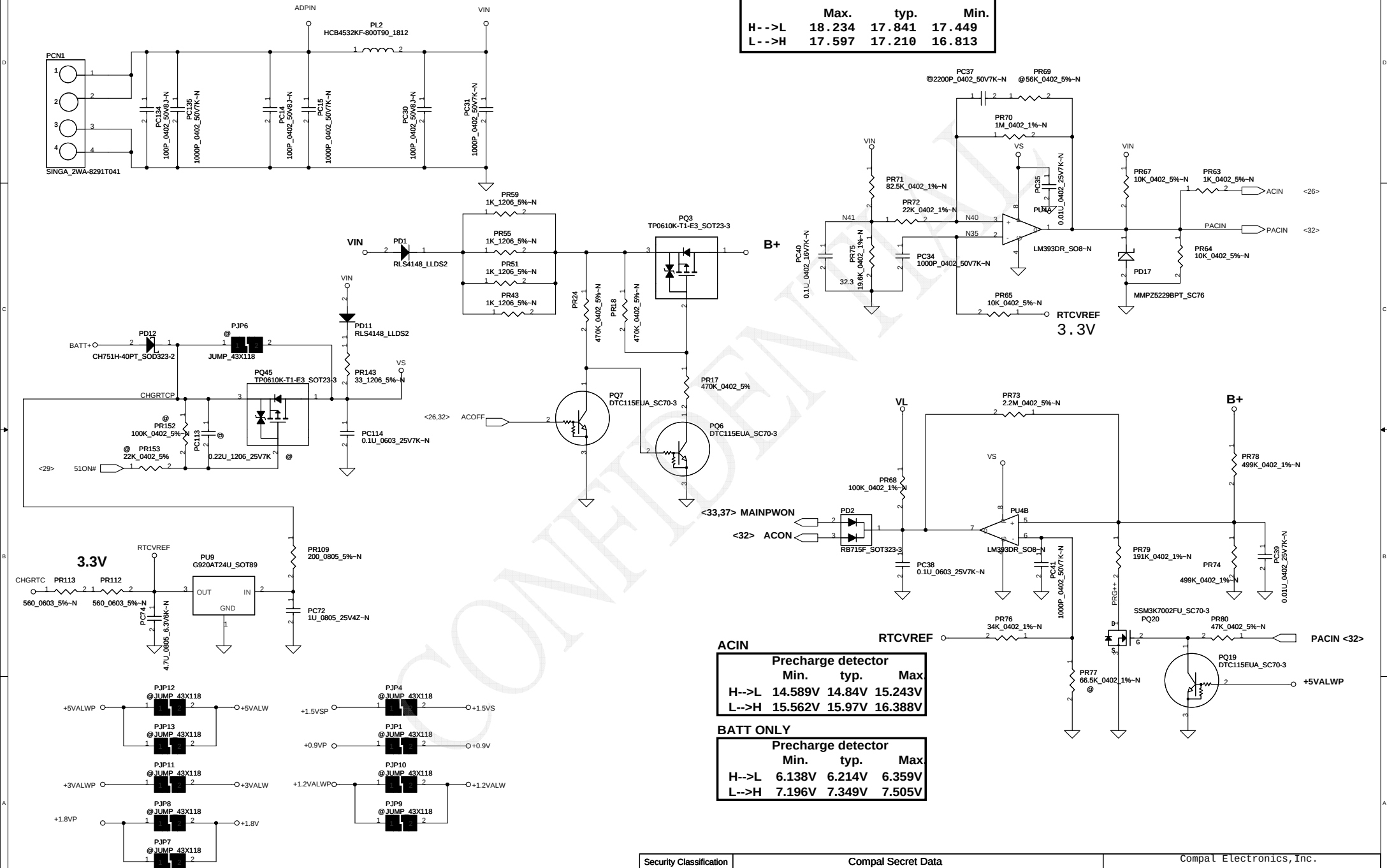
Change C468 from 0.22uF to 0.1uF (SE042104K80) for mount



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Vin Detector

	Max.	typ.	Min.
H-->L	18.234	17.841	17.449
L-->H	17.597	17.210	16.813



ACIN			
Precharge detector			
Min.	typ.	Max	
H-->L	14.589V	14.84V	15.243V
L-->H	15.562V	15.97V	16.388V

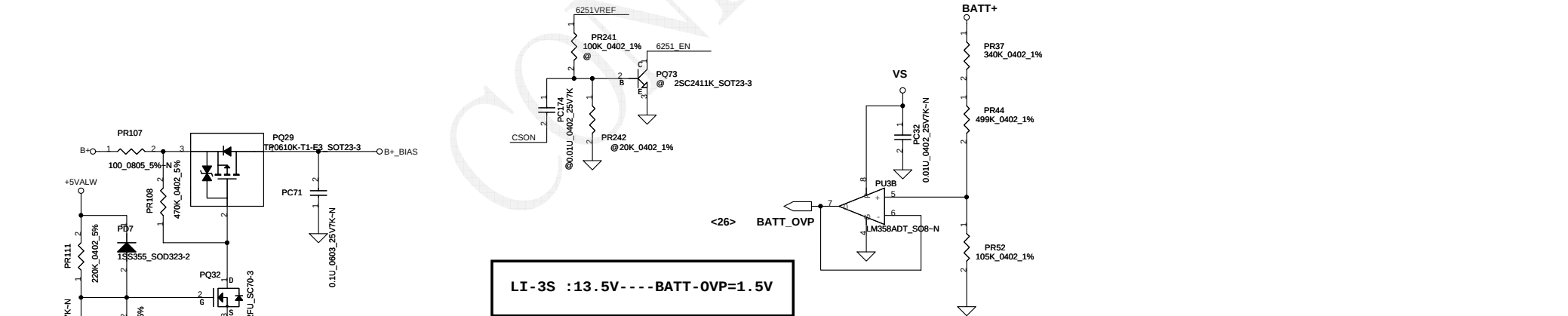
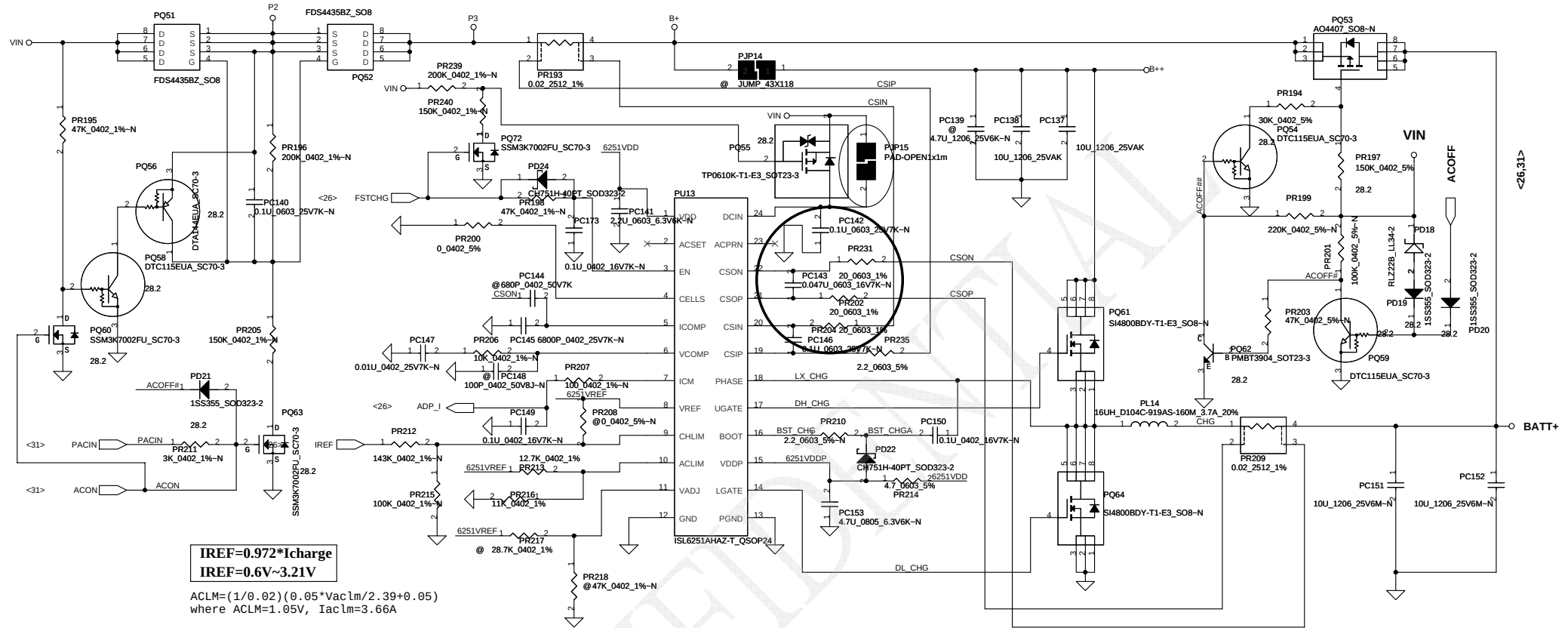
BATT ONLY			
Precharge detector			
Min.	typ.	Max	
H-->L	6.138V	6.214V	6.359V
L-->H	7.196V	7.349V	7.505V

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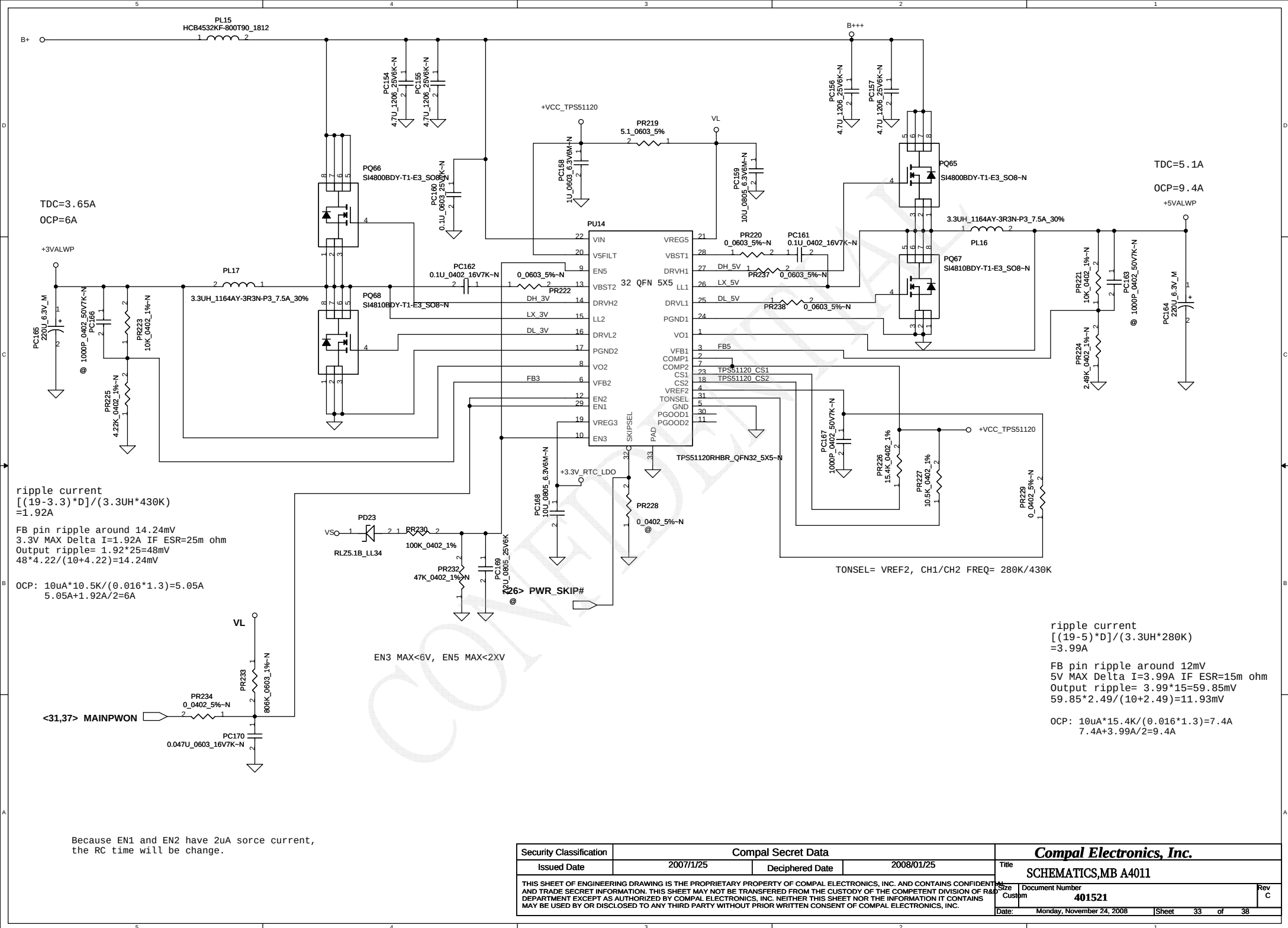
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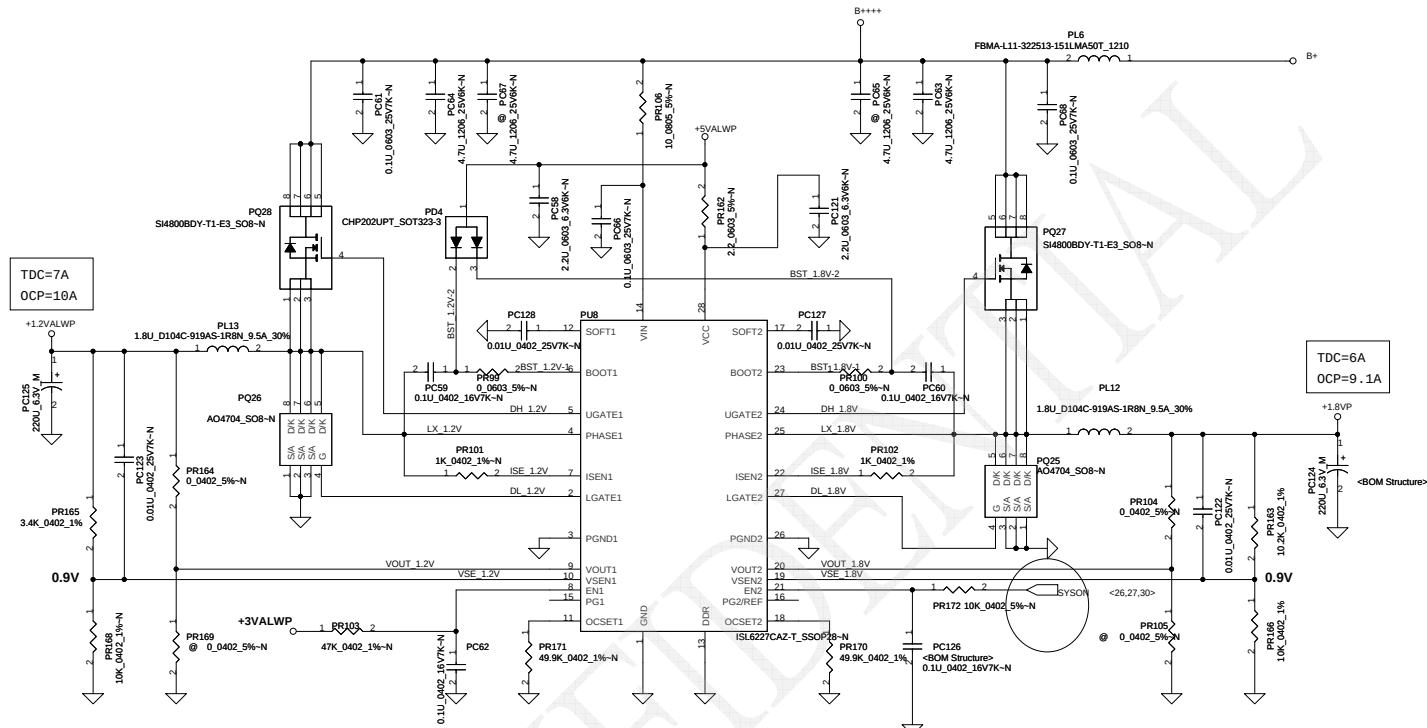
Fosc=14100/Rt=14100/47=300KHz

Charger



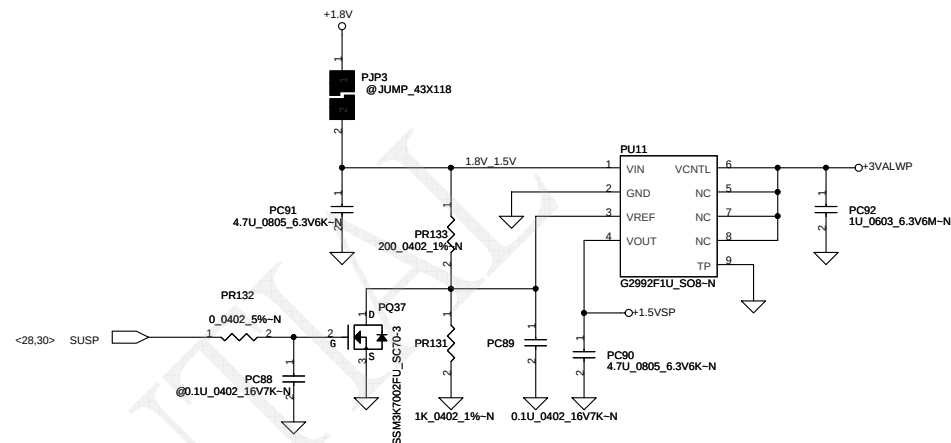
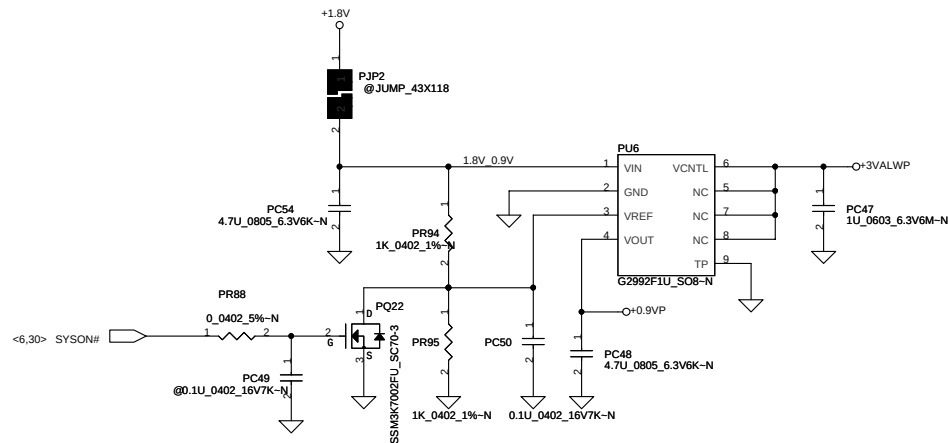
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+1.5VSP/+0.9VSP



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	page	Reason for change	Modify list
DVT		Modify 1.2 OCP to 12A	PR171 to 73.2K, PR101 to 1K
PVT		Modify 1.2 OCP to 14A	PR171 to 49.9K,
		Modify 1.8 OCP to 14A	PR170 to 49.9K,
Pre-MP		Change 1.8UH source to TOKO for nosie issue	PL12,PL13
		Change 3.3UH source to TOKO for nosie issue	PL16,PL17
		Change 4.7UF to 10uF for nosie issue	PC101,PC100,PC105,PC109,PC137,PC138
		DEL 4.7UF for nosie issue	PC139

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