

XI'AN

PCB Thickness:1.1mm

CPU :INTEL MEROM/PENRYN
Chip Set :AMD RS600ME & SB600
Remarks :Internal GFX / 2 SODIMMs

Model Name : XIAN-ATI
PBA Name : MAIN
PCB Code : BA41-00859A (HS)
BA41-00860A (GCE)
BA41-00861A (NY)
Dev. Step : PR
Revision : 1.0
T.R. Date : 2008.02.14

DRAW	CHECK	APPROVAL
-	-	-

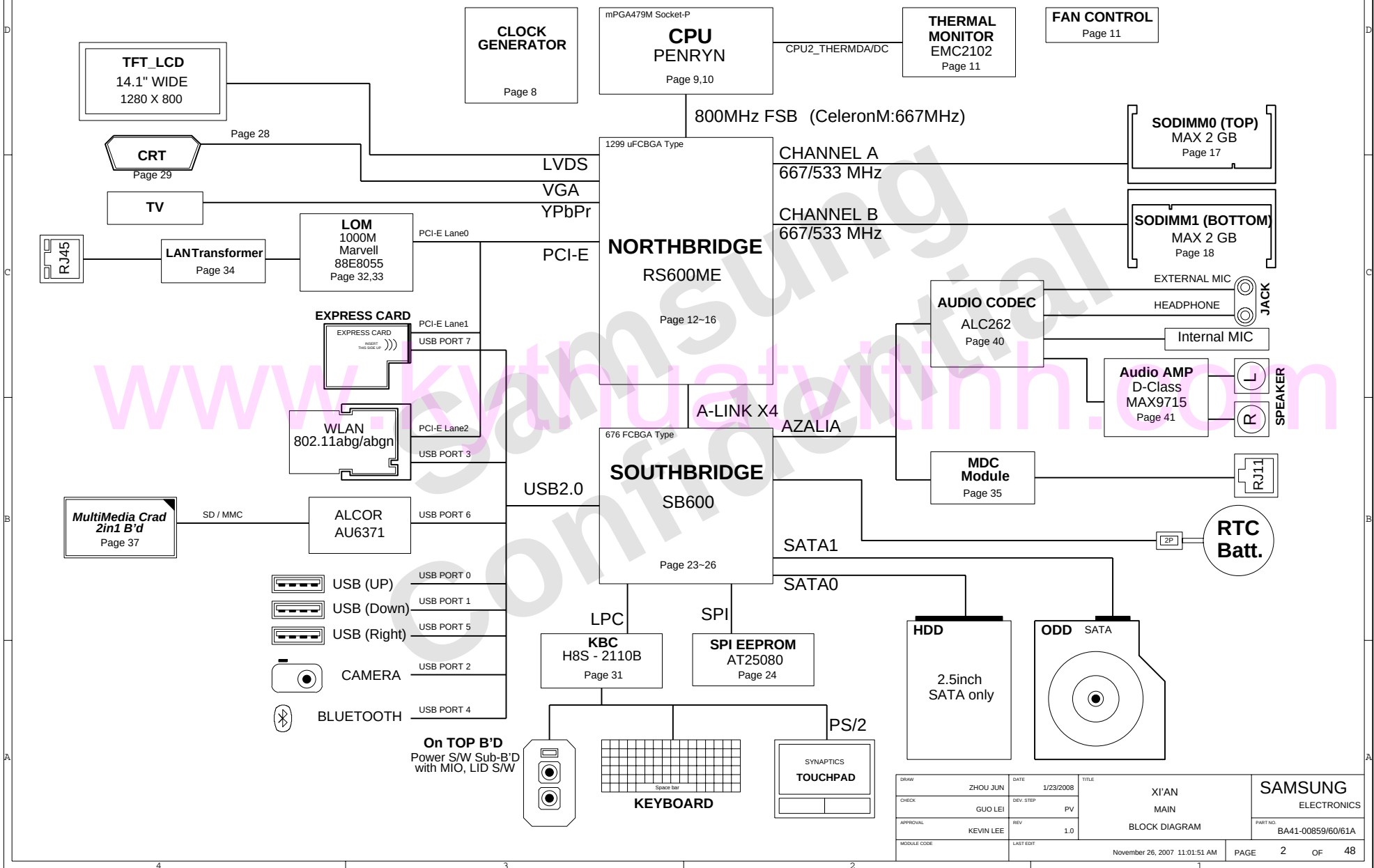
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DRAW	ZHOU JUN	DATE	1/23/2008	TITLE	XI'AN	SAMSUNG
CHECK	GUO LEI	DEV. STEP	PV	COVER		ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0	CONTENTS		PART NO.
MODULE CODE		LAST EDIT	November 26, 2007 11:01:51 AM			BA41-00859/60/61A
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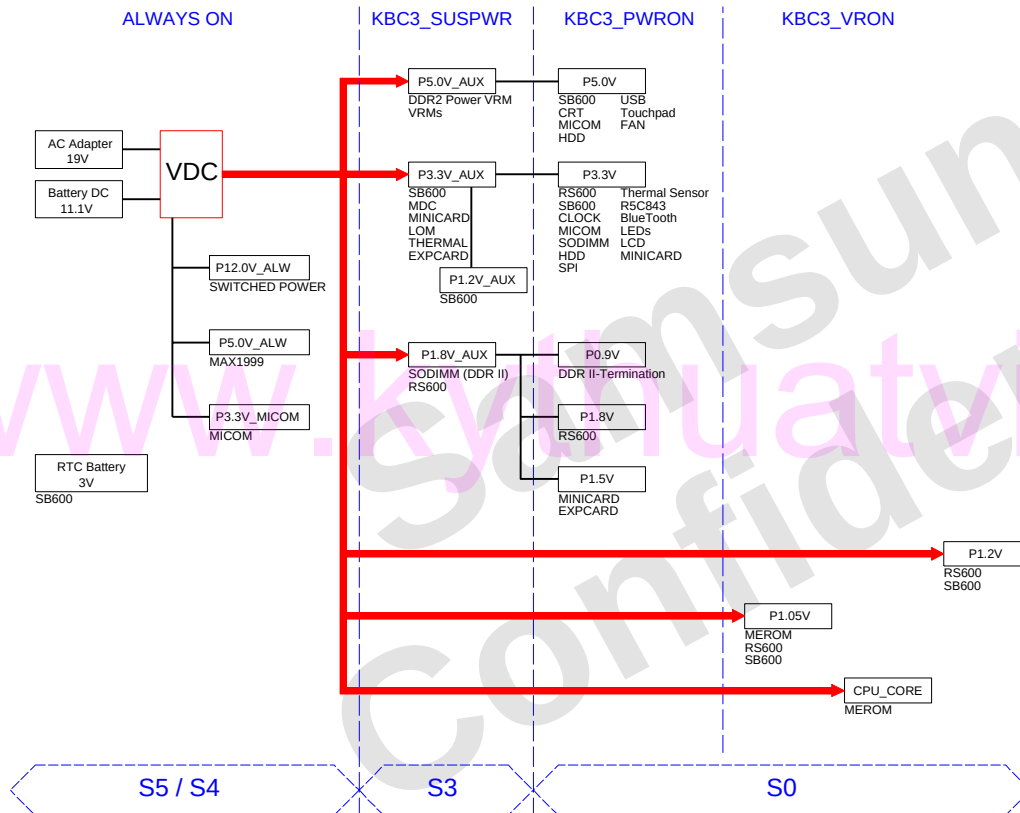
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OPERATION BLOCK DIAGRAM



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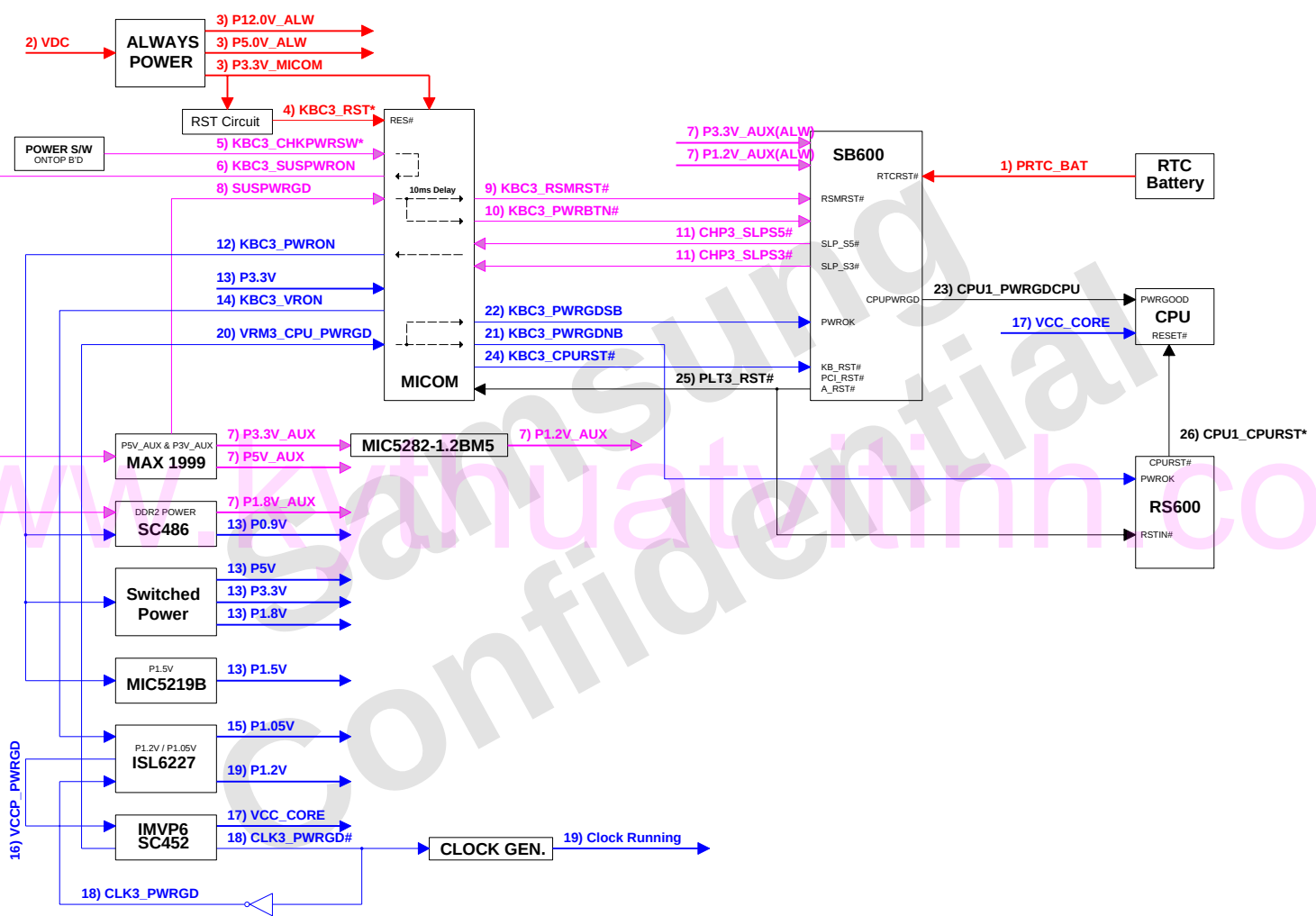
POWER DIAGRAM



Rail State	+V*Always	+V*AUX	+V	SUSPWR	PWRON	VRON
Full On	ON	ON	ON	H	H	H
S3	ON	ON	OFF	H	L	L
S4	ON	OFF	OFF	H	L	L
S5	ON	OFF	OFF	L	L	L

DRAW	ZHOU JUN	DATE	1/23/2008	TITLE	XI'AN MAIN POWER DIAGRAM	SAMSUNG ELECTRONICS
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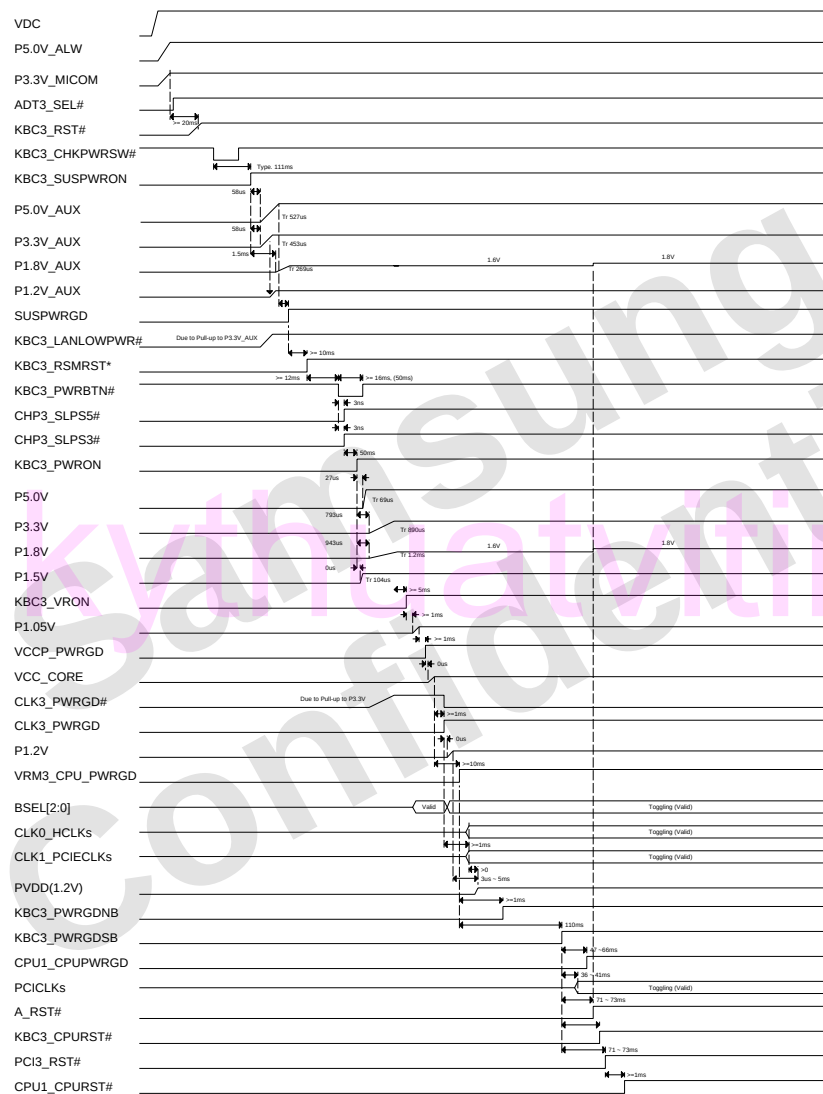
POWER SEQUENCE Rev. 0.1



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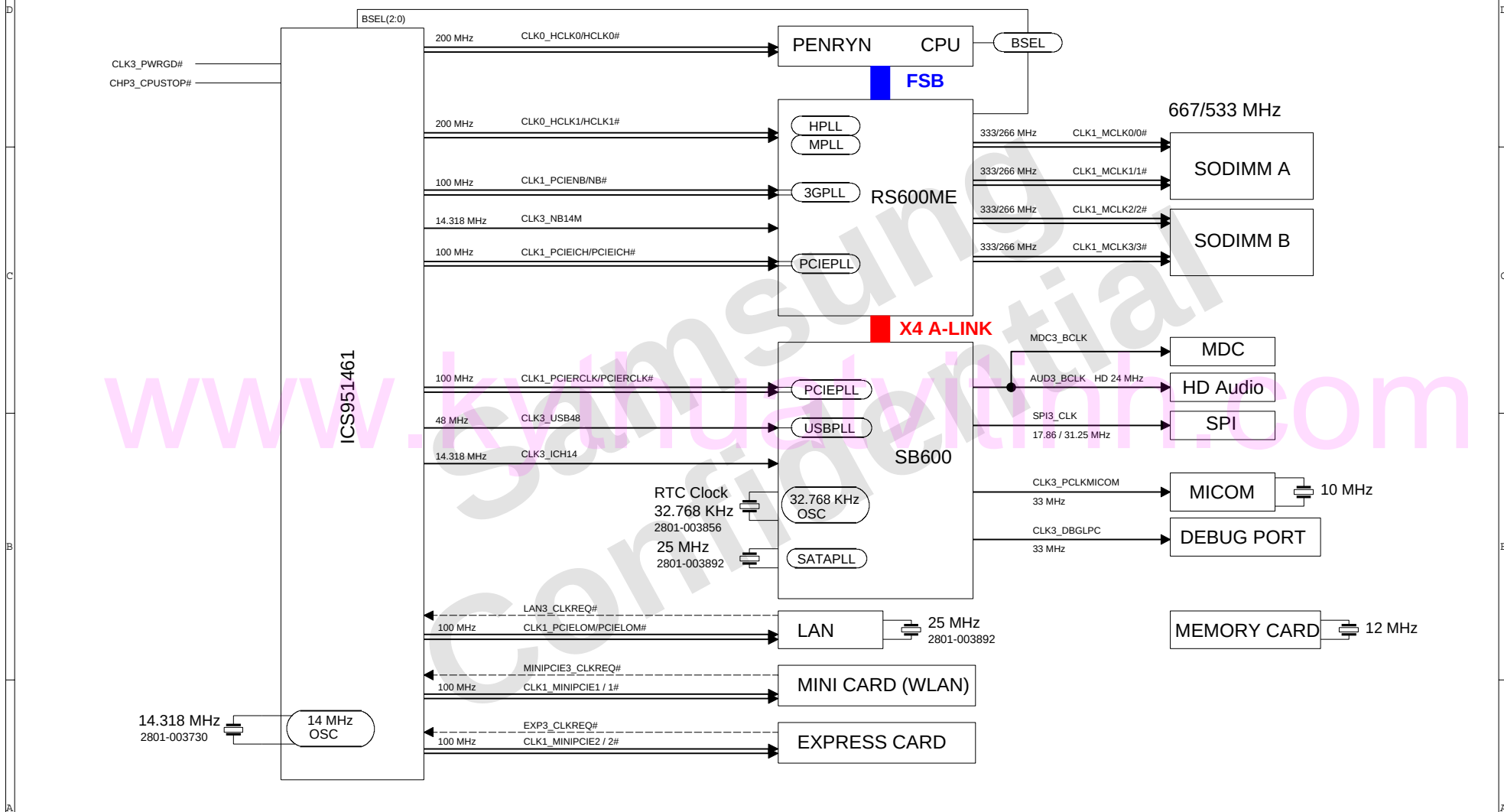
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Timing Diagram, Power ON Rev. 0.1 Phil 2007-06-11



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CHECK	GUO LEI	DEV. STEP	PV			
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CLOCK DISTRIBUTION Rev. 0.1



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CHECK	GUO LEI	DEV. STEP	PV	MAIN	MAIN	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0	CLOCK DISTRIBUTION	PART NO.	BA41-00859/60/61A
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SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

Devices	IDSEL#	REQ/GNT#	Interrupts
---------	--------	----------	------------

VDC	Primary DC system power supply (7 to 21V)
VCC_CORE	Core voltage for Processor (1.308~1.068V)
P1.05V	Processor System Bus(PSB) Termination (1.05V) GMCH & ICH8 Core Voltage
P1.8V_AUX	1.8V power rail for DDR2 (off in S4-S5)
P0.9V	0.9V switched power rail (off in S3-S5)
P1.8V	1.8V power rail for GDDR3 (off in S3-S5)
P1.2V	1.2V switched power rail (off in S3-S5)
P1.5V	1.5V switched power rail (off in S3-S5)
P3.3V	3.3V switched power rail (off in S3-S5)
P5.0V	5.0V switched power rail (off in S3-S5)
P3.3V_AUX	3.3V power rail (off in S4-S5)
P5V_AUX	5.0V power rail (off in S4-S5)
PRTC_BAT	3.0V power rail (ALWAYS ON)
P3.3V_MICOM	3.3V always on power rail for MICOM
P5.0V_ALW	5V power rail (Always On)
P12.0V_ALW	12V power rail (Always On)

Devices	Address	Hex	Bus
SB600	Master	-	SMBUS Master
SODIMM0	1010 000x	A0h	-
SODIMM1	1010 010x	A4h	-
CK-505 (Clock Generator)	1101 001x	D2h	Clock, Unused Clock Output Disable
MICOM	Master	-	SMBUS Master
BATTERY	0001 011x	16h	-
EMC2102 (Thermal Sensor)	0111 101x	7Ah	Thermal Sensor

PORT NUMBER	ASSIGNED TO
0	SYSTEM PORT A
1	SYSTEM PORT B
2	CAMERA
3	MINIPCI-E (WLAN)
4	BLUETOOTH
5	SYSTEM PORT C
6	MEMORY CARD
7	EXPRESS CARD

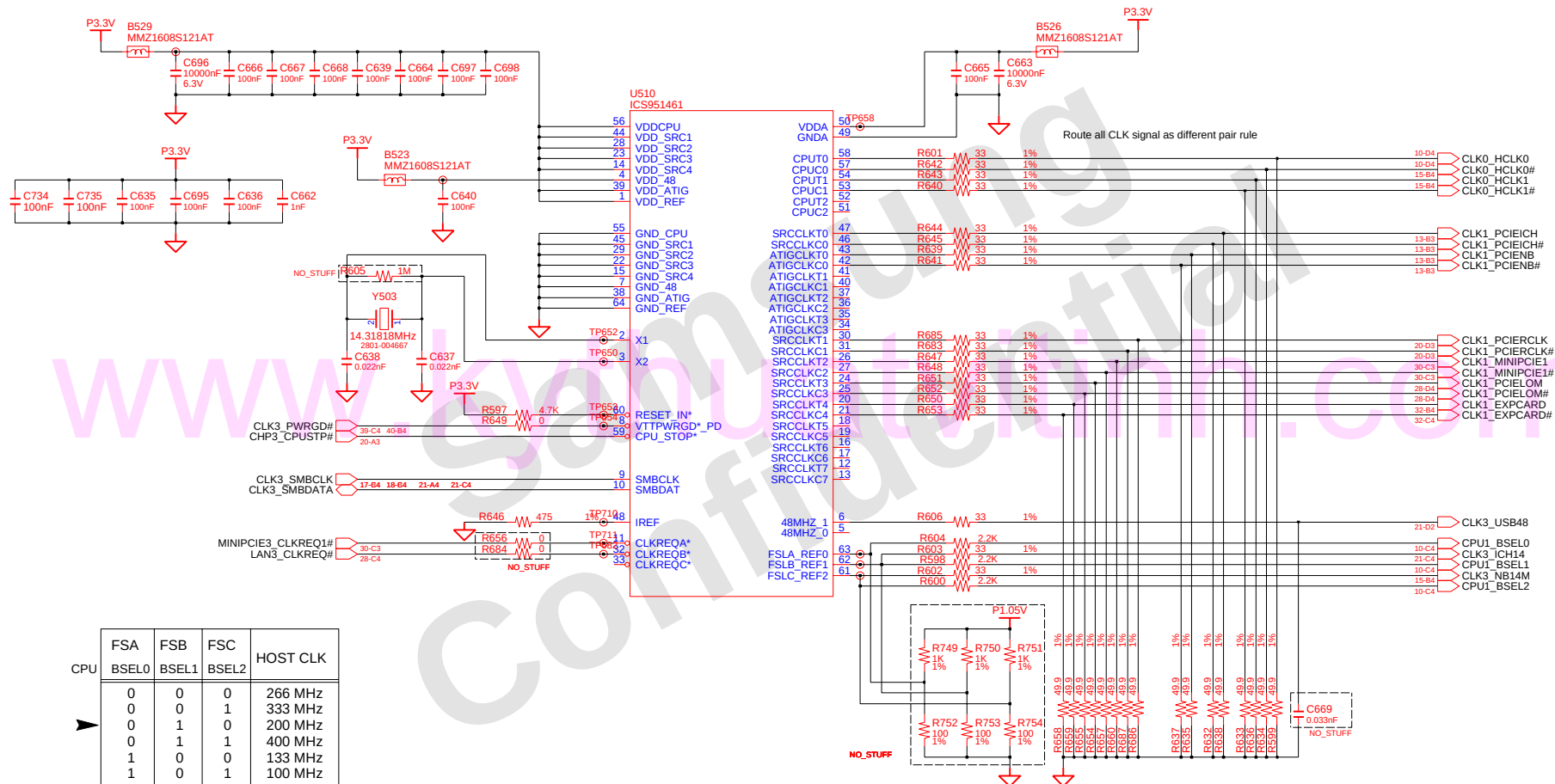
CHP3_SLPS1*	S1, Powered-On-Suspend(POS): In this state, all clocks(except the 32.768KHz clock) are stopped. The system context is maintained in system DRAM. Power is maintained to PCI, the CPU, memory controller, memory, and all other critical subsystems. Note that this state does not preclude power being removed from non-essential devices, such as disk drives. During this state, CPU can be selected for either Deep Sleep or Deeper Sleep. In Deeper Sleep, CPU voltage reduced in this state to reduce the leakage power.
CHP3_SLPS3*	S3, Suspend-to-RAM(STR) : The system context is maintained in system DRAM, but power is shut off to non-critical circuits. Memory is retained, and refreshes continue. All clocks stop except RTC clock.
CHP3_SLPA5*	S4, Suspend-to-Disk(STD) : The Context of the system is maintained on the disk. All power is then shut off to the system except for the logic required to resume. External components are in same as S5, but may have different voltage levels.
CHP3_SLPA5*	S5, Soft Off(SOFF): System context is non-maintained. All power is shut off except for the logic required to restart. A full boot is required when waking.

TYPE	FREQUENCY	DEVICE	USAGE
Crystal	32.768KHz	SB600	Real Time Clock
Crystal	25MHz	SB600	SATA
Crystal	10MHz	MICOM	H8S/2110BV
Crystal	14.318MHz	CLOCK-GENERATOR	ICS951461
Crystal	12MHz	MEMORY CARD	MEMORY CARD
Crystal	25MHz	LAN	LAN

Active Mode		Active/Deeper Sleep Dual Mode Region		Deeper Sleep/Extended Deeper Sleep Dual Mode Region	
VID(6:0)	Voltage	VID(6:0)	Voltage	VID(6:0)	Voltage
0 0 0 0 0 0 0	1.5000 V	0 1 0 1 0 0 0	1.0000 V	1 0 1 0 0 0 0	0.4875 V
0 0 0 0 0 0 1	1.4675 V	0 1 0 1 0 0 1	0.9875 V	1 0 1 0 0 0 1	0.4875 V
0 0 0 0 0 1 0	1.4750 V	0 1 0 1 0 1 0	0.9750 V	1 0 1 0 0 1 0	0.4625 V
0 0 0 0 0 1 1	1.4625 V	0 1 0 1 0 1 1	0.9625 V	1 0 1 0 0 1 1	0.4500 V
0 0 0 0 1 0 0	1.4500 V	0 1 0 1 1 0 0	0.9500 V	1 0 1 0 1 0 0	0.4375 V
0 0 0 0 1 0 1	1.4375 V	0 1 0 1 1 0 1	0.9375 V	1 0 1 0 1 0 1	0.4250 V
0 0 0 0 1 1 0	1.4250 V	0 1 0 1 1 1 0	0.9250 V	1 0 1 0 1 1 0	0.4125 V
0 0 0 0 1 1 1	1.4125 V	0 1 0 1 1 1 1	0.9125 V	1 0 1 1 0 0 0	0.4000 V
0 0 0 1 0 0 0	1.4000 V	0 1 1 0 0 0 0	0.9000 V	1 0 1 1 0 0 1	0.3875 V
0 0 0 1 0 0 1	1.3875 V	0 1 1 0 0 0 1	0.8875 V	1 0 1 1 0 1 0	0.3750 V
0 0 0 1 0 1 0	1.3750 V	0 1 1 0 0 1 0	0.8750 V	1 0 1 1 0 1 1	0.3625 V
0 0 0 1 0 1 1	1.3625 V	0 1 1 0 0 1 1	0.8625 V	1 0 1 1 1 0 0	0.3500 V
0 0 0 1 1 0 0	1.3500 V	0 1 1 0 1 0 0	0.8500 V	1 0 1 1 1 0 1	0.3375 V
0 0 0 1 1 0 1	1.3375 V	0 1 1 0 1 0 1	0.8375 V	1 0 1 1 1 1 0	0.3250 V
0 0 0 1 1 1 0	1.3250 V	0 1 1 0 1 1 0	0.8250 V	1 0 1 1 1 1 1	0.3125 V
0 0 0 1 1 1 1	1.3125 V	0 1 1 0 1 1 1	0.8125 V	1 1 0 0 0 0 0	0.3000 V
0 0 1 0 0 0 0	1.3000 V	0 1 1 1 0 0 0	0.8000 V	1 1 0 0 0 0 1	0.2875 V
0 0 1 0 0 0 1	1.2875 V	0 1 1 1 0 0 1	0.7875 V	1 1 0 0 0 1 0	0.2750 V
0 0 1 0 0 1 0	1.2750 V	0 1 1 1 0 1 0	0.7750 V	1 1 0 0 0 1 1	0.2625 V
0 0 1 0 0 1 1	1.2625 V	0 1 1 1 0 1 1	0.7625 V	1 1 0 0 1 0 0	0.2500 V
0 0 1 0 1 0 0	1.2500 V	0 1 1 1 1 0 0	0.7500 V	1 1 0 0 1 0 1	0.2375 V
0 0 1 0 1 0 1	1.2375 V	0 1 1 1 1 0 1	0.7375 V	1 1 0 0 1 1 0	0.2250 V
0 0 1 0 1 0 1	1.2250 V	0 1 1 1 1 1 0	0.7250 V	1 1 0 0 1 1 1	0.2125 V
0 0 1 0 1 1 0	1.2125 V	0 1 1 1 1 1 1	0.7125 V	1 1 0 1 0 0 0	0.2000 V
0 0 1 0 1 1 0	1.2000 V	1 0 0 0 0 0 0	0.7000 V	1 1 0 1 0 0 1	0.1875 V
0 0 1 1 0 0 0	1.1875 V	1 0 0 0 0 0 0	0.6875 V	1 1 0 1 0 1 0	0.1750 V
0 0 1 1 0 0 1	1.1750 V	1 0 0 0 0 0 1	0.6750 V	1 1 0 1 0 1 1	0.1625 V
0 0 1 1 0 1 0	1.1625 V	1 0 0 0 0 1 0	0.6625 V	1 1 0 1 0 1 0	0.1500 V
0 0 1 1 0 1 0	1.1500 V	1 0 0 0 0 1 0	0.6500 V	1 1 0 1 0 1 1	0.1375 V
0 0 1 1 1 0 0	1.1375 V	1 0 0 0 1 0 0	0.6375 V	1 1 0 1 1 0 0	0.1250 V
0 0 1 1 1 0 0	1.1250 V	1 0 0 0 1 0 0	0.6250 V	1 1 0 1 1 0 1	0.1125 V
0 0 1 1 1 1 0	1.1125 V	1 0 0 0 1 1 0	0.6125 V	1 1 1 0 0 0 0	0.1000 V
0 1 0 0 0 0 0	1.1000 V	1 0 0 0 1 0 0	0.6000 V	1 1 1 0 0 0 1	0.0875 V
0 1 0 0 0 0 1	1.0875 V	1 0 0 0 1 0 1	0.5875 V	1 1 1 0 0 1 0	0.0750 V
0 1 0 0 0 1 0	1.0750 V	1 0 0 0 1 0 1	0.5750 V	1 1 1 0 0 1 1	0.0625 V
0 1 0 0 0 1 1	1.0625 V	1 0 0 0 1 1 0	0.5625 V	1 1 1 0 1 0 0	0.0500 V
0 1 0 0 1 0 0	1.0500 V	1 0 0 0 1 1 0	0.5500 V	1 1 1 0 1 0 1	0.0375 V
0 1 0 0 1 0 1	1.0375 V	1 0 0 0 1 1 0	0.5375 V	1 1 1 1 0 1 0	0.0250 V
0 1 0 0 1 1 0	1.0250 V	1 0 0 0 1 1 1	0.5250 V	1 1 1 1 0 1 1	0.0125 V
0 1 0 0 1 1 1	1.0125 V	1 0 0 0 1 1 1	0.5125 V	1 1 1 1 0 0 0	0.0000 V
		1 0 0 1 0 0 0	0.5000 V	1 1 1 1 0 0 1	0.0000 V
				1 1 1 1 0 1 0	0.0000 V
				1 1 1 1 0 1 1	

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CLOCK GENERATOR



		FSA	FSB	FSC	HOST CLK
		BSEL0	BSEL1	BSEL2	
CPU	0	0	0	266 MHz	
	0	0	1	333 MHz	
	0	1	0	200 MHz	
	0	1	1	400 MHz	
	1	0	0	133 MHz	
	1	0	1	100 MHz	
	1	1	0	166 MHz	
	1	1	1	RSVD	

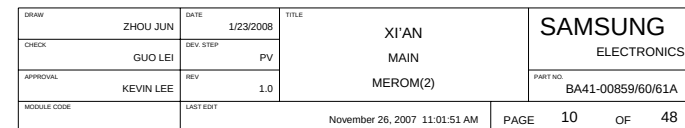
Place all serial termination resistors close to ICS951461 as possible

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MODULE CODE	LAST EDIT				November 26, 2007 11:01:51 AM	PAGE	8 OF 48

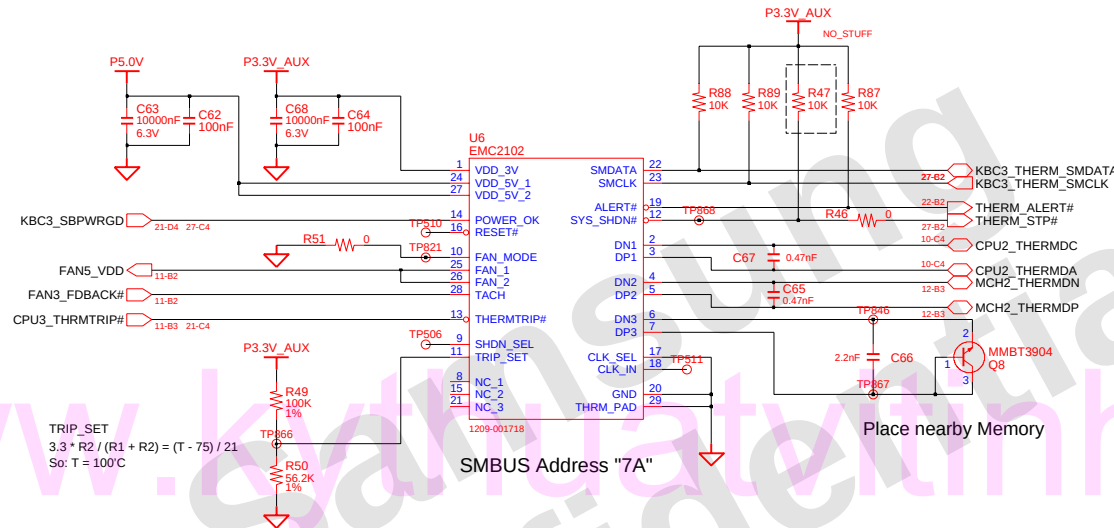
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 E
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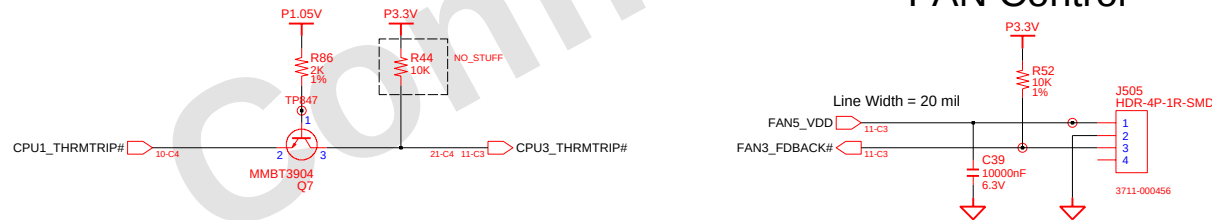
COM-22C-015(1996.6.5) REV. 3



Thermal Monitor

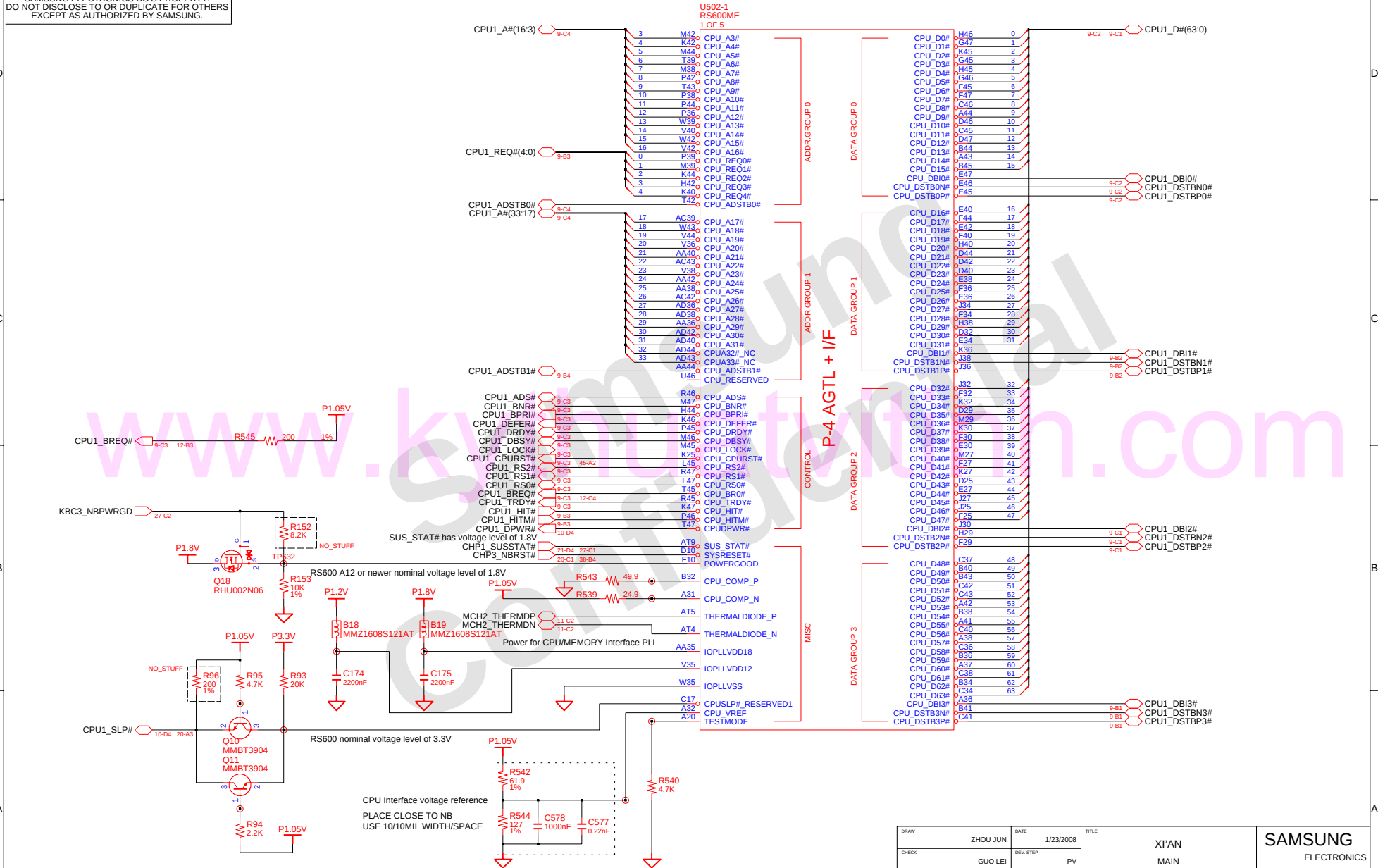


FAN Control



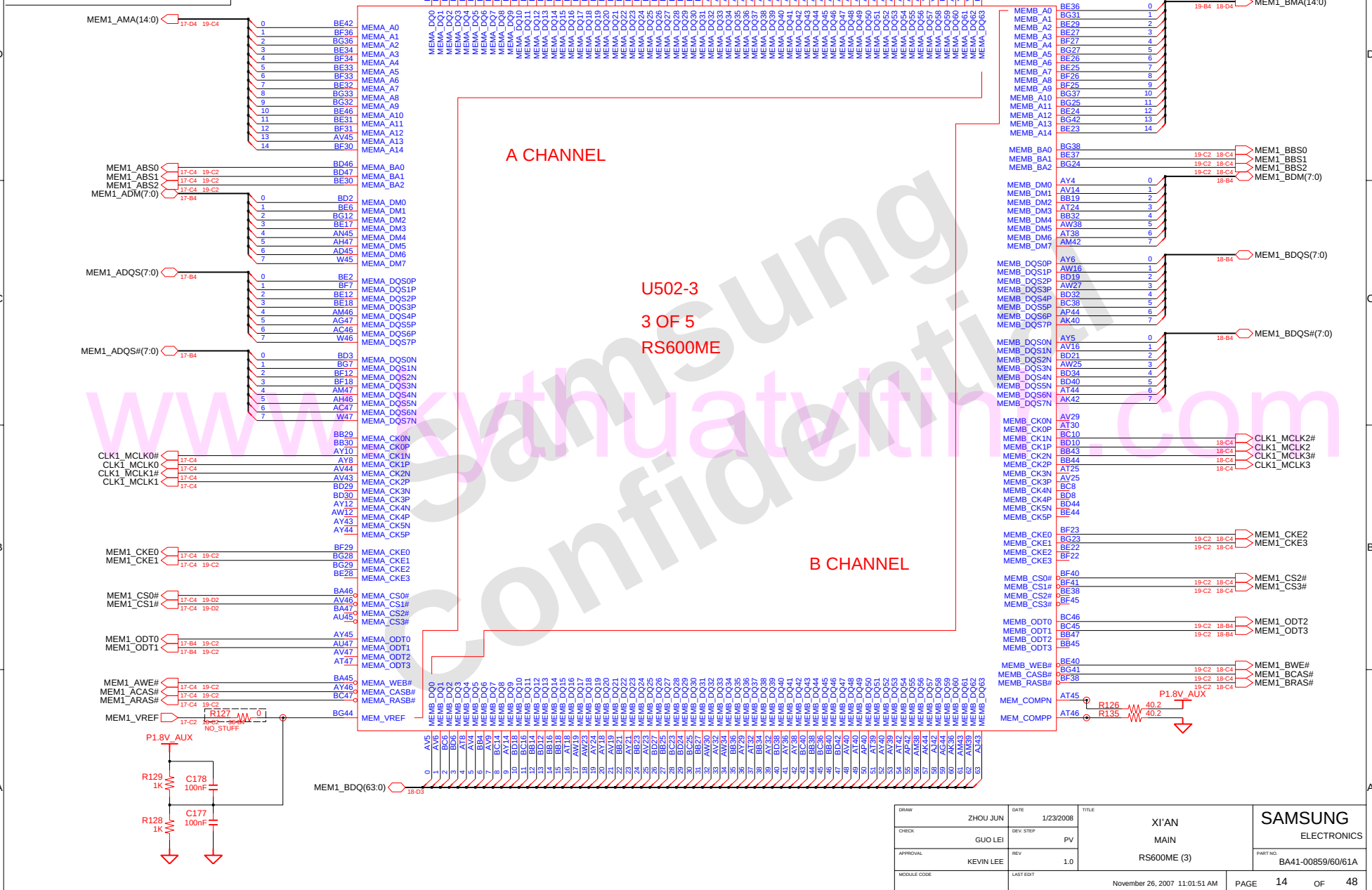
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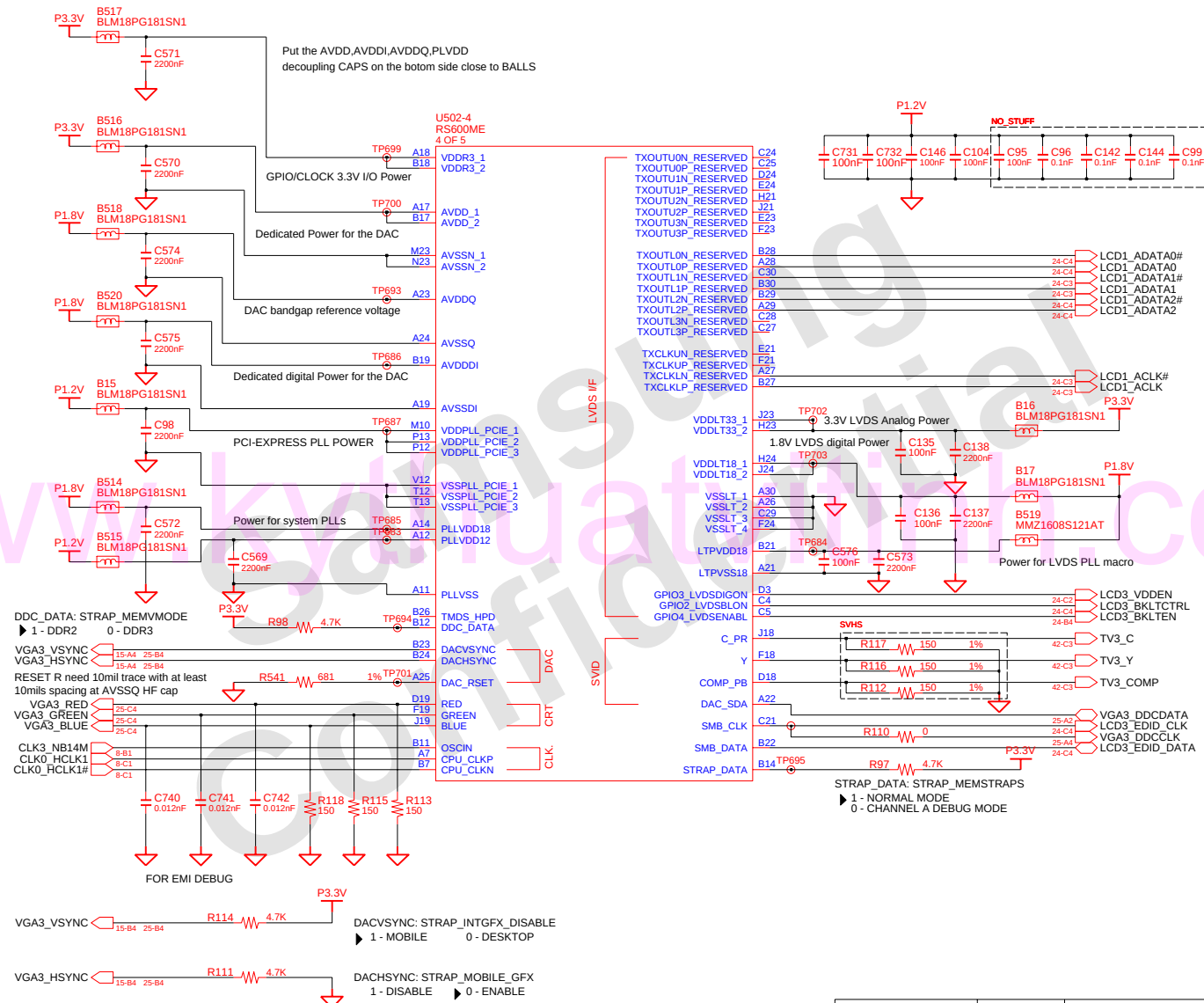
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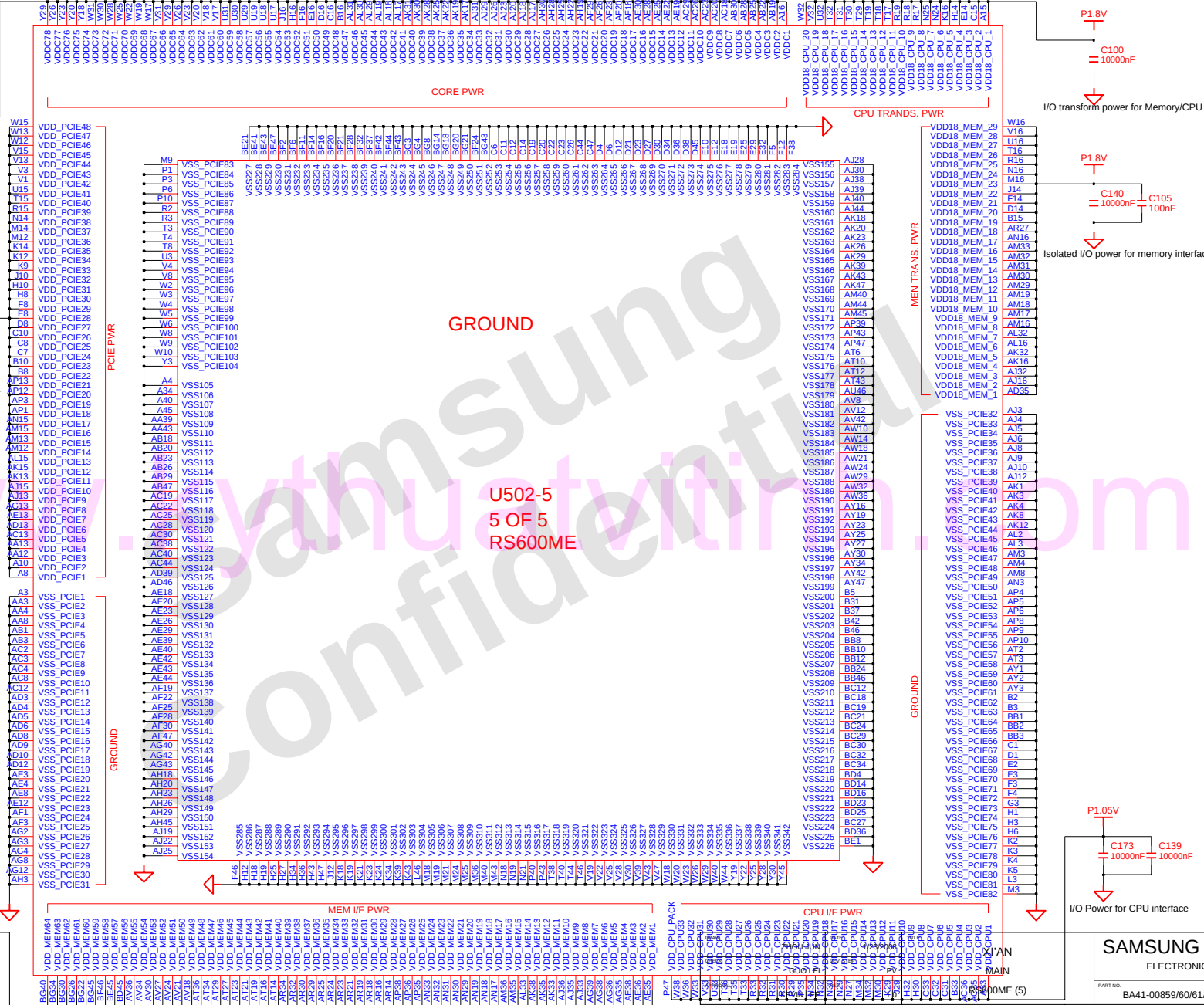
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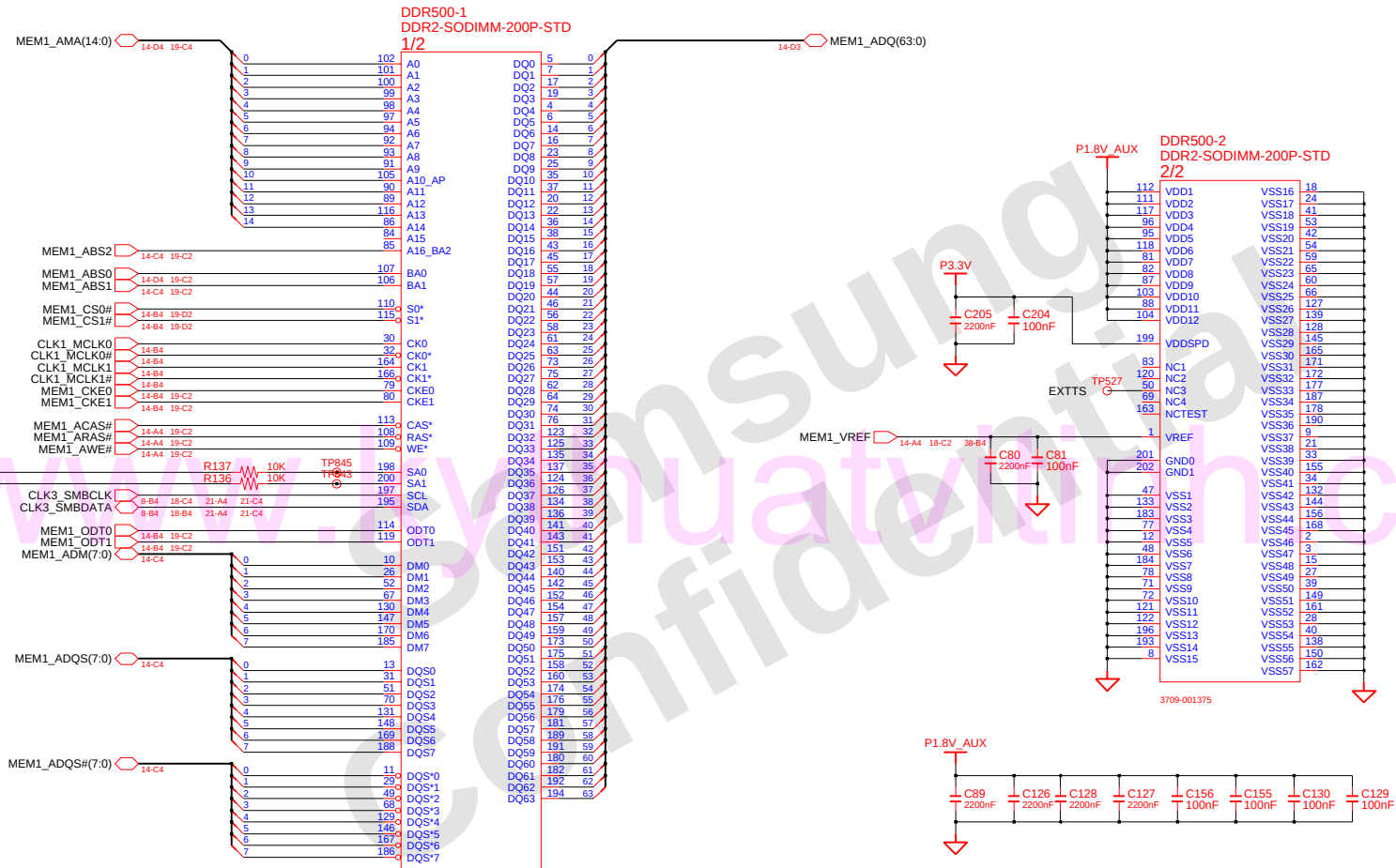
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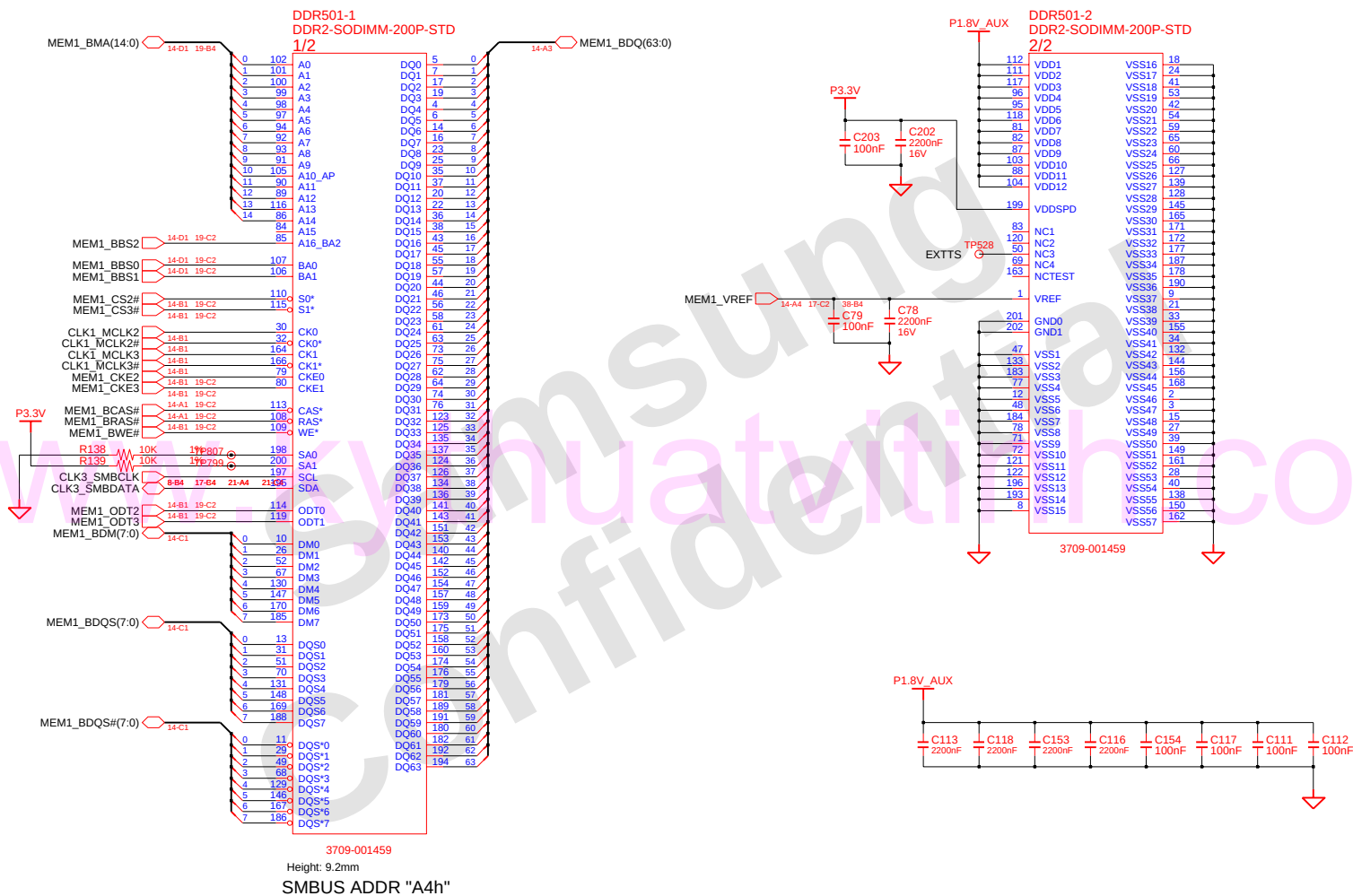
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SODIMM0 (CHANNEL A)



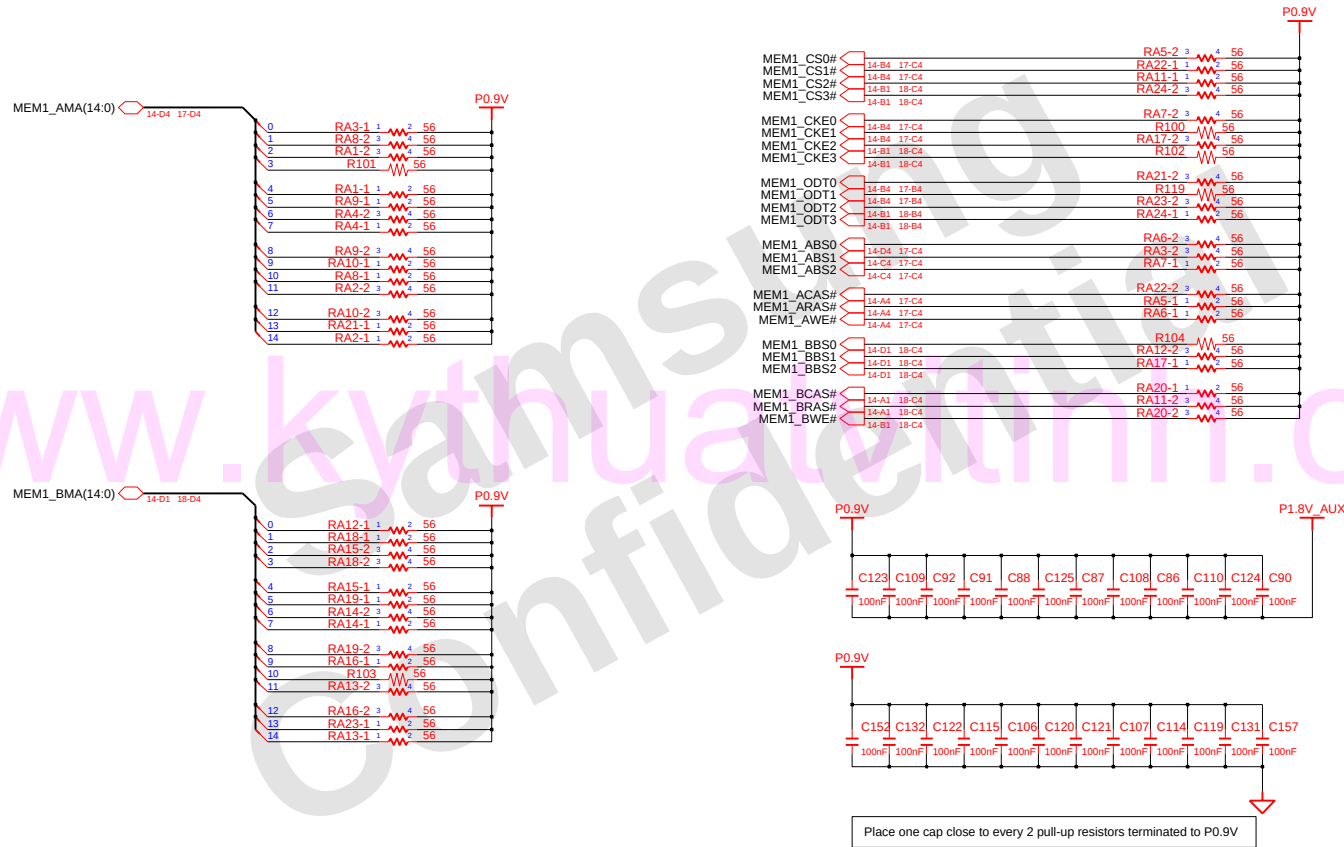
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SODIMM1 (CHANNEL B)



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CHECK	GUO LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	1.0	DDR2 SODIMM 1 (TOP)		PART NO. BA41-00859/60/61A
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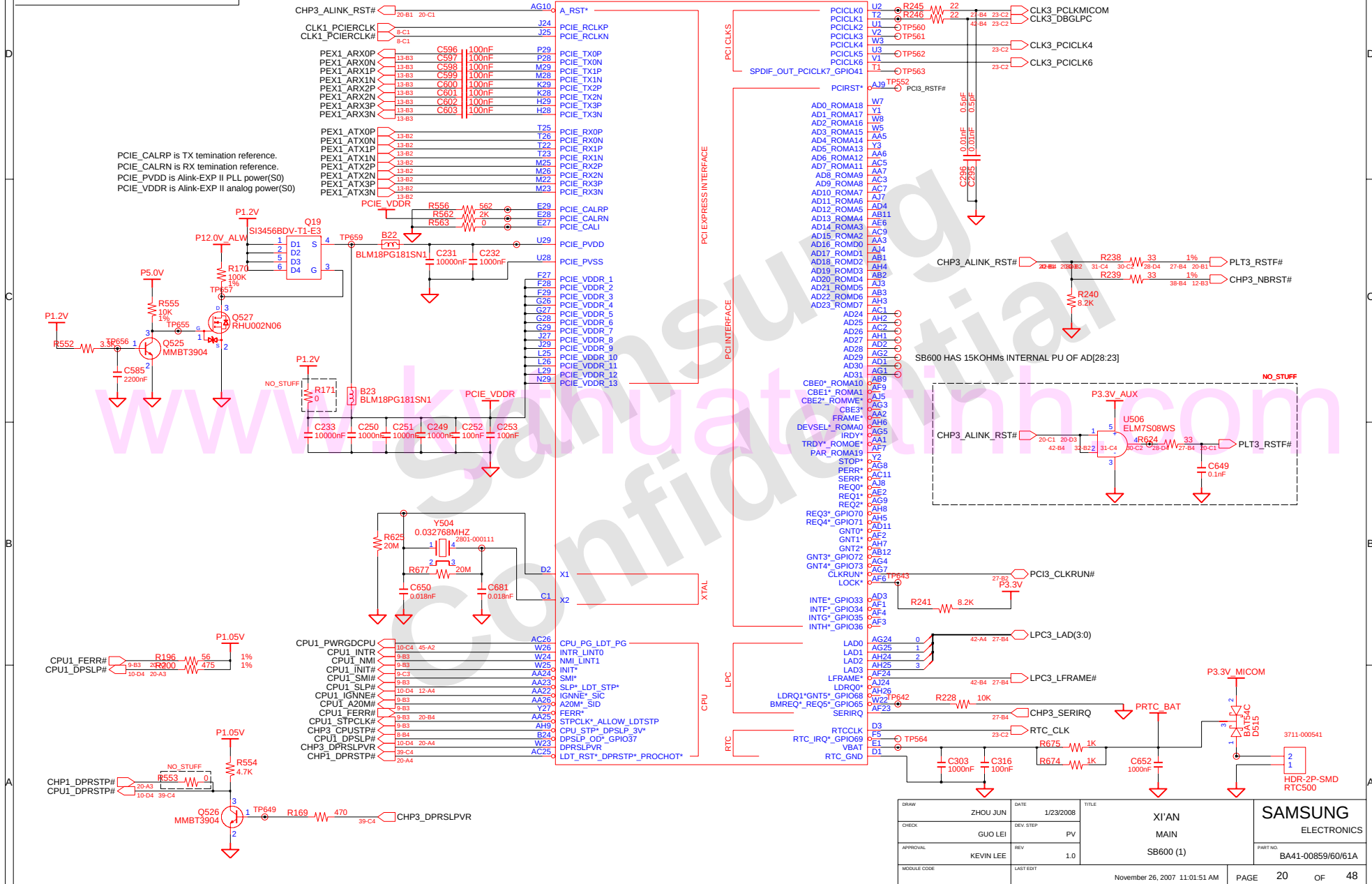
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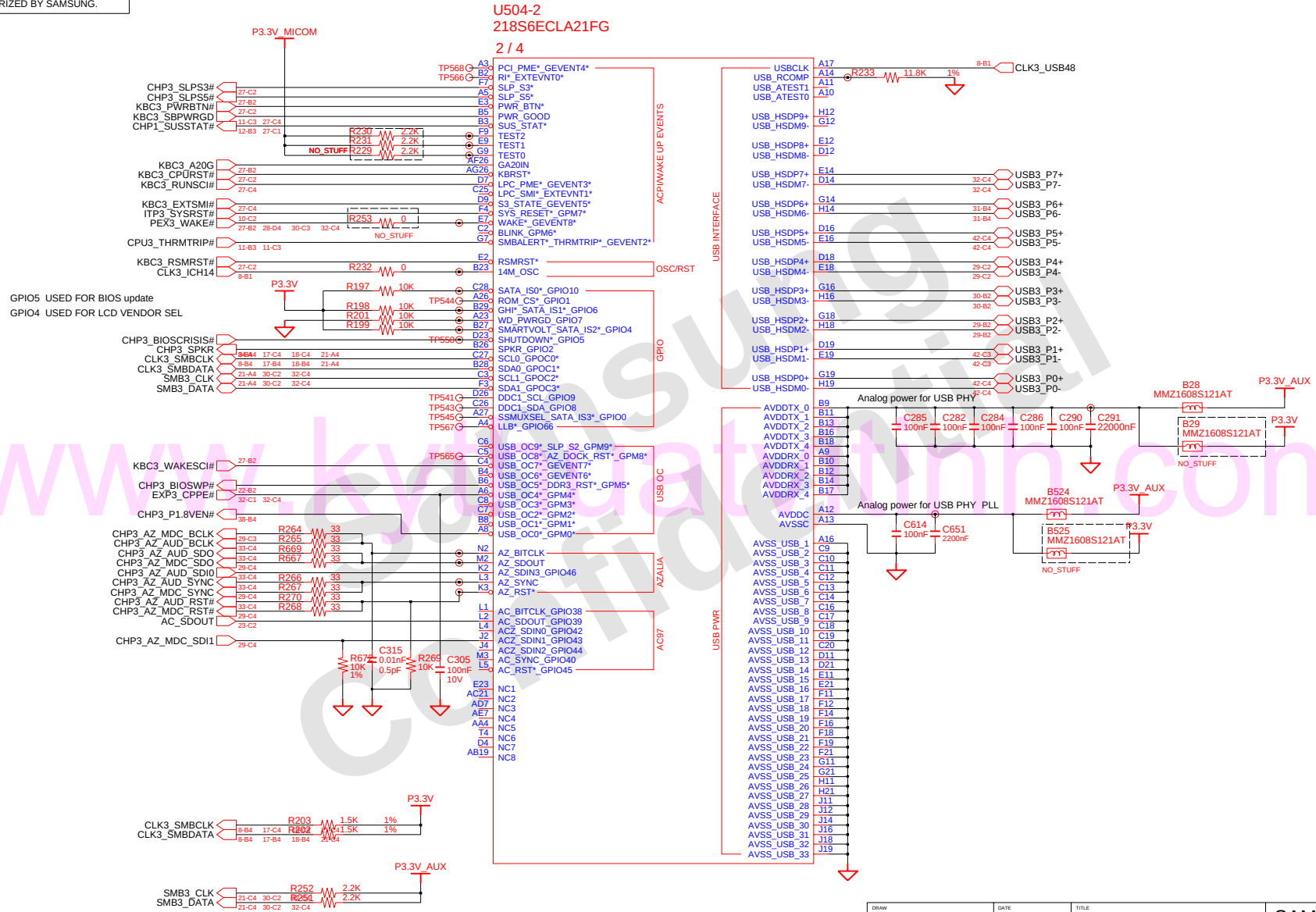
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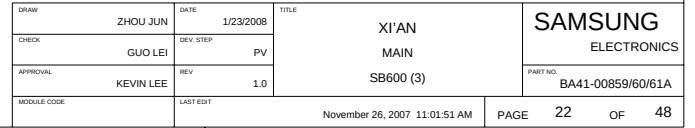
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1 / 4



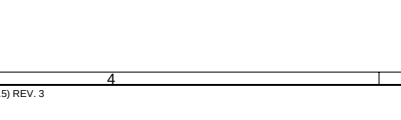
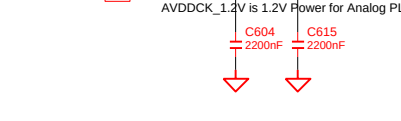
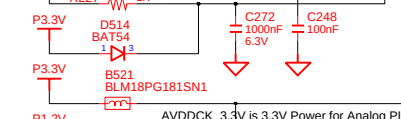
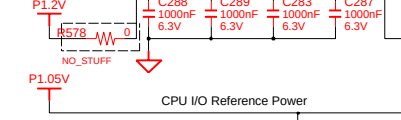
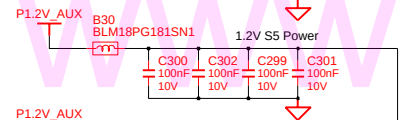
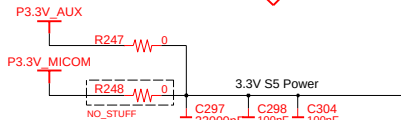
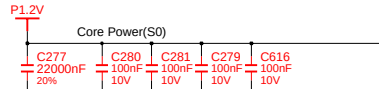
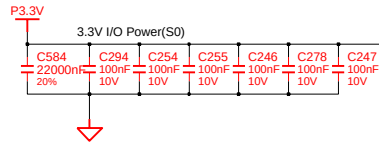
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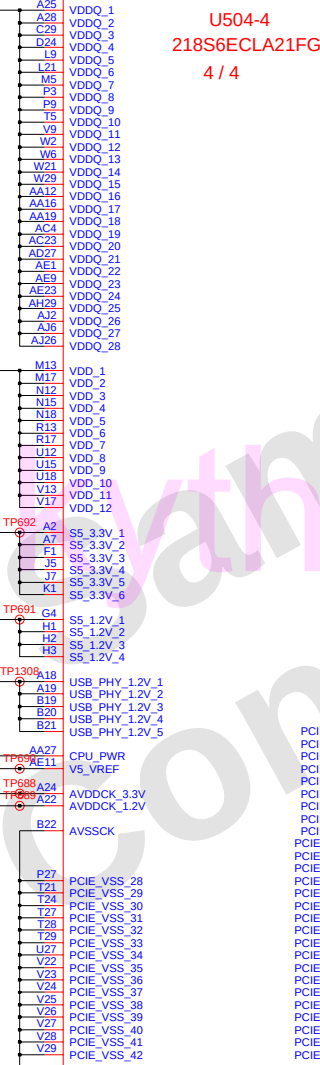
218S6ECLA21FG



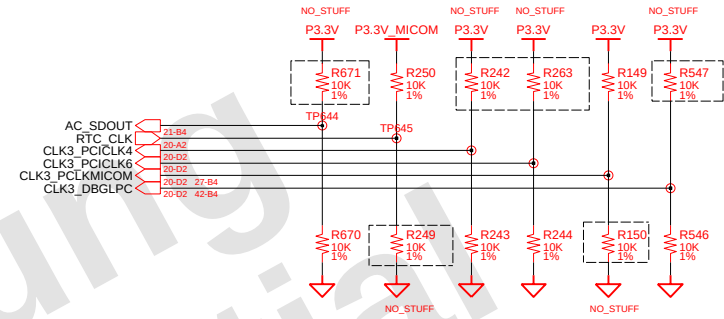
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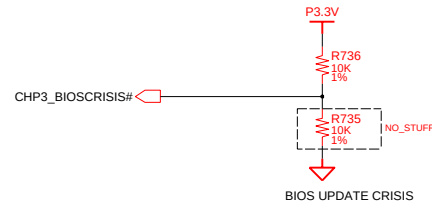
U504-4
218S6ECLA21FG
4 / 4



REQUIRED SYSTEM STRAPS

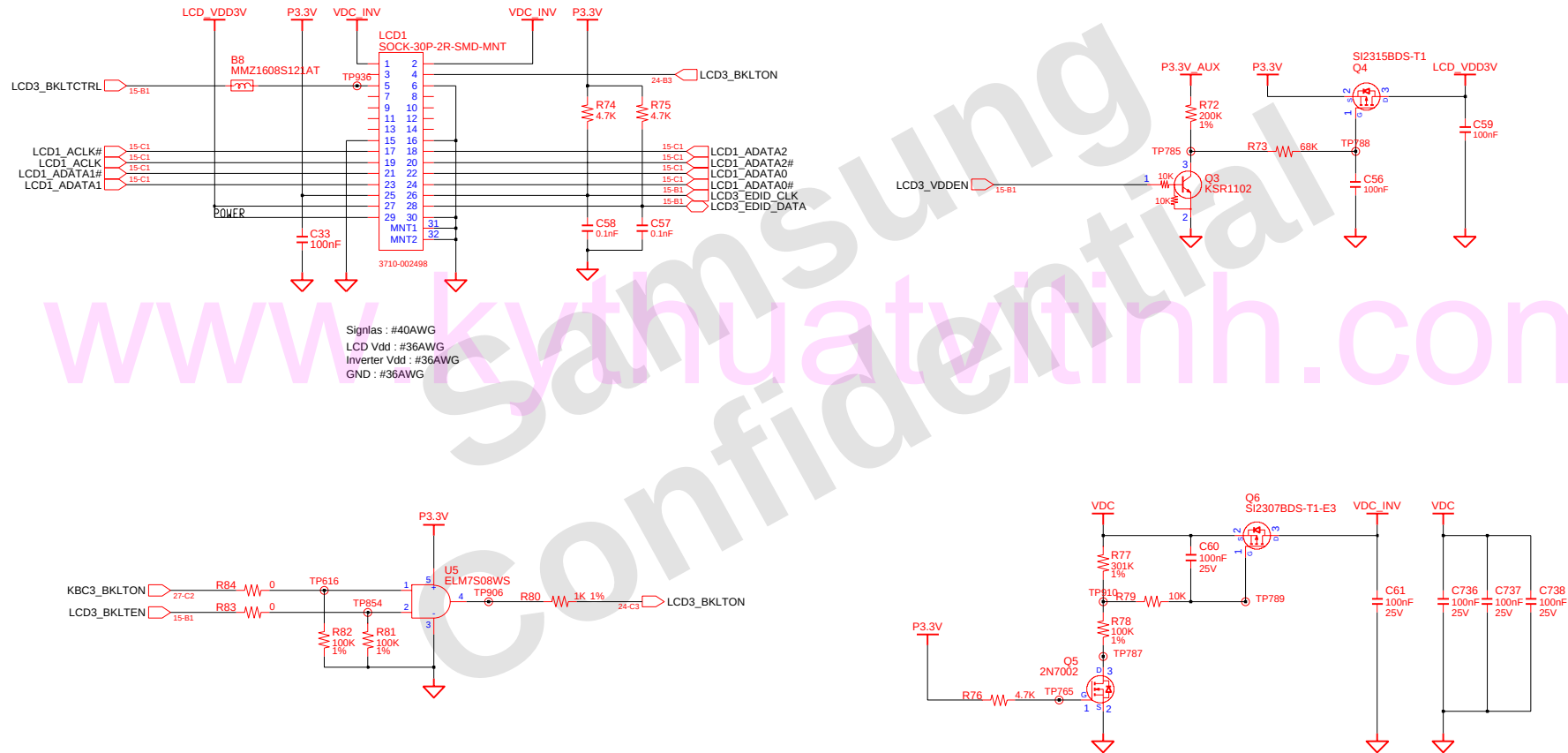


	AC_SDOUT	RTC_CLK	CLK3_PCICLK4	CLK3_PCICLK6	CLK3_PCLKMICOM,CLK3_DBG LPC
STRAP HIGH	USE DEBUG STRAPS	INTERNAL RTC DEFAULT	USE INT PLL 48	CPU I/F = K8	ROM TYPE H.H = PCI ROM H.L = SPI ROM L.H = LPC ROM L.L = FWH ROM
STRAP LOW	IGNORE DEBUG STRAPS DEFAULT	EXRERNAL RTC	USE EXT 48MHZ DEFAULT	CPU I/F = P4	



DRAW	ZHOU JUN	DATE	1/23/2008	TITLE	XI'AN MAIN SB600 (4)	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV STEP	PV			PART NO. BA41-00859/60/61A
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT	November 26, 2007 11:01:51 AM	PAGE	23	OF 48

WXGA LCD CONNECTOR

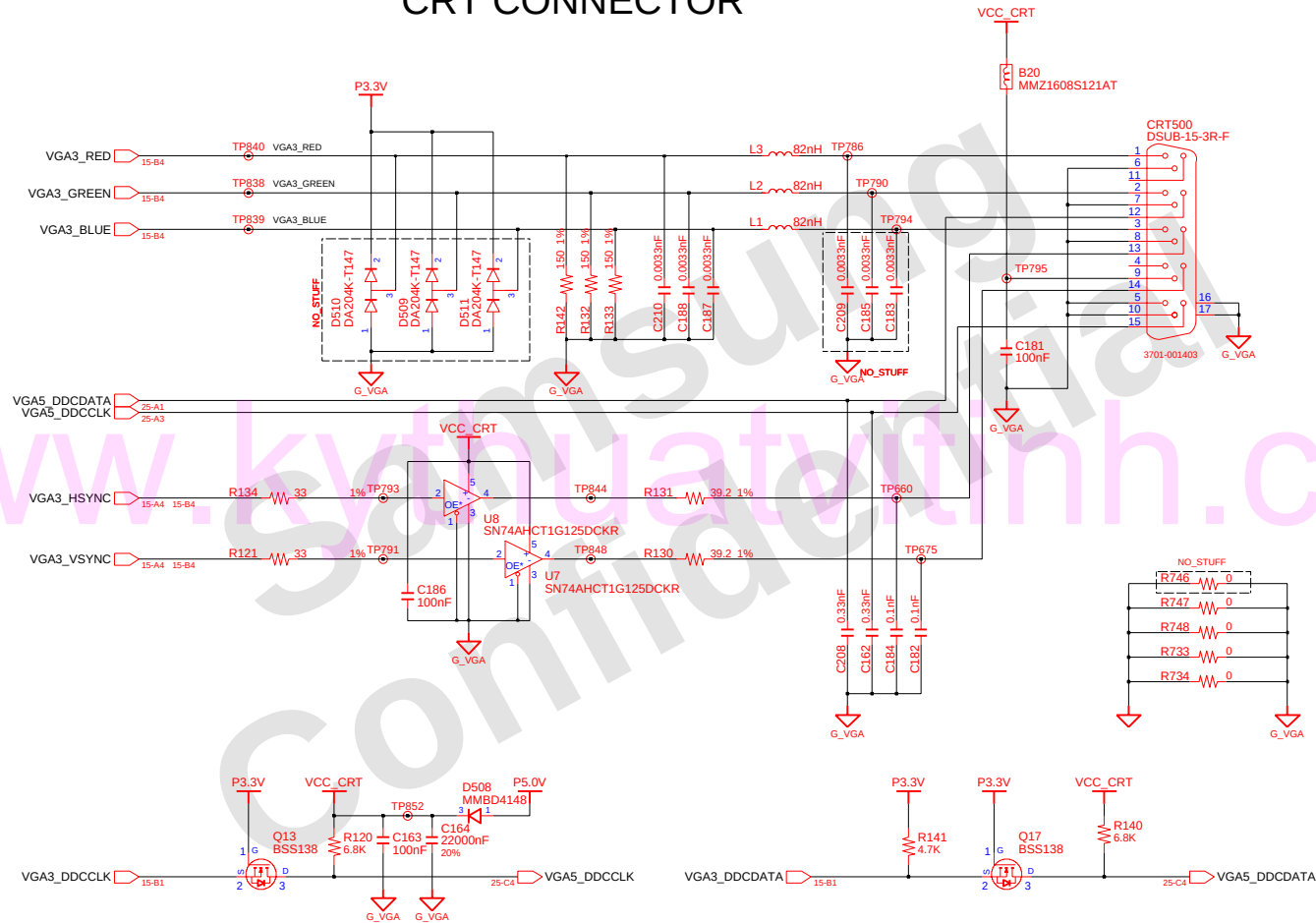


Signal : #40AWG
LCD Vdd : #36AWG
Inverter Vdd : #36AWG
GND : #36AWG

DRAW	ZHOU JUN	DATE	1/23/2008	TITLE	XI'AN	SAMSUNG
CHECK	GUO LEI	DEV. STEP	PV	MAIN	ELECTRONICS	
APPROVAL	KEVIN LEE	REV	1.0	LVDS		
MODULE CODE		LAST EDIT	November 26, 2007 11:01:51 AM	PAGE	24	OF 48
					BA41-00859/60/61A	

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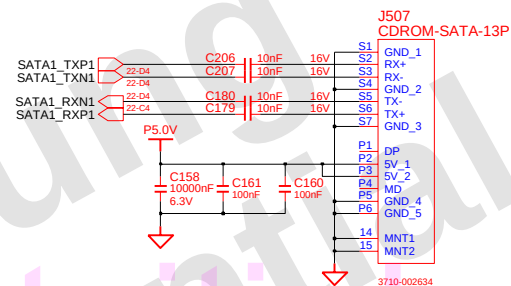
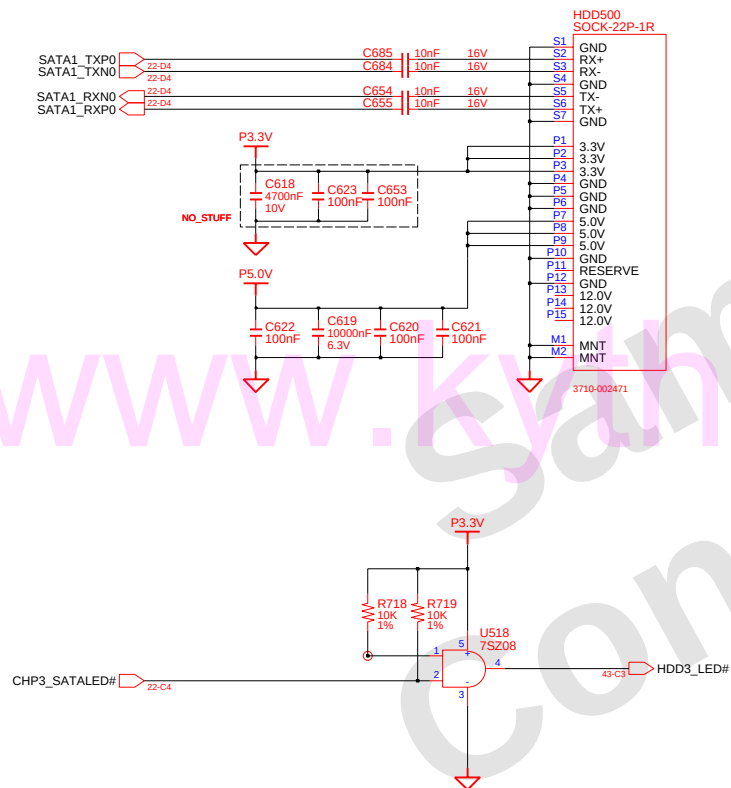
CRT CONNECTOR



DRAW	ZHOU JUN	DATE	1/23/2008	TITLE	XI'AN MAIN CRT	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	PV			PART NO. BA41-00859/60/61A
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT	November 26, 2007 11:01:51 AM	PAGE	25	OF 48

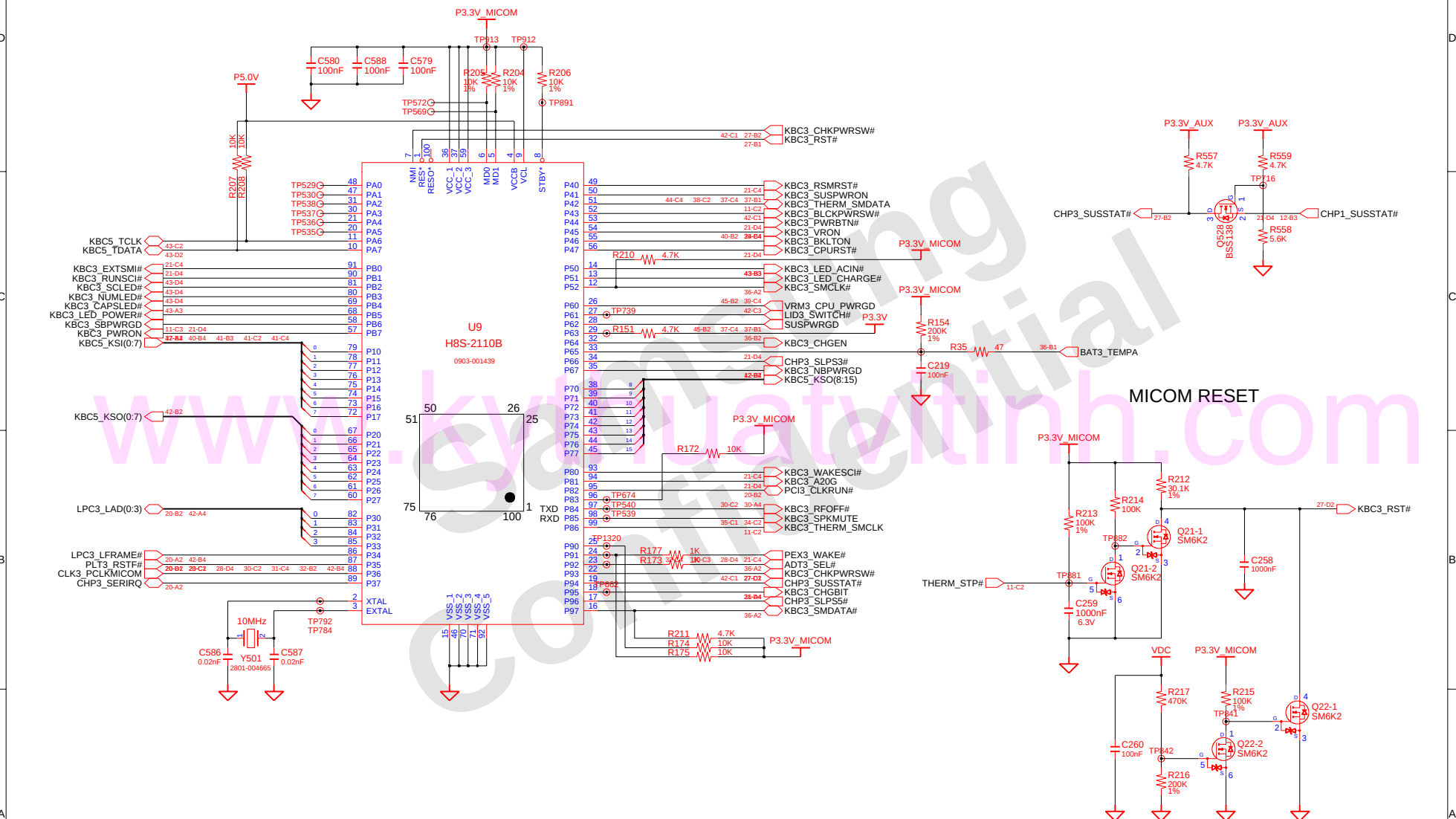
Main to HDD

Main to ODD



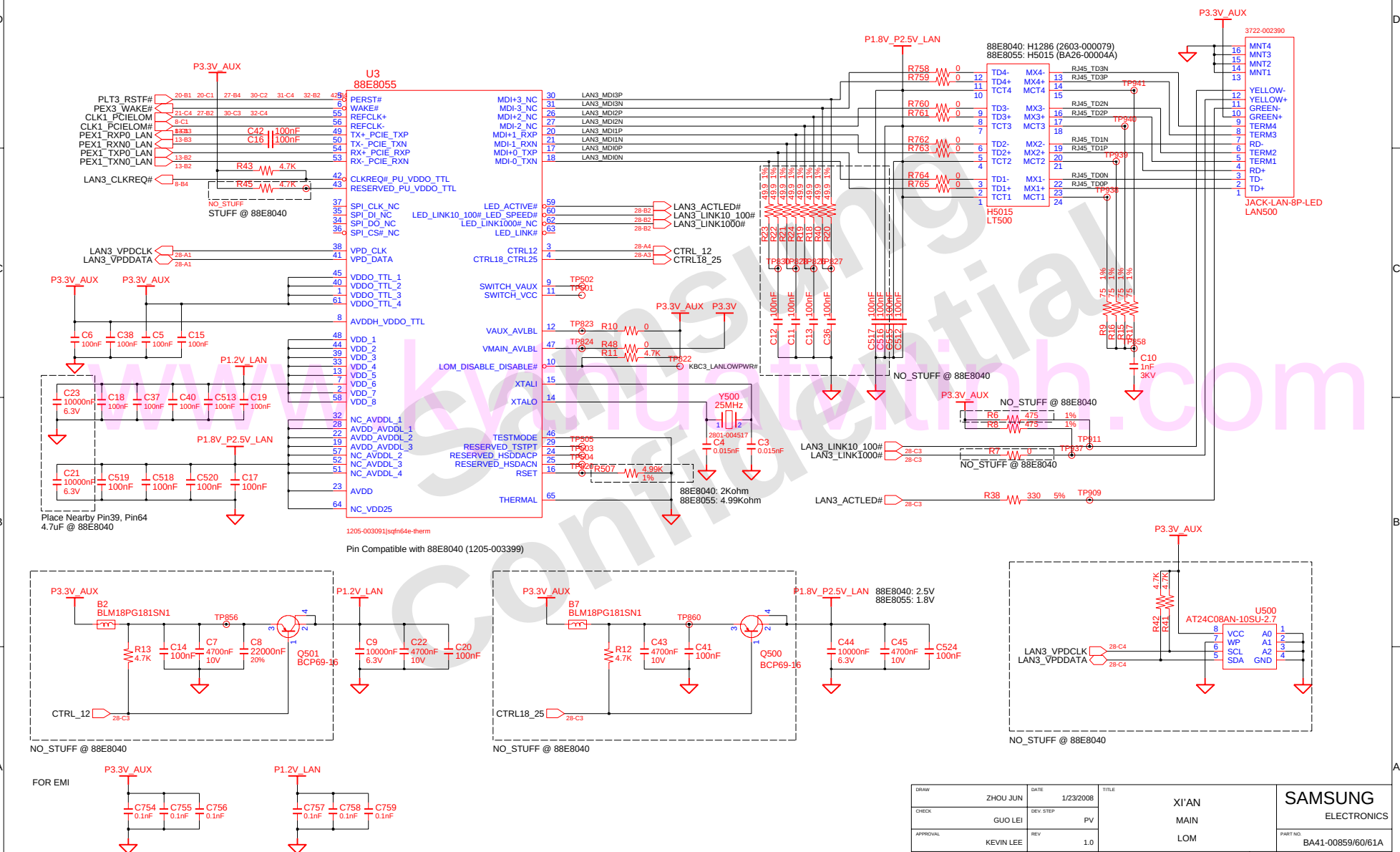
DRAW	ZHOU JUN	DATE	1/23/2008	TITLE	XI'AN	SAMSUNG
CHECK	GUO LEI	DEV. STEP	PV	MAIN	ELECTRONICS	
APPROVAL	KEVIN LEE	REV	1.0	IDE		
MODULE CODE		LAST EDIT	November 26, 2007 11:01:51 AM	PAGE	26	OF 48

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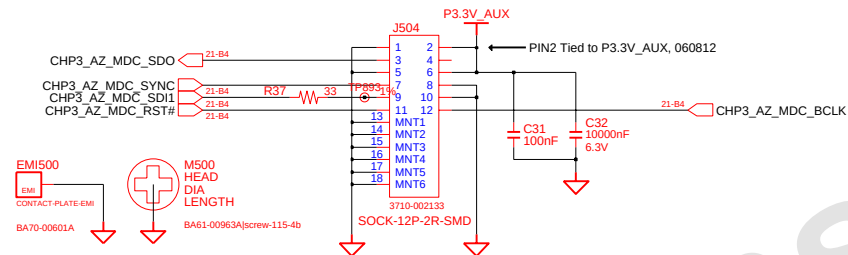


DRAW	ZHOU JUN	DATE	1/23/2008	TITLE	XI'AN MAIN MICOM	SAMSUNG ELECTRONICS			
CHECK	GUO LEI	REV. STEP	PV						
APPROVAL	KEVIN LEE	REV	1.0						
MODULE CODE	LAST EDIT								November 26, 2007 11:01:51 AM
						PART NO.	BA41-00859/60/61A		

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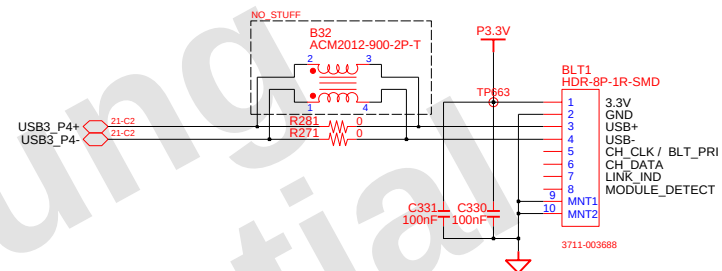


MDC connector



BLUETOOTH

Bluetooth 2.0 Standard

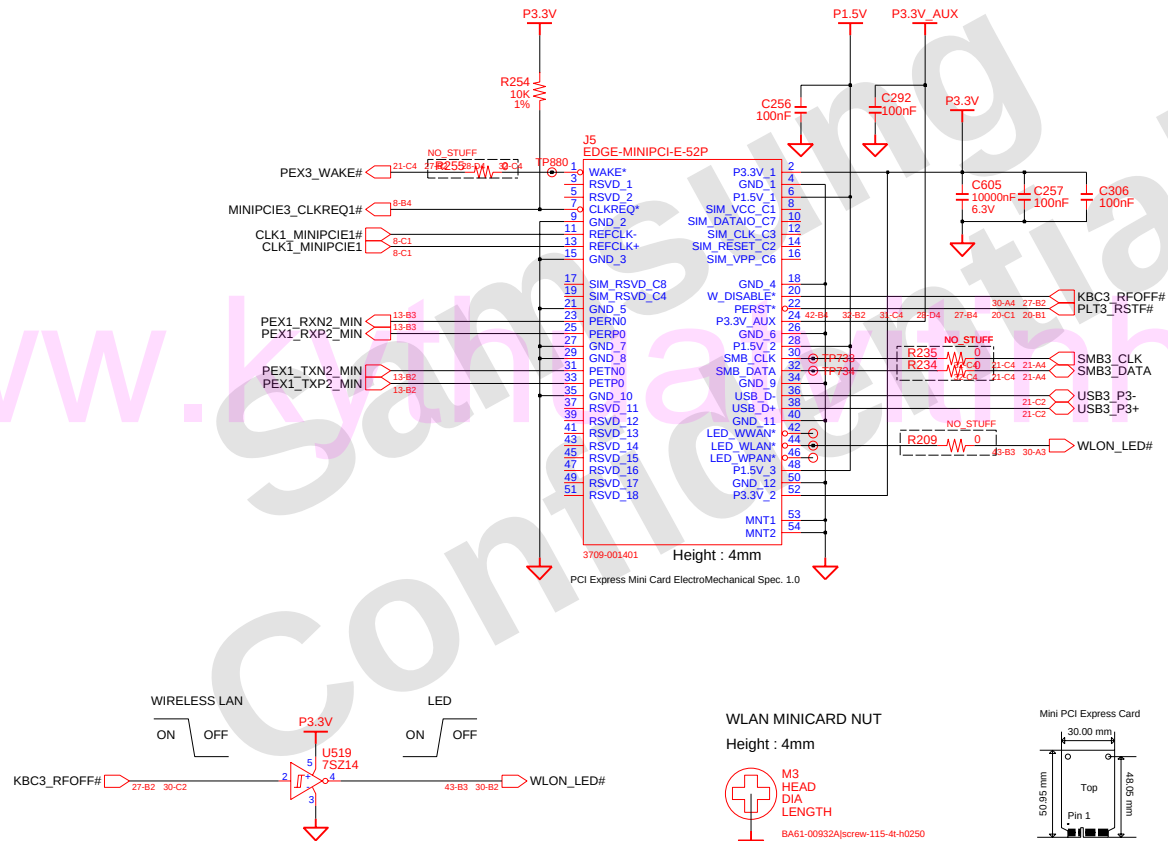


CAMERA



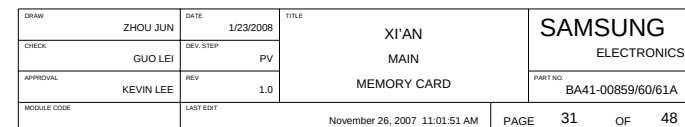
DRAW	ZHOU JUN	DATE	1/23/2008	TITLE	XI'AN MAIN USB,MDC & BLUETOOTH	SAMSUNG ELECTRONICS			
CHECK	GUO LEI	DEV. STEP	PV						
APPROVAL	KEVIN LEE	REV	1.0						
MODULE CODE	LAST EDIT						November 26, 2007 11:01:51 AM	PAGE	29
						PART NO.	BA41-00859/60/61A		

Mini PCI-E Card (WLAN)

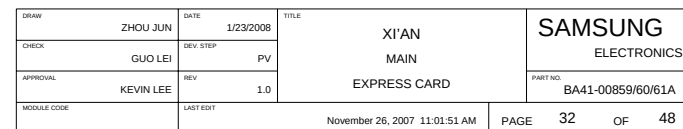


DRAW	ZHOU JUN	DATE	1/23/2008	TITLE	XI'AN MAIN MINI CARD	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	PV			PART NO. BA41-00859/60/61A
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT	November 26, 2007 11:01:51 AM	PAGE	30 OF 48	

MEMORY CARD (2 IN 1)



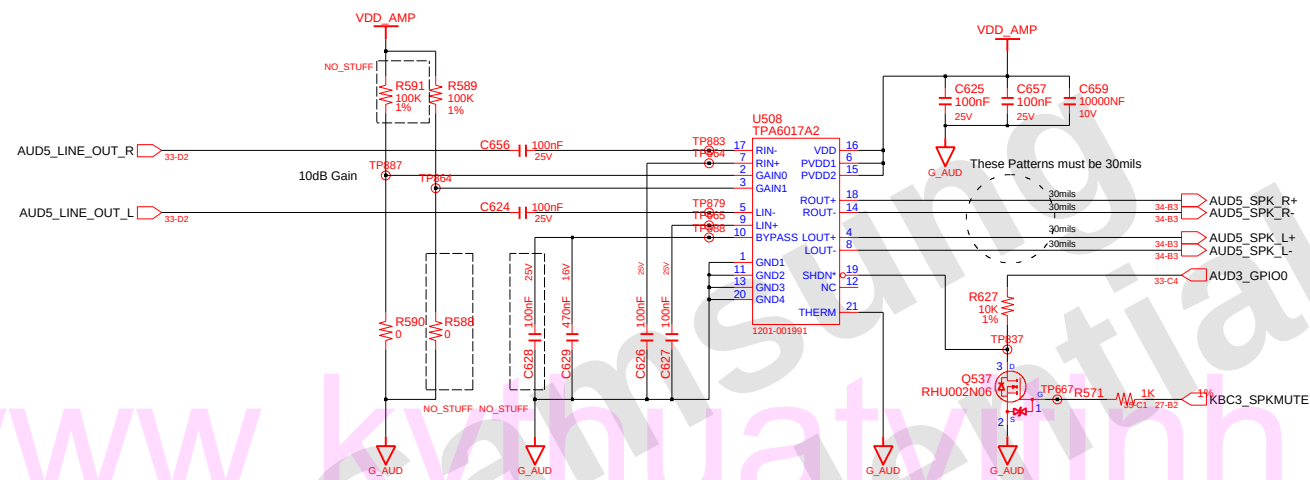
EXPRESS CARD



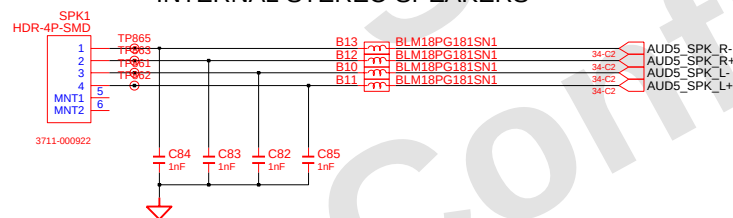
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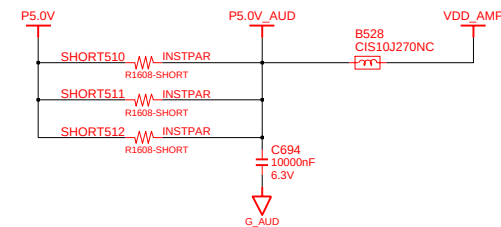
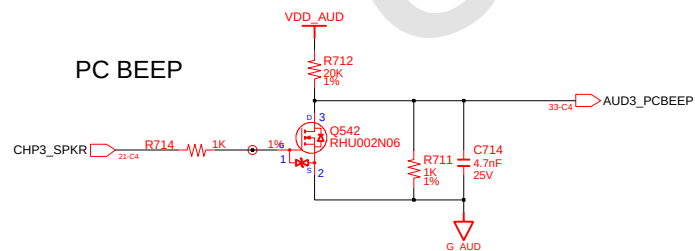
AUDIO AMP



INTERNAL STEREO SPEAKERS

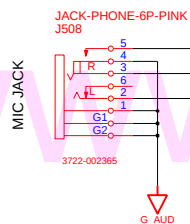
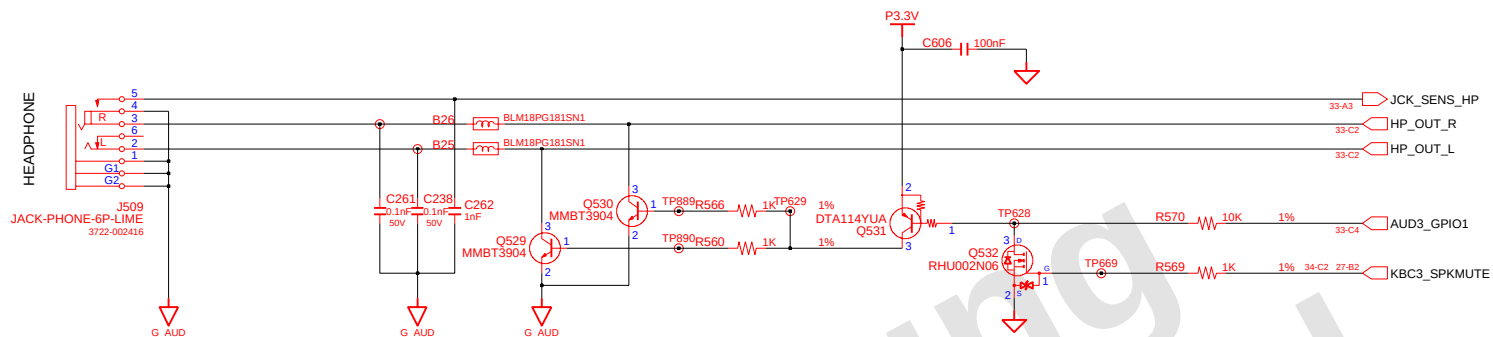


PC BEEP

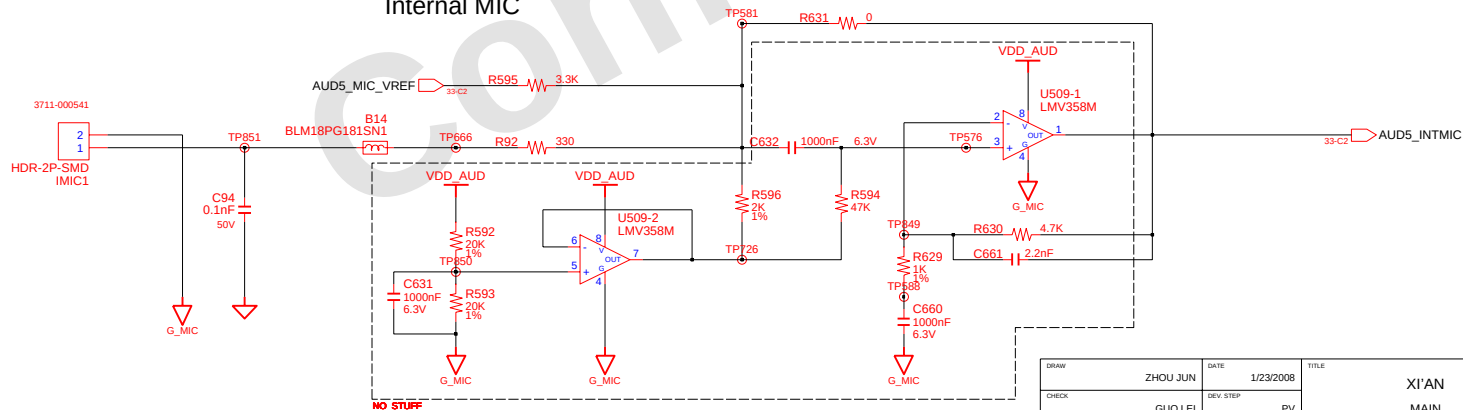


DRAW		DATE	1/23/2008	TITLE	XI'AN MAIN AUDIO AMPLIFIER	SAMSUNG ELECTRONICS	
CHECK		DEV. STEP	PV				
APPROVAL		REV	1.0				
KEVIN LEE							
MODULE CODE		LAST EDIT		November 26, 2007 11:01:51 AM		PAGE	34 OF 48

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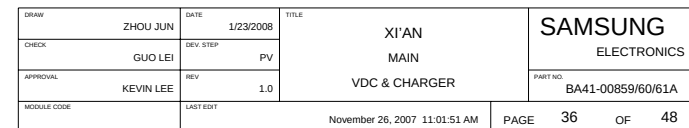


Internal MIC

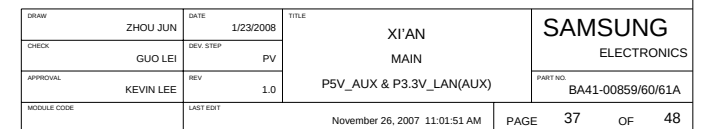


DRAW	ZHOU JUN	DATE	1/23/2008	TITLE	XI'AN MAIN AUDIO JACK	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	PV			PART NO. BA41-00859/60/61A
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT	November 26, 2007 11:01:51 AM	PAGE	35	OF 48

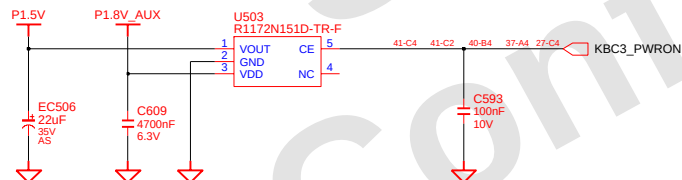
CHARGER & POWER MANAGEMENT



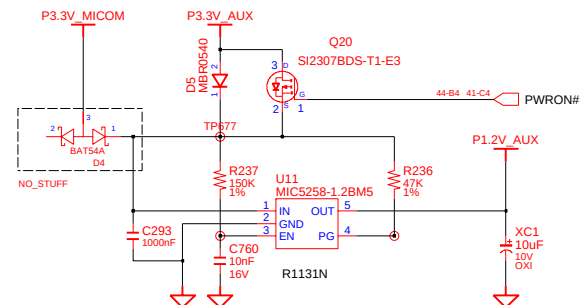
P5V_AUX & P3.3V_AUX



P1.5V POWER



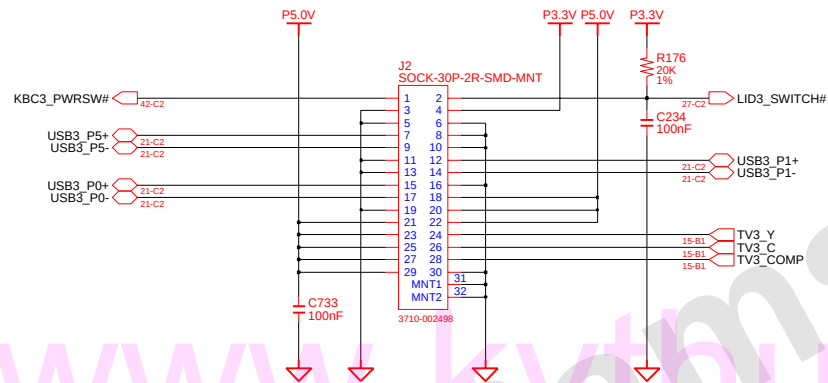
P1.2V AUX



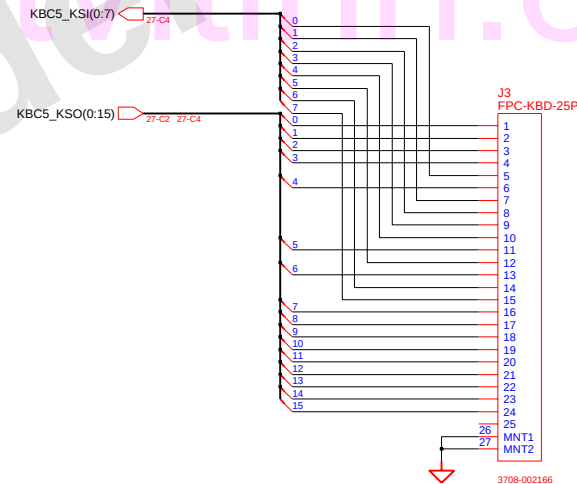
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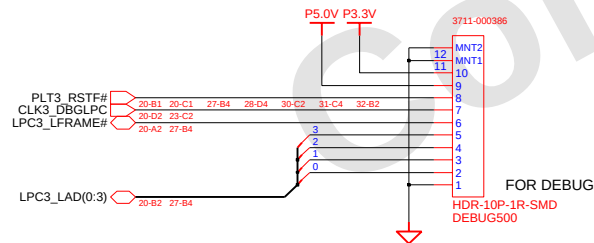
Main to SUB Connector



KEYBOARD

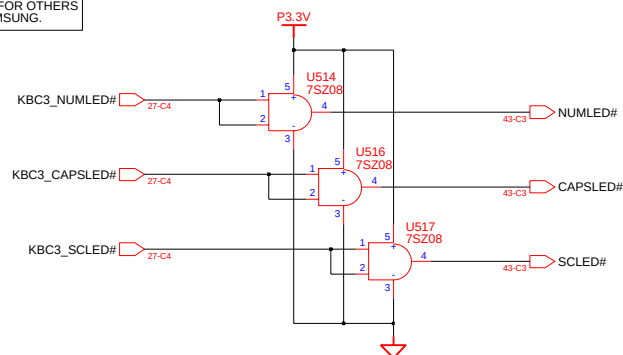


DEBUG PORT

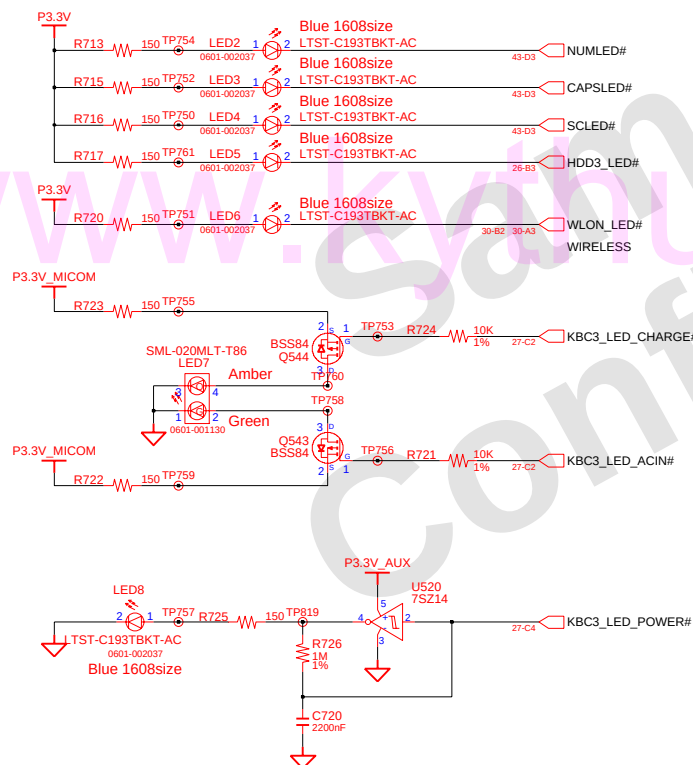


DRAW	ZHOU JUN	DATE	1/23/2008	TITLE	XI'AN MAIN	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	1.0	DMB & KBD & DEBUG & POWER S/W	PART NO. BA41-00859/60/61A	
MODULE CODE		LAST EDIT	November 26, 2007 11:01:51 AM	PAGE	42	OF 48

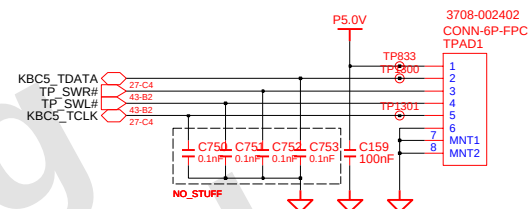
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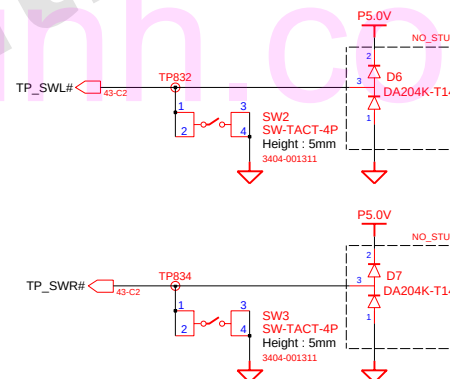
LEDS



TOUCH PAD



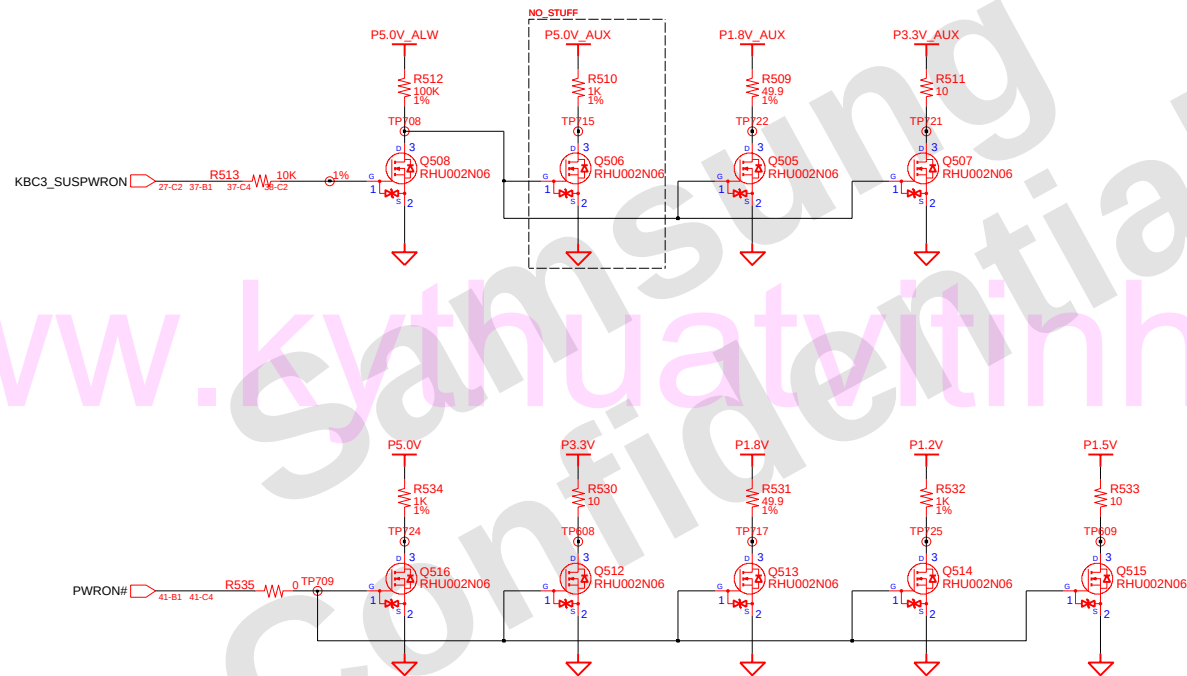
TP SWITCH



DRAW	ZHOU JUN	DATE	1/23/2008	TITLE XI'AN MAIN TOUCH PAD & LEDs	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV STEP	PV		
APPROVAL	KEVIN LEE	REV	1.0		
MODULE CODE	LAST EDIT				
				November 26, 2007 11:01:51 AM	PAGE 43 OF 48

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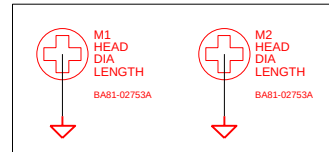
DISCHARGE



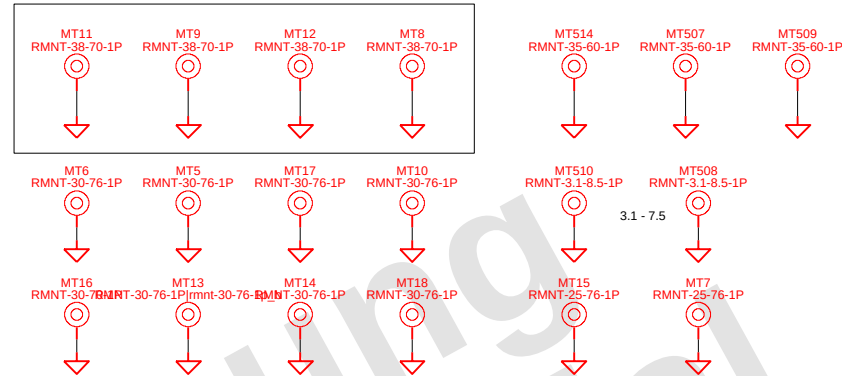
DRAW	ZHOU, JUN	DATE	1/23/2008	TITLE	XI'AN MAIN DISCHARGE	SAMSUNG ELECTRONICS
CHECK	GUO, LEI	DEV. STEP	PV			PART NO. BA41-00859/60/61A
APPROVAL	LEE, KEVIN	REV	1.0			
MODULE CODE		LAST EDIT	November 26, 2007 11:01:51 AM	PAGE	44	OF 48

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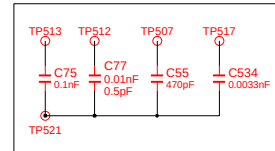
FAN-NUT



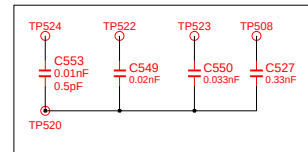
CPU RHE



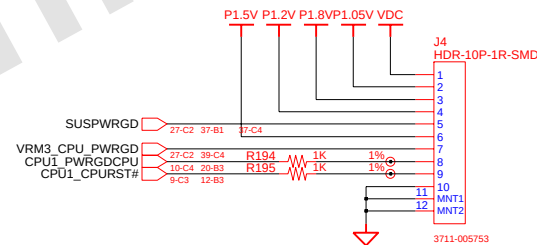
1 ~ 499 SIDE



500 ~ SIDE



ICT PORT



REV1
1 O
2 O 3

PCB REVISION CONTROL (ICT)				
NO	CONNECTION	DATE(Y/M/D)	REVISION	STEP
1	N.C.			
2	1-2			
3	2-3			
4	3-1			
5	1-2-3			
6	N.C.			
7	1-2			
8	2-3			
9	3-1			
10	1-2-3			

DRAW	ZHOU JUN	DATE	1/23/2008	TITLE	XI'AN MAIN MOUNT HOLE	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	PV			PART NO. BA41-00859/60/61A
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT	November 26, 2007 11:01:51 AM	PAGE	45 OF 48	

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TP1283 ADT3_SEL#
TP1238 AUD3_GPI00
TP1239 AUD3_GPI01
TP1264 AUD3_PCBEOP
TP1265 AUD5_INTMIC
TP1124 AUD5_LINE_OUT_L
TP1125 AUD5_LINE_OUT_R
TP1168 AUD5_MIC_VREF
TP1266 AUD5_SPK_L+
TP1203 AUD5_SPK_L-
TP1204 AUD5_SPK_R+
TP1205 AUD5_SPK_R-
TP1206 BAT3_SMCLK#
TP1191 BAT3_SMDATA#
TP1240 BAT3_TEMP#
TP1275 CAPSLDIN#
TP1192 CHP1_DPRSTP#
TP1169 CHP1_SUSSTAT#
TP1126 CHP3_ALINK_RST#
TP1127 CHP3_AZ_AUD_SDO
TP1128 CHP3_AZ_MDC_SDO
TP1131 CHP3_AZ_AUD_BCLK
TP1132 CHP3_AZ_AUD_RST#
TP1133 CHP3_AZ_AUD_SDI0
TP1134 CHP3_AZ_AUD_SYNC
TP1135 CHP3_AZ_MDC_BCLK
TP1136 CHP3_AZ_MDC_RST#
TP1137 CHP3_AZ_MDC_SDI1
TP1138 CHP3_AZ_MDC_SYNC
TP1193 CHP3_CPDIST#
TP1170 CHP3_DPRSLPVR
TP1207 CHP3_NBRST#
TP1107 CHP3_P1_BVEN#
TP1108 CHP3_SATALED#
TP1208 CHP3_SERIRQ#
TP1209 CHP3_SLPS3#
TP1210 CHP3_SLPS5#
TP1284 CHP3_SPKR
TP1109 CHP3_SUSSTAT#
TP1241 CLK3_ICHI4
TP1242 CLK3_NB14M

TP1211 CLK3_SMCLK
TP1194 CLK3_SMDATA
TP1243 CLK3_USB48
TP1244 CPU1_A20M#
TP1245 CPU1_BSEL0
TP1246 CPU1_BSEL1
TP1247 CPU1_BSEL2
TP1195 CPU1_CPURST#
TP1196 CPU1_DPRSTP#
TP1212 CPU1_DPSLP#
TP1248 CPU1_FERR#
TP1213 CPU1_IGNNE#
TP1249 CPU1_INIT#
TP1286 CPU1_INTR
TP1287 CPU1_PSH#
TP1110 CPU1_PWRGDPCPU
TP1288 CPU1_SLP#
TP1289 CPU1_SMI#
TP1197 CPU1_STPCLK#
TP1117 CPU1_THRMTRIP#
TP1111 CPU1_VCCSENSE
TP1112 CPU1_VSSSENSE
TP1214 CPU1_VID(0)
TP1215 CPU1_VID(1)
TP1216 CPU1_VID(2)
TP1217 CPU1_VID(3)
TP1218 CPU1_VID(4)
TP1219 CPU1_VID(5)
TP1220 CPU1_VID(6)
TP1198 CPU2_THERMDA
TP1199 CPU2_THERMDC
TP1118 CPU3_THRMTRIP#
TP1290 CTRL18_25
TP1273 CTRL12

TP1250 EXP3_CPPER#
TP1221 EXP3_CPU5B#
TP1222 EXP3_PERST#
TP1291 HDD3_LED#
TP1276 HP_OUT_L
TP1277 HP_OUT_R
TP1200 JTP3_SYSTRST#
TP1223 JCK_SENS_HP
TP1201 JCK_SENS_MIC
TP1292 KBC3_A20G
TP1224 KBC3_BKLTON
TP1129 KBC3_BLKCPWRSW#
TP1113 KBC3_CAPSLD#
TP1251 KBC3_CHGEN
TP1119 KBC3_CHKPWRSW#
TP1272 KBC3_CPURST#
TP1139 KBC3_EXTSMI#
TP1120 KBC3_LED_ACIN#
TP1075 KBC3_LED_CHARGE#
TP1130 KBC3_LED_POWER#
TP1140 KBC3_NBPWRGD
TP1141 KBC3_NUMLED#
TP1142 KBC3_PWRBTN#
TP1252 KBC3_PWRON
TP1225 KBC3_PWRSW#
TP1143 KBC3_RSMRST#
TP1293 KBC3_RST#
TP1144 KBC3_RUNSCI#
TP1145 KBC3_SBPWRGD
TP1193 KBC3_SLED#
TP1227 KBC3_SMCLK#
TP1146 KBC3_SMDATA#
TP1114 KBC3_SUSPWRRON
TP1075 KBC3_THERM_SMCLK
TP1077 KBC3_THERM_SMDATA
TP1294 KBC3_VRON
TP1115 KBC3_WAKESCI#
TP1228 KBC5_KSI(0)
TP1229 KBC5_KSI(1)
TP1230 KBC5_KSI(2)
TP1231 KBC5_KSI(3)
TP1232 KBC5_KSI(4)
TP1233 KBC5_KSI(5)
TP1234 KBC5_KSI(6)
TP1171 KBC5_KSI(7)
TP1172 KBC5_KSO(0)
TP1173 KBC5_KSO(1)
TP1174 KBC5_KSO(2)
TP1175 KBC5_KSO(3)
TP1176 KBC5_KSO(4)
TP1177 KBC5_KSO(5)
TP1178 KBC5_KSO(6)
TP1179 KBC5_KSO(7)
TP1180 KBC5_KSO(8)
TP1181 KBC5_KSO(9)
TP1147 KBC5_KSO(10)
TP1148 KBC5_KSO(11)
TP1149 KBC5_KSO(12)
TP1150 KBC5_KSO(13)
TP1151 KBC5_KSO(14)
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TP1153 LAN3_ACTLED#
TP1154 LAN3_CLKREQ#
TP1121 LAN3_LINK1000#
TP1182 LAN3_VPDCLK
TP1155 LAN3_VPDATA
TP1116 LCD3_BKLTCTRL
TP1183 LCD3_BKLTEN
TP1184 LCD3_BKLTEN
TP1122 LCD3_EDID_DATA
TP1223 LCD3_VPDEN
TP1185 LPC3_LAD(0)
TP1186 LPC3_LAD(1)
TP1187 LPC3_LAD(2)
TP1188 LPC3_LAD(3)
TP1156 LPC3_LFRAME#

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TP1162 MCH2_THERMDP
TP1254 MCD3_SDCD#
TP1255 MCD3_SDCCLK
TP1256 MCD3_SDCMD
TP1157 MCD3_SSDATA0
TP1158 MCD3_SSDATA1
TP1159 MCD3_SSDATA2
TP1160 MCD3_SSDATA3
TP1295 MCD3_SOWP
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TP1271 PWRON#
TP1272 SCLDIN#
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TP1279 SPI3_CLK
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TP1225 SPI3_MISO
TP1255 SPI3_MOSI
TP1281 SUSPWRRGD
TP1164 THERM_ALERT#
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TP1226 TV3_COMP
TP1270 TV3_Y
TP1259 VCCP_PWRGD
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TP1261 VGA3_VSYN
TP1166 VGA5_DDCDATA
TP1190 VGA5_DDCCLK
TP1123 VRM3_CPU_PWRGD
TP1262 VRM3_SEN1
TP1263 VRM3_SEN2
TP1236 VRM3_VSUM
TP1167 VRM3_VCC_FRM
TP1237 WBLON_LED#

TP1079 CHG_REF
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TP1081 CPU_CORE
TP1082 CPU_CORE
TP1083 CPU_CORE
TP1084 G_AUD
TP1085 G_CHG
TP1086 G_CPU
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TP1043 PWR_DC
TP1044 PWR_DC
TP1045 PWR_DC

TP1049 PRTC_BAT
TP1050 P1.2V_AUX
TP1051 P1.2V_LAN
TP1052 P1.5V_EXP
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TP1054 P1.8V_AUX
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TP1057 P3.3V_AUX
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TP1066 P5.0V_AUX
TP1067 P5.0V_AUX
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TP1036 VDDC_VDDR
TP1037 VDDC
TP1038 VDDC
TP1039 VSFILT
TP1040 VDDC_INV
TP1041 VDDC_AMP
TP1042 VDD_AUD

DRAW	ZHOU JUN	DATE	1/23/2008	TITLE	XI'AN MAIN TEST POINT	SAMSUNG ELECTRONICS PART NO. BA41-00859/60/61A
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APPROVAL	KEVIN LEE	REV	1.0			
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REVISION HISTORY

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NOTES

Note1. Schematic Revision: MP 1 . 0 . 0
Development Stage
PCB Version
Schematic Version

Note2. PCB SEC Code
Vendor: SGCE P/N: BA41-??????
Vendor: TPT P/N: BA41-??????

DRAW	ZHOU JUN	DATE	1/23/2008	TITLE	XI'AN MAIN	SAMSUNG ELECTRONICS
CHECK	GUO LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	1.0	REVISION HISTORY		PART NO. BA41-00859/60/61A
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