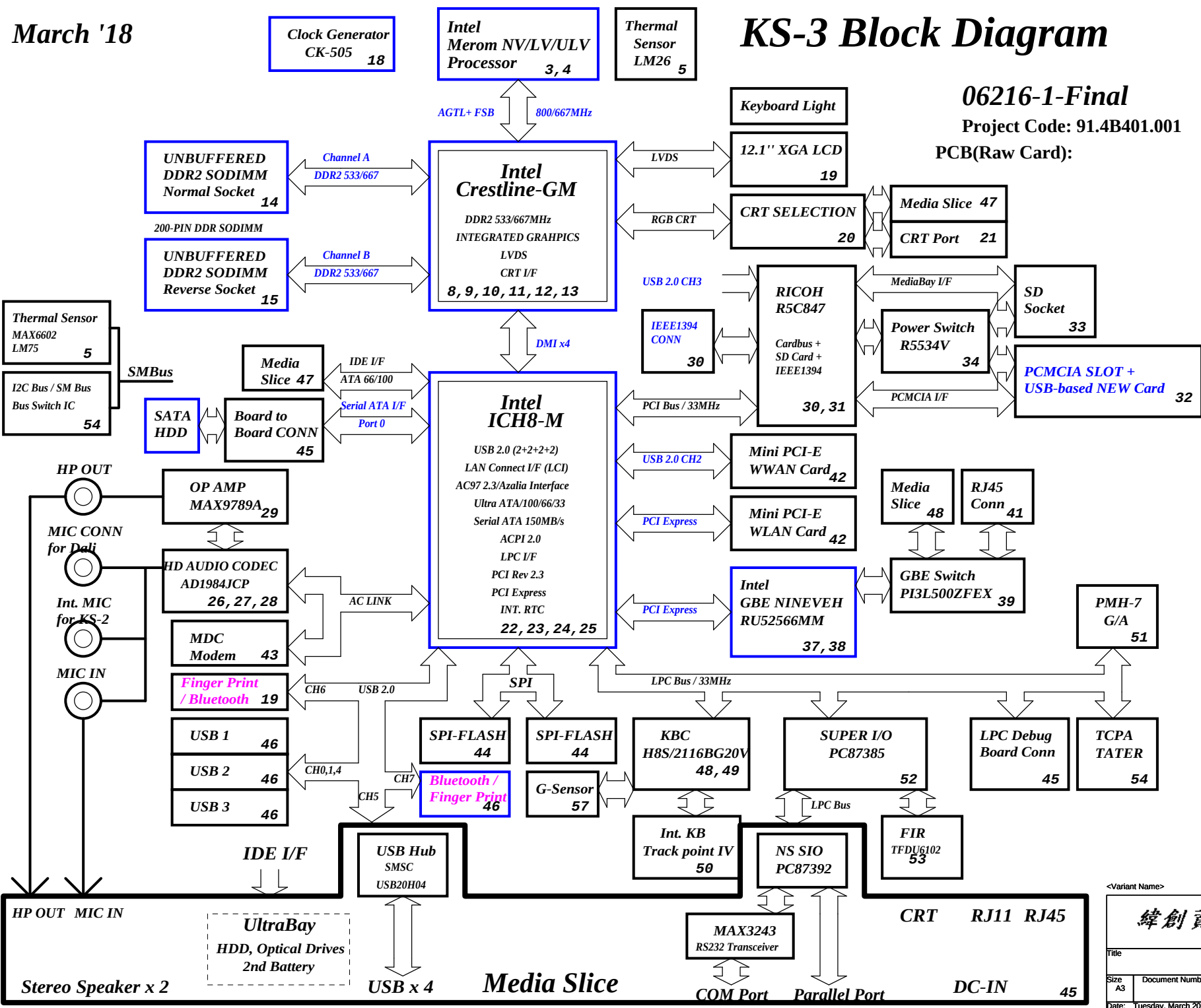


March '18



KS-3 Block Diagram

06216-1-Final

Project Code: 91.4B401.001

PCB(Raw Card):

PCB Layer Stackup	
L1:Component	
L2:GND	
L3:Signal 1	
L4:VCC	
L5:Signal 2	
L6:Signal 3	
L7:GND	
L8:Component	

Battery Charger/Selector	
MAX8765 60	
INPUTS	OUTPUTS
DOCK_PWR19_F	M-BAT-PWR
	S-BAT-PWR
System DC/DC	
MAX8744 65	
VINT19	VCC5M
	VCC3M
CPU DC/DC	
ADP3207 66	
VINT19	VCC5M
	VCC3M
GMCH GFX CORE	
ADP3209 68	
VINT19	VCCGFXCORE
VCC1R8M	
MAX8632 69	
VINT19	VCC1R8M
VCC1R05B	
MAX1540ETJ+T 70	
VCC3M	VCC1R05M
VCC1R5M	
MAX1540ETJ+T 70	
VCC3M	VCC1R5M
VCC1R25AMT	
APL5912 71	
VCC1R5M	VCC1R25AMT

緯創資通 Wistron Corporation	
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Title	
Block Diagram	
Size A3	Document Number
KS note-3	
Date: Tuesday, March 20, 2007	Sheet 1 of 78

RESISTOR

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

The naming rule is value + R + size + tolerance
For the value, it can be read by the number before R. (R means resistor)
For the tolerance, it can be read from the last letter.
For the rating, we don't show on the symbol name.
For the size, R2=>0402, R3=>0603, R5=>0805,.....

CAPACITOR

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is
Capacitor type + value + rating + size + tolerance + material
SCD1U10V2MX-1
SC=> SMT Ceremic, TC=> POS cap or SP cap
D1U => 0.1uF
10V => the voltage rating is 10V
2=> 0402, 3=>0603, 5=>0805
M=>tolerance M, K, Z
X=> X7R/X5R, Y=> Y5V
-1 => symbol version, nonsense to EE characteristic

PLANAR_ID[3..0]

ICH8-M GPION	39	38	37	36	Planar ID Version	Planar PCB Version
PLANAR_IDn	3	2	1	0		
	0	0	0	0	KShote-3 Pre-DV	SA
	0	0	0	1	DV1	SB
	0	0	1	0	FVT	SC
	0	0	1	1	DV2	SD
	0	1	0	0	DV2-R, SIT	SE, SF
	0	1	0	1	SIT (Dali-2)	SG
	0	1	1	0	SIT-R	SH
	0	1	1	1	SVT	-1

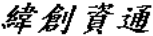
EC HISTORY

Stage	Date	EC No.	Page	Note
DV	7/19	EC019	69	Add R856 connect U22 pin.5 and MPWRG
DV	7/19	EC019	74	Add R857(DY)
DV1	7/21	EC001	56	Add 2nd FAN Control logic in P.56
DV1	7/21	EC001	18	Delete C590,C593,C589,C579,R833,R834 make DREFCLK_96M connect to U67 Pin.5 -DREFCLK_96M connect to U67 Pin.6 Reset R514,R515,R573 to 1ohm
DV1	7/21	EC001	11	Add C590,C905,C906,C593,C579,C589 and connect to U53F Pin.AC16
DV1	7/21	EC001	12	Reset C139,C145,C701 to 6.3V 10%
DV1	7/21	EC001	10	Reset U27 to Dummy, R250 to ASM
DV1	7/21	EC001	48	Reset R212 to DY Add -90W AC connect to U26A Pin.11
DV1	7/21	EC001	49	Reset R291 to DY Add GSENSE_TST connected to U26B Pin.F3 Add FAN2_ON, FAN2_FRQ
DV1	7/21	EC001	24	Reset R337 to DY,R341 to ASM
DV1	7/21	EC001	19	Reset R649,U12,R49 to DY, R648 to ASM
DV1	7/21	EC001	46	Delete F6,Q25,R133,C224 and -2NDFAN_ON
DV1	DV1	EC001	68	Reset R427,R451,R442,R439,R420 to 22k ohm Change the power from VCC5M to VCC3M

PCI TABLE

DEVICE	IDSEL	IRQ (Default)	REQ# / GNT#
MINIPCI SLOT	AD18	F, G	REQ# 3/ GNT#3
CARDBUS R5C811	AD16	SERIRQ	REQ#0 / GNT#0
USB UHCI	AD29	A, C, D	
USB 2.0 EHCI	AD29	H	
DMI-to-PCI/ AC97 Modem/ AC97 Audio	AD30	B B	
LPC Bridge IDE SATA SMBus	AD31	C C B	
PCI Express	AD28	A, B, C, D	

<Variant Name>



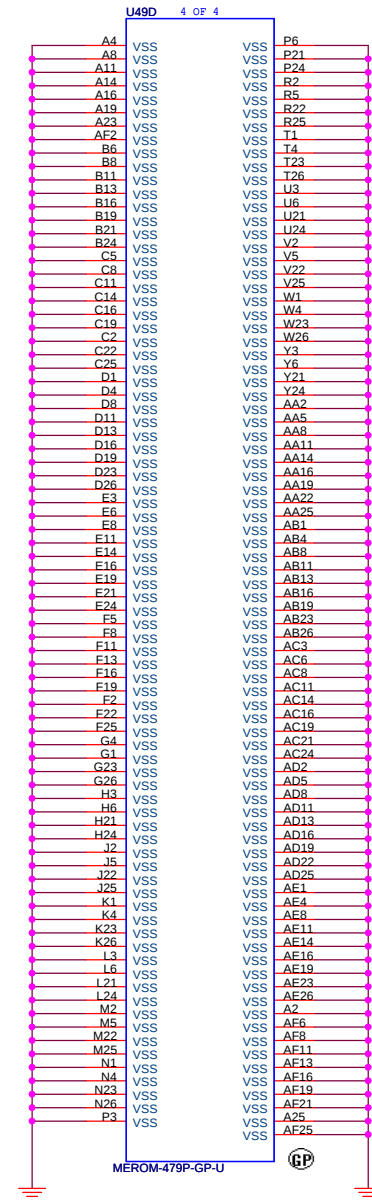
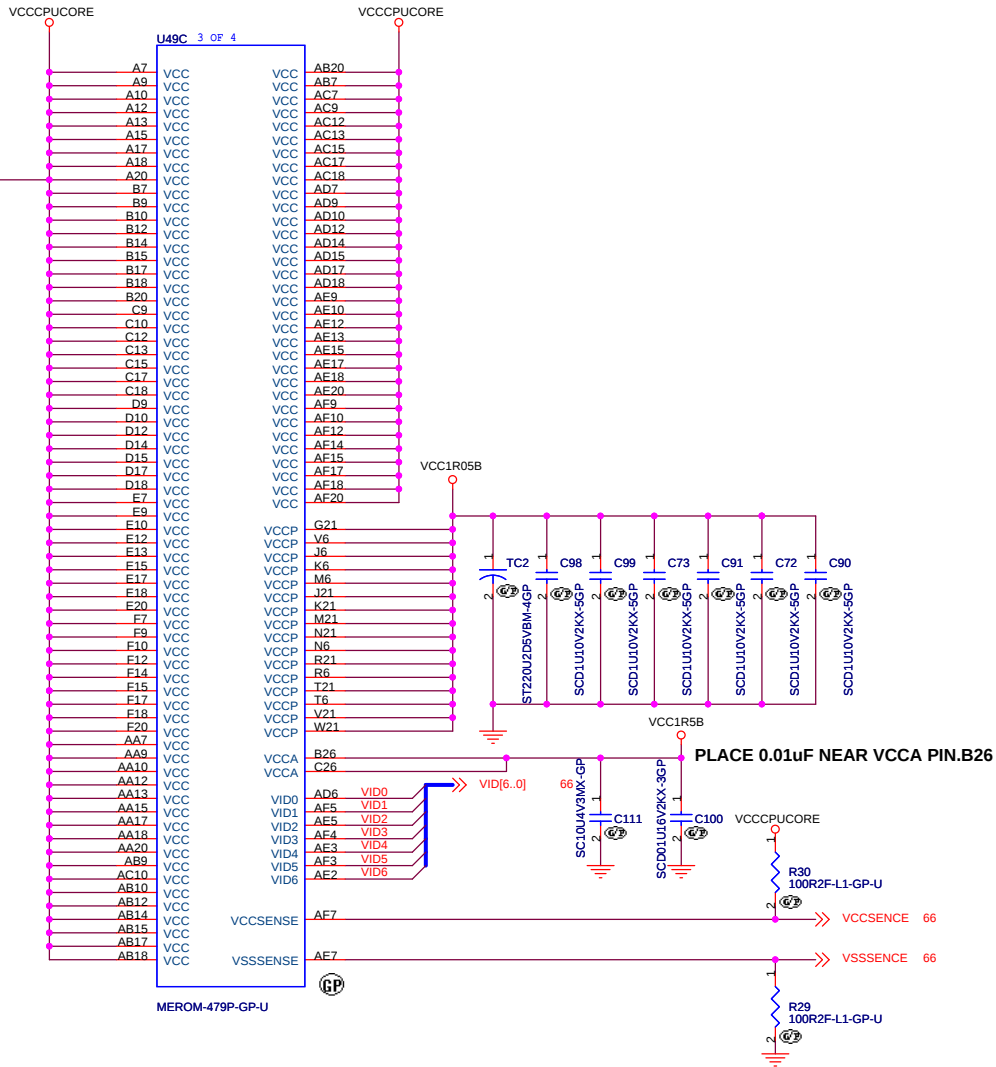
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

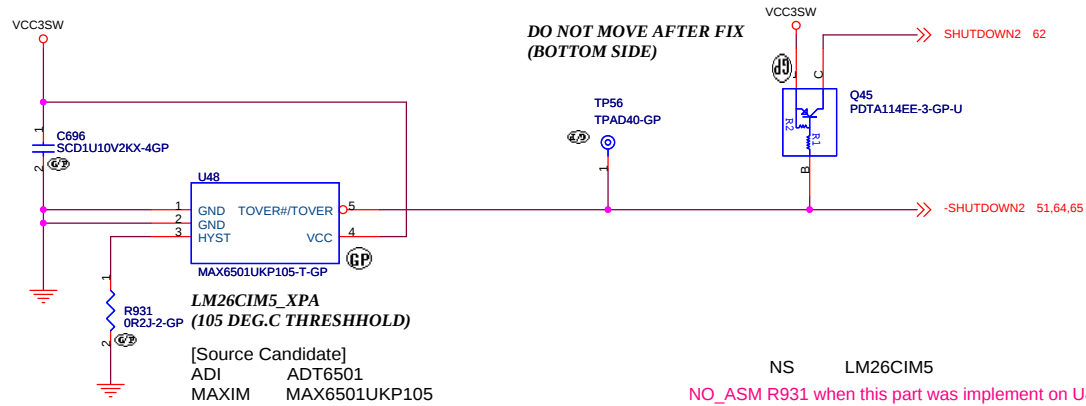
Reference

Size A3Document NumberKS Note-3Rev-1

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Thermal Sensor LM26 for CPU

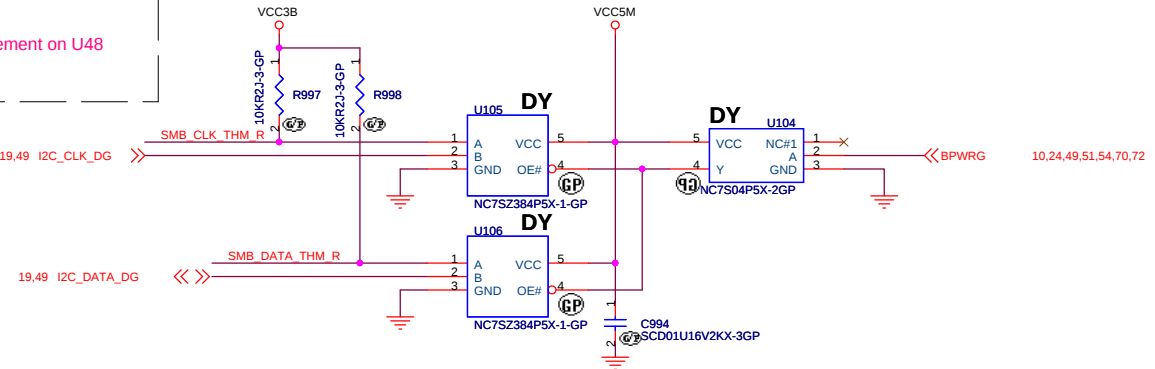
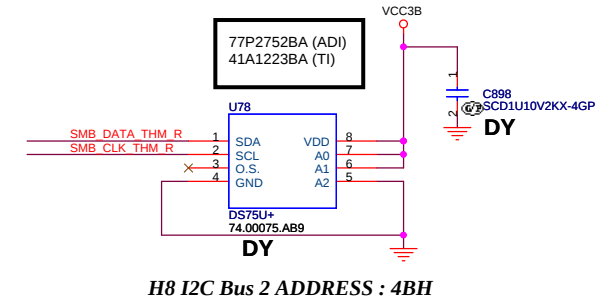


DALI2 uP/Thermal Sensor IIC BUS 19,49 I2C_CLK_DG

	KS3	Dali2
U104	NoASM	ASM
U105	NoASM	ASM
U106	NoASM	ASM

Thermal Sensor LM75 for Gbe module

Place near Gbe



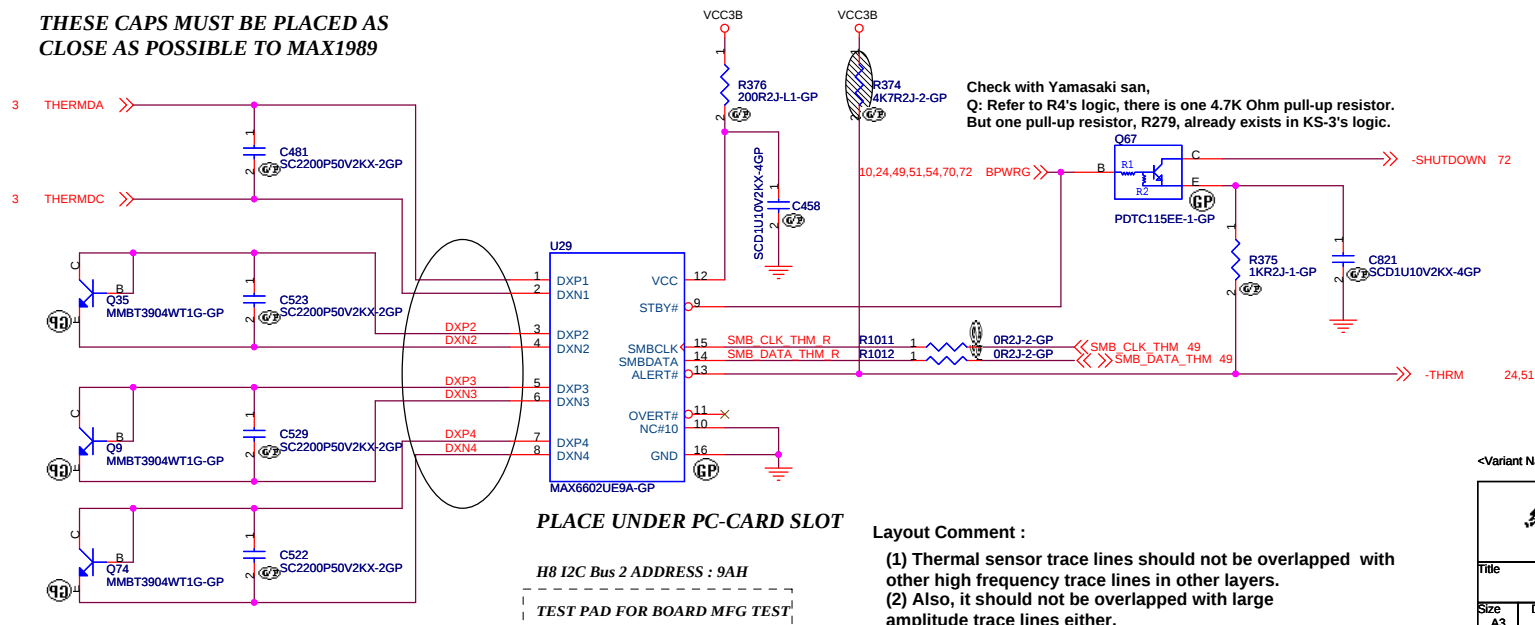
THESE CAPS MUST BE PLACED AS
CLOSE AS POSSIBLE TO MAX1989

TO CPU DIE

DDR MODULE

TO VCORE FET

TO MINI CARD

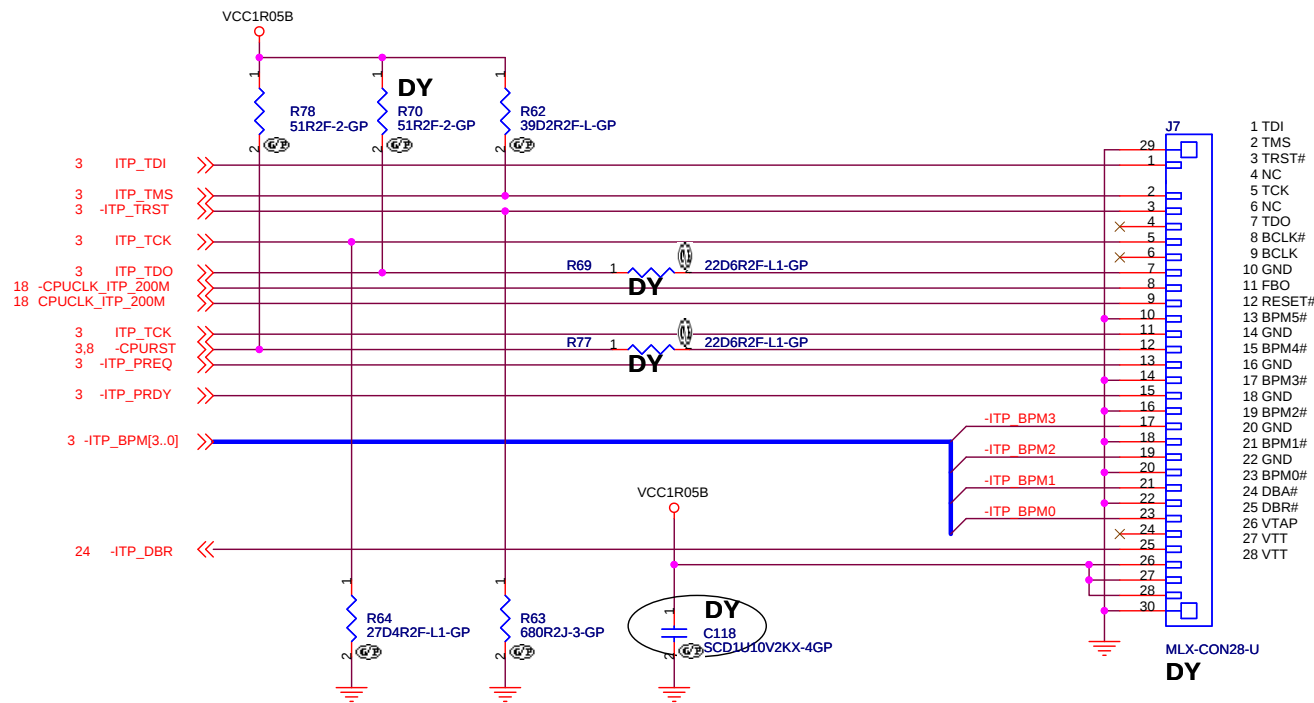


Layout Comment :

- (1) Thermal sensor trace lines should not be overlapped with other high frequency trace lines in other layers.
- (2) Also, it should not be overlapped with large amplitude trace lines either.

<Variant Name>

緯創資通 Wistron Corporation		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title THERMAL SENSOR			
Size A3	Document Number	KS note-3	
Date: Tuesday, March 20, 2007	Sheet 5	of 78	Rev -1



(*1) TCK SIGNAL IS BRANCHED AT CPU's PIN

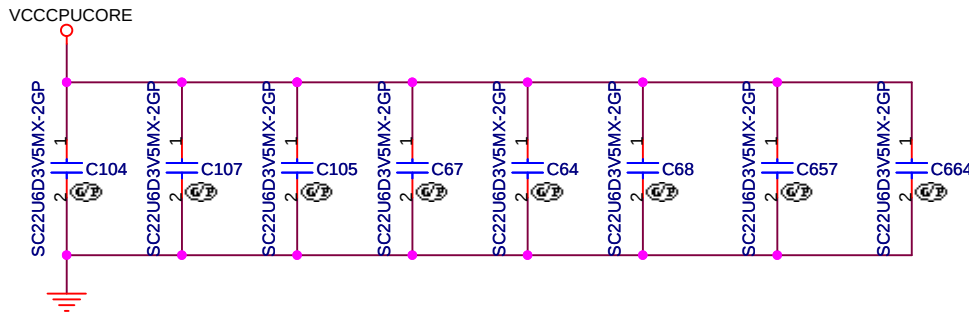
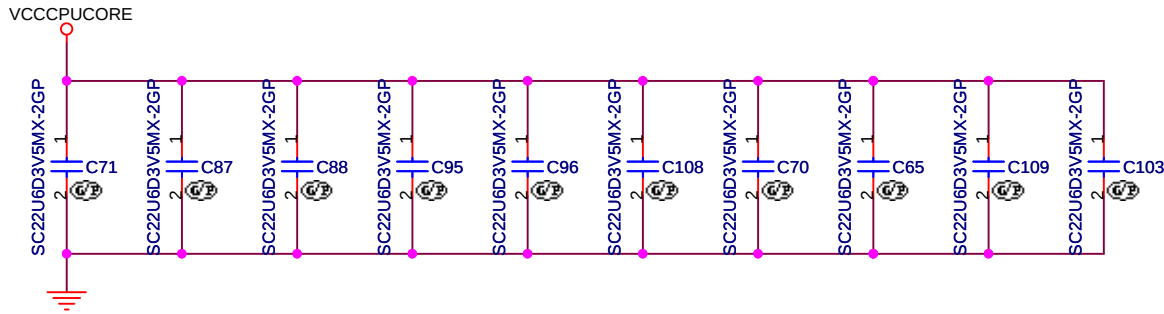
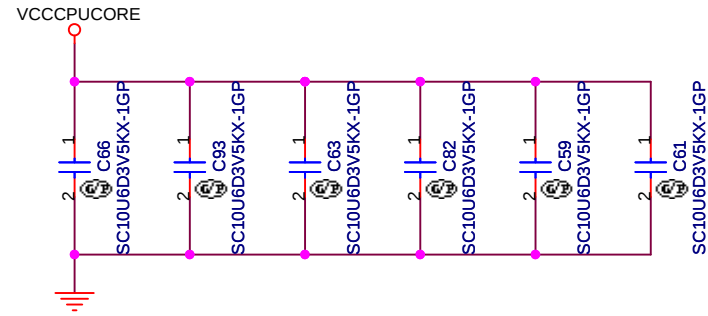
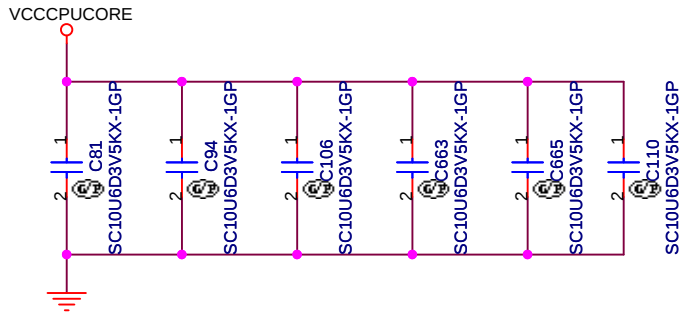
(*2) CPURST# SIGNAL IS BRANCHED AT GMCH's PIN

Ref Des	For ITP 700Flex	For ITP-XDP
J7	NO_ASM-->ASM	NO_ASM-->ASM
C118	NO_ASM-->ASM	NO_ASM-->ASM
R658	ASM (No Change)	ASM--> 51 5% ASM
R69	NO_ASM-->ASM	NO_ASM-->ASM
R77	NO_ASM-->ASM	NO_ASM-->1K 5% ASM
R63	ASM (No Change)	ASM (No Change)
R78	ASM (No Change)	ASM-->NO_ASM
R70	ASM (No Change)	ASM (No Change)
R64	ASM (No Change)	ASM-->51 5% ASM
R62	ASM (No Change)	ASM-->51 5% ASM

Rxxx(*) is the pull-up resistor on ITP_TDI

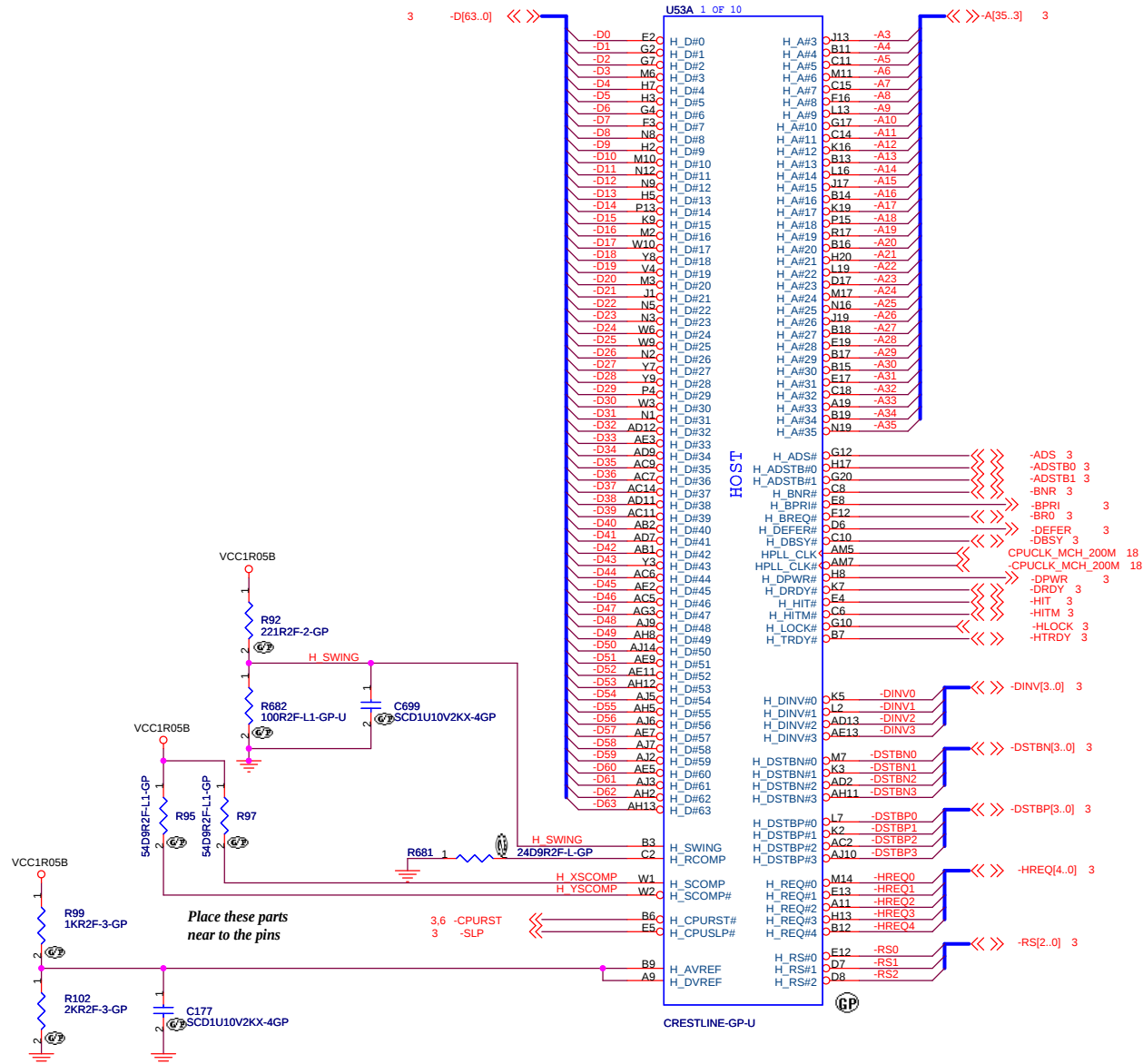
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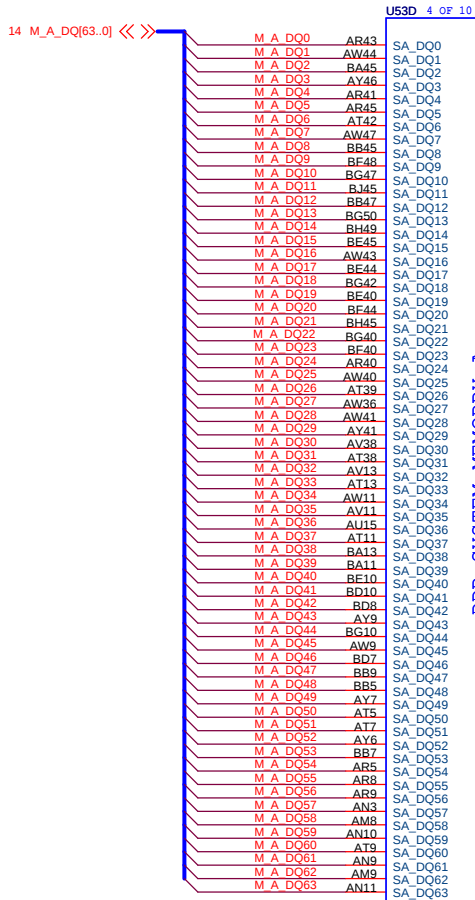
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Title			
ITP CONN			
Size	Document Number	Rev	
Custom	KS note-3	-1	
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<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
VCCCPUCORE DECOUPLING	
Size A4	Document Number KS note-3
Date: Tuesday, March 20, 2007	Sheet 7 of 78
Rev -1	





CRESTLINE-GP-U

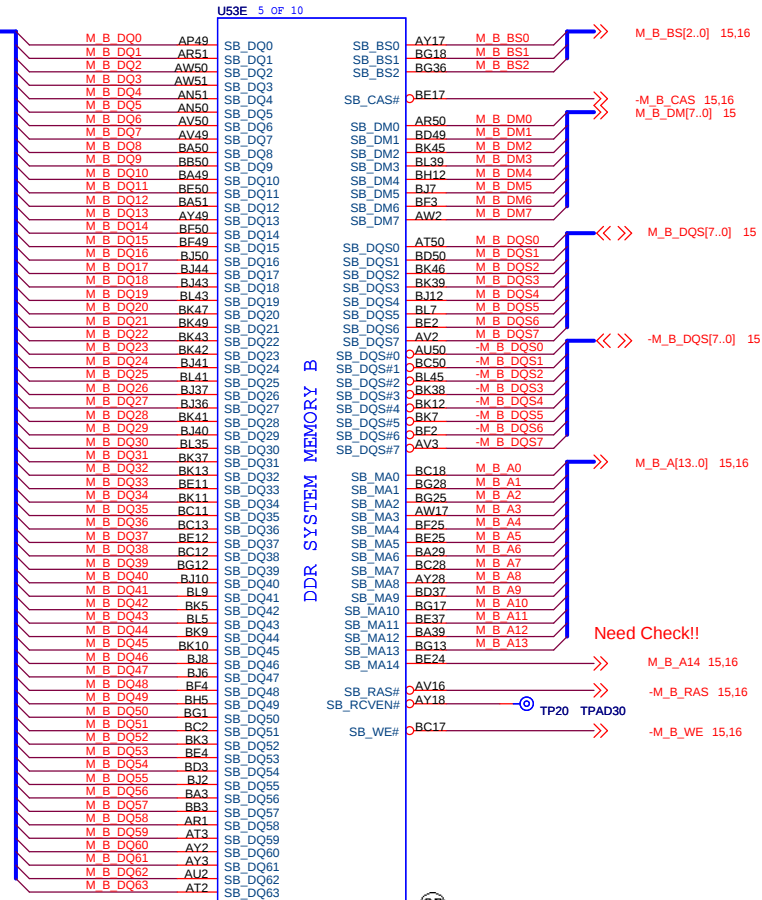
DDR SYSTEM MEMORY A

Need Check!!

M_A_A14 14,16

-M_A_RAS 14,16

-M_A_WE 14,16



CRESTLINE-GP-U

DDR SYSTEM MEMORY B

Need Check!!

M_B_A14 15,16

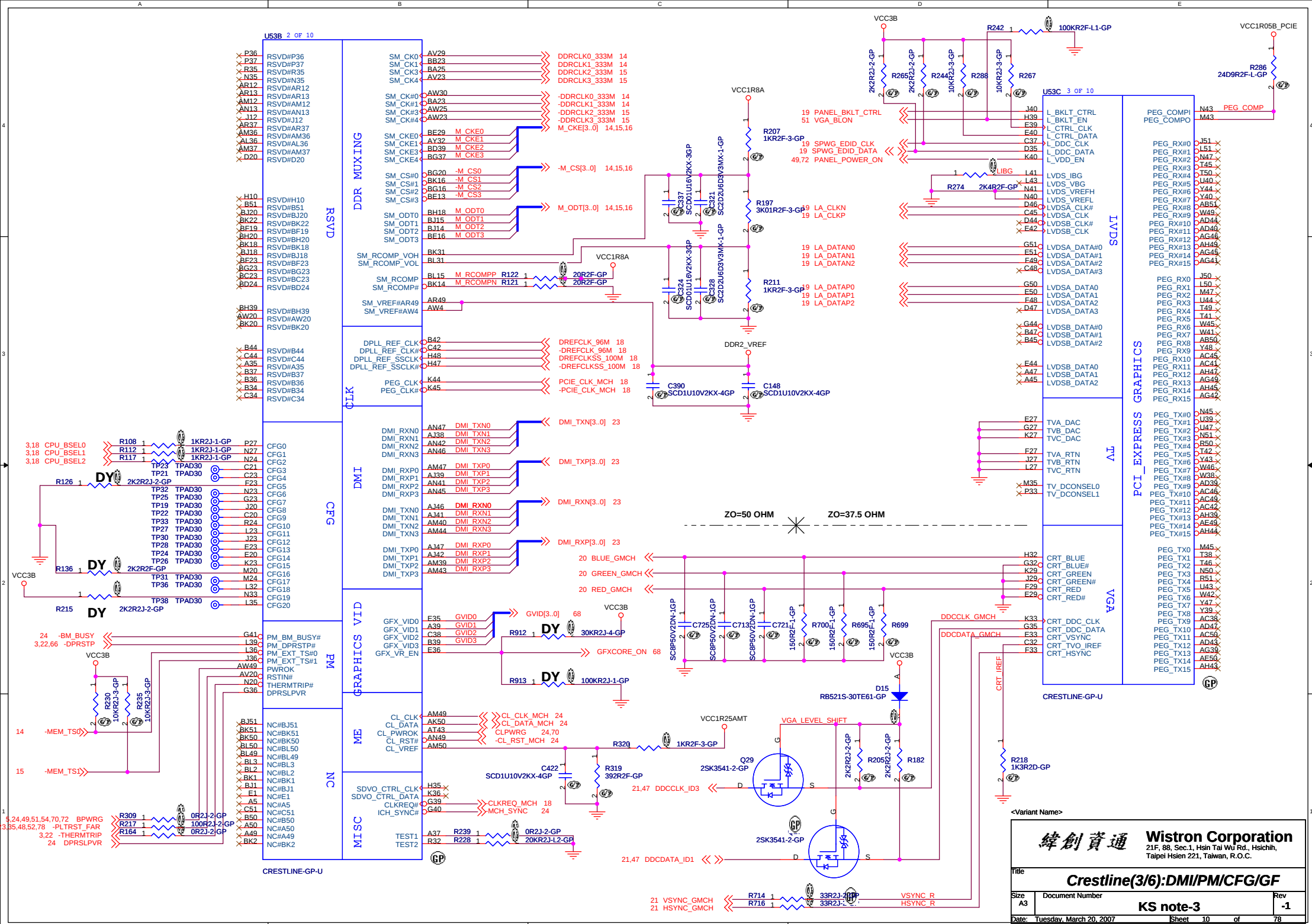
-M_B_RAS 15,16

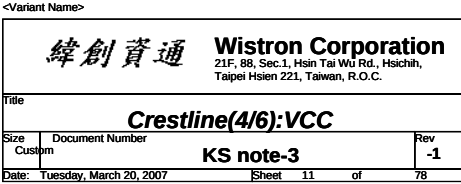
-M_B_WE 15,16

<Variant Name>

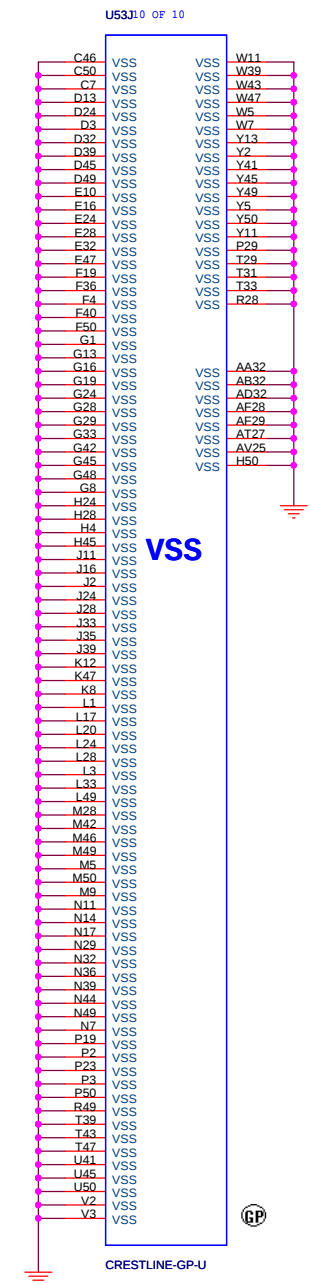
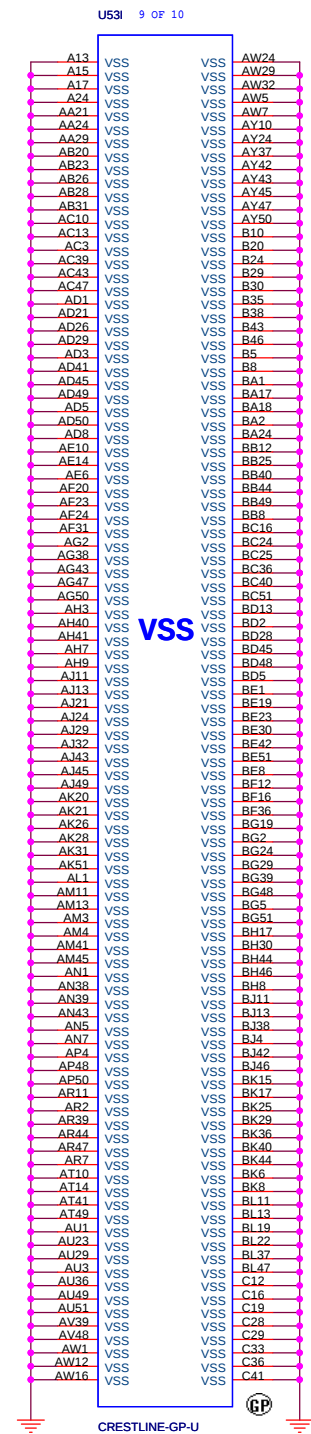
緯創資通 Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

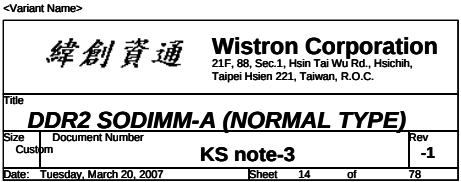
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Size	Document Number	Rev
A3	KS note-3	-1
Date:	Tuesday, March 20, 2007	Sheet 9 of 78

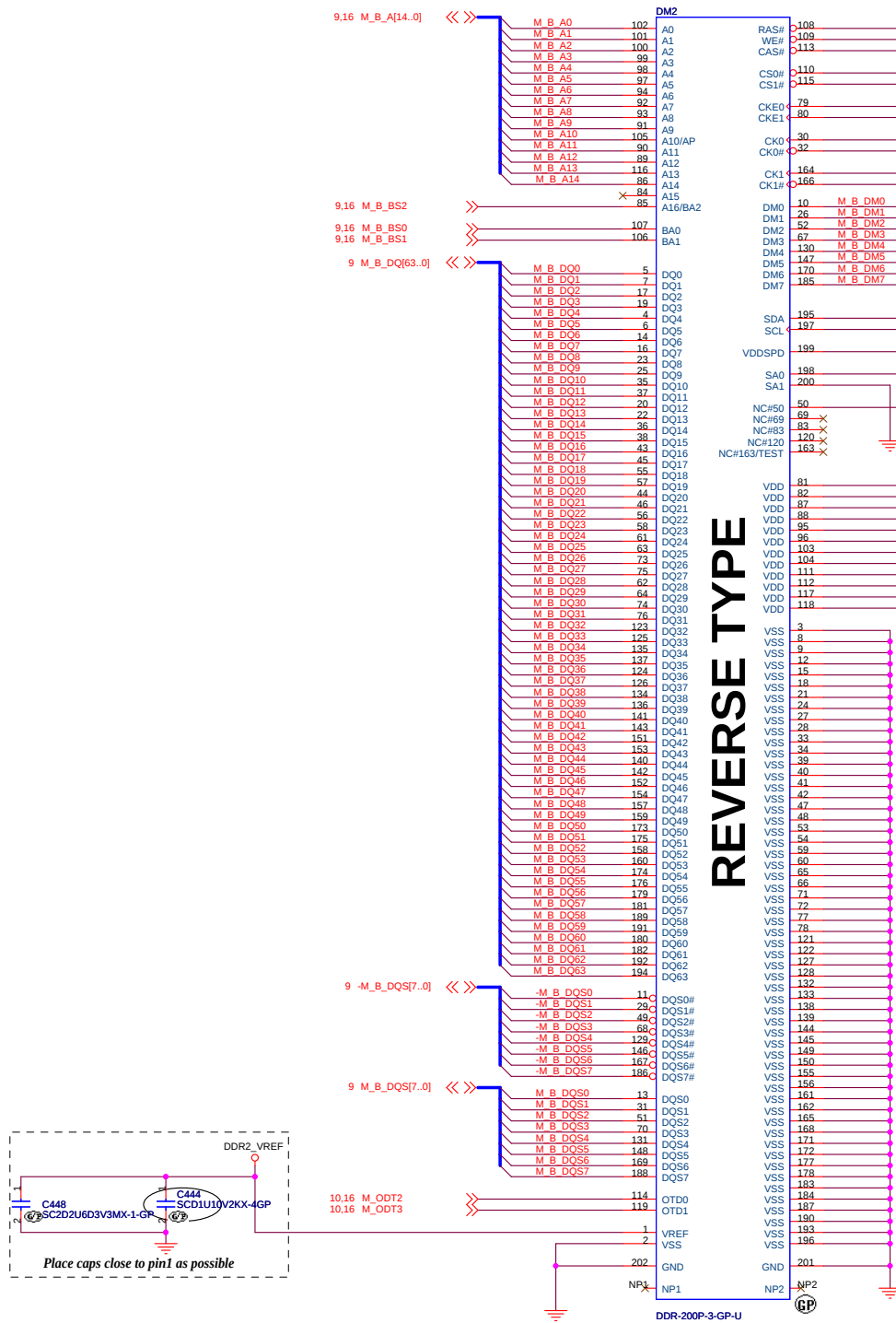










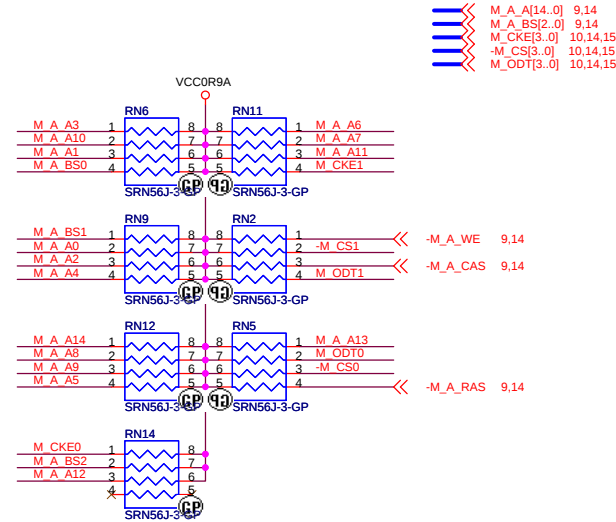


REVERSE TYPE

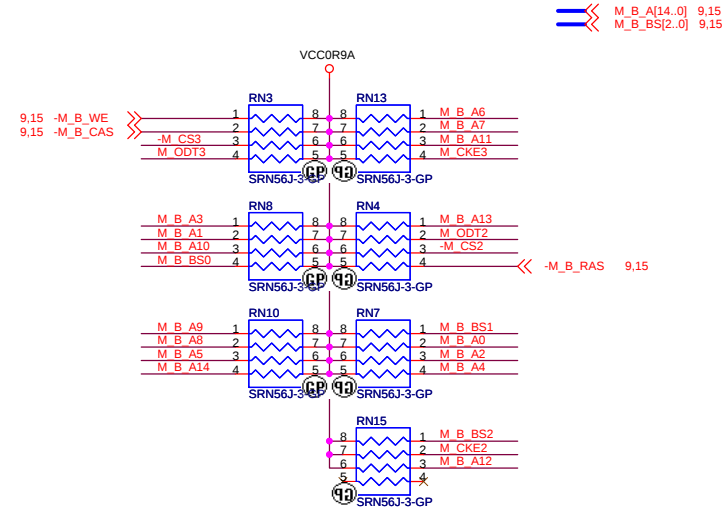
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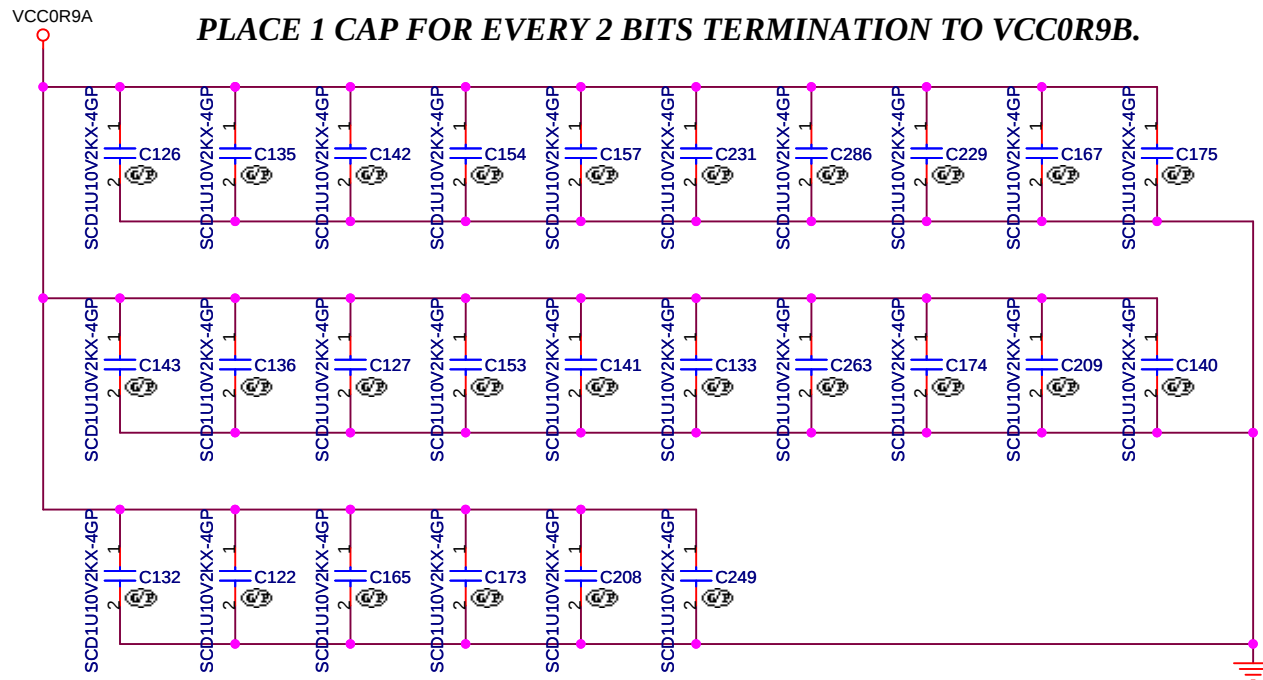
緯創資通 Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title DDR2 SODIMM-B (REVERSE TYPE)	
Size Custom	Document Number KS note-3
Date Tuesday, March 20, 2007	Rev -1
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CHANNEL A PARALLEL TERMINATION



CHANNEL B PARALLEL TERMINATION





<Variant Name>

緯創資通

Wistron Corporation

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Title

DDR-2 DECOUPLING

Size
A4

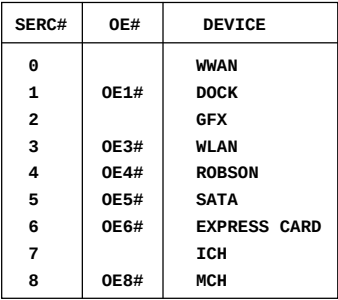
Document Number

KS note-3

Rev
-1

Date: Tuesday, March 20, 2007

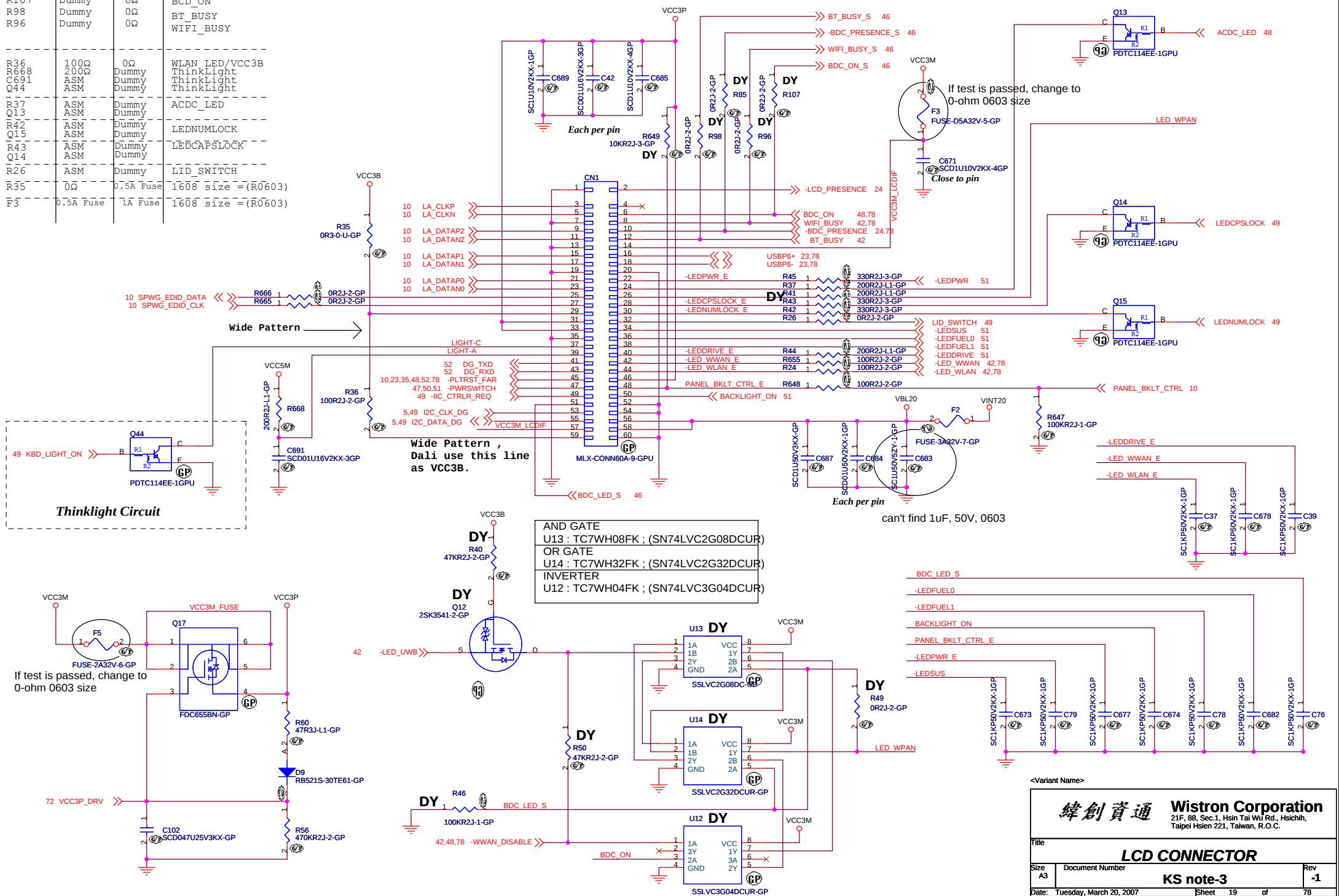
Sheet 17 of 78

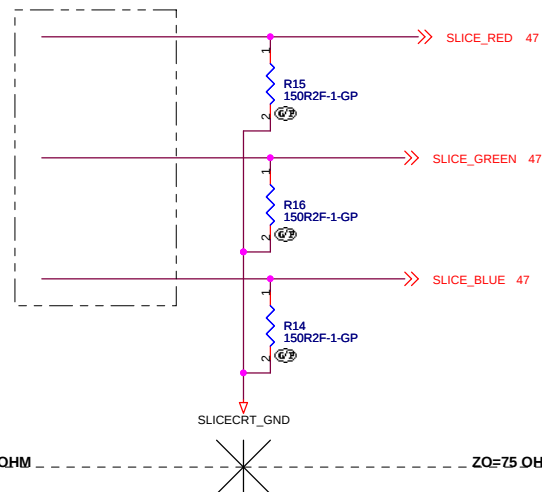
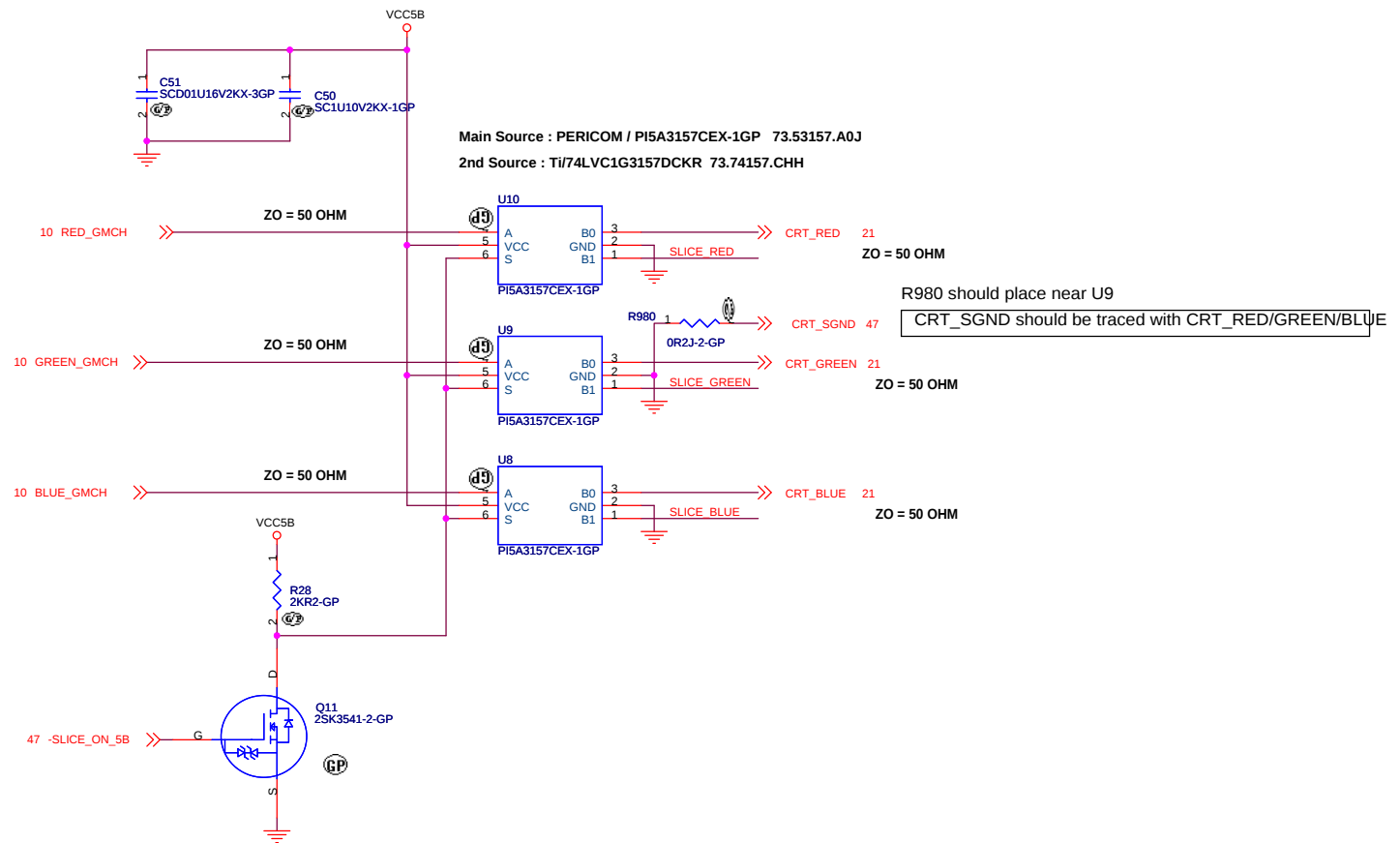


<Variant Name>			
		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
CLOCK GEN(CK505)			
Size A3	Document Number		Rev -1
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LCD / Inverter Connector

Place	Ks-3	Dali-2	Function
R85	Dummy	0Ω	-BDC_PRESENCE
R107	Dummy	0Ω	BCD_ON
R98	Dummy	0Ω	BT_BUSY
R96	Dummy	0Ω	WIFI_BUSY
R36	100Ω	0Ω	WLAN_LED/VCC3B
R668	200Ω	Dummy	ThinkLight
C691	ASM	Dummy	ThinkLight
Q44	ASM	Dummy	ThinkLight
R37	ASM	Dummy	ACDC_LED
Q13	ASM	Dummy	
R42	ASM	Dummy	
Q15	ASM	Dummy	LEDNUMLOCK
R43	ASM	Dummy	LEDCAPSLOCK
Q14	ASM	Dummy	
R26	ASM	Dummy	LID_SWITCH
R35	0Ω	0.5A Fuse	1608_size=(R0603)
F3	0.5A Fuse	1A Fuse	1608_size=(R0603)





<Variant Name>

緯創資通

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Title

CRT SELECTOR

Size
A3

Document Number

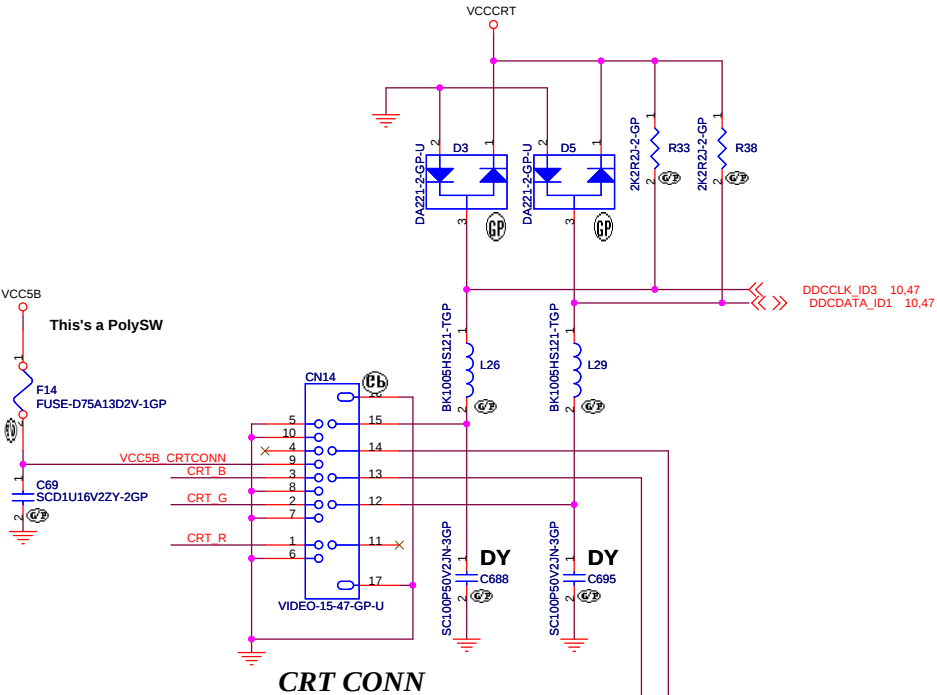
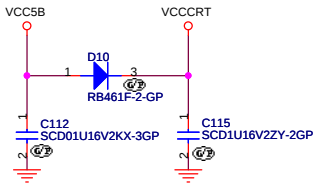
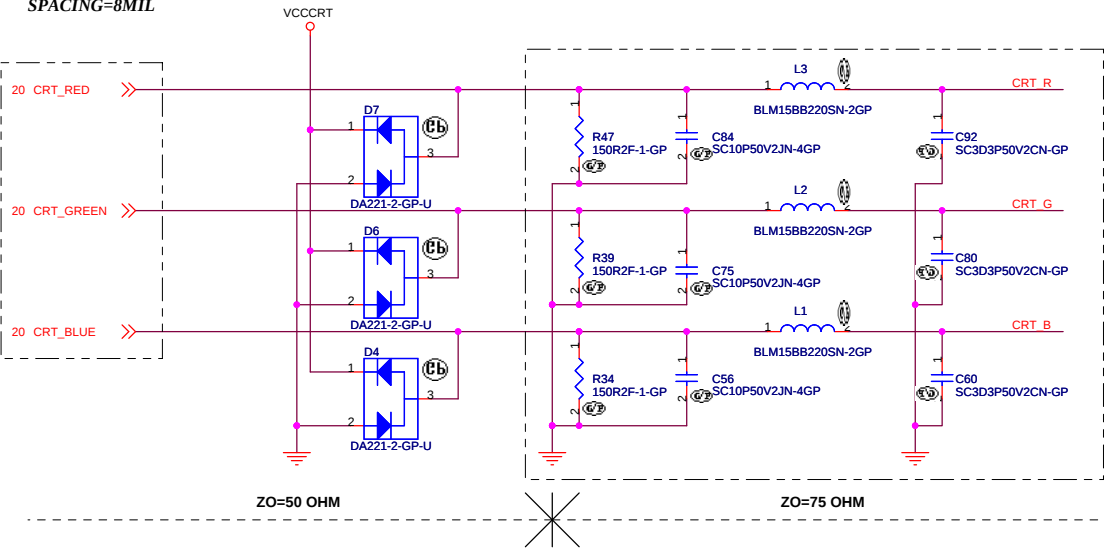
KS note-3

Rev
-1

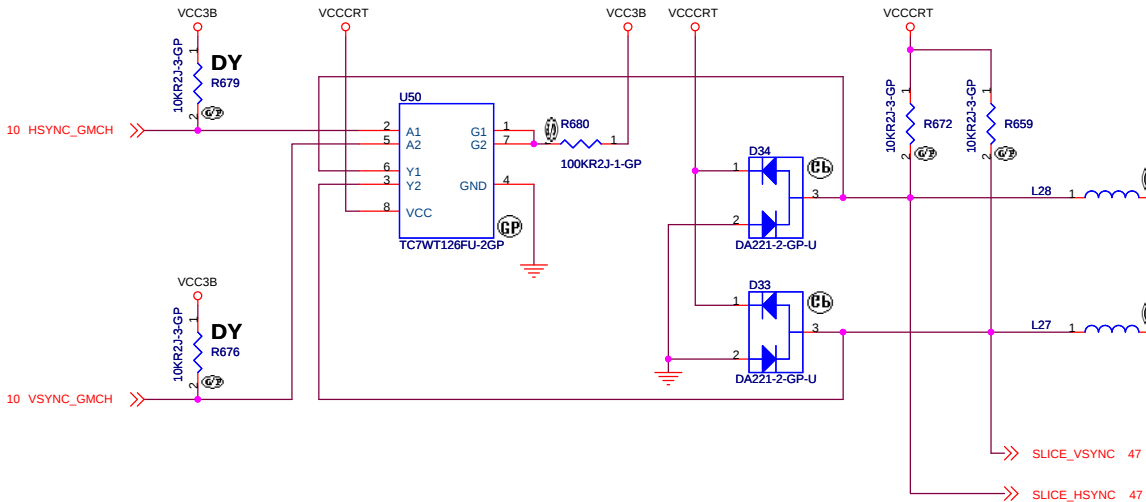
Date: Tuesday, March 20, 2007

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GND GUARDING
EACH SIGNAL WIDTH DEPENDS ON ZO(TRACE IMPEDANCE)
SPACING=8MIL

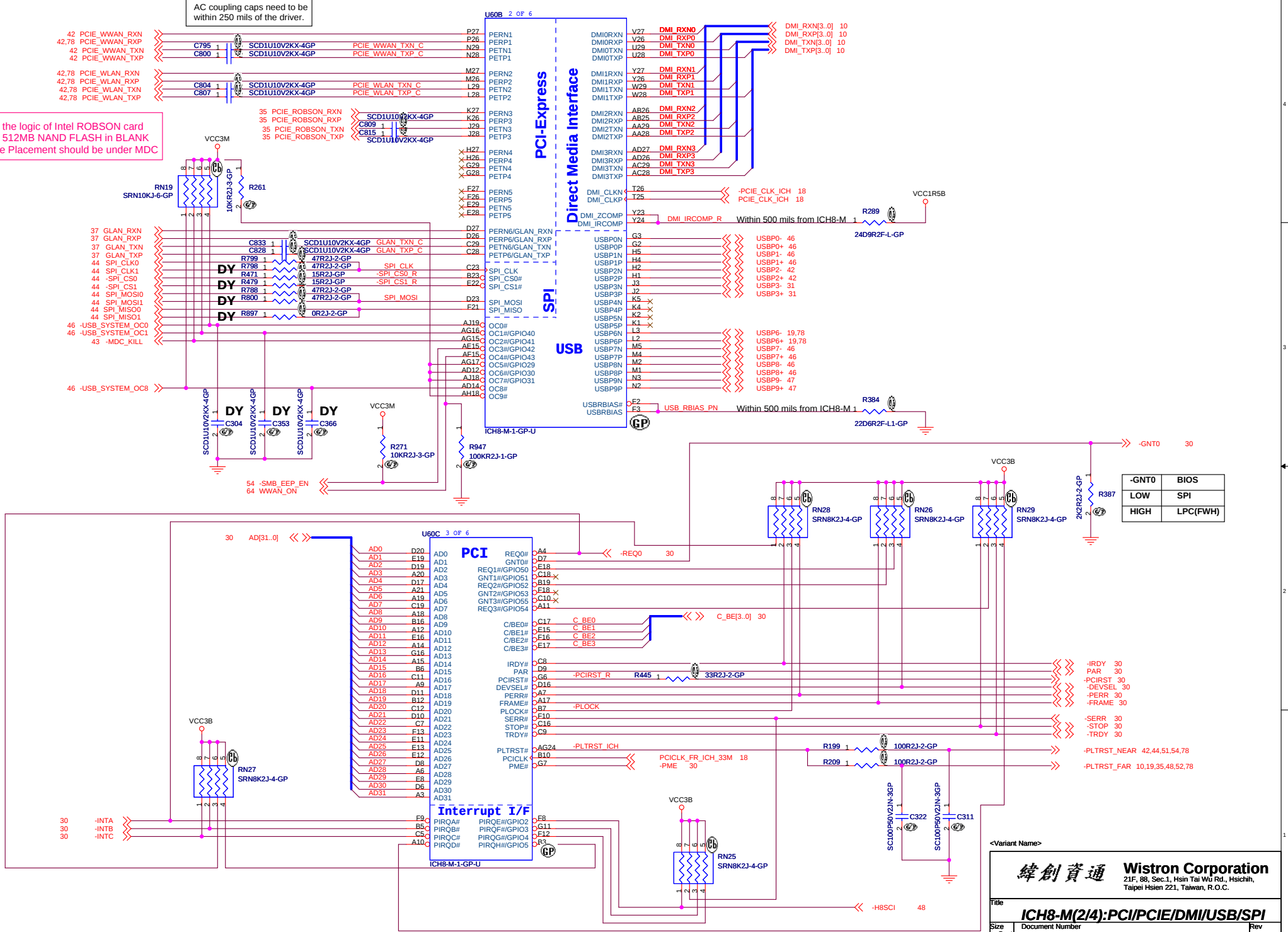


CRT CONN



AC coupling caps need to be within 250 mils of the driver.

Add the logic of Intel ROBSON card with 512MB NAND FLASH in BLANK Page Placement should be under MDC

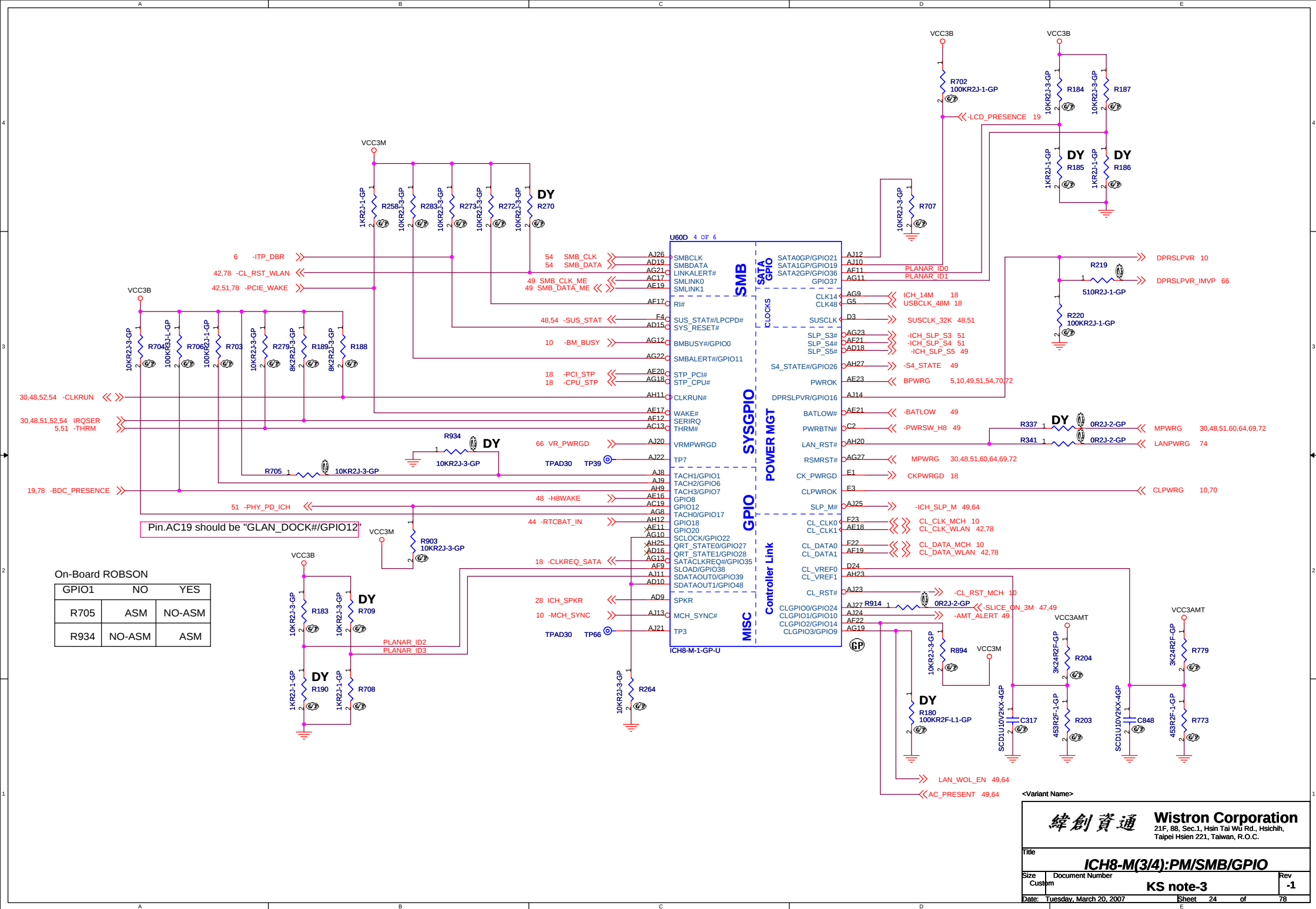


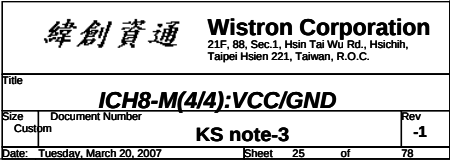
-GNT0	BIOS
LOW	SPI
HIGH	LPC(FWH)

<Variant Name>

緯創資通 Wistron Corporation
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Title	ICH8-M(2/4):PCI/PCIE/DMI/USB/SPI	
Size	Document Number	Rev
Custom	KS note-3	-1
Date:	Tuesday, March 20, 2007	Sheet 23 of 78



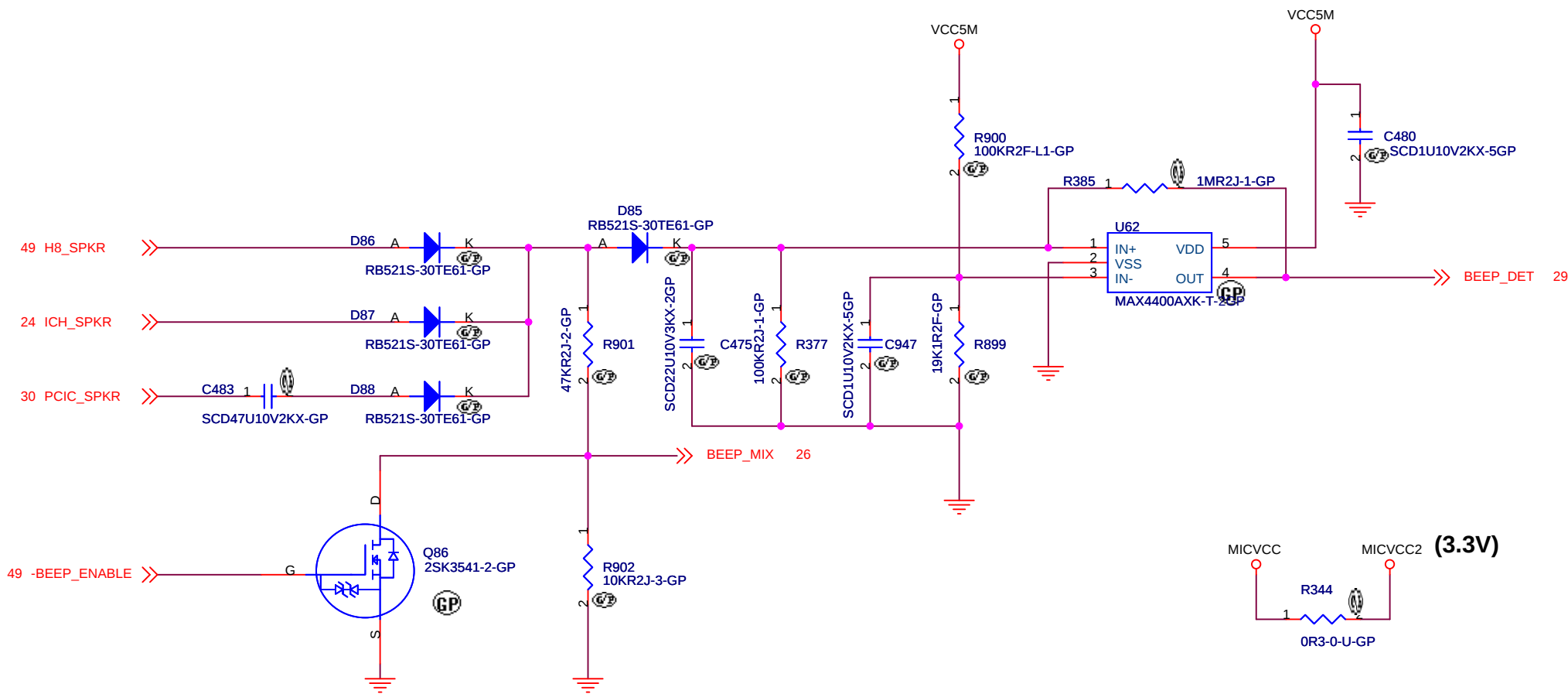


HEADPHONE

EXT MIC

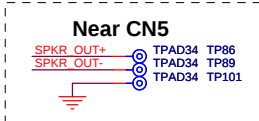
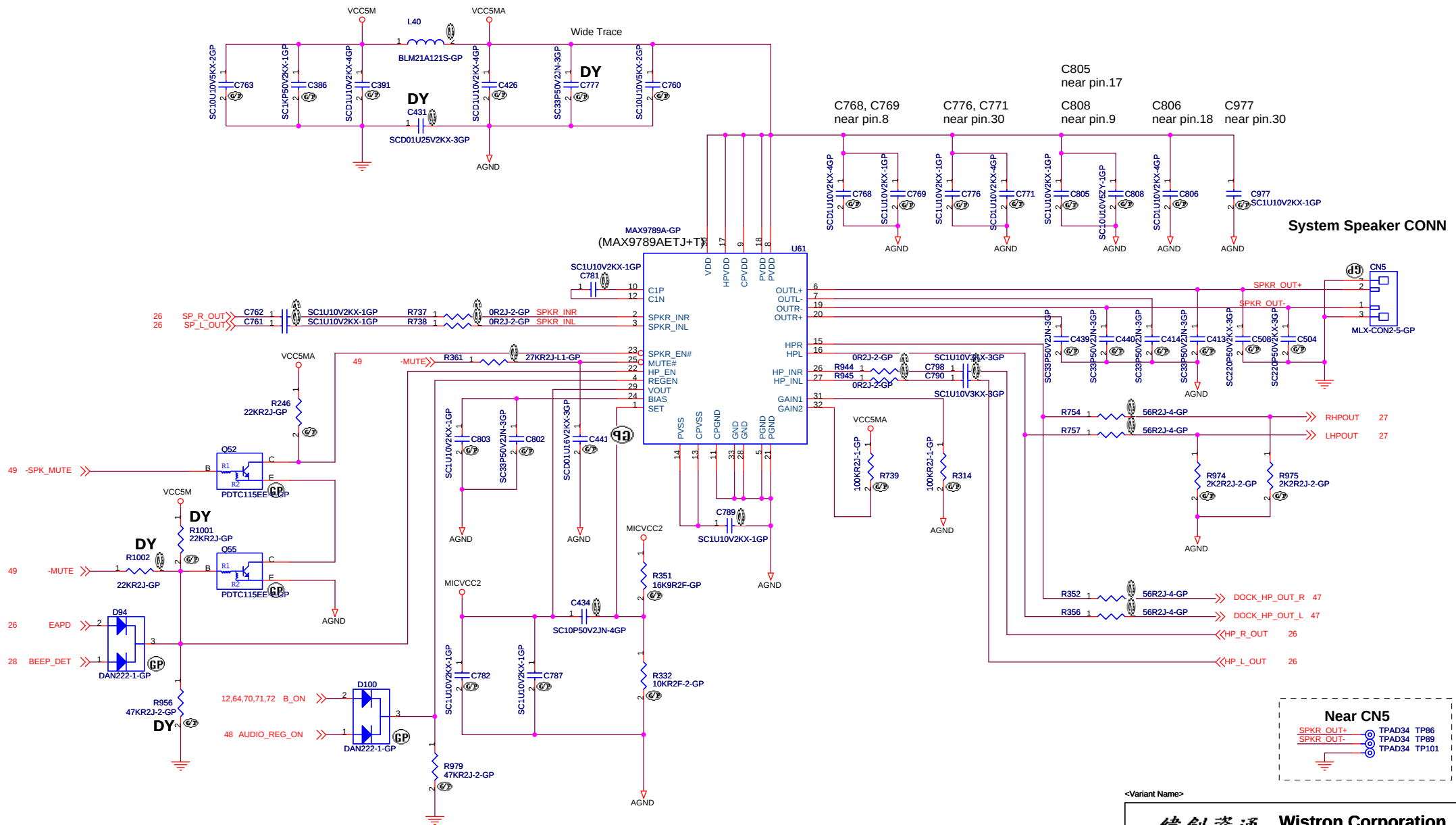
ALL PARTS MUST BE
PLACED NEAR TO
INTERNAL MICROPHONE

Place	KS-3	Dali-2
R819	ASM	Dummy
R804	ASM	Dummy
C872	ASM	Dummy
R813	ASM	Dummy
C877	ASM	Dummy
CN7	ASM	Dummy
CN6	Dummy	ASM
R567	Dummy	ASM
C578	Dummy	ASM

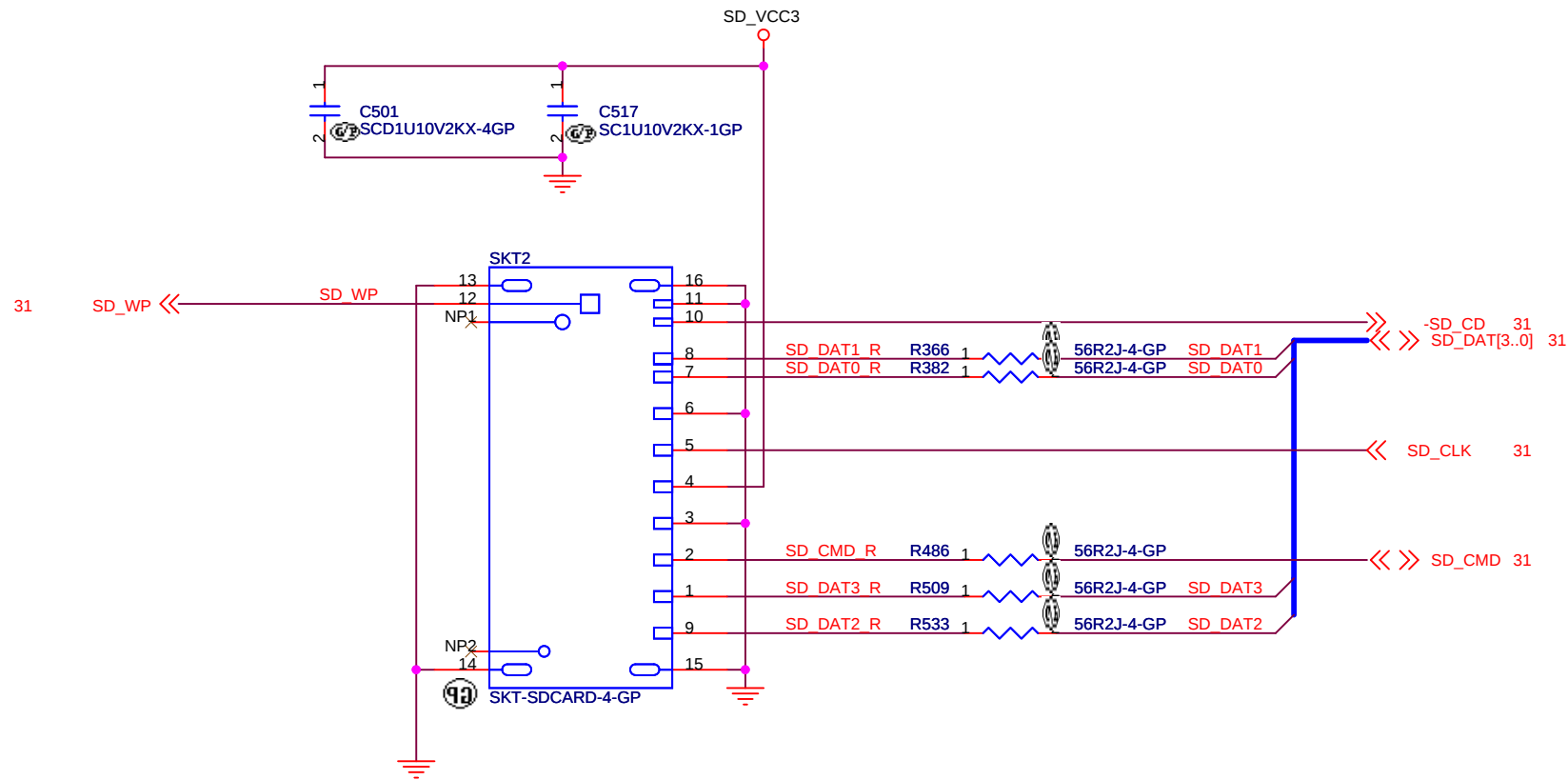


<Variant Name>

緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		Title	
Size A4		Document Number	
Date: Tuesday, March 20, 2007		Sheet 28 of 78	
KS note-3		Rev -1	



SD Slot



<Variant Name>

緯創資通

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Taipei Hsien 221, Taiwan, R.O.C.

[illegible]

SD SLOT

Size
A4

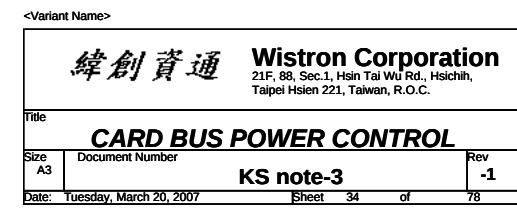
Document Number

KS note-3

Rev	-1
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Date: Tuesday, March 20, 2007

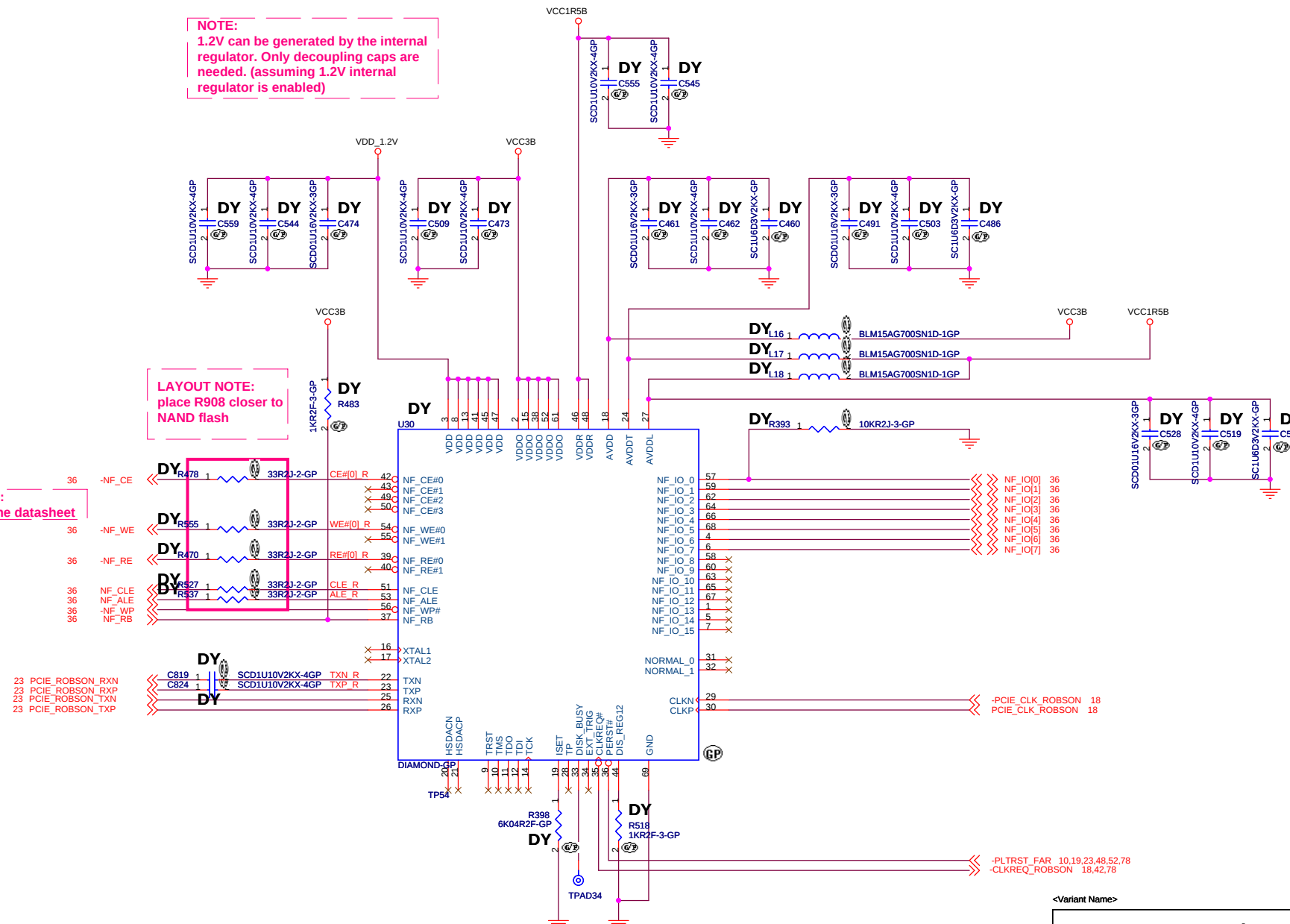
Sheet 33 of 78

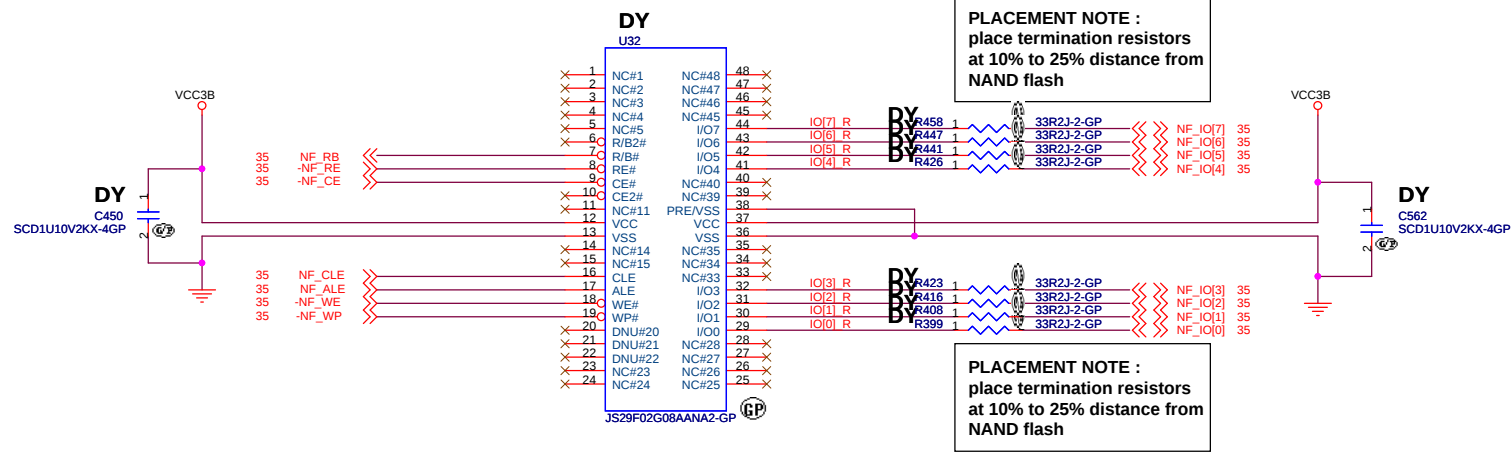


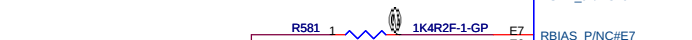
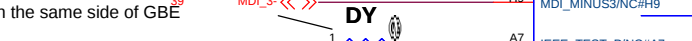
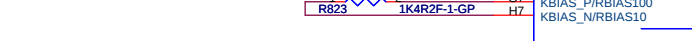
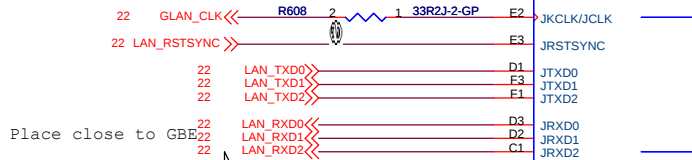
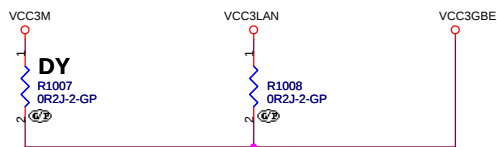
NOTE:
1.2V can be generated by the internal
regulator. Only decoupling caps
are needed. (assuming 1.2V internal
regulator is enabled)

LAYOUT NOTE:
place R908 closer to
NAND flash

LAYOUT NOTE:
please check the datasheet







LAN Nineveh

IC1

IC2

IC3

IC4

IC5

IC6

IC7

IC8

IC9

IC10

IC11

IC12

IC13

IC14

IC15

IC16

IC17

IC18

IC19

IC20

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IC213

IC214

IC215

IC216

IC217

IC218

IC219

IC220

IC221

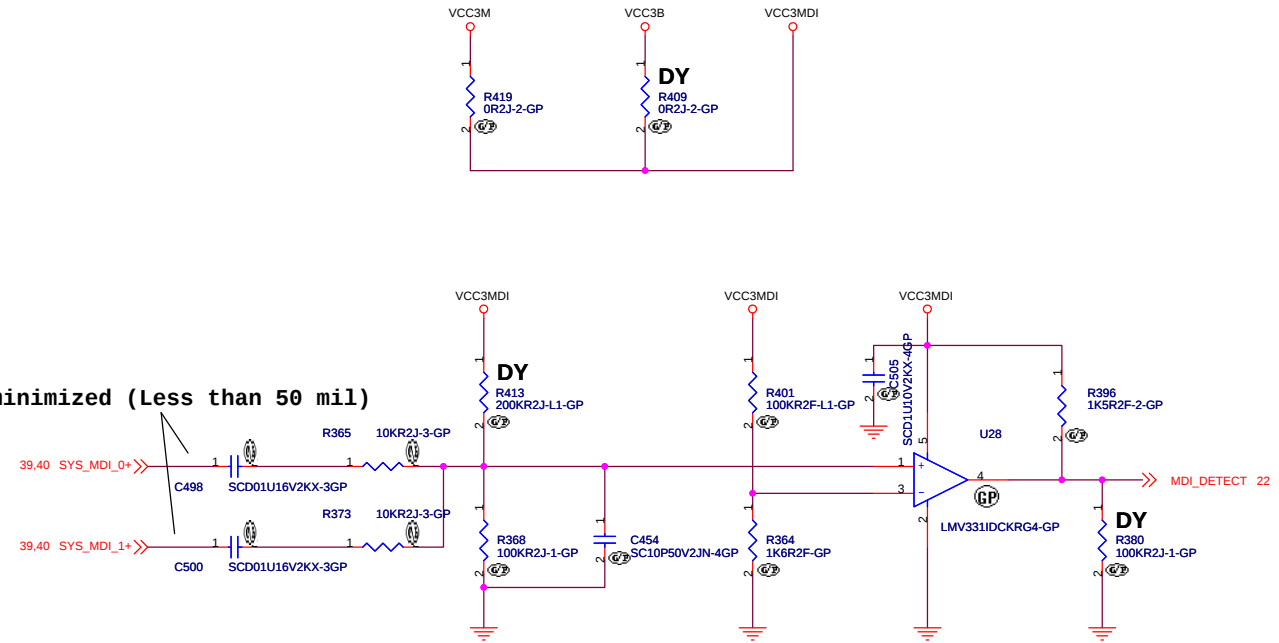
IC222

IC223

IC224

IC225

STUB should be minimized (Less than 50 mil)

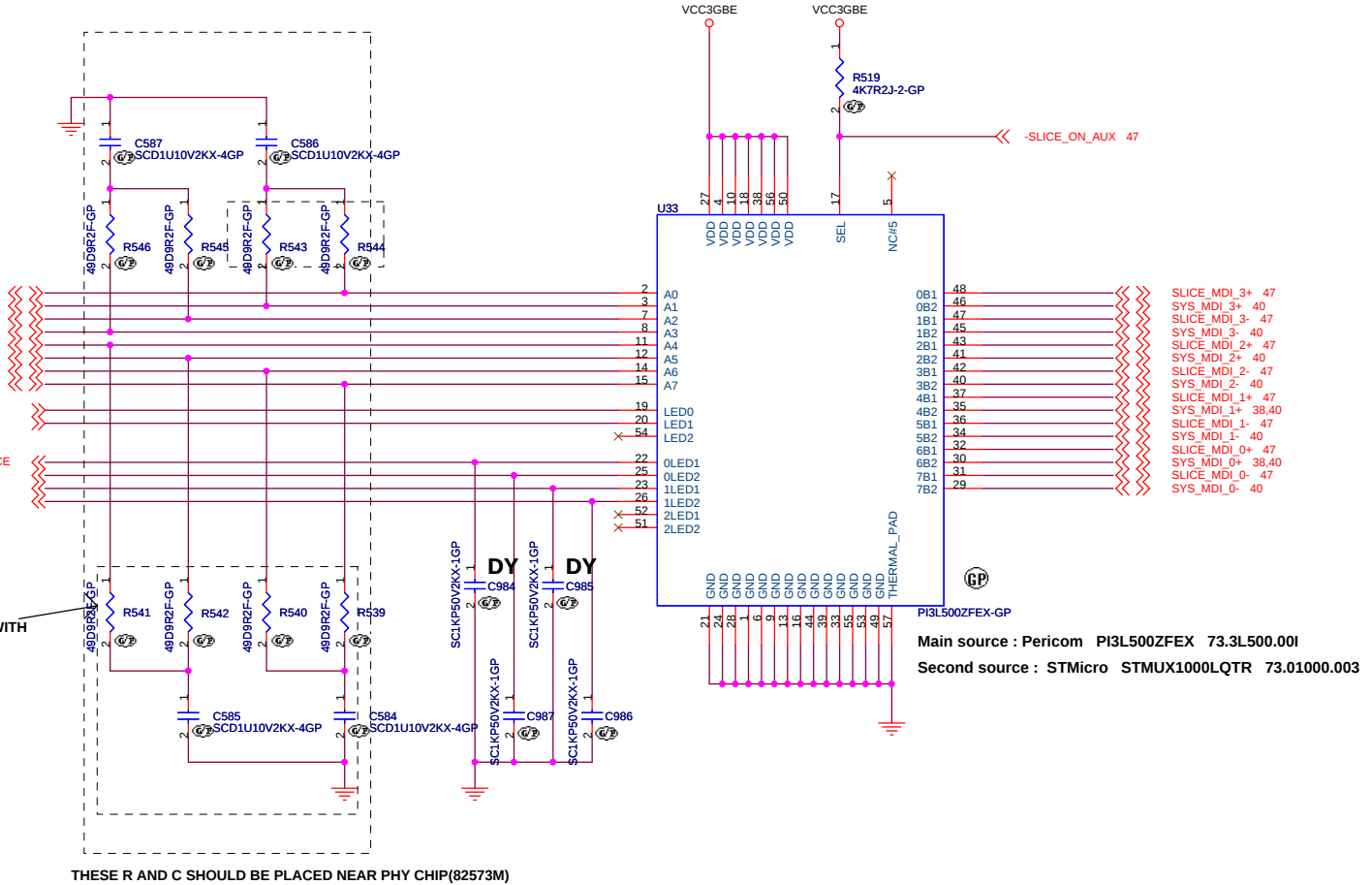


37 MDI_3+
37 MDI_3-
37 MDI_2+
37 MDI_2-
37 MDI_1+
37 MDI_1-
37 MDI_0+
37 MDI_0-

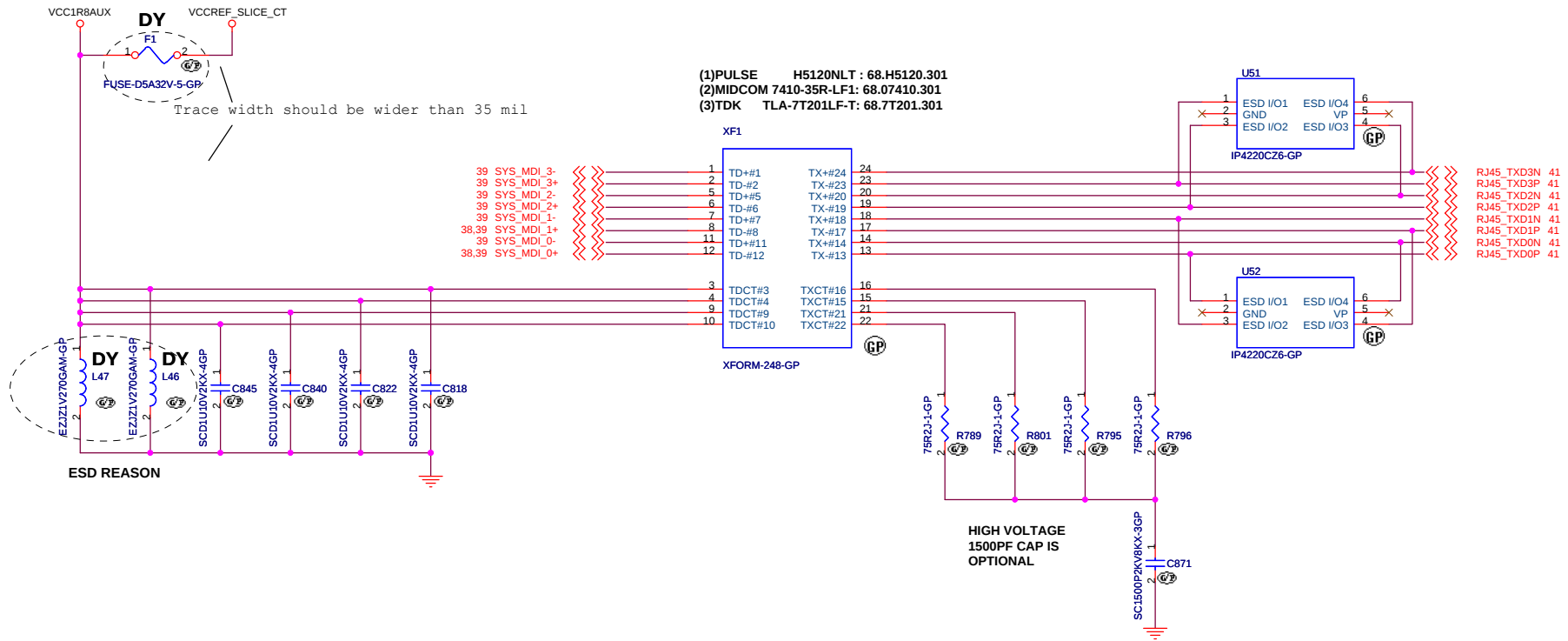
37 -RJ45_ACTIVITY
37 -RJ45_LINKUP

47 -RJ45_ACTIVITY_SLICE
41,78 -RJ45_ACTIVITY_SYS
47 -RJ45_LINKUP_SLICE
41,78 -RJ45_LINKUP_SYS

INSTALL 1% 49 OHM RESISTORS WITH
INTEL TEKOA 82573M



This should be placed near LAN Magnetics, not Slice conn.



<Variant Name>



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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

RJ11/RJ45 CONN

Size

ze

Document Number

KS note-3

Rev

—

Date: Tuesday, March 20, 2007

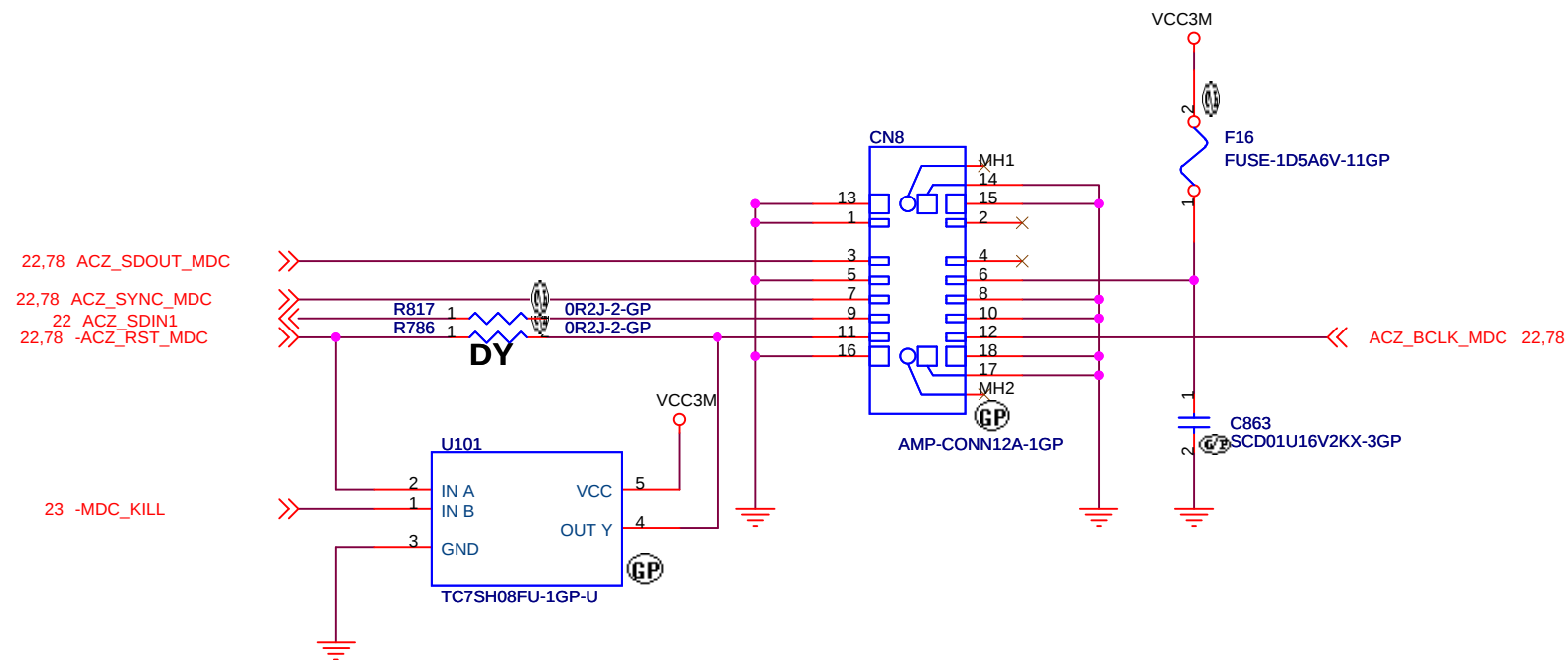
Sheet 41

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of

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MDC CONN

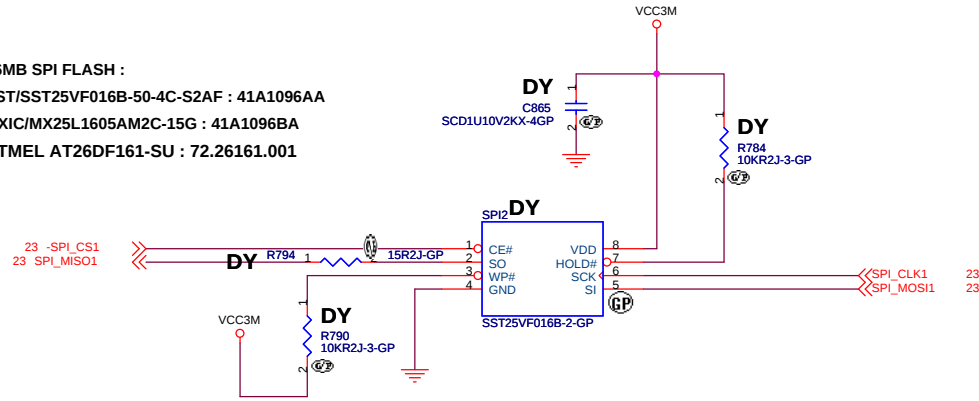


<Variant Name>

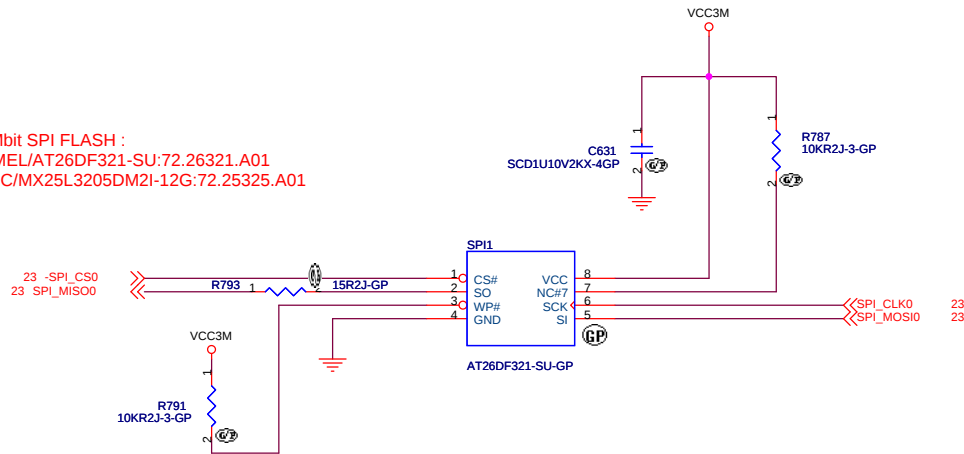
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title		
MDC 1.5 CONNECTOR		
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Golden Finger for Debug Board

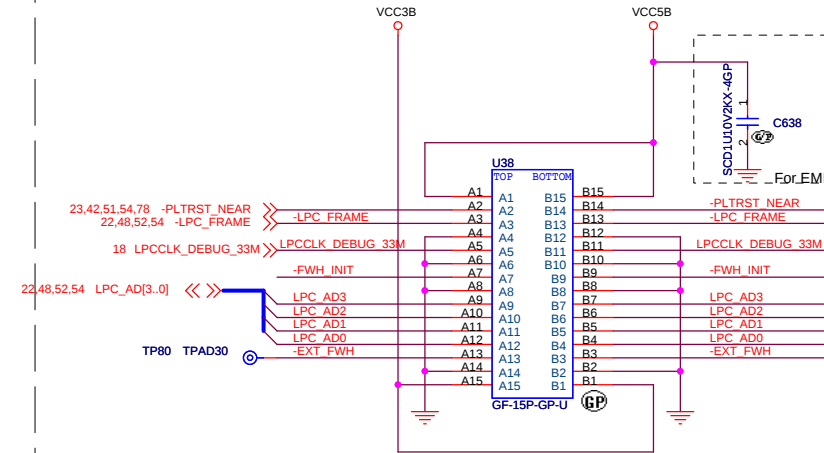
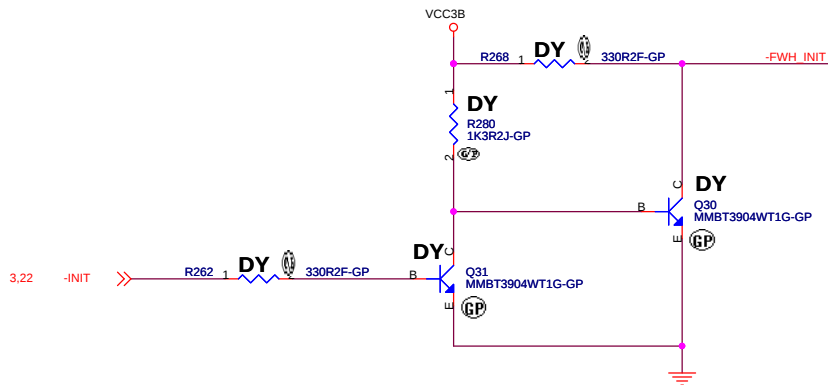
16MB SPI FLASH :
SST/SST25VF016B-50-4C-S2AF : 41A1096AA
MXIC/MX25L1605AM2C-15G : 41A1096BA
ATMEL AT26DF161-SU : 72.26161.001



32Mbit SPI FLASH :
ATMEL/AT26DF321-SU:72.26321.A01
MXIC/MX25L3205DM2I-12G:72.25325.A01



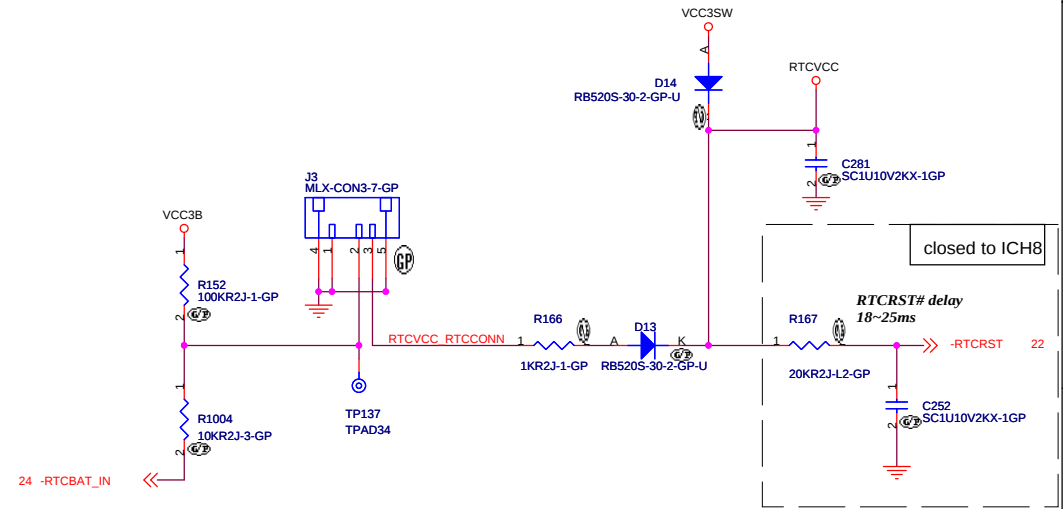
All Dummy in SVT stage



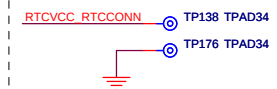
TOP VIEW

A15	(B1)
A14	(B2)
...	...
A2	(B14)
A1	(B15)

(BOTTOM VIEW)



Near J3

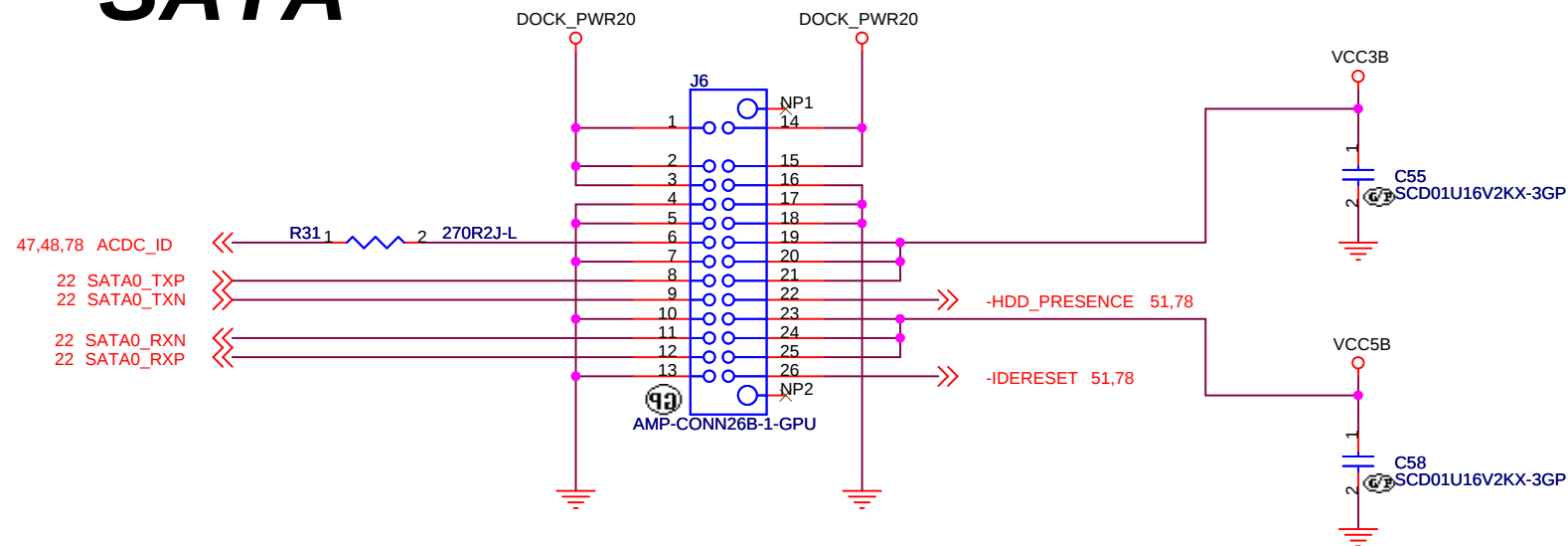


<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title		
RTC BATTERY & SPI		
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SATA



<Core Design>

緯創資通

Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

Title

Board To Board CONN

Size
A4

Document Number

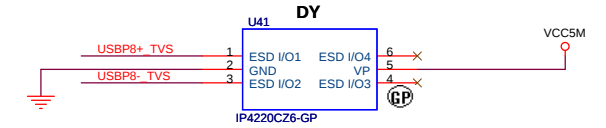
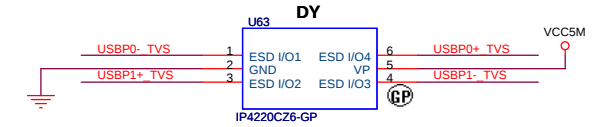
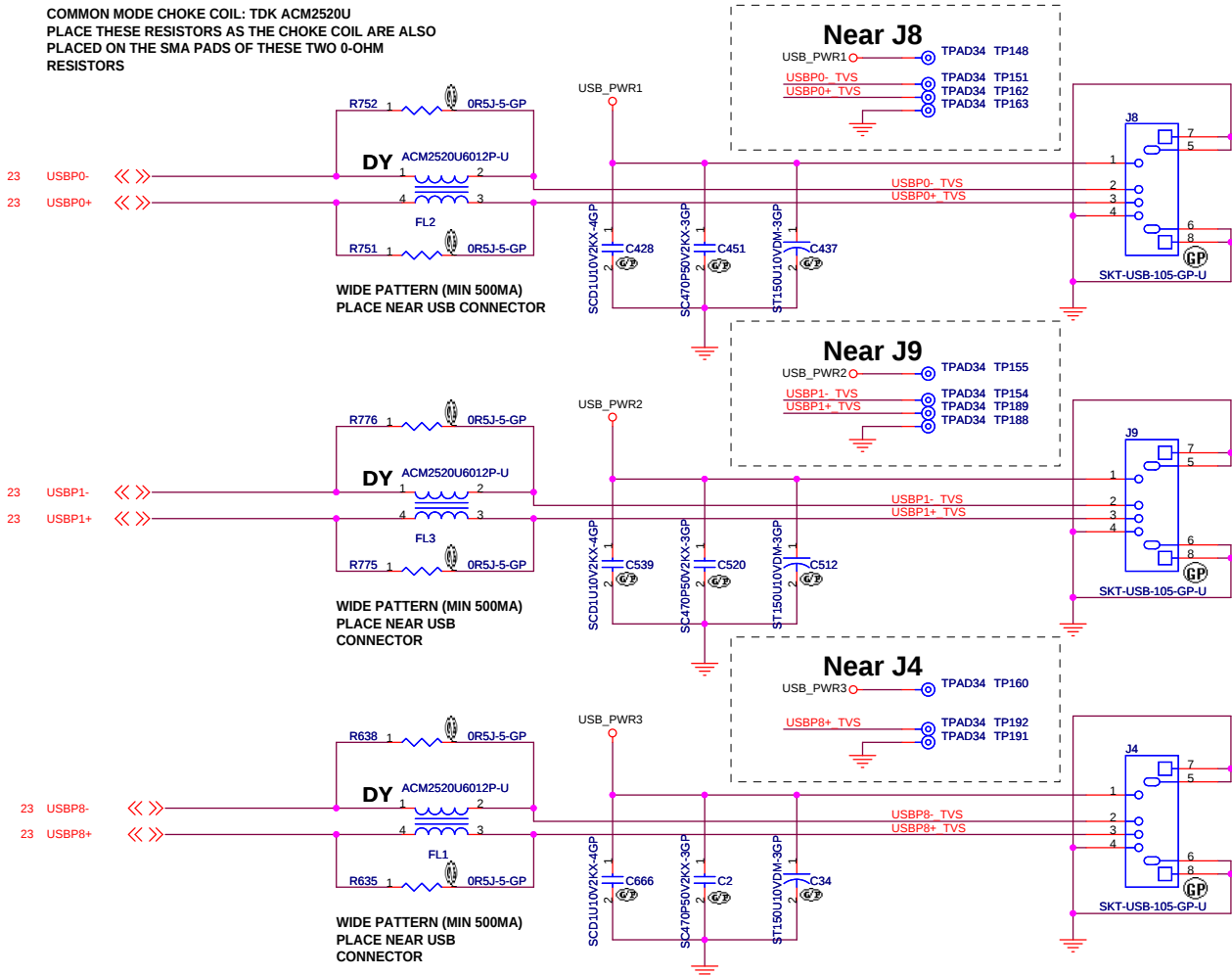
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Rev
-1

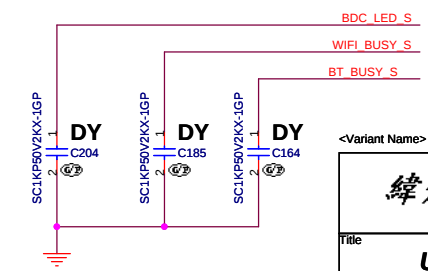
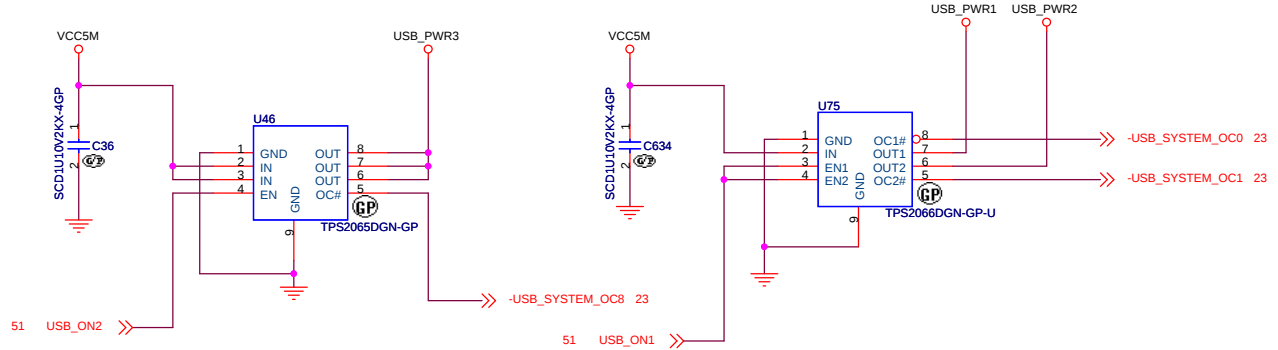
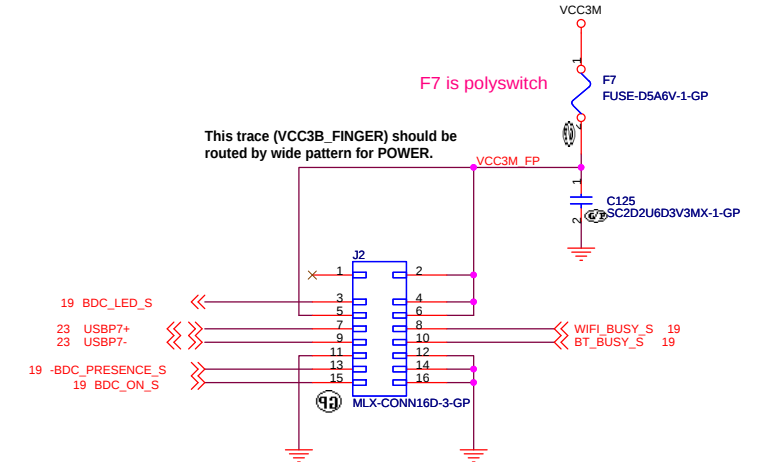
Date: Tuesday, March 20, 2007

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COMMON MODE CHOKE COIL: TDK ACM2520U
PLACE THESE RESISTORS AS THE CHOKE COIL ARE ALSO
PLACED ON THE SMA PADS OF THESE TWO 0-OHM
RESISTORS



Fingerprint Reader for KS-3 BDC for Dali-2



<Variant Name>

緯創資通 Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

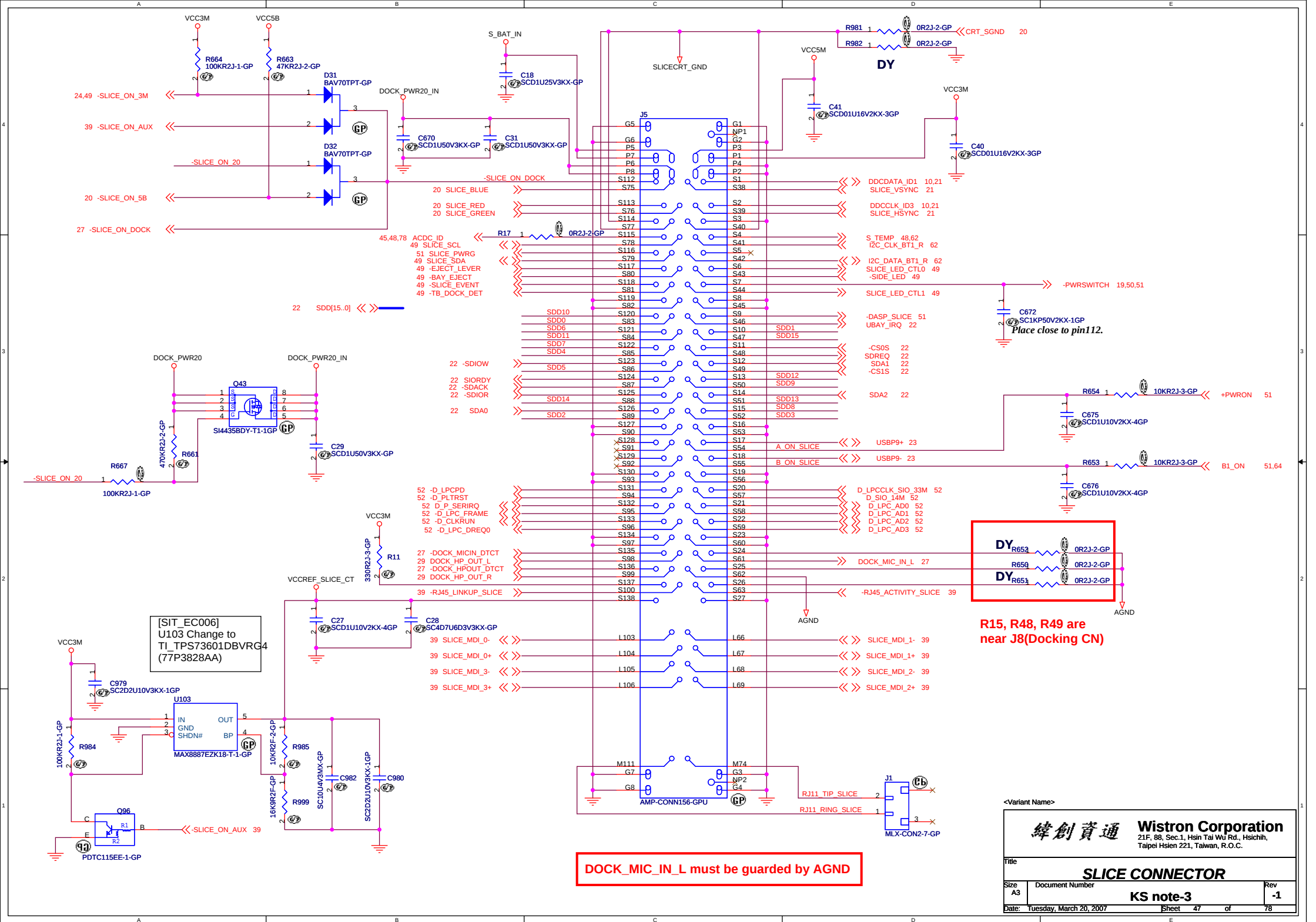
USB POWER / CONNECTOR

Size A3 Document Number

KS note-3

Rev -1

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1. NEAR H8S/2116:
2. SHORT PATTERN
3. NO PATTERN
UNDER THIS AREA

E10A Debug I/F	Enable	Disable
R301	NO ASM	ASM
R315	NO ASM	ASM
D18	ASM	NO ASM
D19	ASM	NO ASM
D20	ASM	NO ASM
R302	ASM	NO ASM
R304	ASM	NO ASM
R331	ASM	NO ASM
R343	ASM	NO ASM

<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsieh, Taipei Hsien 221, Taiwan, R.O.C.

Title

H8S2116 (1/2)

Size

Document Number

KS note-3

Rev

-1

Date:

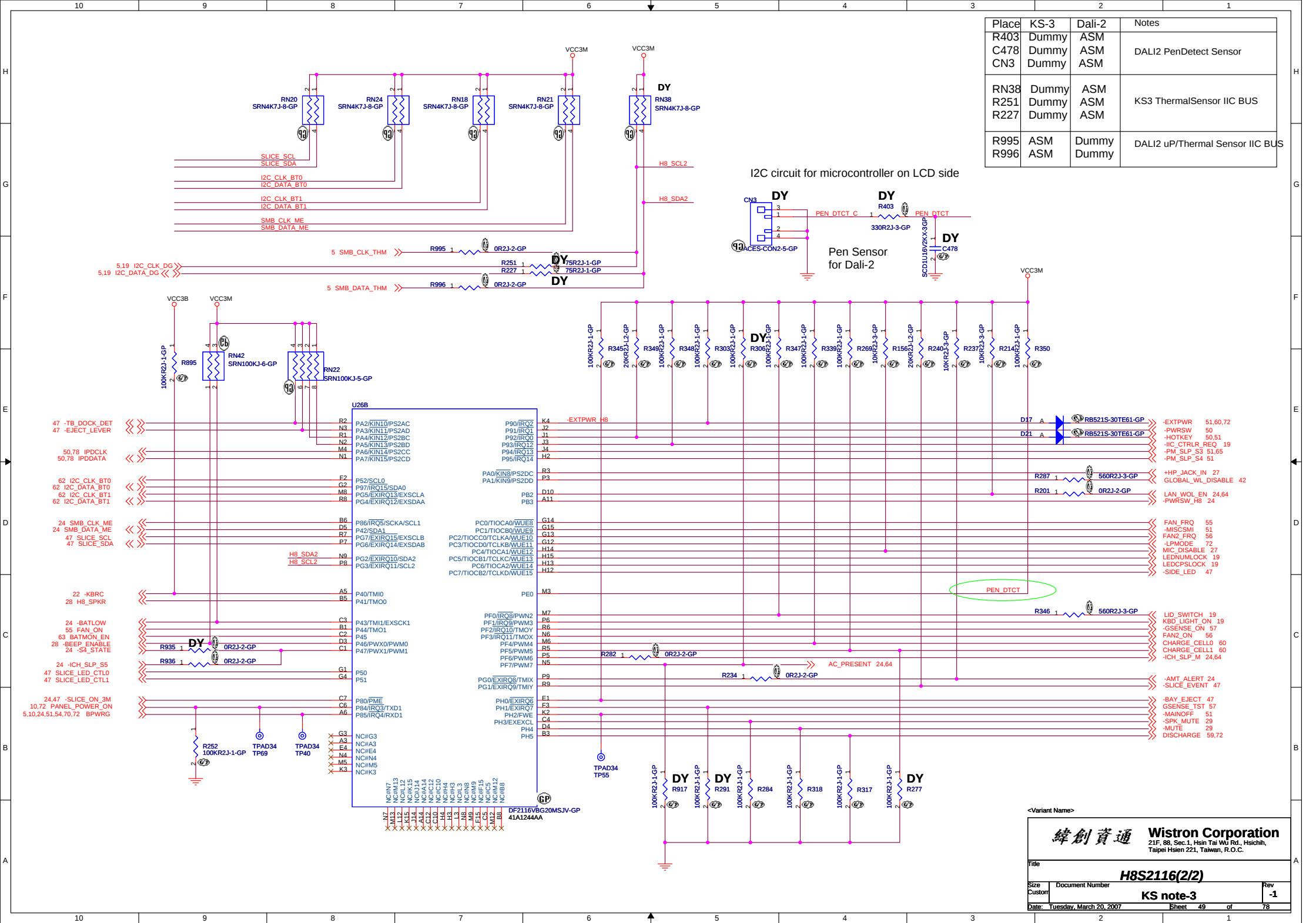
Tuesday, March 20, 2007

Sheet

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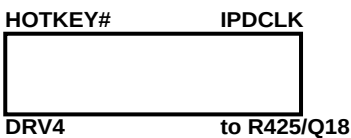
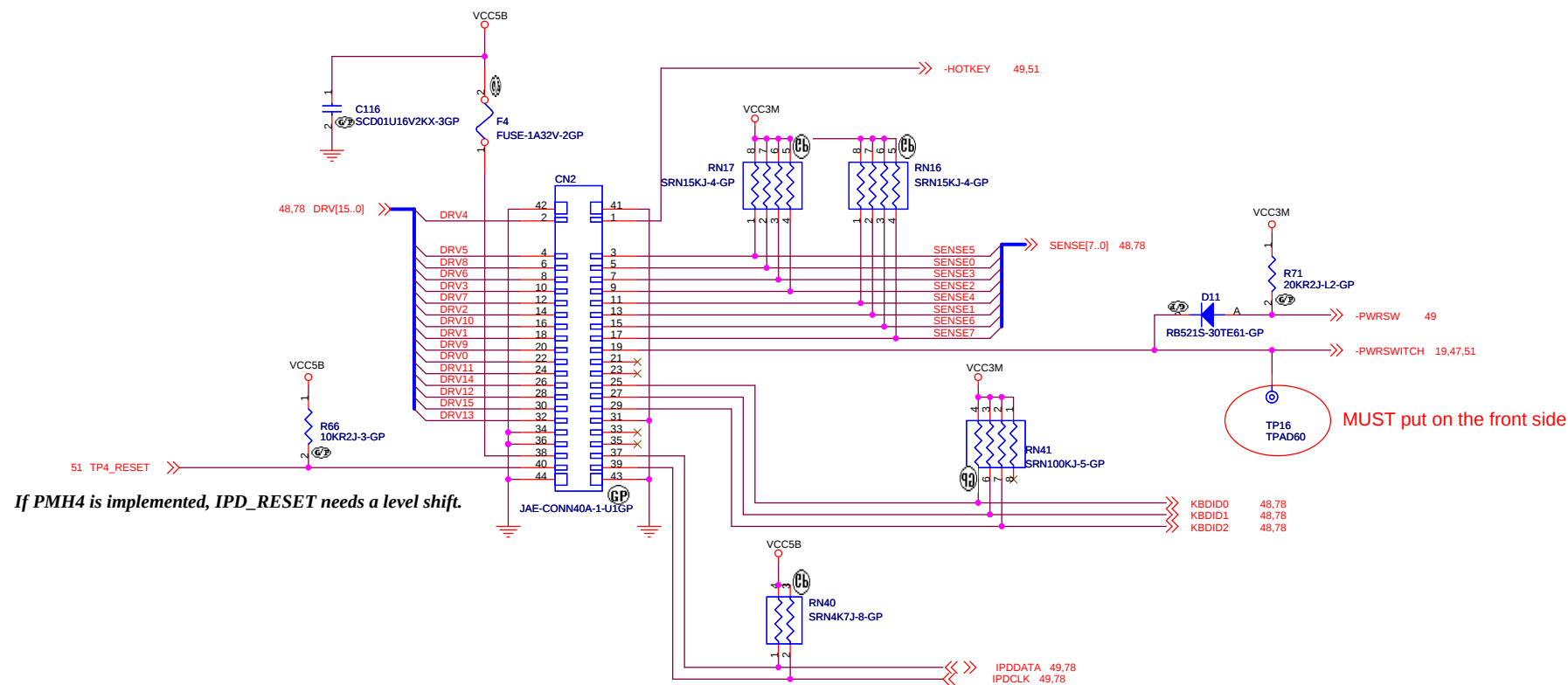
of

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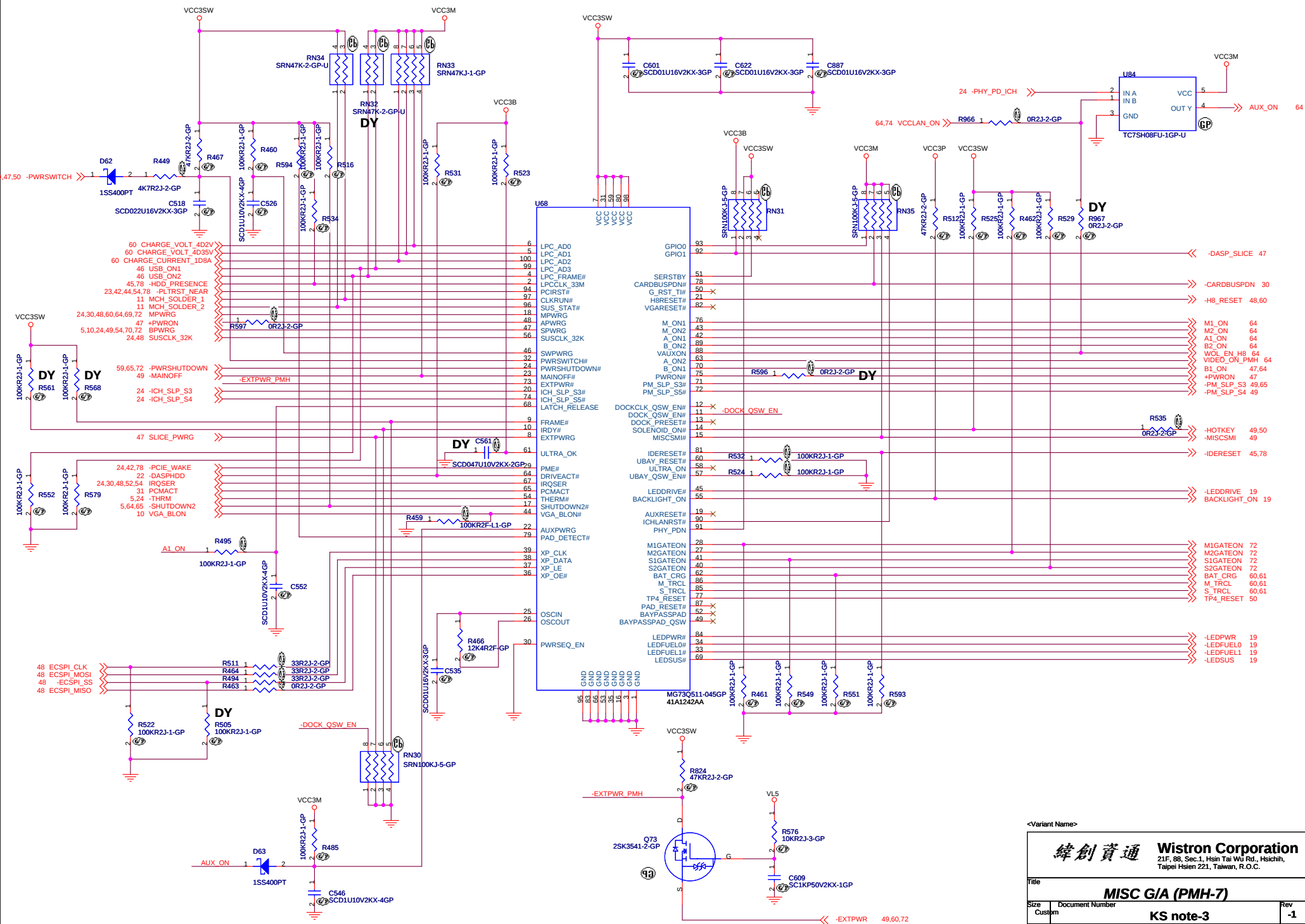


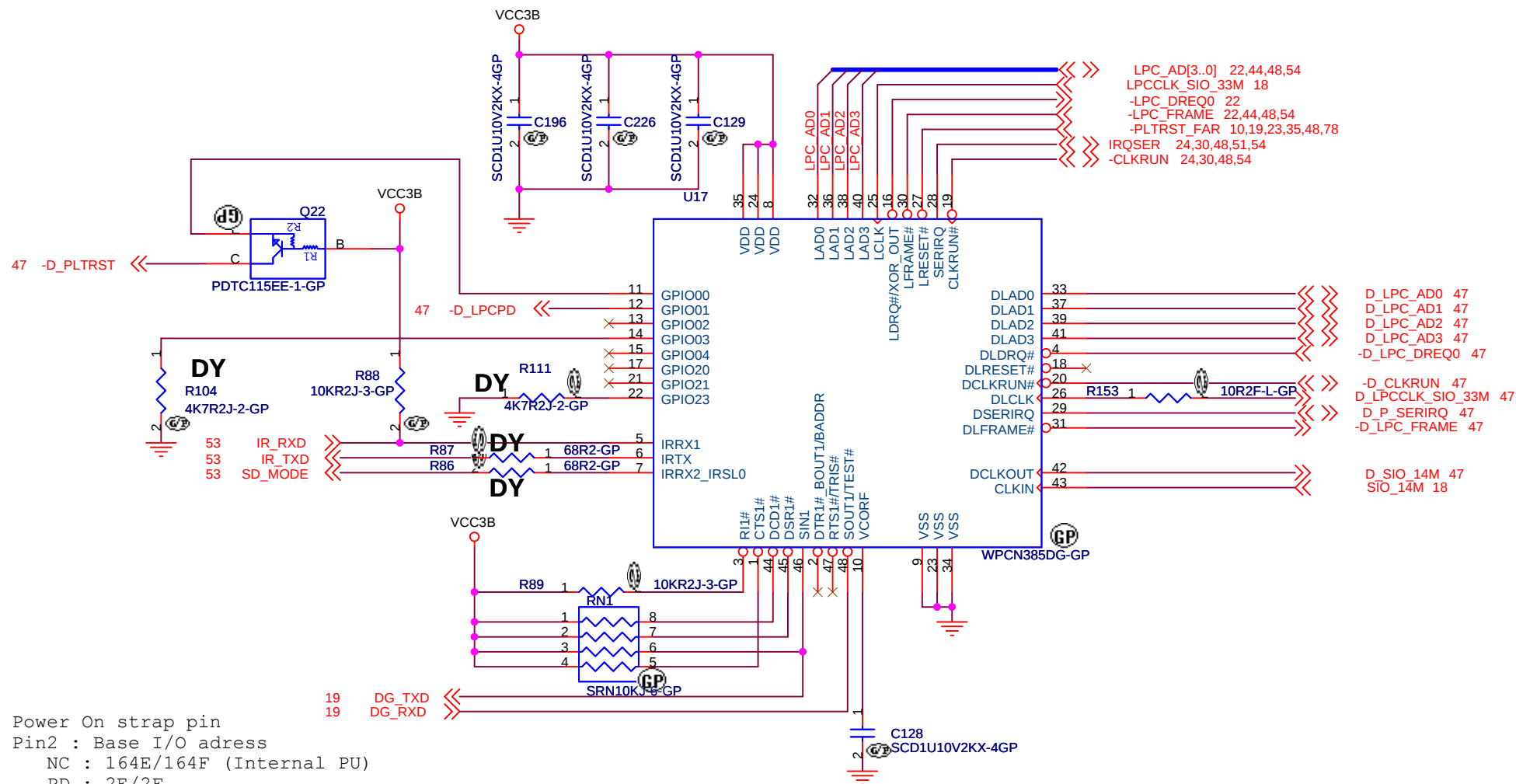
Place	KS-3	Dali-2	Notes
R403 C478 CN3	Dummy Dummy Dummy	ASM ASM ASM	DALI2 PenDetect Sensor
RN38 R251 R227	Dummy Dummy Dummy	ASM ASM ASM	KS3 ThermalSensor IIC BUS
R995 R996	ASM ASM	Dummy Dummy	DALI2 uP/Thermal Sensor IIC BU

Keyboard Connector



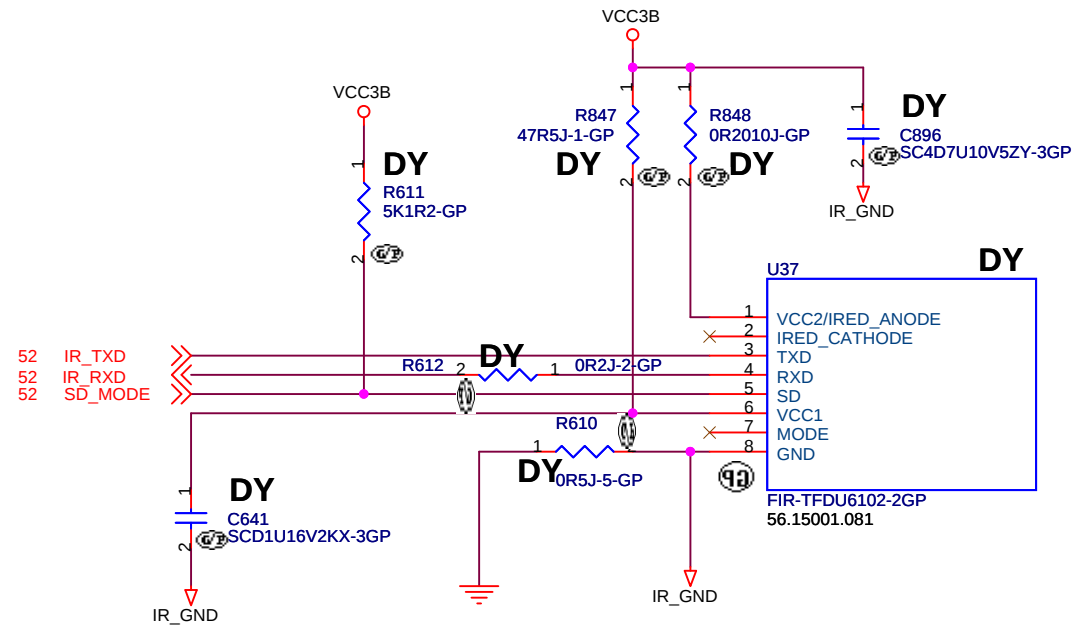
Keyboard Connector Top View





<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
SIO NS87382	
Size A4	Document Number KS note-3
Date: Tuesday, March 20, 2007	Rev -1
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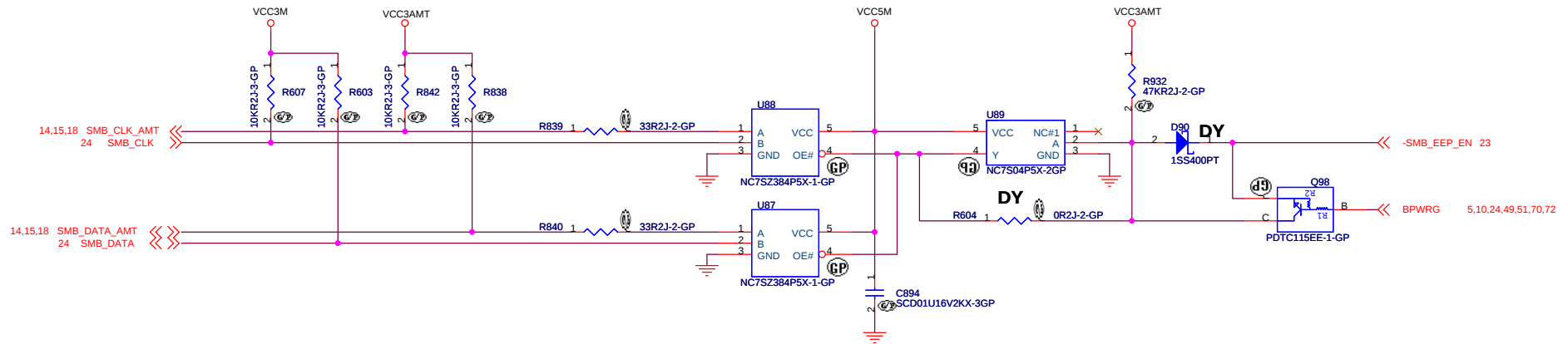
	VISHAY	ZILOG
R848	0Ω	0Ω
R847	47Ω	68Ω
C896	4.7uF	10uF
C641	0.1uF	1uF

VISHAY	TFDU6102-TR3	: 56.15001.081
ZILOG	ZHX2022	: 56.15004.011

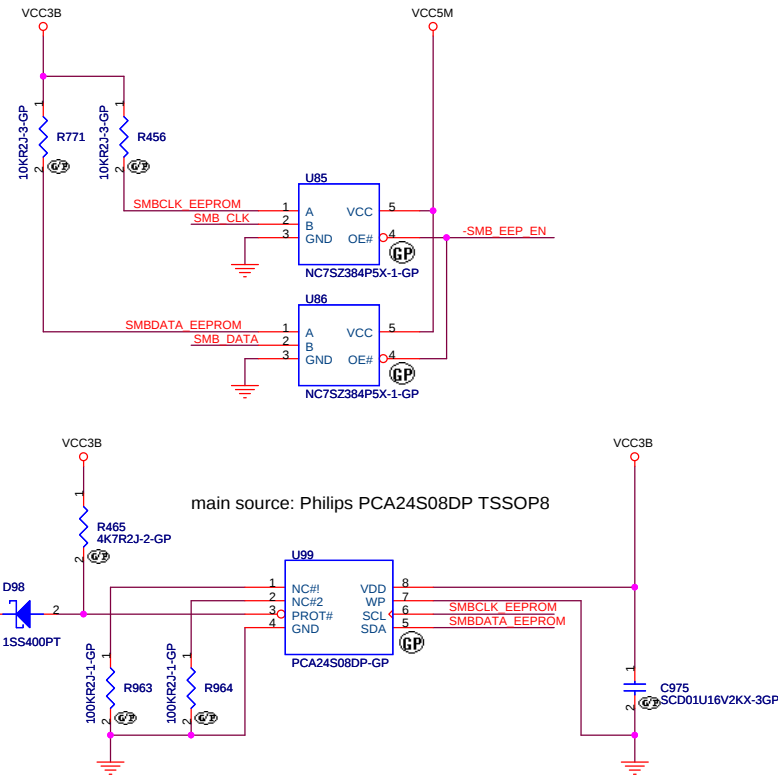
<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title IR MODULE		
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U85, U86 U87, U88
[Sourcing Candidate]
Toshiba TC7SB384FU
TI SN74CBT1G384DCKR
Fairchild NC7SZ384

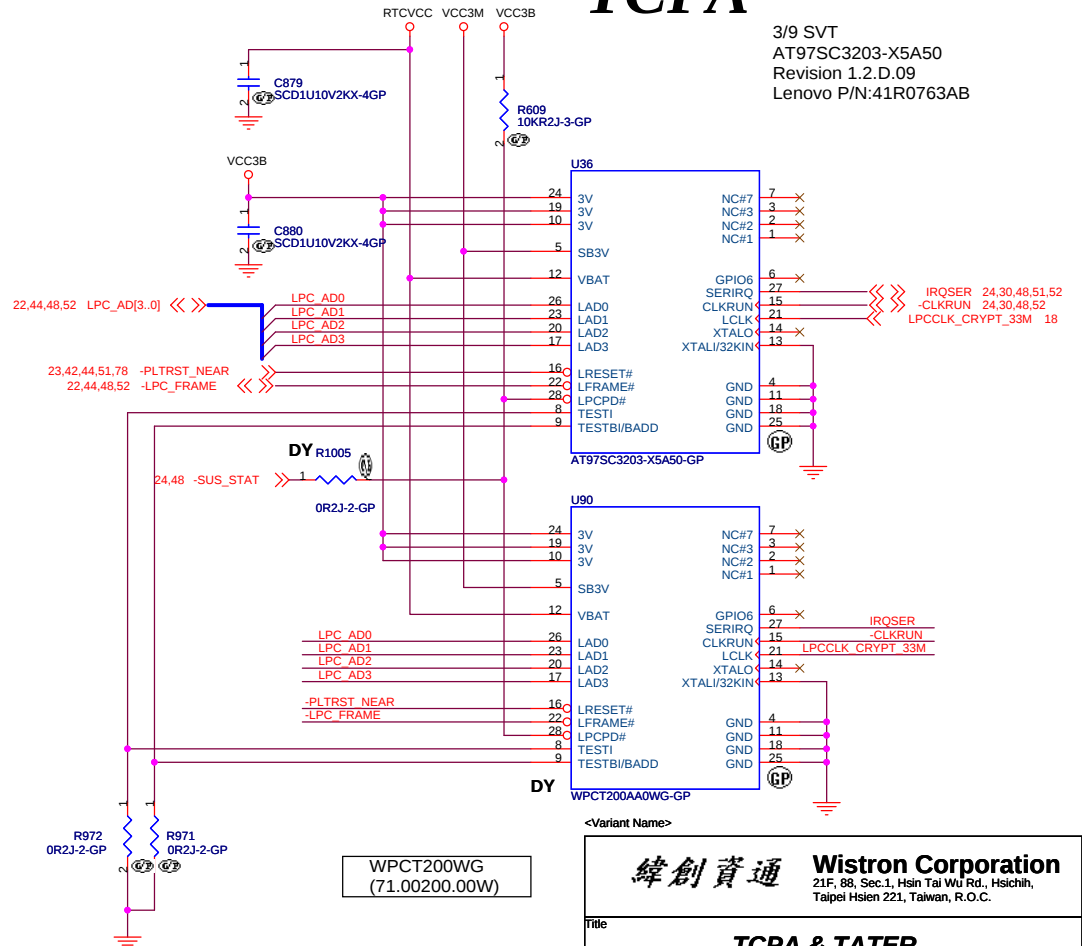


TATER

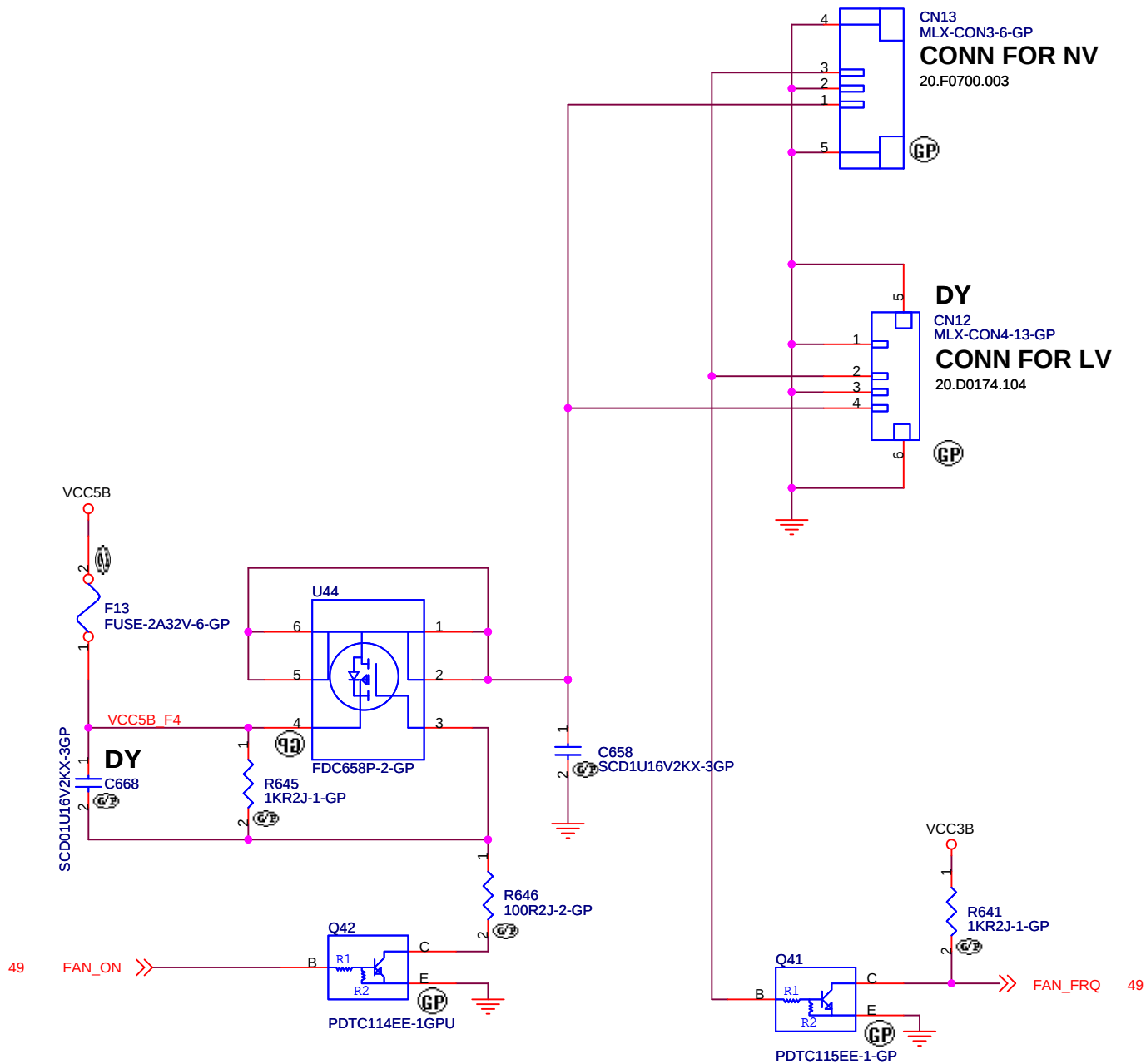


TCPA

3/9 SVT
AT97SC3203-X5A50
Revision 1.2.D.09
Lenovo P/N:41R0763AB



緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title TCPA & TATER	
Size A3	Document Number KS note-3
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Rev -1	



<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

FAN CONTROL

Size
A4

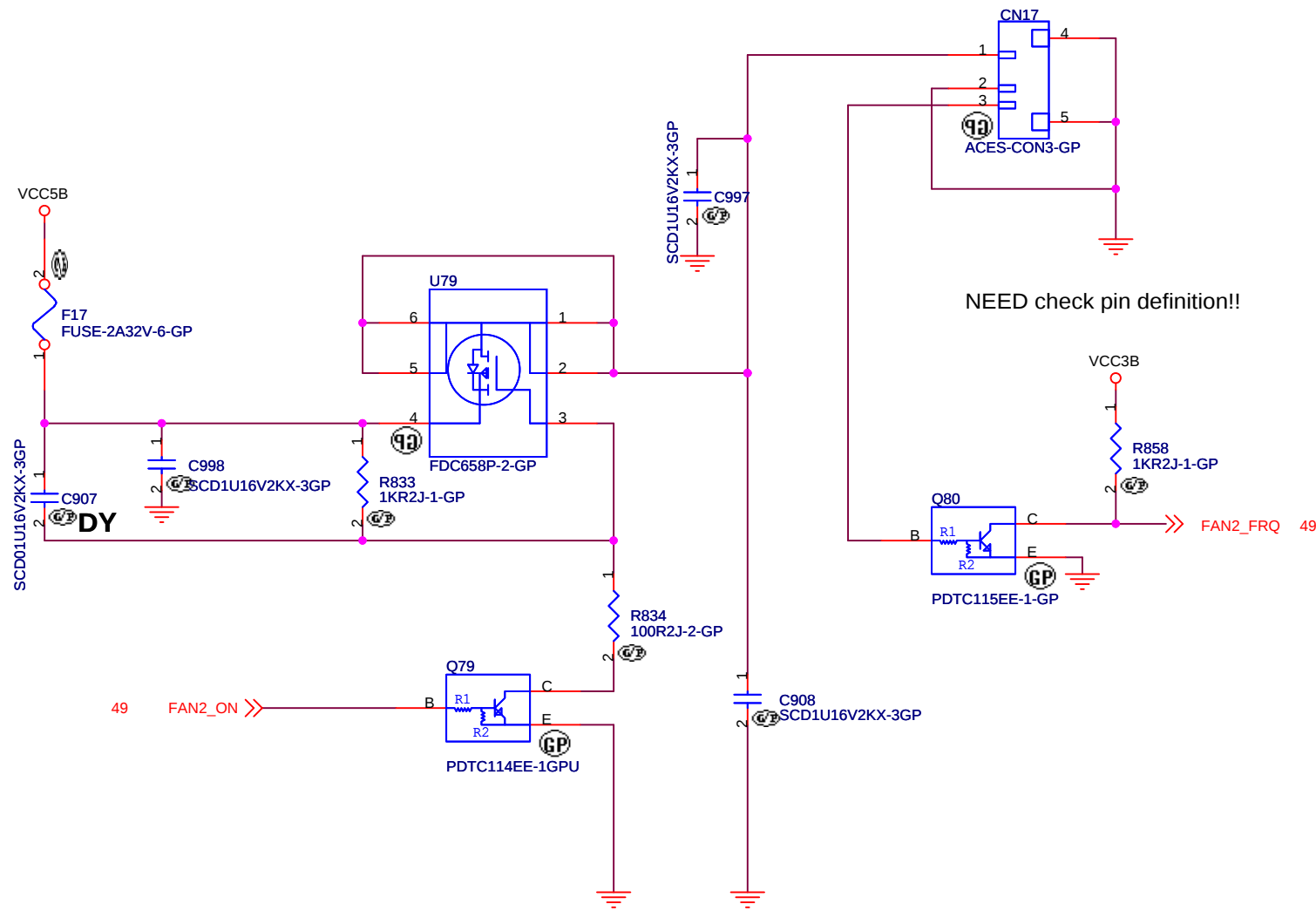
Document Number

KS note-3

Rev
-1

Date: Tuesday, March 20, 2007

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<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

2nd FAN CONTROL

Size
A4

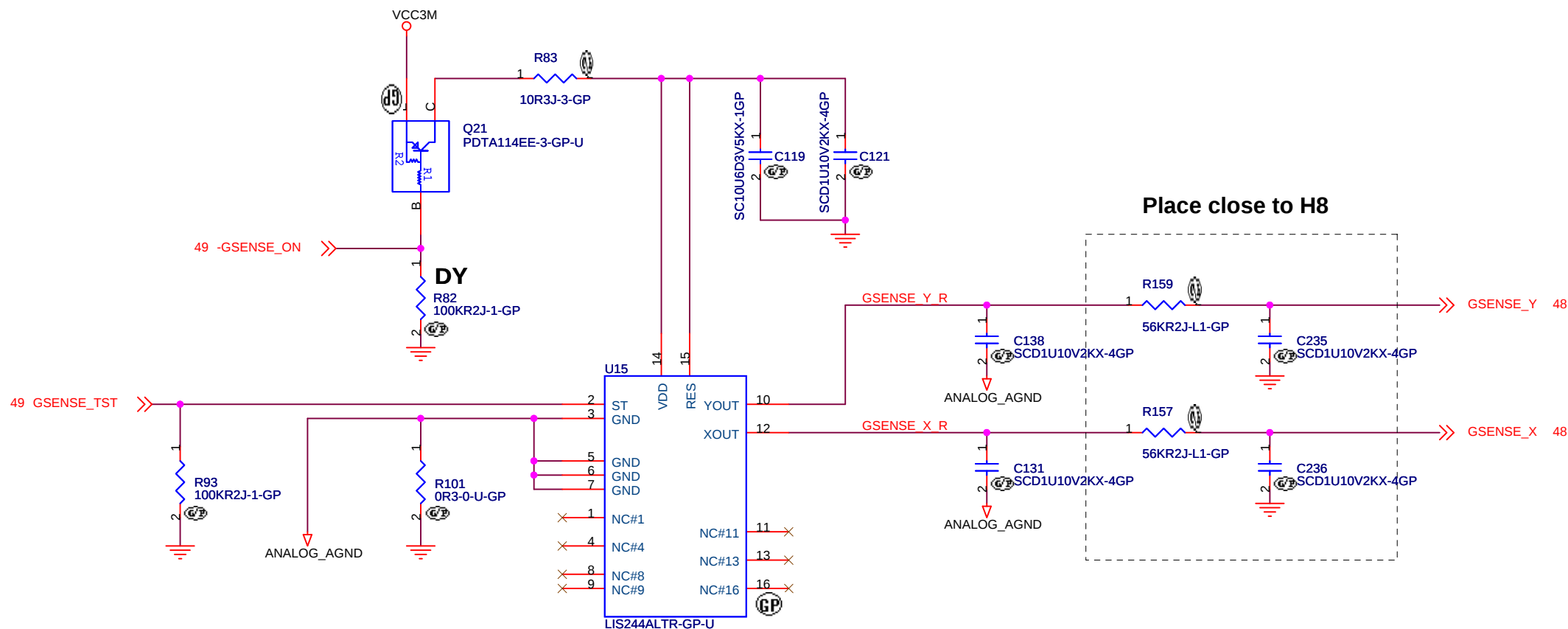
Document Number

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-1

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Primary : ADI ADXL322
2nd : STMicro LIS244AL

Width = 6 mil & Spacing = 10 mil
for three Output traces

Layout Comment :

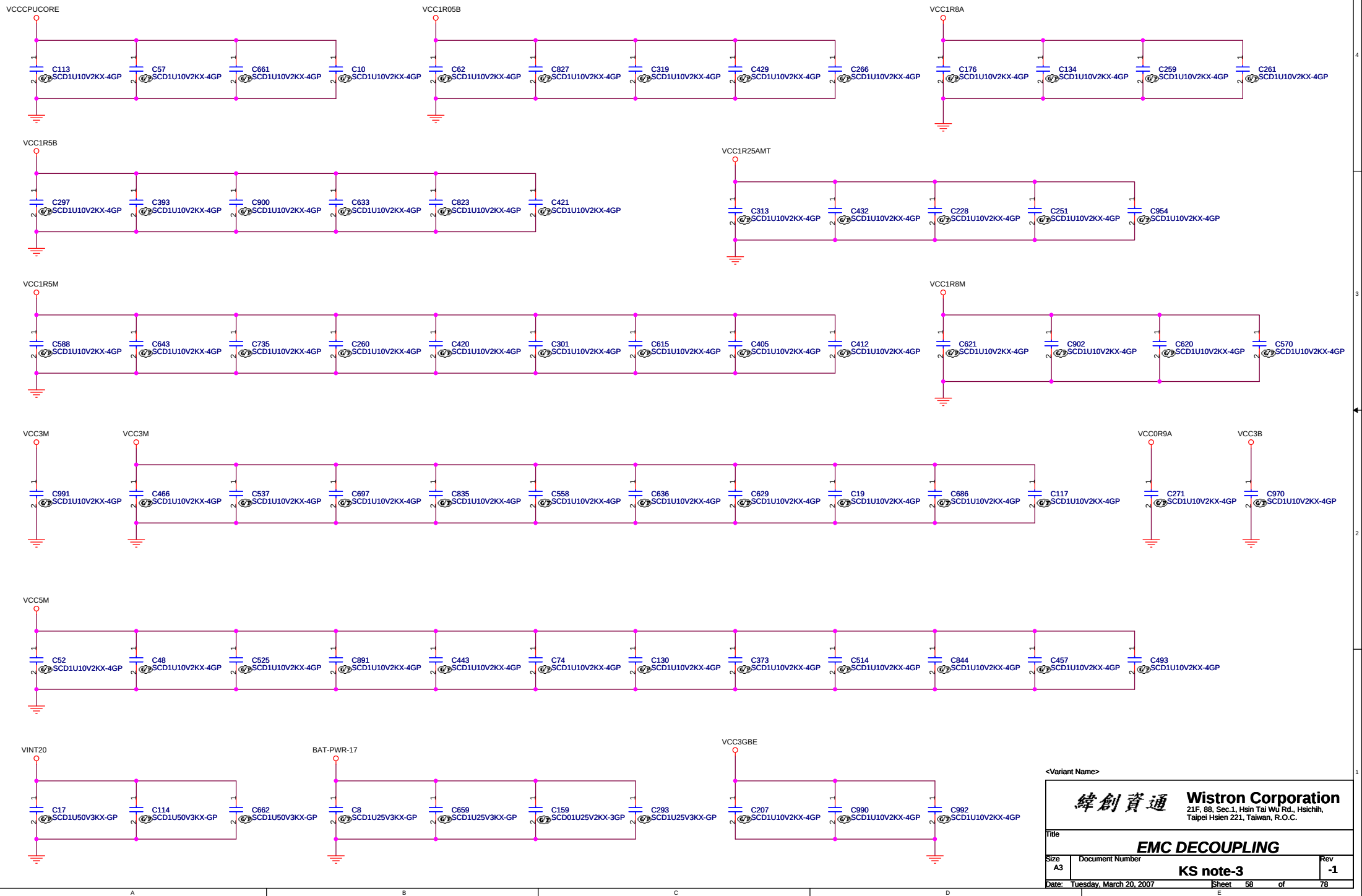
(1) Place C138, C131, Q21, R82, R83, C119, C121, R93, R101 close to U15.

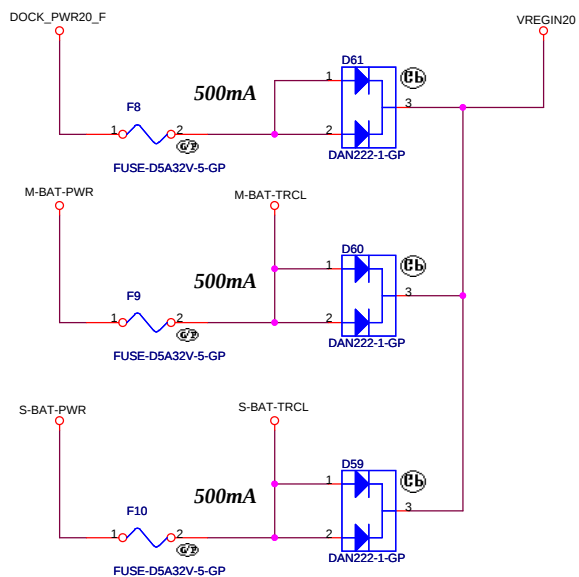
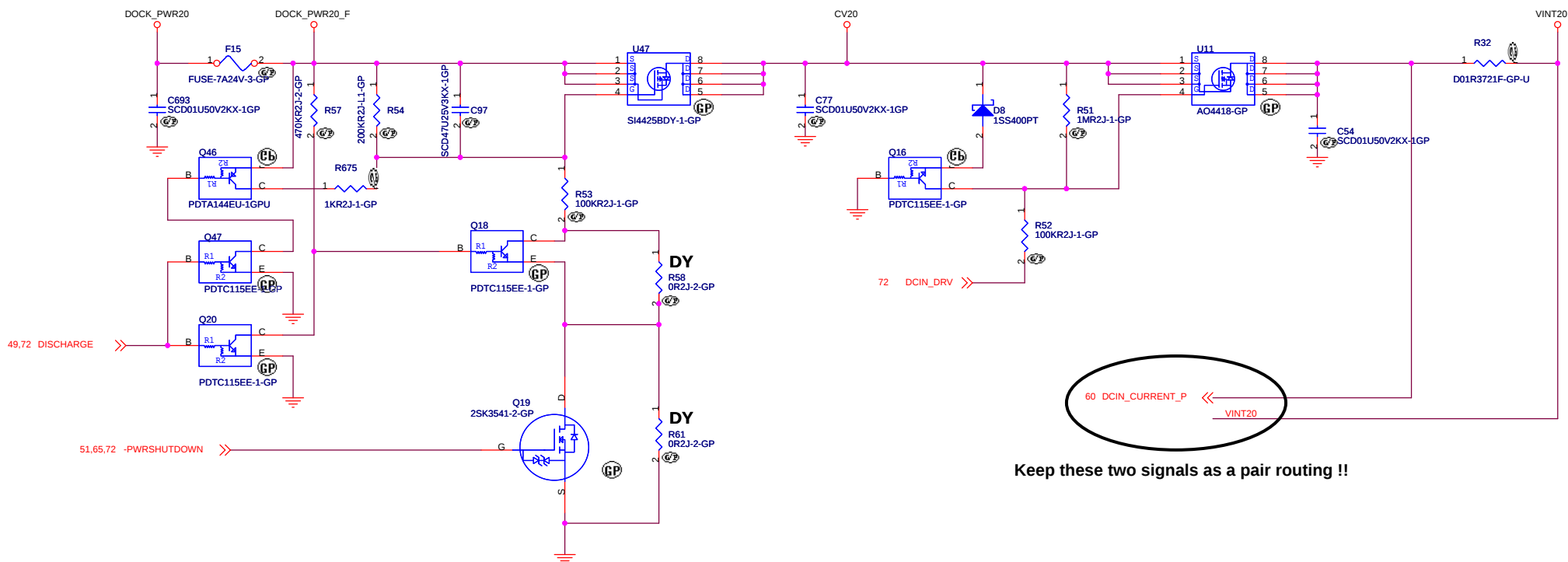
(2) Avoid routing under DCDC switching area.

	ADXL322 LIS244AL	No Accel
R82	NO_ASM	ASM
R93	ASM	ASM
All other	ASM	NO_ASM

<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
G-SENSOR	
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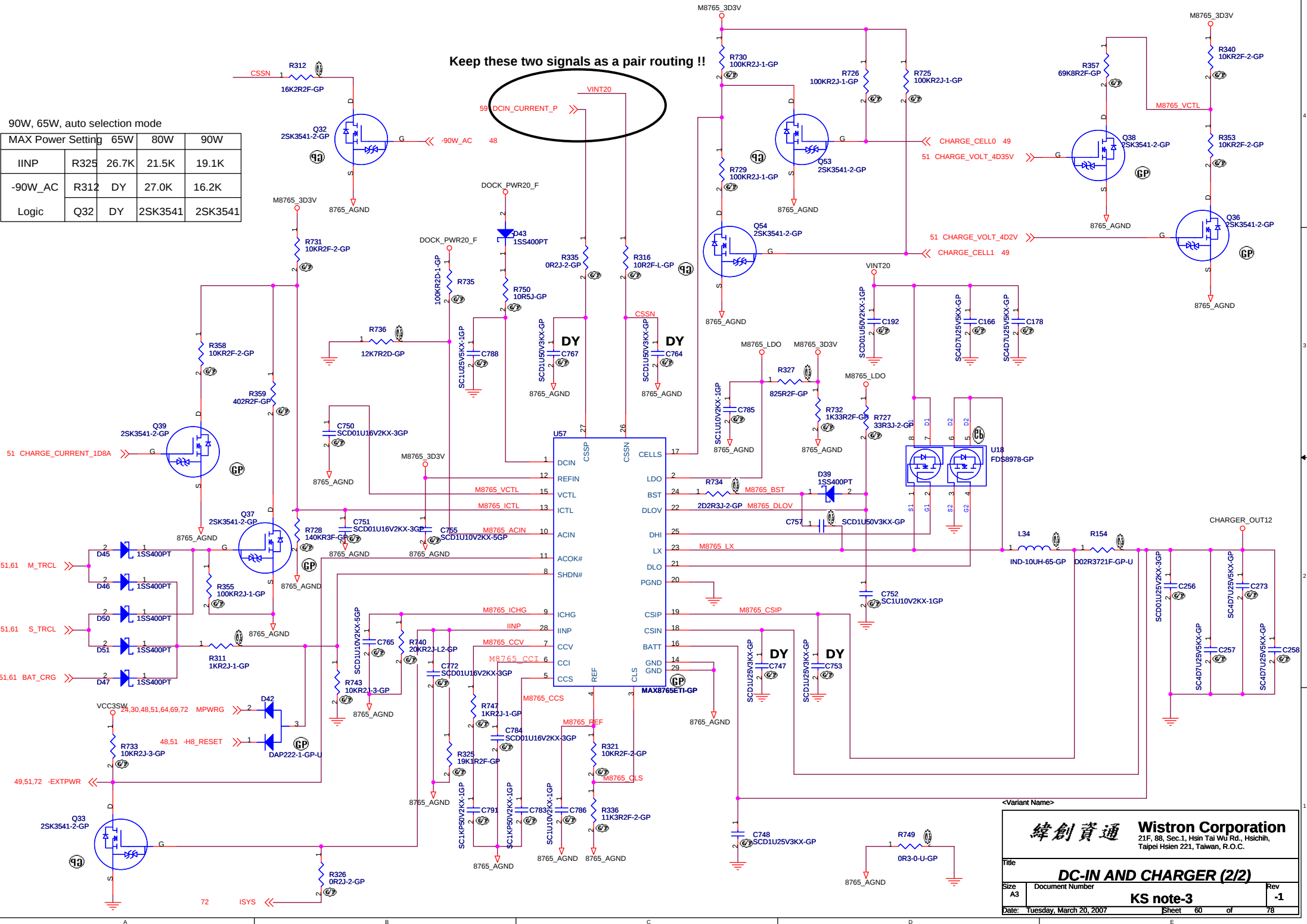


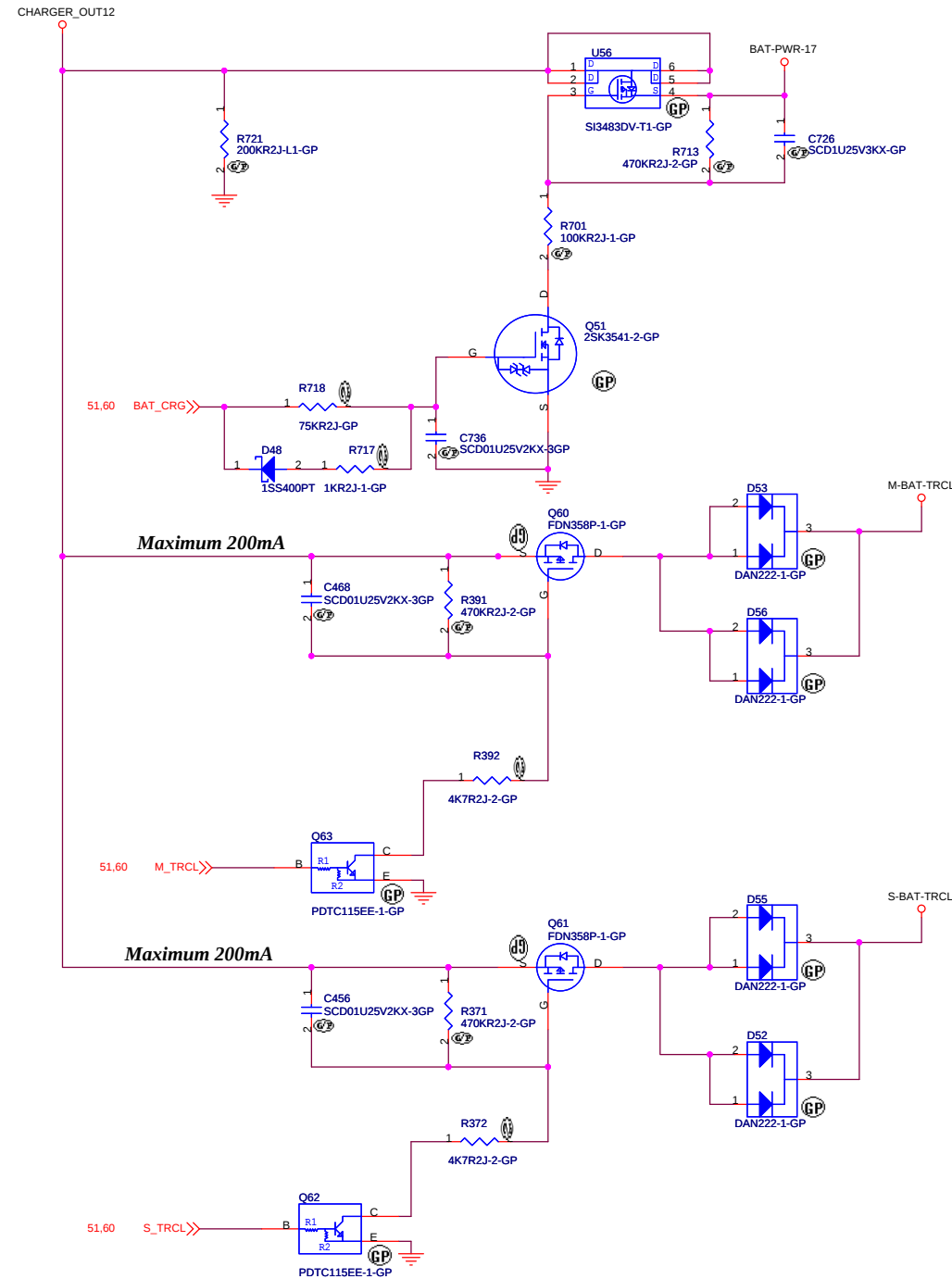
<Variant Name>

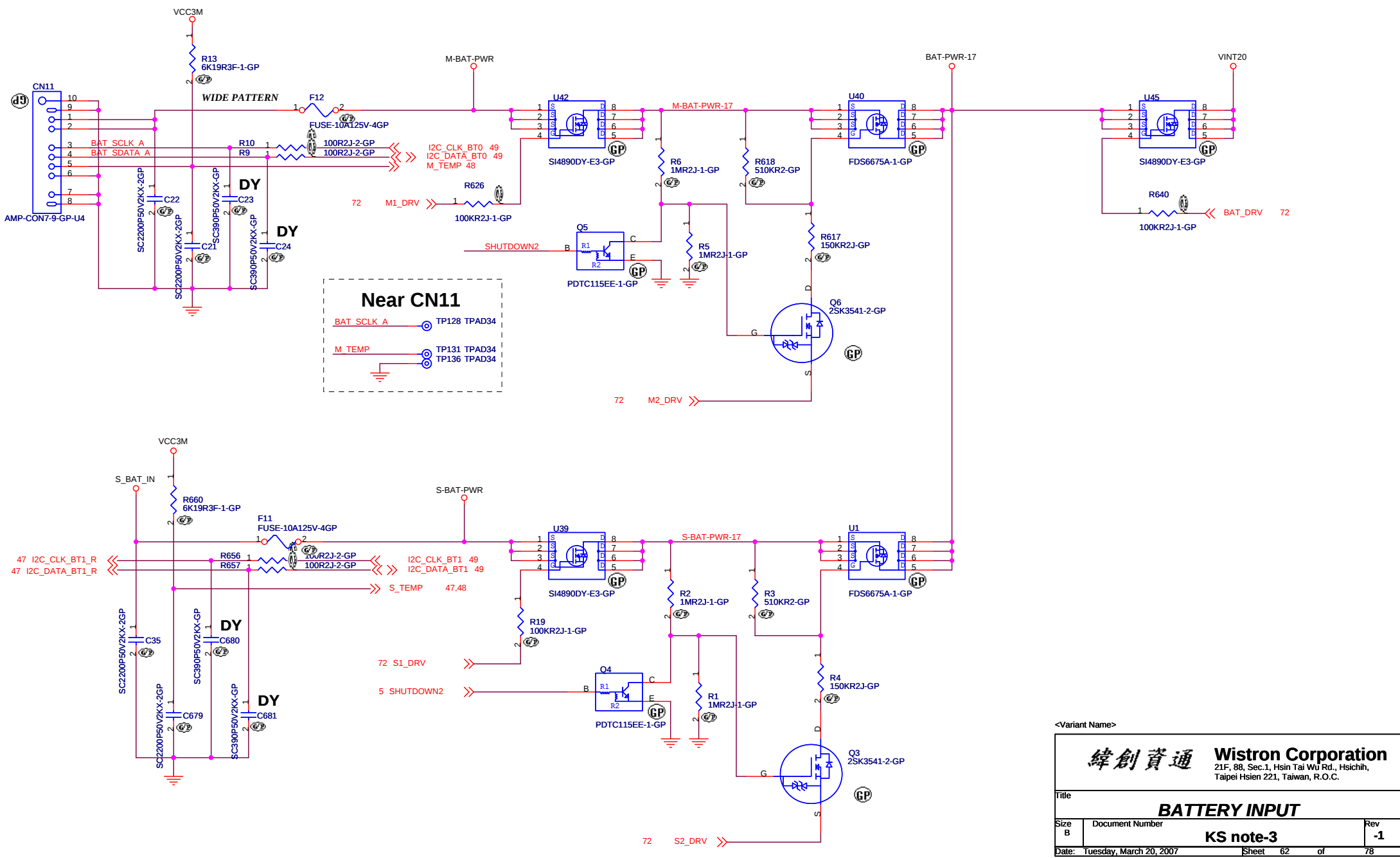
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title		
DC-IN AND CHARGER (1/2)		
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MAX Power Setting		65W	80W	90W
IINP	R325	26.7K	21.5K	19.1K
-90W_AC	R312	DY	27.0K	16.2K
Logic	Q32	DY	2SK3541	2SK3541







<Variant Name>

緯創資通

Wistron Corporation

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Title

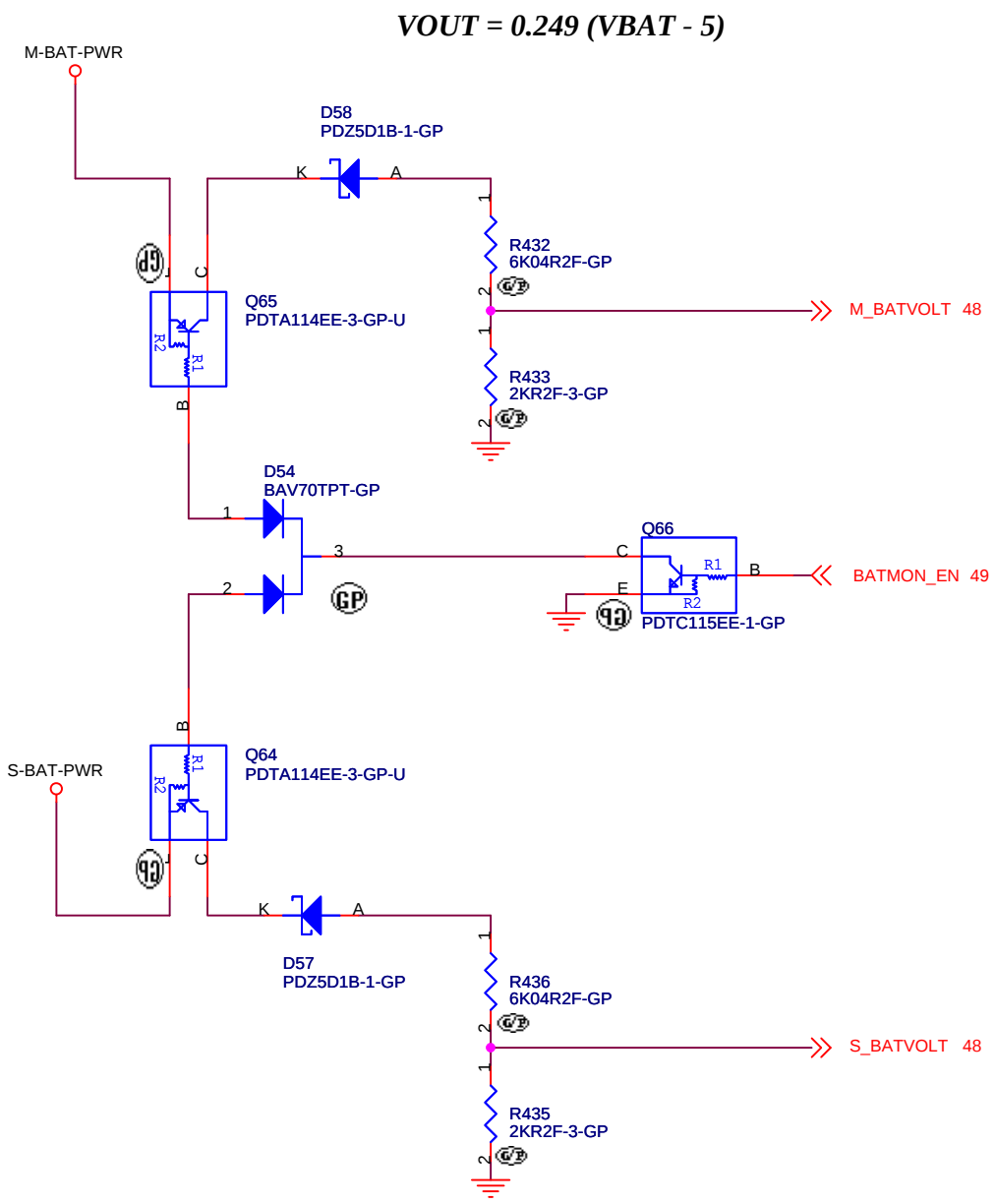
BATTERY INPUT

KS note-3

Rev -1

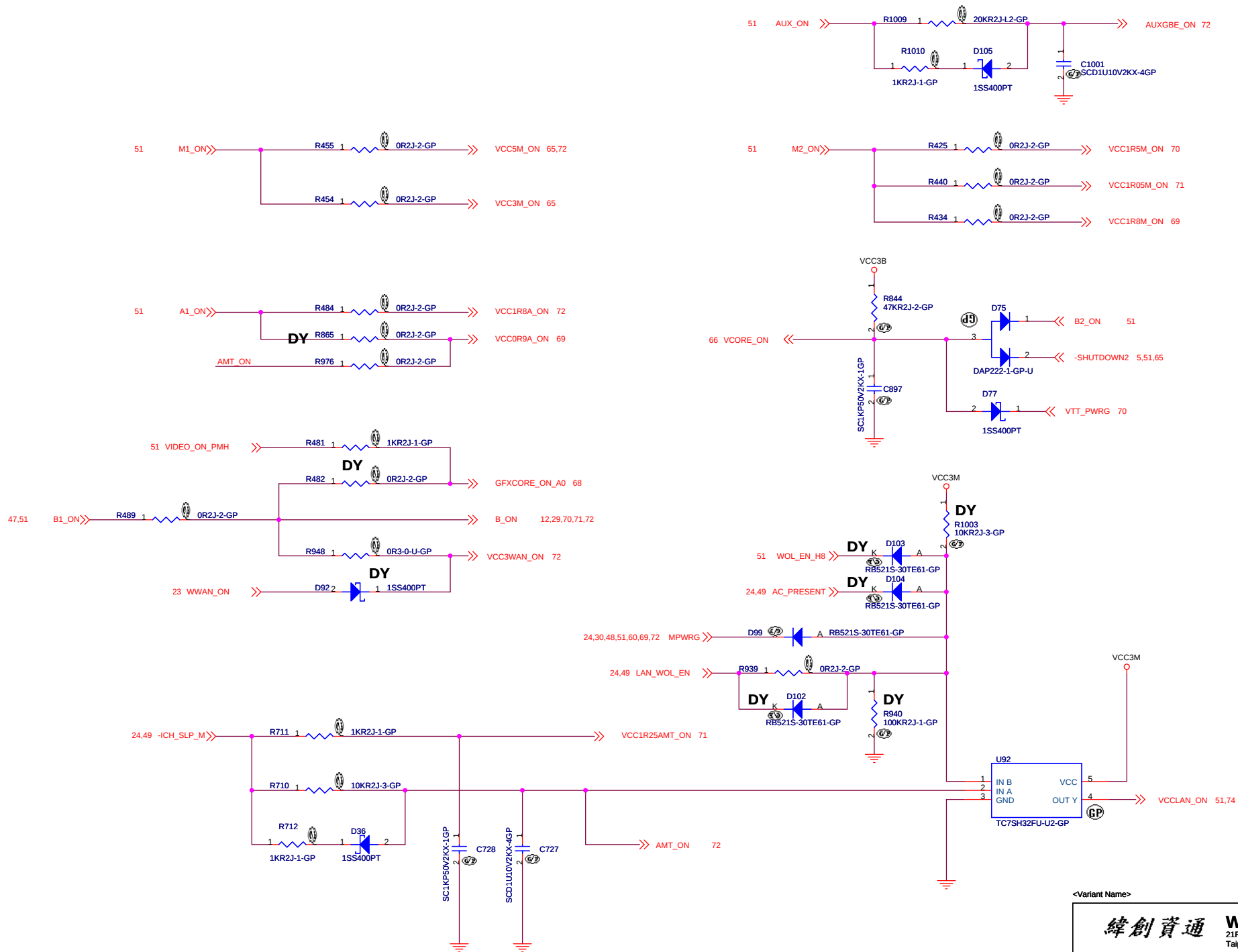
Date: Tuesday, March 20, 2007

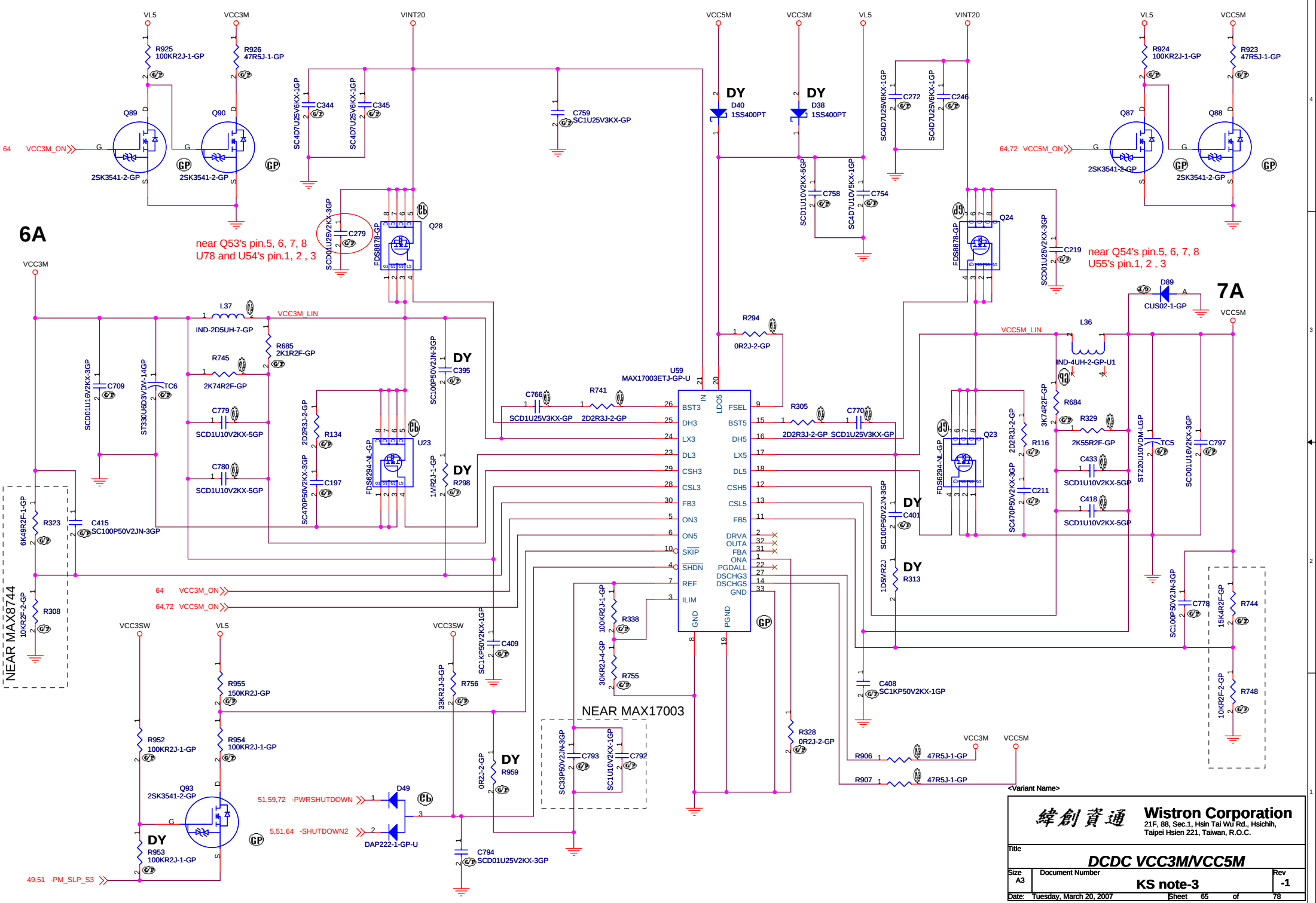
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<Variant Name>

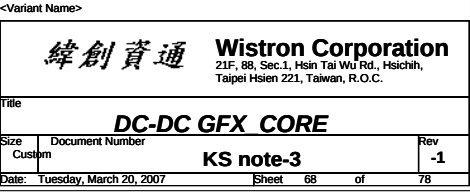
緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
BATTERY MONITOR			
Size	Document Number		Rev
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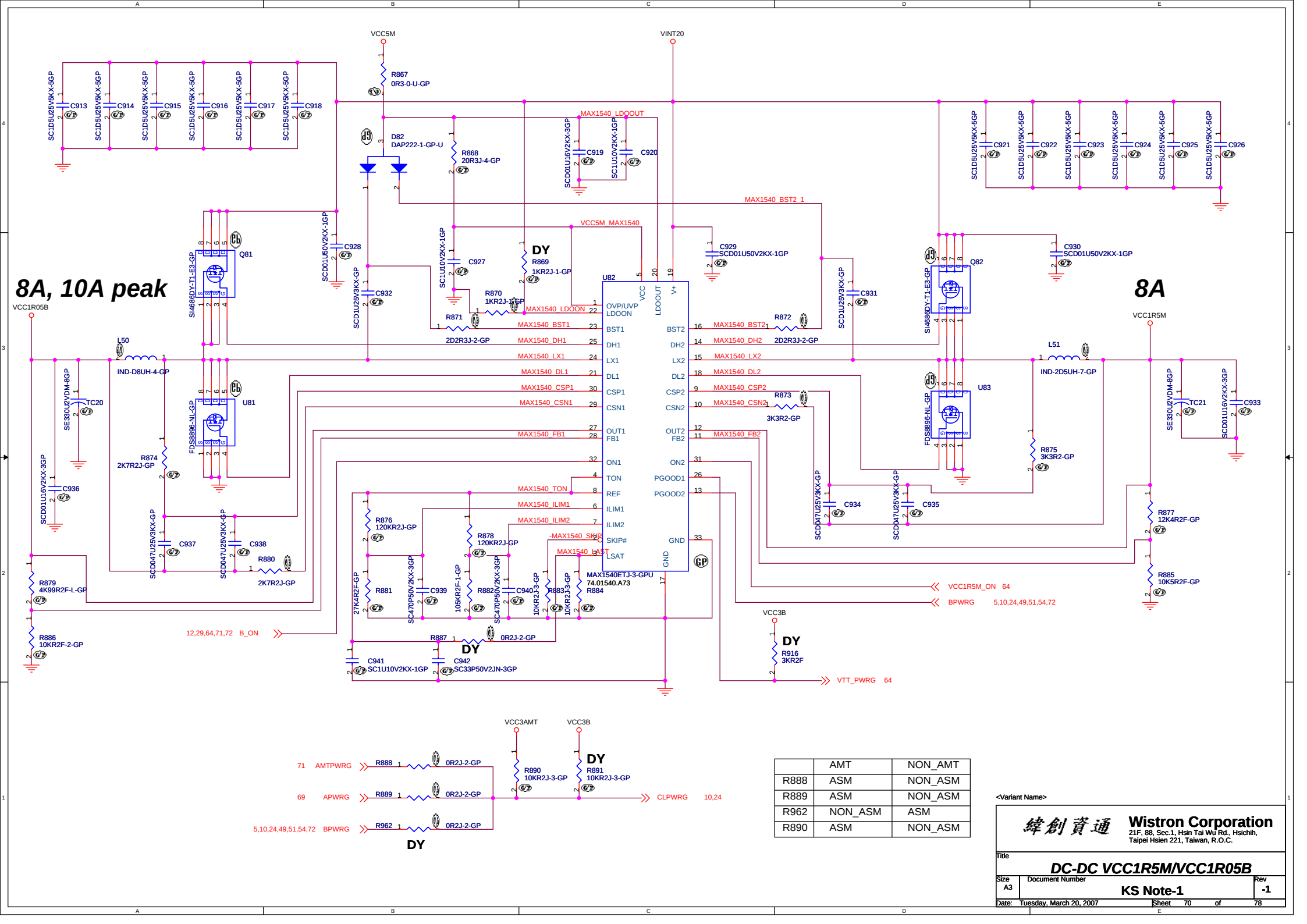




1.NORMAL: 1.05V
2.GRAPHICS RENDER STANDBY MODE: 0.775V

VID <4..0>
01000 = 1.05V





8A, 10A peak

8A

	AMT	NON_AMT
R888	ASM	NON_ASM
R889	ASM	NON_ASM
R962	NON_ASM	ASM
R890	ASM	NON_ASM

<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

DC-DC VCC1R5M/VCC1R05B

Size

Document Number

Rev

A3

KS Note-1

-1

Date:

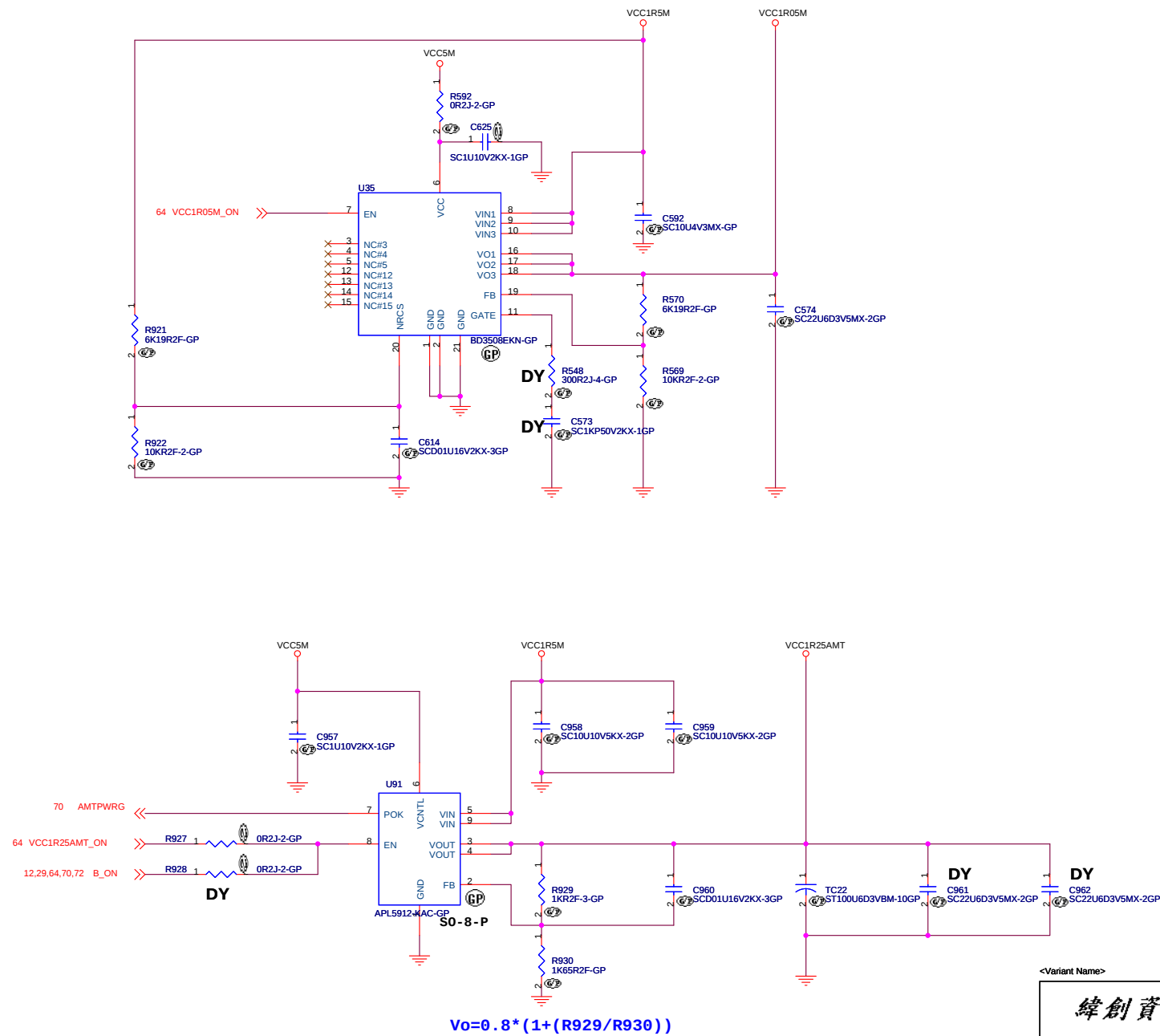
Tuesday, March 20, 2007

Sheet

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of

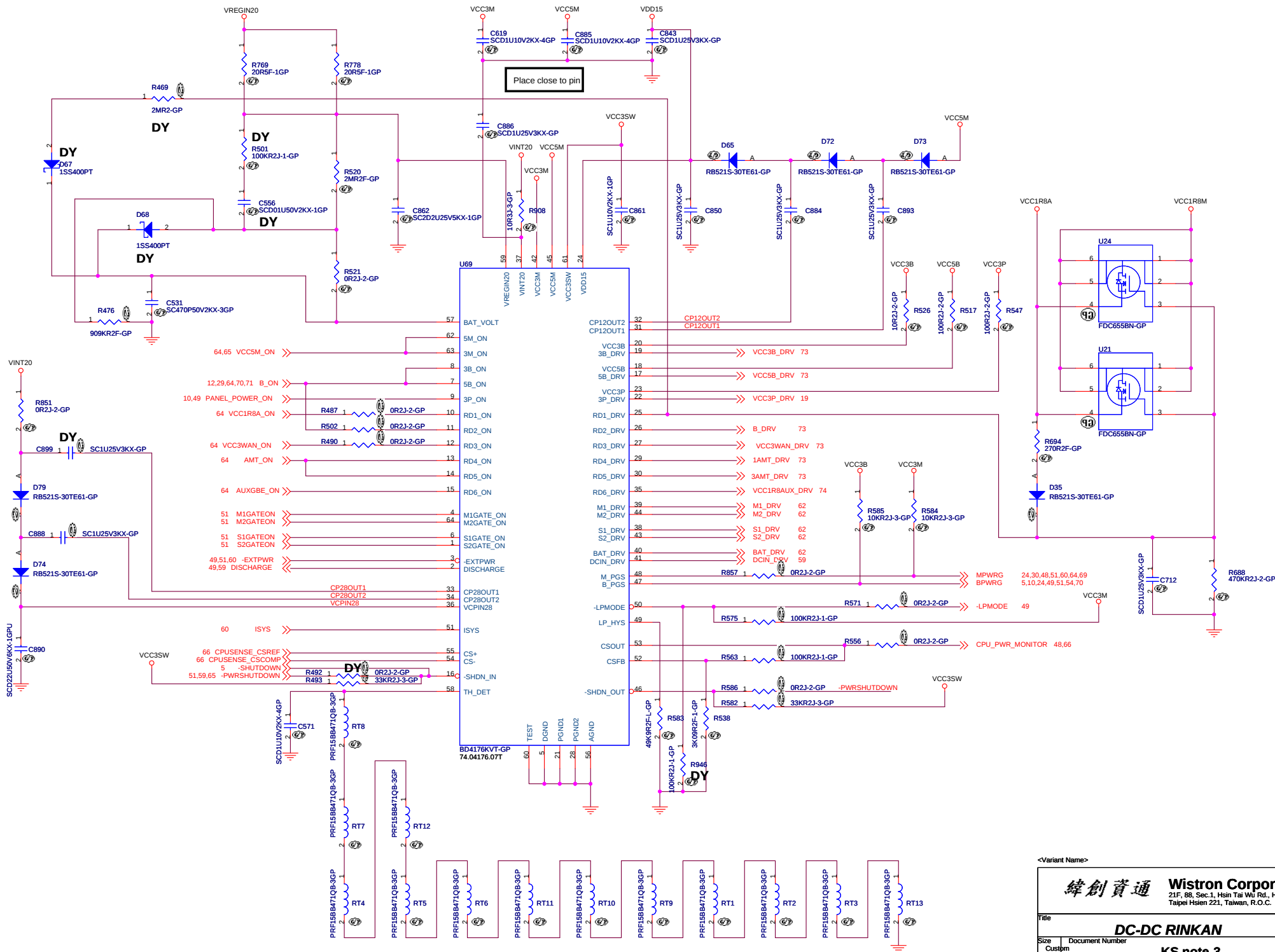
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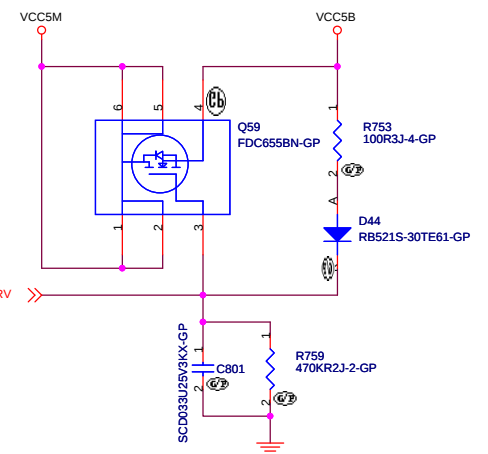
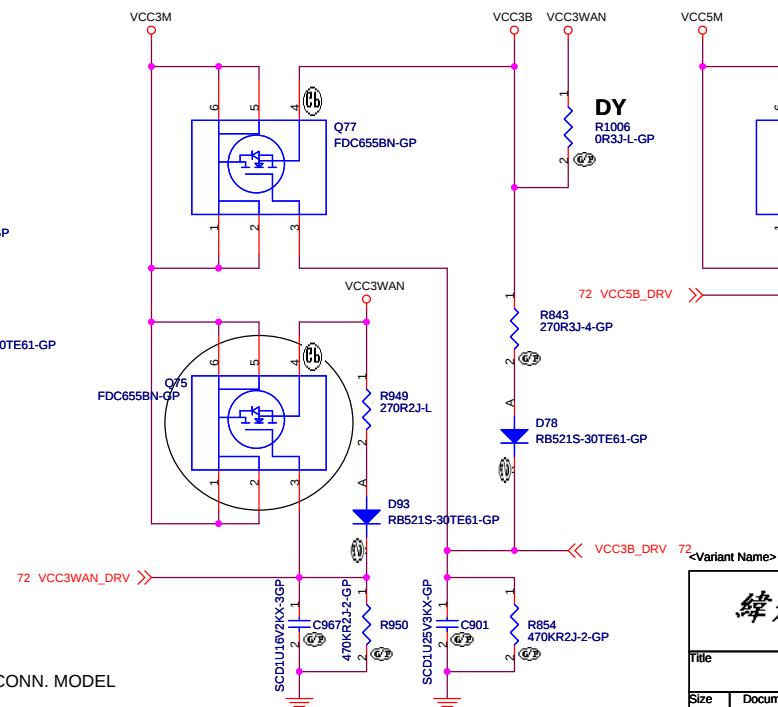
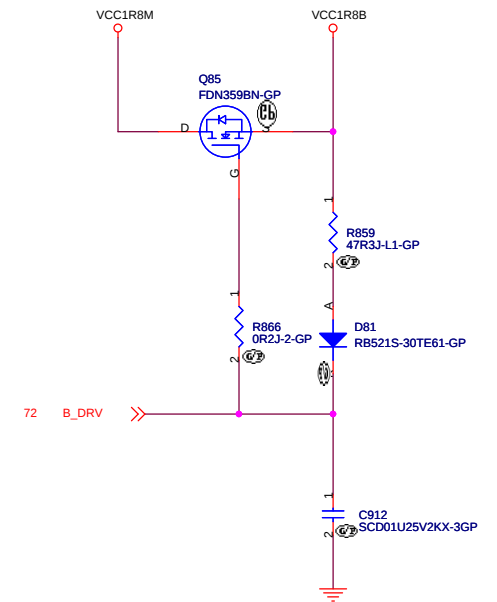


<Variant Name>

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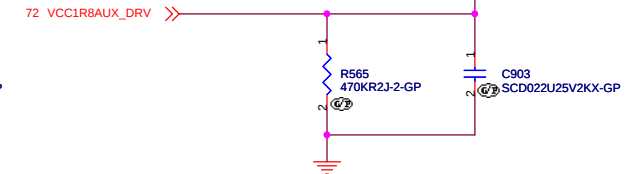
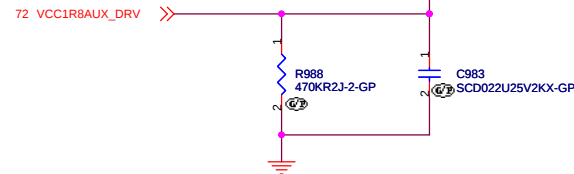
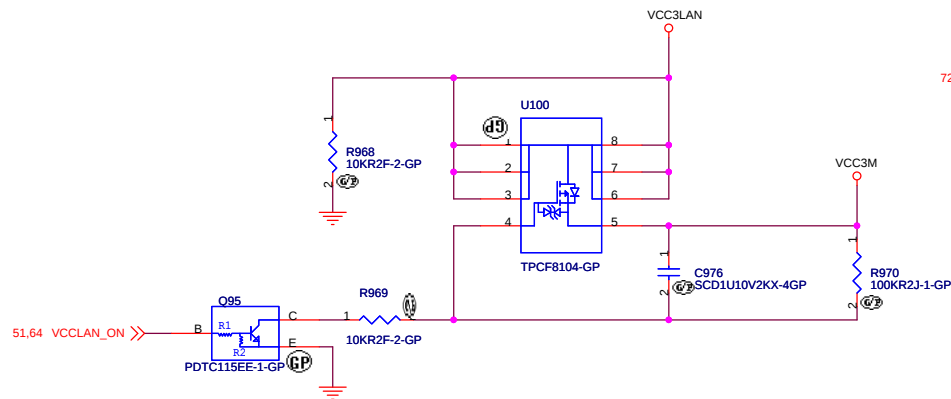
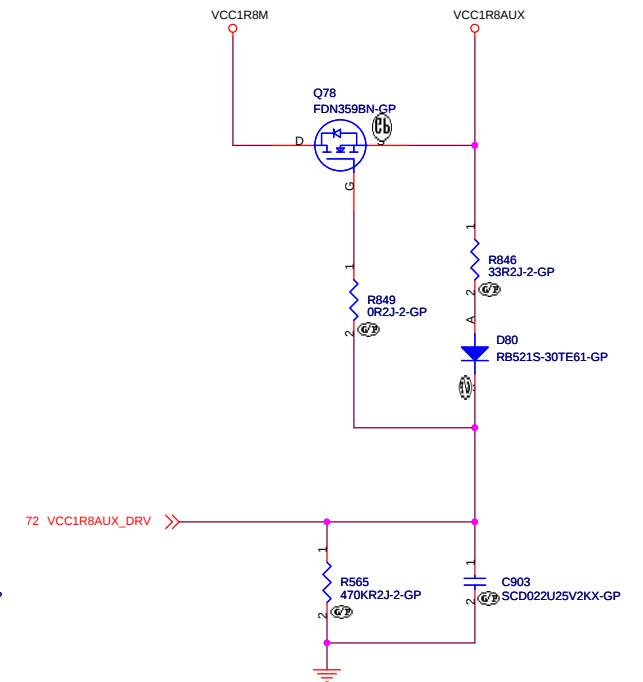
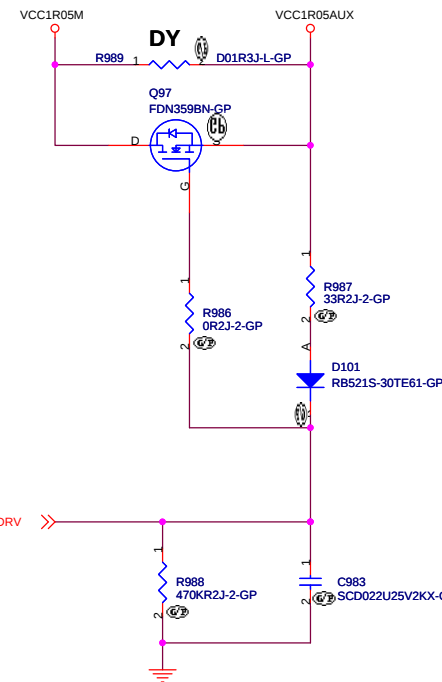
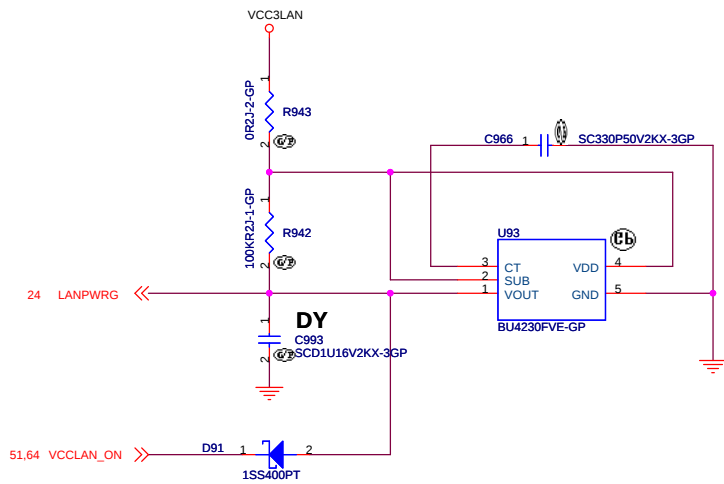




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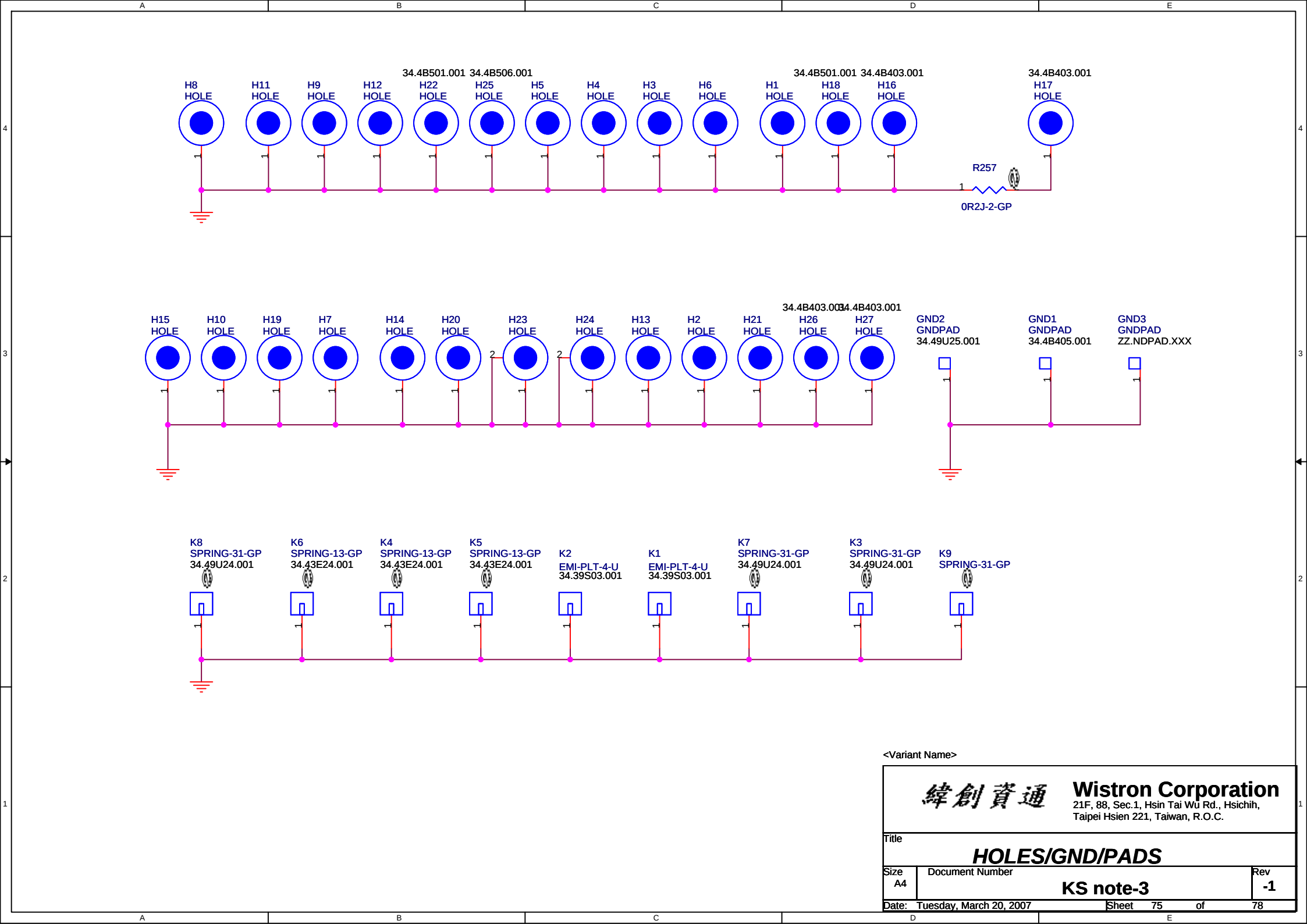
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HOLES/GND/PADS			
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Comparison chart , KS-3 and Dali-2

Item	Page	Location Netname	KS-3	Dali-2	Modify	Note
1	5	U104	Dummy	ASM		DALI2 uP/Thermal Sensor IIC BUS
		U105	Dummy	ASM		
		U106	Dummy	ASM		
2	19	R36	100Ω	0Ω		KS3: WLAN LED/Dali2 VCC3B
		R85	Dummy	0Ω		
		R107	Dummy	0Ω		
		R98	Dummy	0Ω		Bluettoth IO
		R96	Dummy	0Ω		
		R668	200Ω	Dummy		
		C691	ASM	Dummy		Think Light
		Q44	ASM	Dummy		
		Q13,Q14,Q15	PDTC114EE	Dummy		
		R37	200Ω	Dummy		LED ACDC
		R42	330Ω	Dummy		
		R43	330Ω	Dummy		
		R26	ASM	Dummy		LED CAPSLOCK
		R35	0Ω	0.5A Fuse		
3	27	CN6	Dummy	ASM		DALI2 INTMIC LOGIC
		C578	Dummy	0.1uF		
		R567	Dummy	ASM		
		CN7	ASM	Dummy		MIC
		R819	1.2KΩ	Dummy		
		R804	2.2KΩ	Dummy		
		R813	ASM	Dummy		KS3 INTMIC LOGIC
		C872	10uF	Dummy		
		C877	1uF	Dummy		
4	49	R403	Dummy	330Ω		DALI2 PenDetect Sensor
		C478	Dummy	0.1uF		
		CN3	Dummy	ASM		
		RN38	Dummy	ASM		KS3 ThermalSensor IIC BUS
		R251	Dummy	ASM		
		R227	Dummy	ASM		
		R995	ASM	Dummy		DALI2 uP/Thermal Sensor IIC BUS
		R996	ASM	Dummy		
6	56	CN17	ASM	Dummy		2nd FAN connector
		C908	ASM	Dummy		
		F17	ASM	Dummy		
		Q79	ASM	Dummy		2nd FAN logic
		Q80	ASM	Dummy		
		R833	ASM	Dummy		
		R834	ASM	Dummy		
		R858	ASM	ASM		
		U79	ASM	Dummy		
5	75	GND1	ASM	Dummy		ME ANTENNA ROUTING CLIP
		GND2	ASM	Dummy		

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Comparison chart for AMT function

Item	Page	Location Netname	AMT	NON_AMT	Modify	Note
1	22	U60	ICH8m Enhanced	ICH8m		
	24	R270	NO_ASM	ASM		
	37	U74	Nineveh 82566MM	Nineveh 82566MC		
	44	SPI1	32Mbit	16Mbit		
	64	R711	ASM	NO_ASM		
		C728	ASM	NO_ASM		
	70	R888	ASM	NO_ASM		
		R889	ASM	NO_ASM		
		R962	NO_ASM	ASM		
		R890	ASM	NO_ASM		
		R891	NO_ASM	NO_ASM		
	71	R927	ASM	NO_ASM		
		R928	NO_ASM	ASM		
	73	R862	NO_ASM	ASM		
		Q69	ASM	NO_ASM		
		R766	ASM	NO_ASM		
		R772	ASM	NO_ASM		
		D66	ASM	NO_ASM		
		C841	ASM	NO_ASM		
		R767	ASM	NO_ASM		
		R941	ASM	NO_ASM		
		Q58	ASM	NO_ASM		
		R758	ASM	NO_ASM		
		R746	ASM	NO_ASM		
		D41	ASM	NO_ASM		
		C799	ASM	NO_ASM		
		R863	NO_ASM	ASM		
		Q34	ASM	NO_ASM		
		R342	ASM	NO_ASM		
		R330	ASM	NO_ASM		
		D22	ASM	NO_ASM		
		R864	NO_ASM	ASM		

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Title AMT FUNCTION CONTROL TABLE			
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