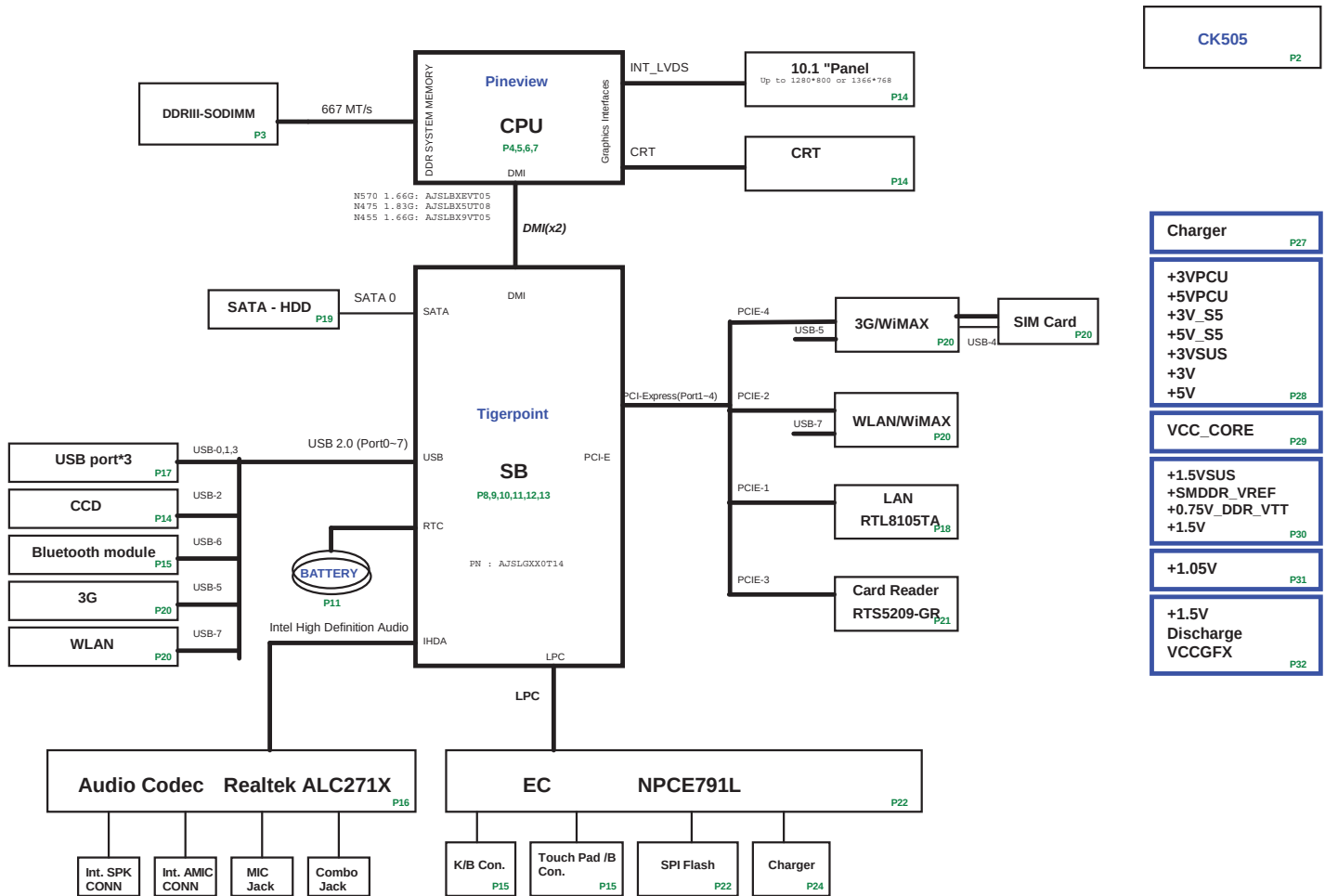
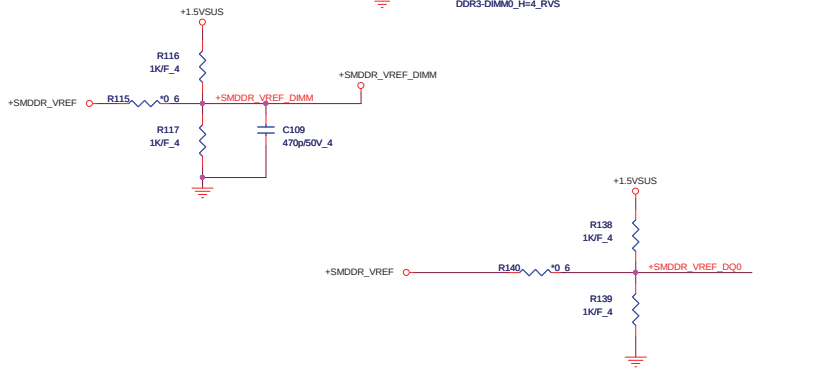
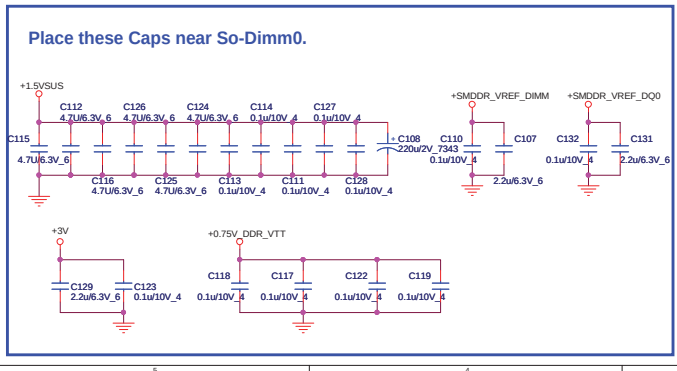
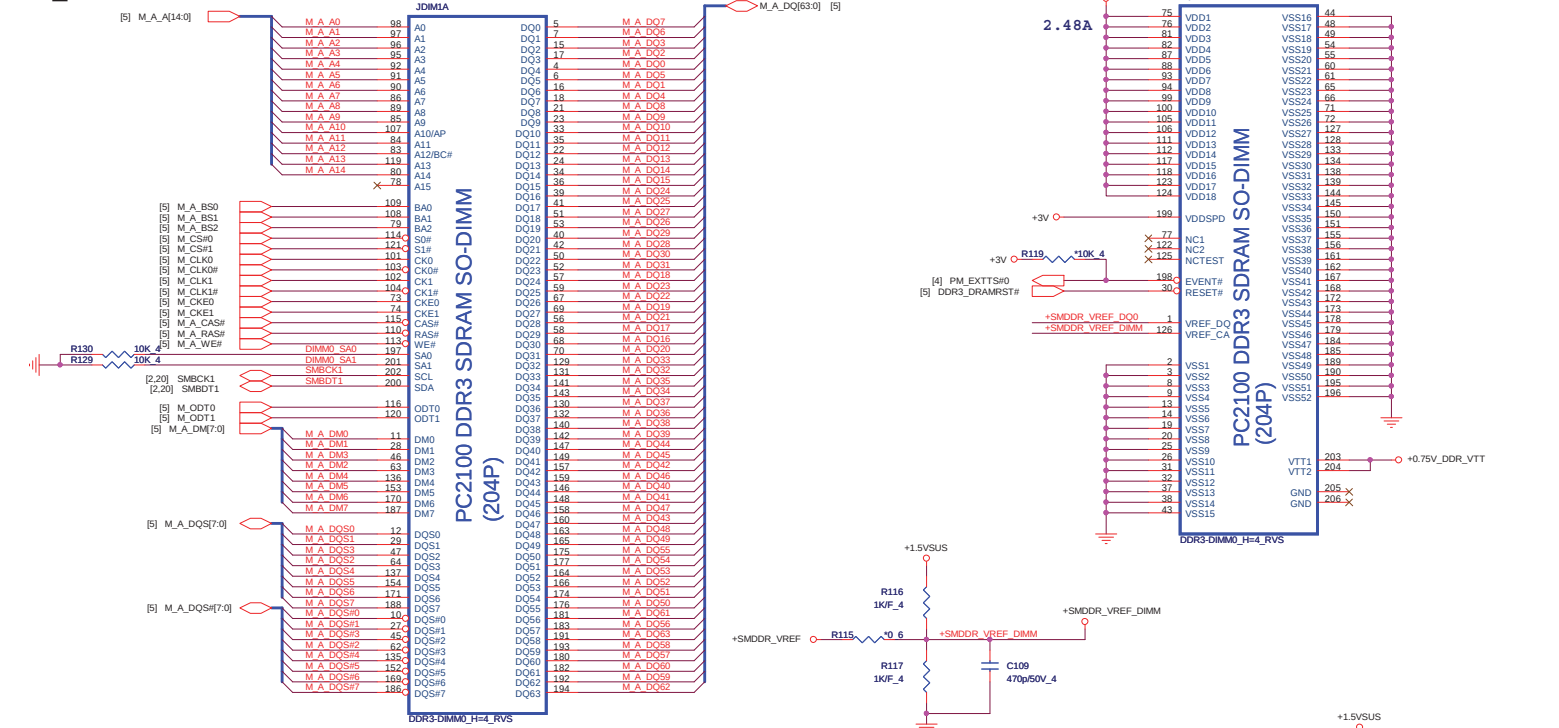


ZE6 Block Diagram





DDR_STD (DDR)

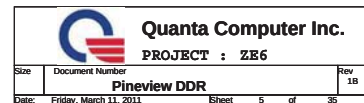


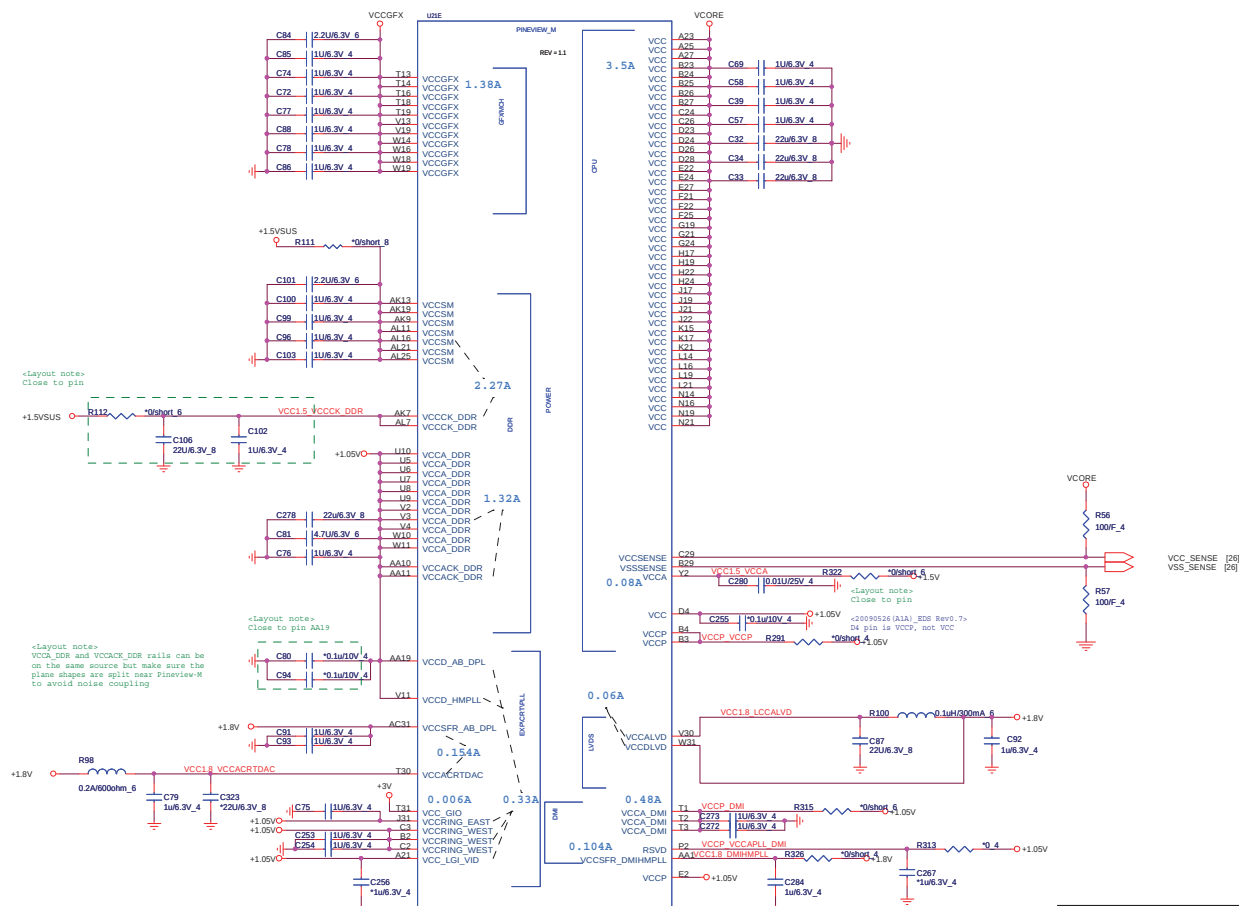

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PROJECT : ZE6

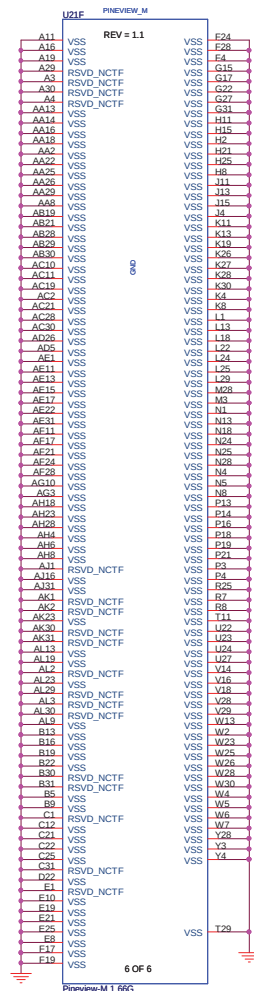
Size	Document Number	Rev
	DDRIII SO-DIMM-0	1A
Date:	Friday, March 11, 2011	

Sheet 3 of 35

<http://laptop-motherboard-schematic.blogspot.com/>

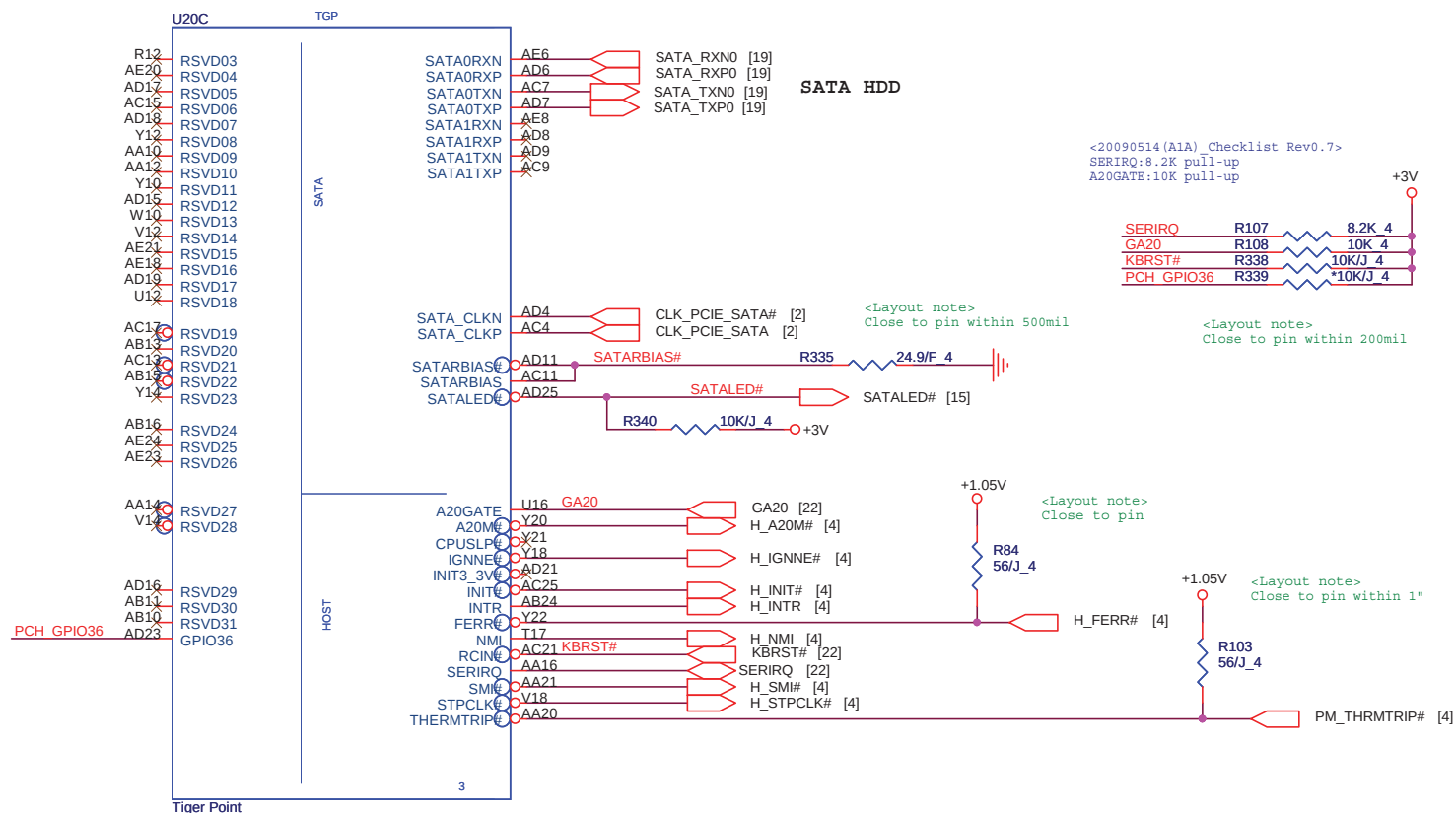






 Quanta Computer Inc. PROJECT : ZE6		
Size	Document Number	Rev 1B
Pineview GND		
Date: Friday, March 11, 2011	Sheet 7 of 35	

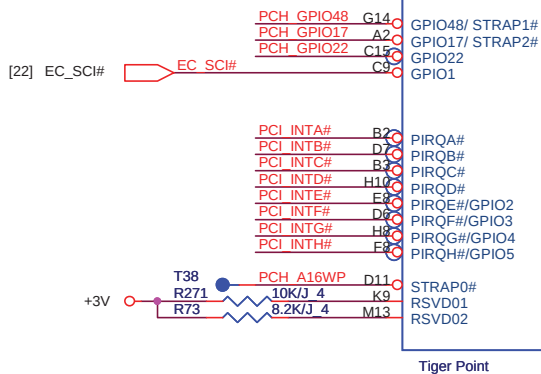
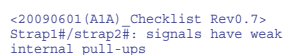
<http://laptop-motherboard-schematic.blogspot.com/>



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PROJECT : ZE6

Size	Document Number	Rev
		1B
Tiger Point Sata/Host		
Date:	Friday, March 11, 2011	Sheet 9 of 35



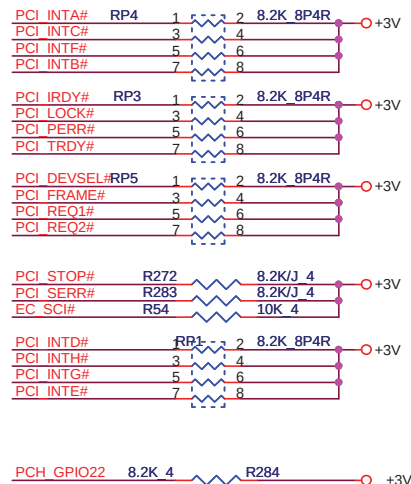
ICH Boot BIOS select

PCH_GPIO17 (INT PU)	PCH_GPIO48 (INT PU)	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	PCI (Default)



A16 SWAP Override strap

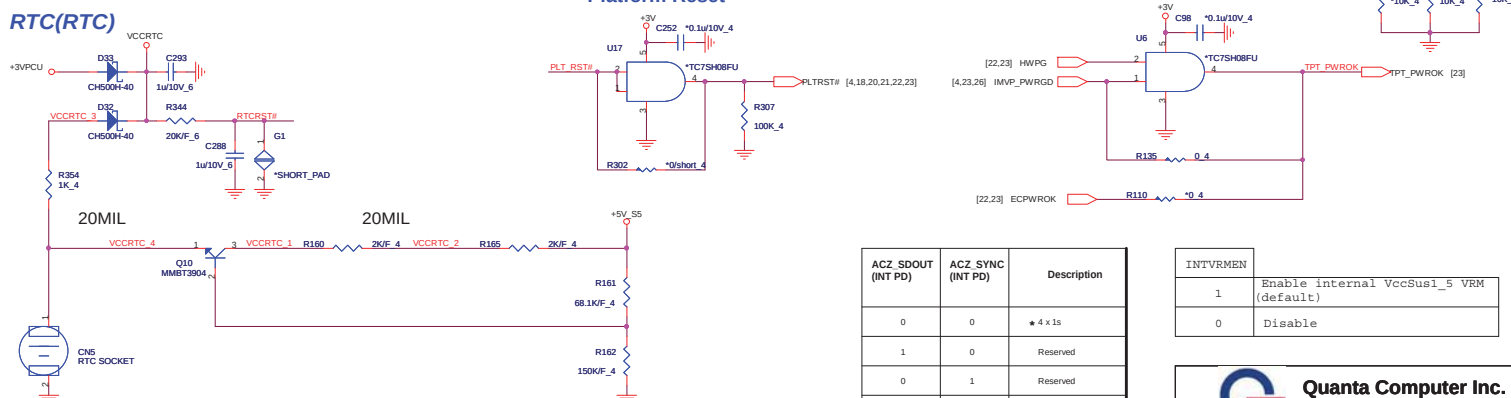
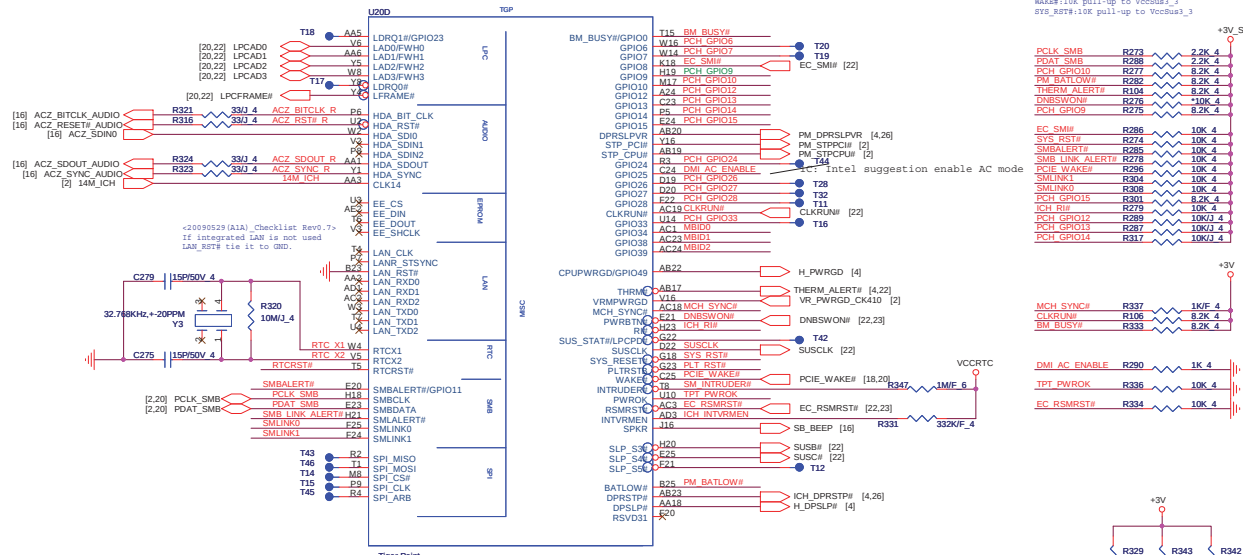
PCH_A16WP (INT PU)	Low = A16 swap override enabled High = Default
-----------------------	---



IRQ	Description
PIRQA	USB UHCI Controller #1, #4
PIRQB	AC'97 Codec; option for SMBUS
PIRQC	USB UH Controller #3; SATA/IDE Native Mode
PIRQD	USB UHCI Controller #2
PIRQE	Internal LAN; Option for SCI, TCO, HPET#0,1,2
PIRQF	Option for SCI, TCO, HPET#0,1,2
PIRQG	Option for SCI, TCO, HPET#0,1,2
PIRQH	USB EHCI Controller; Option for SCI, TCO, HPET#0,1,2

PCI_GNT#2	Internal PU Should not be PD
-----------	---------------------------------

 Quanta Computer Inc. PROJECT : ZE6				
Size	Document Number			Rev
	TigerPoint PCI(3/6)			1B
Date:	Friday, March 11, 2011	Sheet	10 of 35	



ACZ_SDOUT (INT PD)	ACZ_SYNC (INT PD)	Description
0	0	4 x 1s
1	0	Reserved
0	1	Reserved
1	1	1 x 4s(1 port/4 lanes)

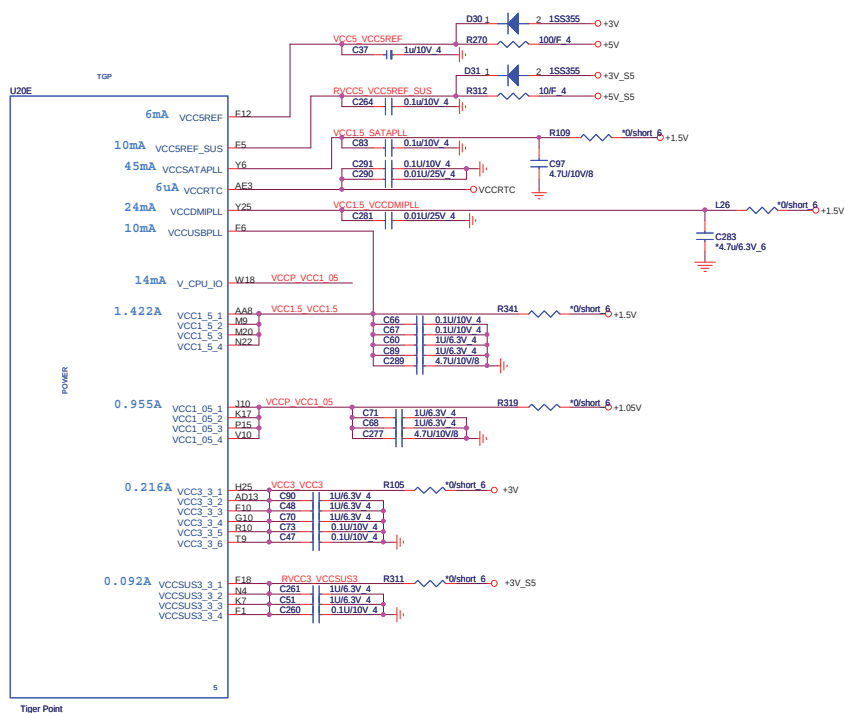
INTVRMEN	
1	Enable internal VccSus1_5 VRM (default)
0	Disable



PROJECT : ZE6

Size	Document Number	Rev
	TigerPoint GPIO	
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<Layout note>
Place 0402 caps close to ball
Place 0603/0805 caps close to ICH

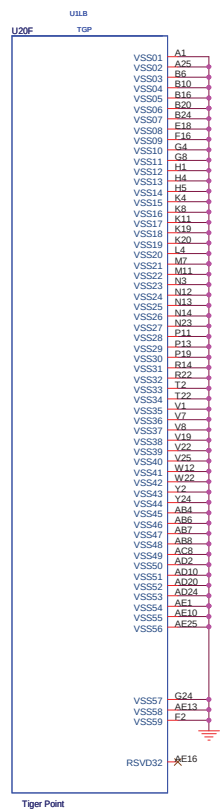


Tiger Point

1. Level 1 Environment-related Substances should NEVER be used.
2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

 Quanta Computer Inc. PROJECT : ZE6			
Size	Document Number		Rev
			1B
TigerPoint Power Date: Friday, March 11, 2011 Sheet 12 of 35			

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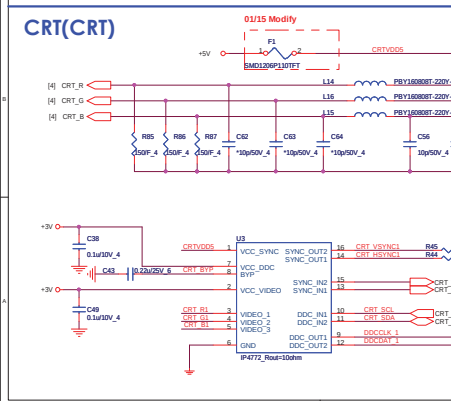


1. Level 1 Environment-related Substances Should NEVER be Used.
 2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

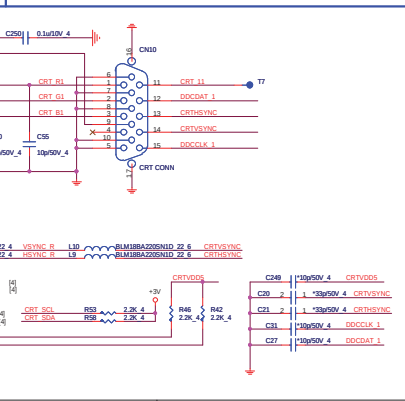
 Quanta Computer Inc. PROJECT : ZE6		Rev
		1B
TigerPoint GND		
Date:	Friday, March 11, 2011	Sheet 13 of 35

<http://laptop-motherboard-schematic.blogspot.com/>

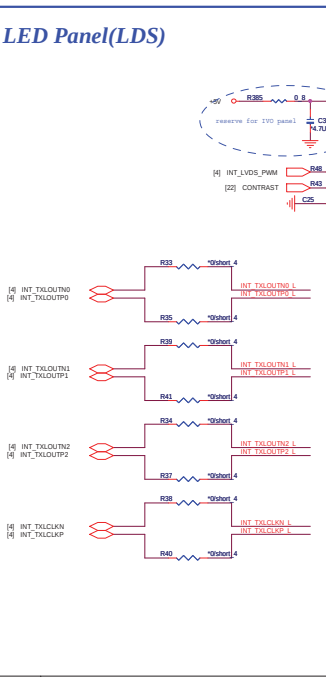
<20090724(R2A)>
Change R5 from CS41002.HB20 to



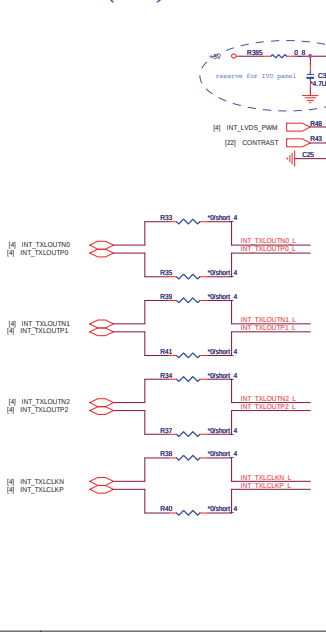
LED Panel POWER SWITCH(LDS)



CAMERA POWER(CCD)

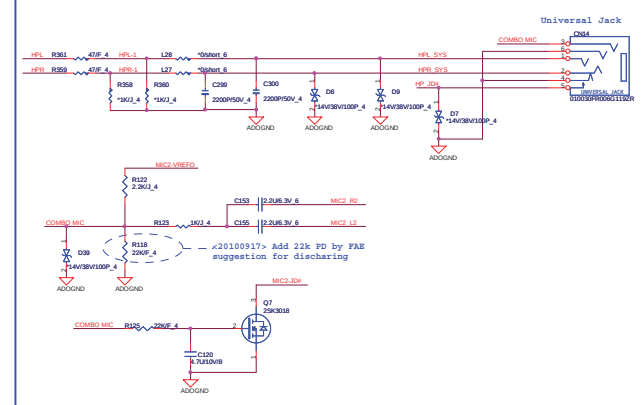


LED Panel(LDS)



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CRT/LVDS	
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HEADPHONE



The schematic diagram illustrates the power supply section of the ADXL345 evaluation board. It features a 5V regulator (U1) with an input from a 5V source and an output to the ADXL345 (U2). The regulator is bypassed with capacitors C36 and C37. The ADXL345 is bypassed with capacitor C38. The schematic includes labels for various components and their values, as well as a note about the normal open jack.

Components and values shown in the schematic:

- Resistors: R356 (0.000), R355 (4.700k), R354 (0.000), R353 (0.000), R352 (0.000), R351 (0.000), R350 (0.000), R349 (0.000), R348 (0.000), R347 (0.000), R346 (0.000), R345 (0.000), R344 (0.000), R343 (0.000), R342 (0.000), R341 (0.000), R340 (0.000), R339 (0.000), R338 (0.000), R337 (0.000), R336 (0.000), R335 (0.000), R334 (0.000), R333 (0.000), R332 (0.000), R331 (0.000), R330 (0.000), R329 (0.000), R328 (0.000), R327 (0.000), R326 (0.000), R325 (0.000), R324 (0.000), R323 (0.000), R322 (0.000), R321 (0.000), R320 (0.000), R319 (0.000), R318 (0.000), R317 (0.000), R316 (0.000), R315 (0.000), R314 (0.000), R313 (0.000), R312 (0.000), R311 (0.000), R310 (0.000), R309 (0.000), R308 (0.000), R307 (0.000), R306 (0.000), R305 (0.000), R304 (0.000), R303 (0.000), R302 (0.000), R301 (0.000), R300 (0.000), R299 (0.000), R298 (0.000), R297 (0.000), R296 (0.000), R295 (0.000), R294 (0.000), R293 (0.000), R292 (0.000), R291 (0.000), R290 (0.000), R289 (0.000), R288 (0.000), R287 (0.000), R286 (0.000), R285 (0.000), R284 (0.000), R283 (0.000), R282 (0.000), R281 (0.000), R280 (0.000), R279 (0.000), R278 (0.000), R277 (0.000), R276 (0.000), R275 (0.000), R274 (0.000), R273 (0.000), R272 (0.000), R271 (0.000), R270 (0.000), R269 (0.000), R268 (0.000), R267 (0.000), R266 (0.000), R265 (0.000), R264 (0.000), R263 (0.000), R262 (0.000), R261 (0.000), R260 (0.000), R259 (0.000), R258 (0.000), R257 (0.000), R256 (0.000), R255 (0.000), R254 (0.000), R253 (0.000), R252 (0.000), R251 (0.000), R250 (0.000), R249 (0.000), R248 (0.000), R247 (0.000), R246 (0.000), R245 (0.000), R244 (0.000), R243 (0.000), R242 (0.000), R241 (0.000), R240 (0.000), R239 (0.000), R238 (0.000), R237 (0.000), R236 (0.000), R235 (0.000), R234 (0.000), R233 (0.000), R232 (0.000), R231 (0.000), R230 (0.000), R229 (0.000), R228 (0.000), R227 (0.000), R226 (0.000), R225 (0.000), R224 (0.000), R223 (0.000), R222 (0.000), R221 (0.000), R220 (0.000), R219 (0.000), R218 (0.000), R217 (0.000), R216 (0.000), R215 (0.000), R214 (0.000), R213 (0.000), R212 (0.000), R211 (0.000), R210 (0.000), R209 (0.000), R208 (0.000), R207 (0.000), R206 (0.000), R205 (0.000), R204 (0.000), R203 (0.000), R202 (0.000), R201 (0.000), R200 (0.000), R199 (0.000), R198 (0.000), R197 (0.000), R196 (0.000), R195 (0.000), R194 (0.000), R193 (0.000), R192 (0.000), R191 (0.000), R190 (0.000), R189 (0.000), R188 (0.000), R187 (0.000), R186 (0.000), R185 (0.000), R184 (0.000), R183 (0.000), R182 (0.000), R181 (0.000), R180 (0.000), R179 (0.000), R178 (0.000), R177 (0.000), R176 (0.000), R175 (0.000), R174 (0.000), R173 (0.000), R172 (0.000), R171 (0.000), R170 (0.000), R169 (0.000), R168 (0.000), R167 (0.000), R166 (0.000), R165 (0.000), R164 (0.000), R163 (0.000), R162 (0.000), R161 (0.000), R160 (0.000), R159 (0.000), R158 (0.000), R157 (0.000), R156 (0.000), R155 (0.000), R154 (0.000), R153 (0.000), R152 (0.000), R151 (0.000), R150 (0.000), R149 (0.000), R148 (0.000), R147 (0.000), R146 (0.000), R145 (0.000), R144 (0.000), R143 (0.000), R142 (0.000), R141 (0.000), R140 (0.000), R139 (0.000), R138 (0.000), R137 (0.000), R136 (0.000), R135 (0.000), R134 (0.000), R133 (0.000), R132 (0.000), R131 (0.000), R130 (0.000), R129 (0.000), R128 (0.000), R127 (0.000), R126 (0.000), R125 (0.000), R124 (0.000), R123 (0.000), R122 (0.000), R121 (0.000), R120 (0.000), R119 (0.000), R118 (0.000), R117 (0.000), R116 (0.000), R115 (0.000), R114 (0.000), R113 (0.000), R112 (0.000), R111 (0.000), R110 (0.000), R109 (0.000), R108 (0.000), R107 (0.000), R106 (0.000), R105 (0.000), R104 (0.000), R103 (0.000), R102 (0.000), R101 (0.000), R100 (0.000), R99 (0.000), R98 (0.000), R97 (0.000), R96 (0.000), R95 (0.000), R94 (0.000), R93 (0.000), R92 (0.000), R91 (0.000), R90 (0.000), R89 (0.000), R88 (0.000), R87 (0.000), R86 (0.000), R85 (0.000), R84 (0.000), R83 (0.000), R82 (0.000), R81 (0.000), R80 (0.000), R79 (0.000), R78 (0.000), R77 (0.000), R76 (0.000), R75 (0.000), R74 (0.000), R73 (0.000), R72 (0.000), R71 (0.000), R70 (0.000), R69 (0.000), R68 (0.000), R67 (0.000), R66 (0.000), R65 (0.000), R64 (0.000), R63 (0.000), R62 (0.000), R61 (0.000), R60 (0.000), R59 (0.000), R58 (0.000), R57 (0.000), R56 (0.000), R55 (0.000), R54 (0.000), R53 (0.000), R52 (0.000), R51 (0.000), R50 (0.000), R49 (0.000), R48 (0.000), R47 (0.000), R46 (0.000), R45 (0.000), R44 (0.000), R43 (0.000), R42 (0.000), R41 (0.000), R40 (0.000), R39 (0.000), R38 (0.000), R37 (0.000), R36 (0.000), R35 (0.000), R34 (0.000), R33 (0.000), R32 (0.000), R31 (0.000), R30 (0.000), R29 (0.000), R28 (0.000), R27 (0.000), R26 (0.000), R25 (0.000), R24 (0.000), R23 (0.000), R22 (0.000), R21 (0.000), R20 (0.000), R19 (0.000), R18 (0.000), R17 (0.000), R16 (0.000), R15 (0.000), R14 (0.000), R13 (0.000), R12 (0.000), R11 (0.000), R10 (0.000), R9 (0.000), R8 (0.000), R7 (0.000), R6 (0.000), R5 (0.000), R4 (0.000), R3 (0.000), R2 (0.000), R1 (0.000).
- Capacitors: C36 (0.000), C35 (0.000), C34 (0.000), C33 (0.000

[illegible][illegible]

Demodulation Filter L17 Place close to Codec

The diagram shows a circuit for a demodulation filter. It consists of a 5V supply connected to a 1.5k resistor, which is in series with a 0.8uF capacitor. The other side of the capacitor is connected to a 5V supply. The circuit is labeled 'DIGITAL' and 'ANALOG'.

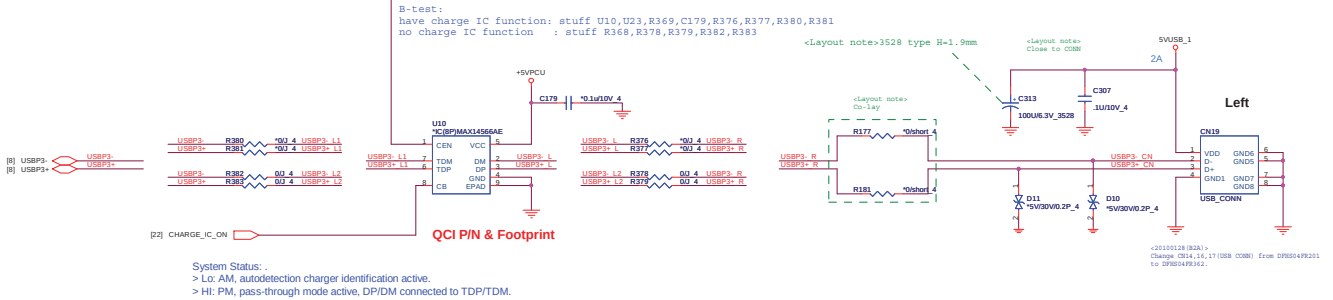


Enable USB Charger				
	AC	DC (battery > Setting%)	DC (battery < Setting%)	S4/S5
Charge Feature	O	O	Δ	X
Wake Feature	X	X	X	

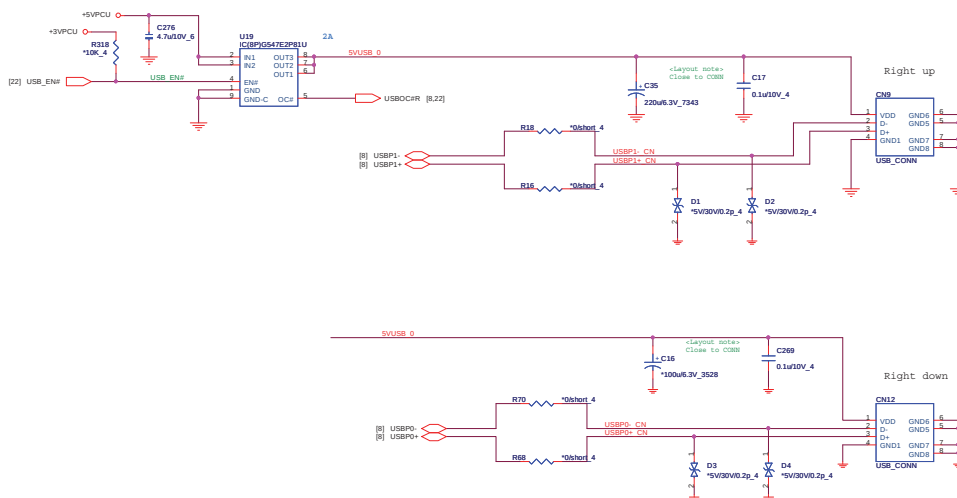
Disable USB Charger				
	AC	DC (battery > Setting%)	DC (battery < Setting%)	S4/S5
Charge Feature	Δ	Δ	X	X
Wake Feature	O	O	O	

Note 1 Devices can be charged or not should same as other USB ports

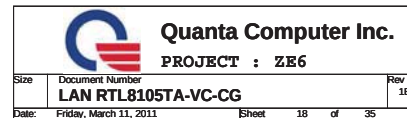
Note.1	Devices can be charged
--------	------------------------

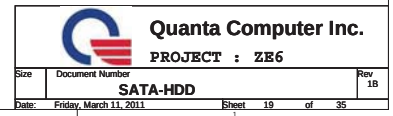


USB(USB)



 Quanta Computer Inc. PROJECT : ZE6		
Size	Document Number	Rev
	USB on Board/LED/SW/HOLE	1B
Date:	Friday, March 11, 2011	Sheet 17 of 35

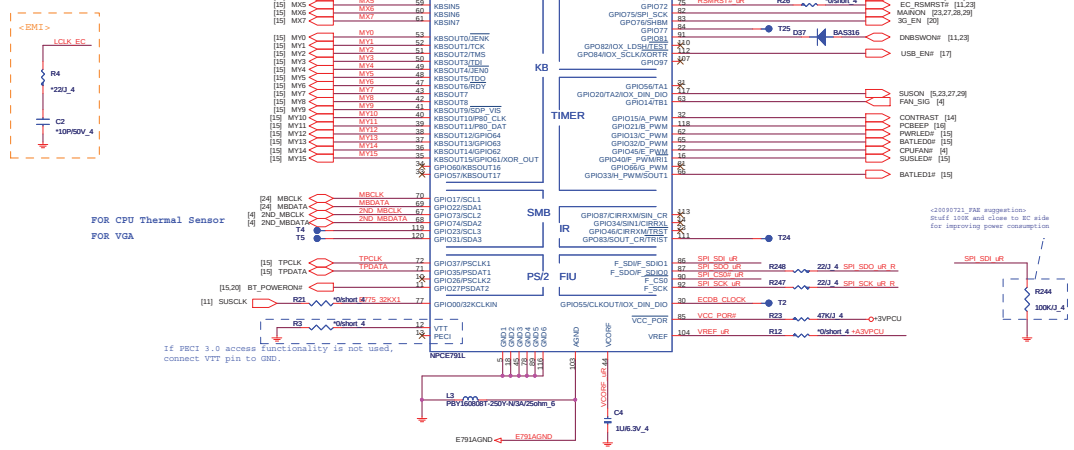




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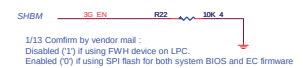




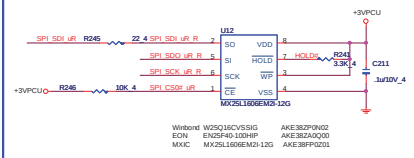


SM Bus 1	Battery
SM Bus 2	CPU thermal sensor

SHBM=0: Enable shared memory with host BIOS

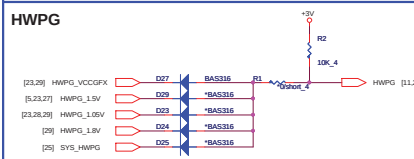


1/13 Confirm by vendor mail :
Disabled ('1') if using FWH device on LPC.
Enabled ('0') if using SPI flash for both system BIOS and EC firmware



Winbond	W25Q16CVSSIG	AKE38ZP0N02
EON	EN25F40-100HIP	AKE38ZA0Q00
MXIC	MX25L1606EM2I-12G	AKE38FP0Z01

1/13 Confirm by vendor mail :
If the Southbridge enables 'Long Wait Abort' by default, the flash device should be 50MHz (or faster)

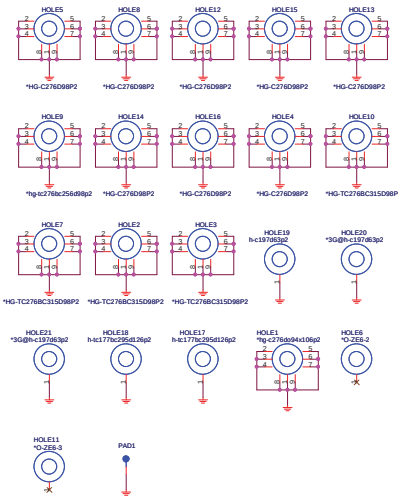


Timing diagram for HWP_VCCGFX [1]. The diagram shows five input signals: [23,29] HWP_VCCGFX, [5,23,27] HWP_G_1SV, [23,28,29] HWP_G_105V, [29] HWP_G_18V, and [29] SYS_HWP. These signals are connected to a multiplexer with inputs D22, D29, D23, D24, and D25. The output of the multiplexer is connected to a block labeled 'HWP_VCCGFX'.

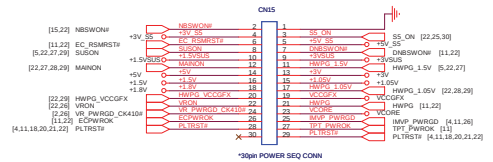


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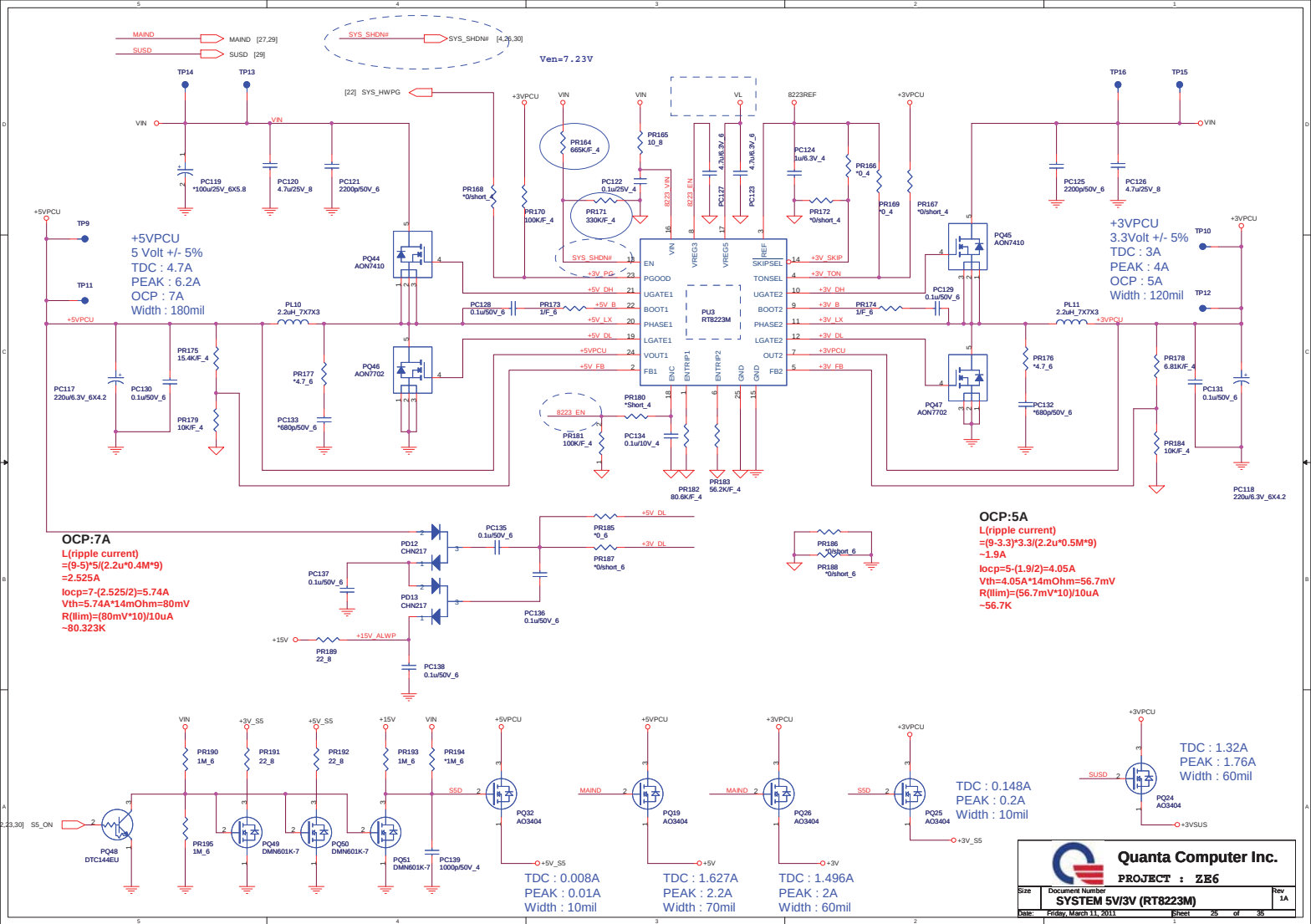
Size		Document Number		Rev	
		WPCE781 & FLASH		1	
Date	Order	March 11, 2011	Sheet	22	of 25



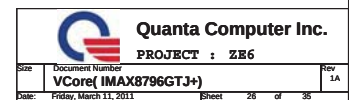
Power Sequence Connector 30pin (CPU)



1	GND	11	HWPG_1.5V	21	HWPG
2	NBSMON#	12	MAINON	22	VRON
3	SS_ON	13	+3V	23	VCCORE
4	+3V_SS	14	+5V	24	VR_PWRGD_CK410
5	+5V_SS	15	+1.05V	25	LMVF_PWRGD
6	EC_RSTNRST#	16	+1.5V	26	BCFWRK
7	DMSBMON#	17	HWPG_1.05V	27	TPT_PWRK
8	SUSON	18	+3.0V	28	H_PWRGD
9	+3VSUS	19	VCC0FX	29	FLTRST#
10	+1.5VSUS	20	HWPG_VCC0FX	30	RESERVE

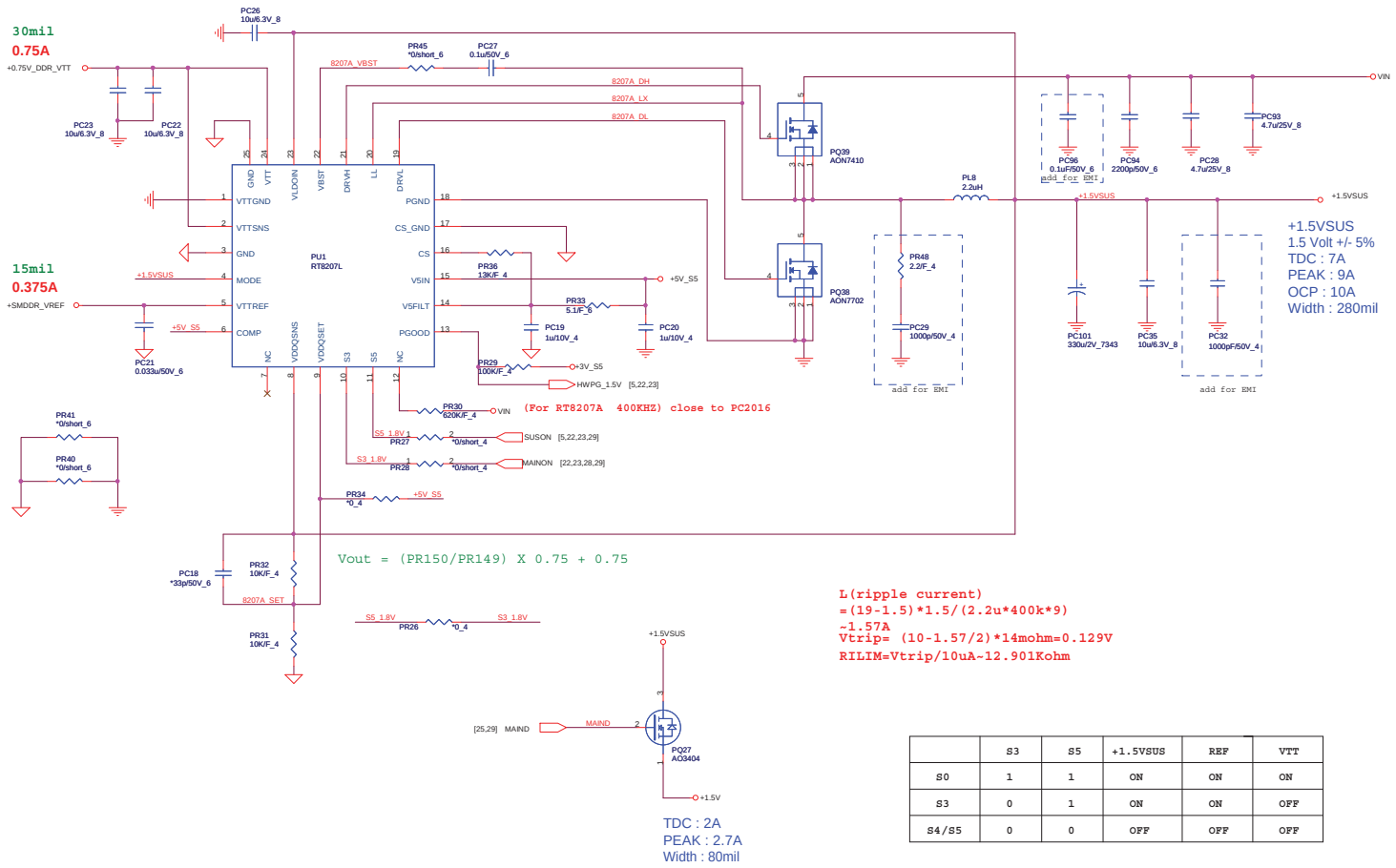


<http://laptop-motherboard-schematic.blogspot.com/>



<http://laptop-motherboard-schematic.blogspot.com/>

[PWM]

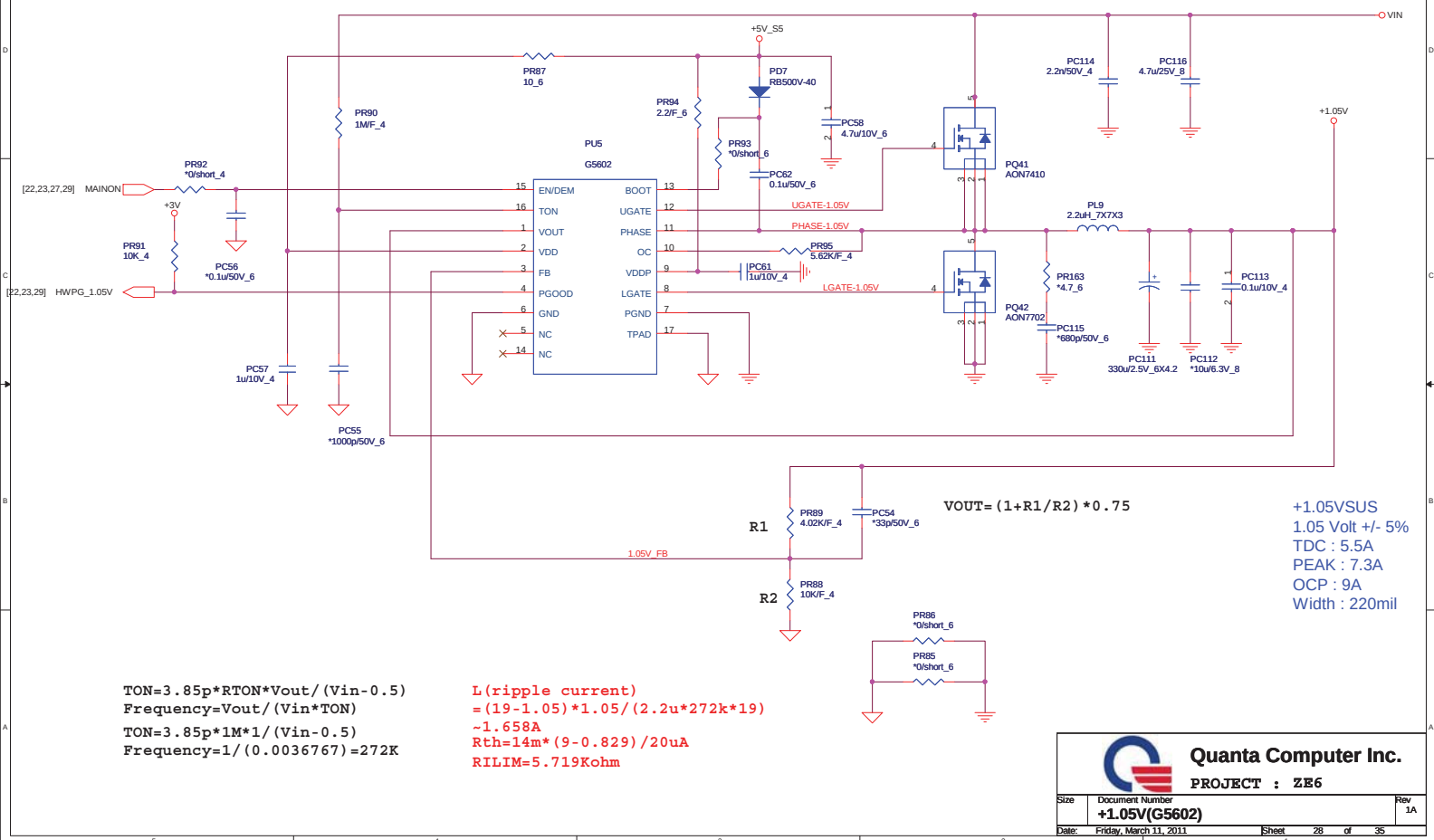


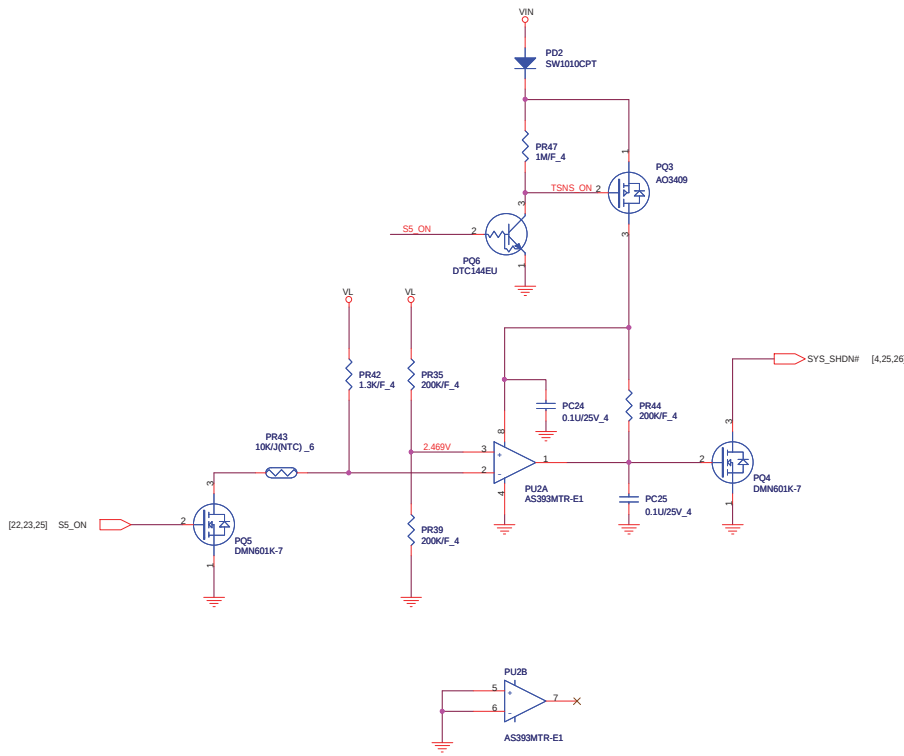
Quanta Computer Inc.
PROJECT : ZE6

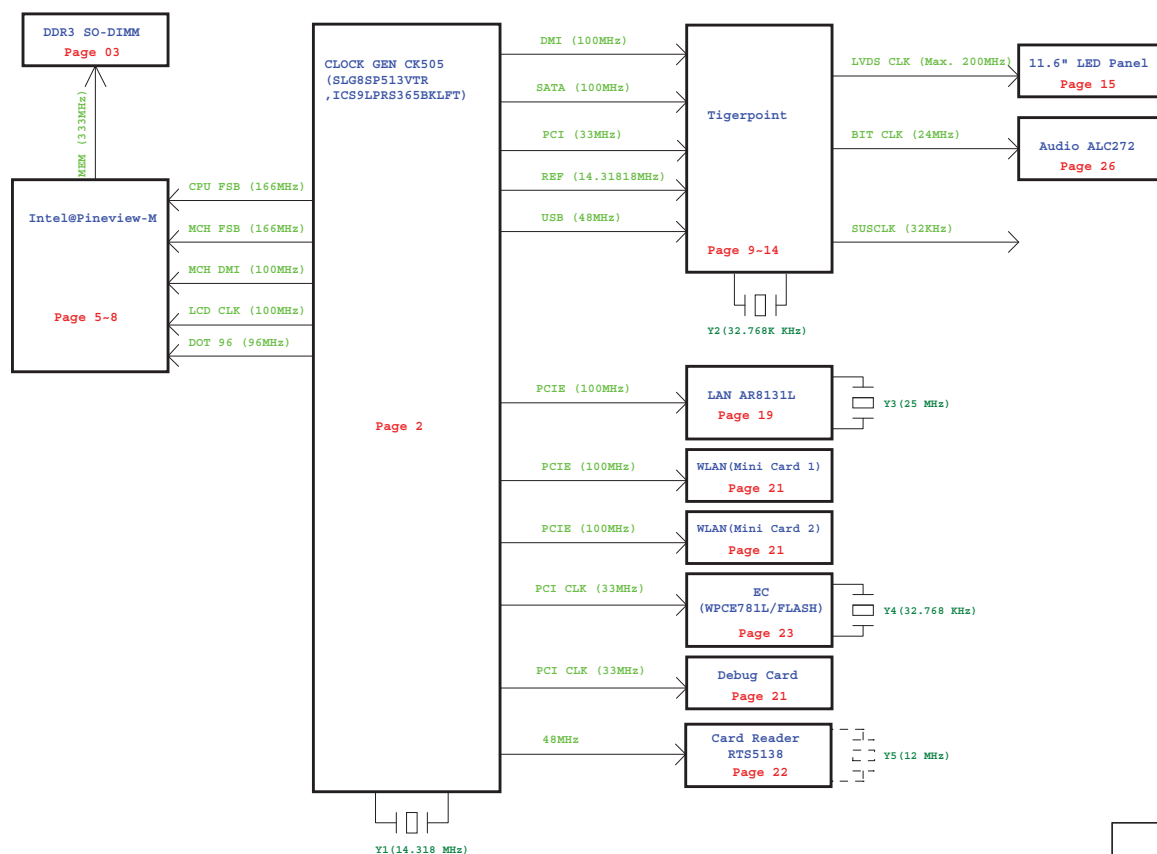
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DDR 1.5V(TPS51116)

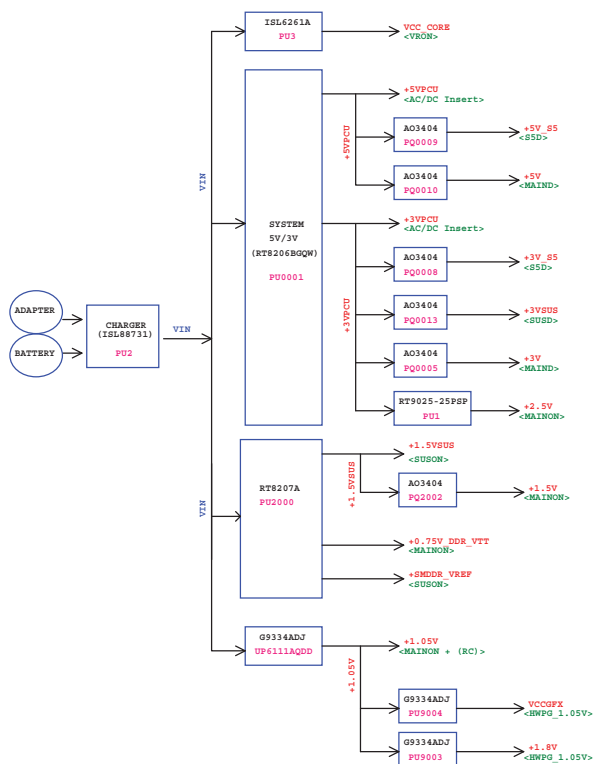
Date: Friday, March 11, 2011 Sheet 27 of 35

<http://laptop-motherboard-schematic.blogspot.com/>

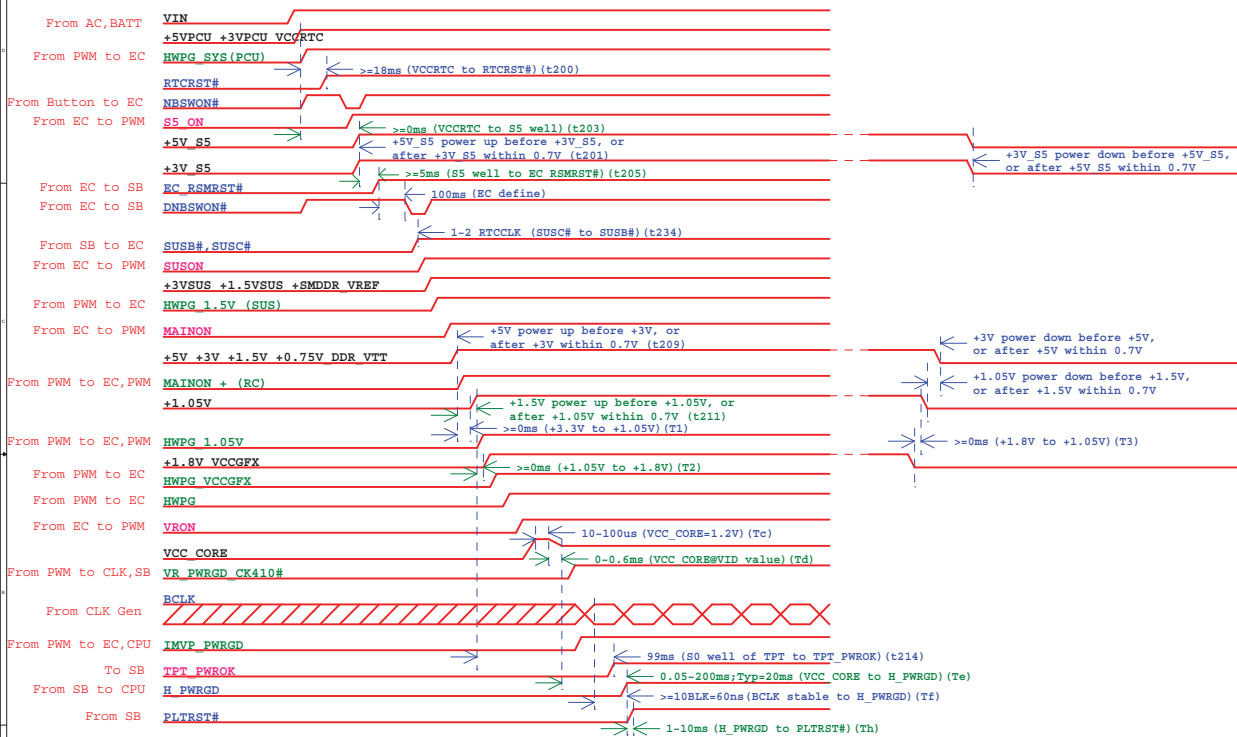








POWER	Distribution
VIN	LCD Backlight
VCC_CORE	CPU
+5V9CPU	USB Connector
+5V_S5	RTC, TPT
+5V	TPT, CRT, TouchPad, Codec, SATA, FAN, HDMI
+3V9CPU	RTC, Hall Sensor, Light Sensor, EC, BIOS
+3V_S5	TPT, LAN, LAN EEPROM, RJ45 LED
+3VSUS	3G
+3V	CLK_GEN, CPU, TPT, LCD, CCD, DMIC, BT, Codec, WLAN/Wimax, Card reader, EC, DDR, HDMI
+1.5VSUS	DDR
+1.8V	CPU, HDMI
+1.5V	CPU, TPT
+0.75V_DDR_VTT	DDR
+SMDR_VREF	CPU, DDR
+1.05V	CLK_GEN, CPU, TPT
VCCGFX	CPU
+2.5V	HDMI



*Note: EC will sampling SUSB# & SUSC# every 5ms.

ICH SMBUS Table

	CLK GEN	RAM	Mini Card (WLAN)	Mini Card (3G)
(SMB_DATA)/(SMB_CLK) (+3V_S5)	V	V	V	V
Power Plane	+3V	+3V	+3V	+3V_SUS
MOS CKT (Level shift)	Stuff	Stuff	*Reserve	Stuff

*Reserve: There is not SMBUS function in AVL

EC SMBUS Table

	Battery	CPU thermal Sensor	
EC781 SDA1 / SCL1 (+3VPCU)	V		
EC781 SDA2 / SCL2 (+3V)		V	
EC781 SDA3 / SCL3 (+3VPCU)			
Power Plane	+3VPCU	+3V	
MOS CKT (Level shift)	X	X	



