

5V / 3.3V / 12V
Page : 35

1.8V / 0.9V
Page : 36

1.5V / 1.05V / 1.8V
Page : 37

CPU CORE
Page : 34

+1.2V
Page : 38

BATTERY CHARGER
Page : 39

BATTERY SELECT
Page : 40

5VPCU
3V_ALWAYS
+12V
+5V
3V_S5
3VSUS
5VSUS
2.5VSUS
+2.5V
+1.8V
MVREF_DM
SMDDR_VTERM
1.5V_S5
+1.5V
AGP_VCC (+1.5V)
1.2VCCT
VTT
VCC_CORE
VGA_CORE
2.5V_VGA

**CLOCK GEN
ICS
ICS954217**
Page : 2

**Centrino
DOTHAN
CELEROM-M**
INTEL Mobile 479 CPU Page : 3 , 4

CRANE3 (ZL7)

ED@ INT. VGA WITH DOCK
ID@ INT. VGA WITH DOCK
ND@ W/O DOCKING要打

BOM MARK
E@ EXT VGA 要打
I@ INTVGA 要打
SA@ SATA 要打
F@ FIXED ODD要打
SW@ SWAPPABLE ODD 要打
3@ 3in1 要打
N@ NEW CARD 要打
4@ 4401 要打
5@ 5788M 要打
D@ DOCKING 要打

DDR2-SODIMM1
Page:9~10

DDR2-SODIMM2
Page:9~10

**ALVISO
1257 BGA**
Page : 5 ~ 8

**ATI
M26P/M24P
64M /
128M**
Page : 11 ~ 14

**SWITCH
CIRCUIT**

CRT
Page:17

LVDS
Page:16

TV-OUT
Page:16

**DVI
CH7307**
Page:15

DOCKING/DVI
Page: 33

SATA - HDD
Page:21

IDE - HDD
Page:21

IDE-ODD
Page:21

MEDIA BAY
Page:21

**ICH6-M
609 BGA**
Page : 18 ~ 20

NEW CARD
Page : 32

**TI
PCMCIA+1394
+3 IN 1
PCI7411**
Page : 23

3 IN 1
Page: 24

PCMCIA
Page: 24

1394
Page: 23

**AUDIO CODEC
CONEXANT
20468-31**
Page:27

**AMP
MAX9755**
Page:28

**MODEM
CONEXANT
20493-21**
Page:27

**NS
KBC(97551)**
Page : 29

**NS
SIO (87383)**
Page : 31

**MINI-PCI
Wireless LAN
Modem/LAN**
Page : 22

TV-TUNER
Page : 22

**BROADCOM
10/100/1G LAN
4401 / 5705M**
Page:25

**BOTHHAND
TRANSFORMER**
Page:26

RJ45
Page:26

MIC IN
Page:27

LINE IN
Page:27

SPEKER
Page:28

LINE OUT
Page:28

RJ11
Page:27

**DOCKING
PS2**
Page:33

Touchpad
Page:30

Keyboard
Page:30

IrDA
Page:31

**DOCKING
Print Port**
Page:33

**DOCKING
COM Port**
Page:33

CIR
Page:33

**SYSTEM 3
USB PORT**
Page : 22
USB2, 3, 5

**DOCKING 2
USB PORT**
Page : 22
USB0, 1

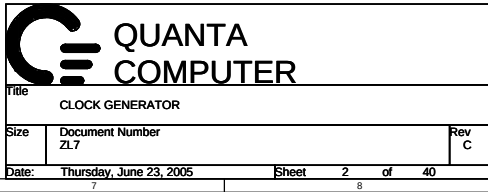
MINI-USB
Page: 22
USB4

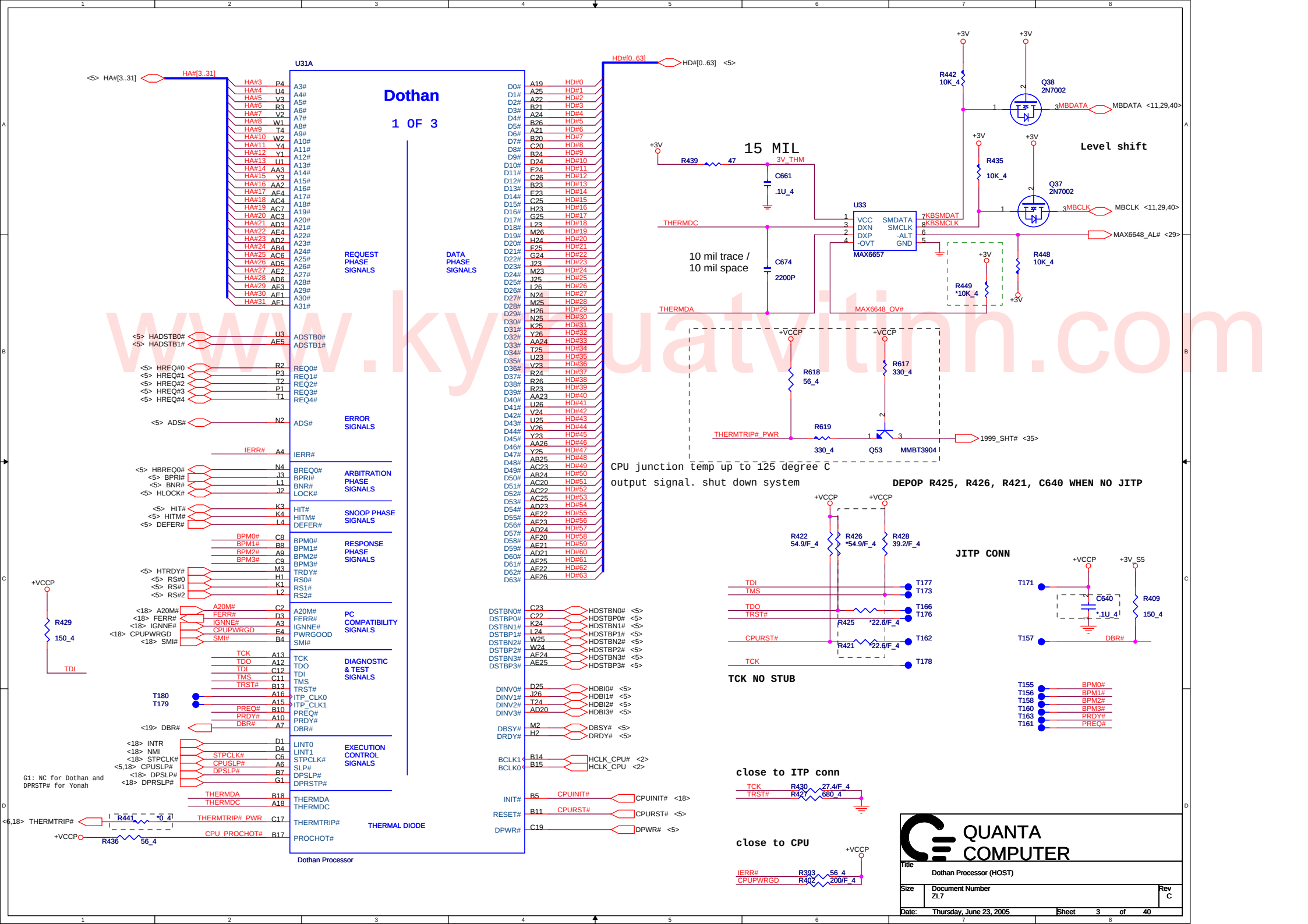
PCI ROUTING TABLE IDSEL INTERRUPT DEVICE

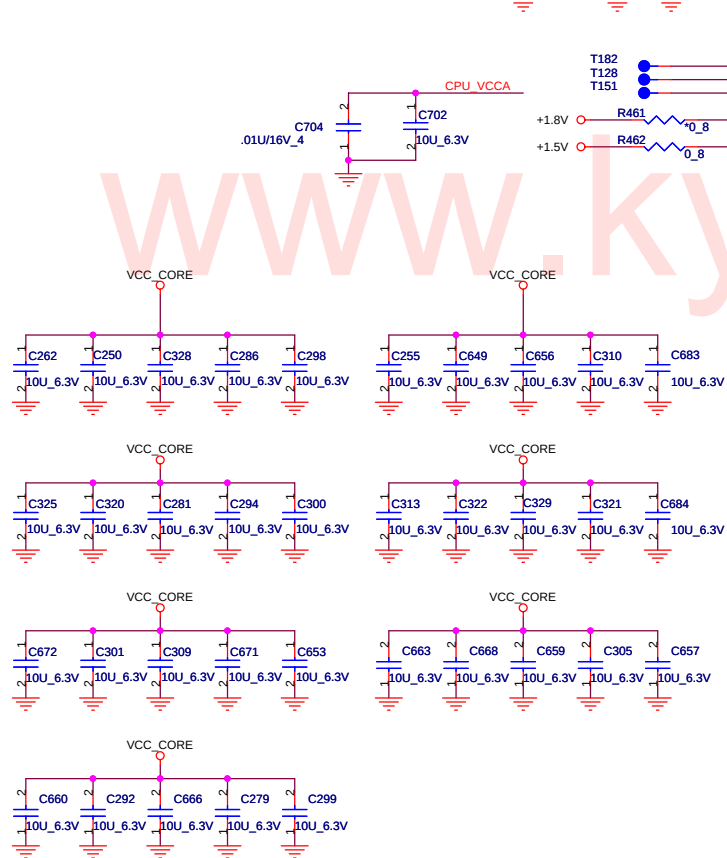
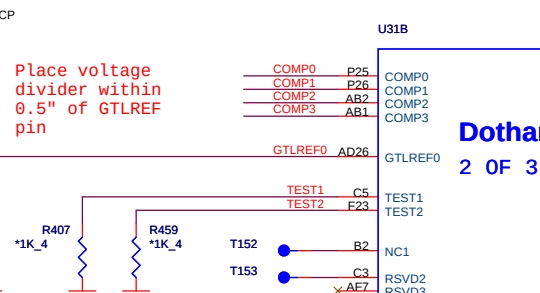
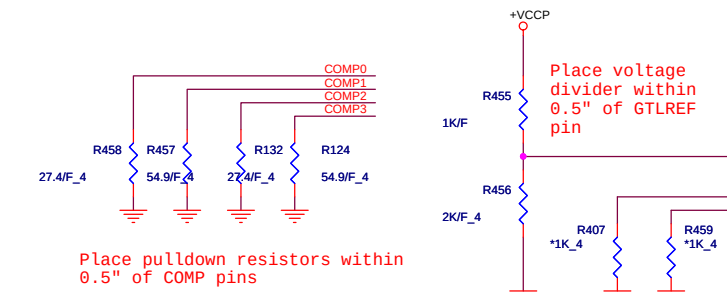
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REQ2# / GNT2#	AD19	INTB# , INTD#	MINI-PCI
REQ1# / GNT1#	AD17	INTC# , INTD# , INTA#	TI 7411
REQ3# / GNT3#	AD18	INTB# , INTD#	MINI-PCI(TV Tuner)

Điện Đàn
KyThuatViTinh.Com

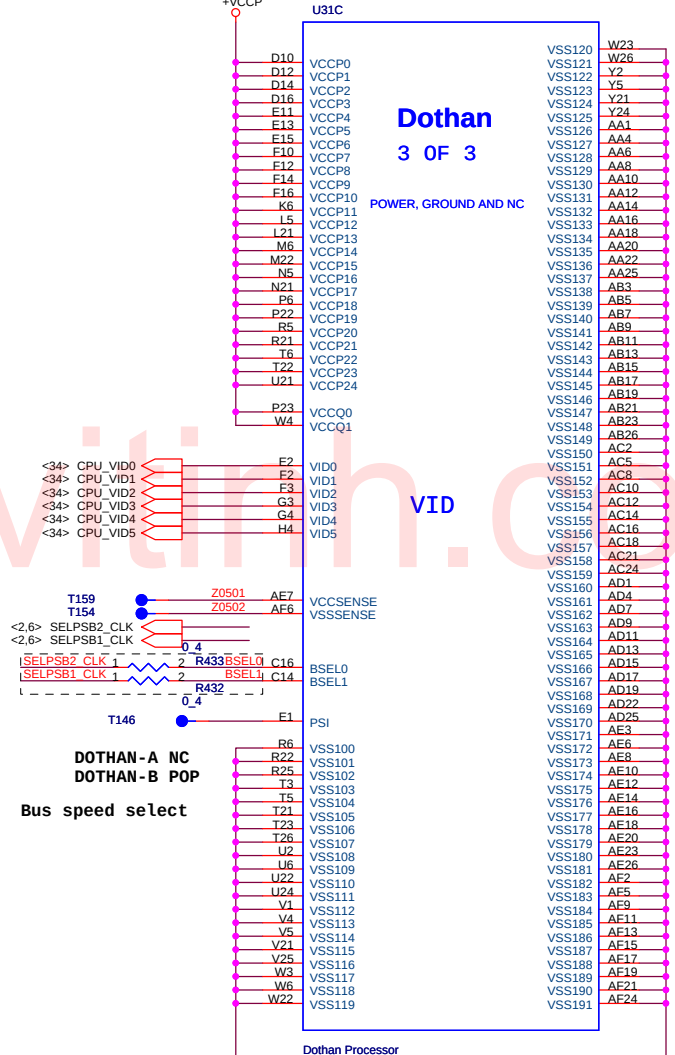
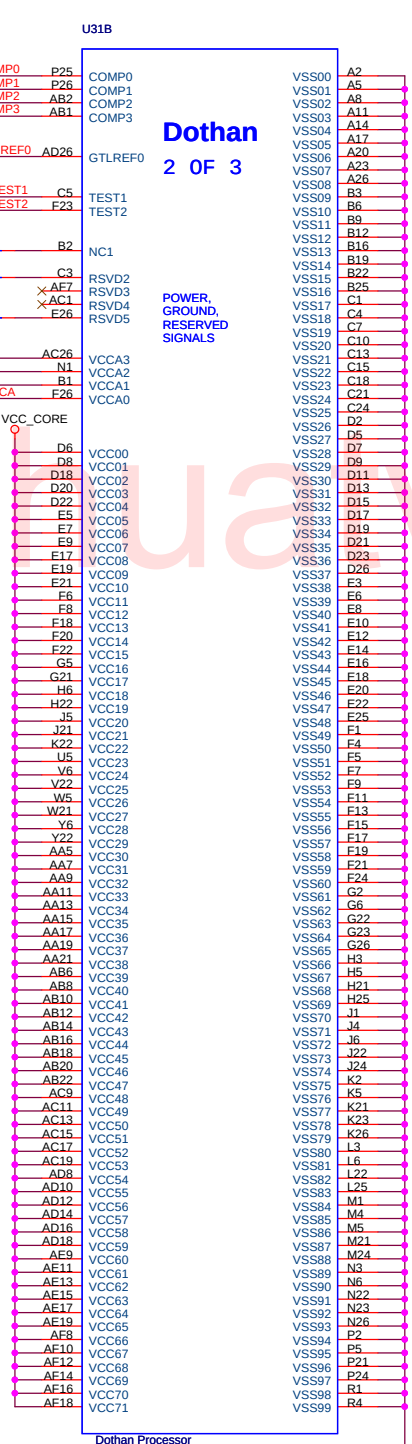
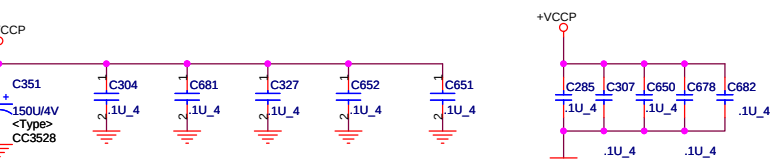
Digitally signed by dd
ECT : ZL7
DN: cn=dd, o=dd, ou=dd
a Computer Inc.
email=dddd@yahoo.com
C=US
Date: 2009.11.29 07:48:16
+07'00'







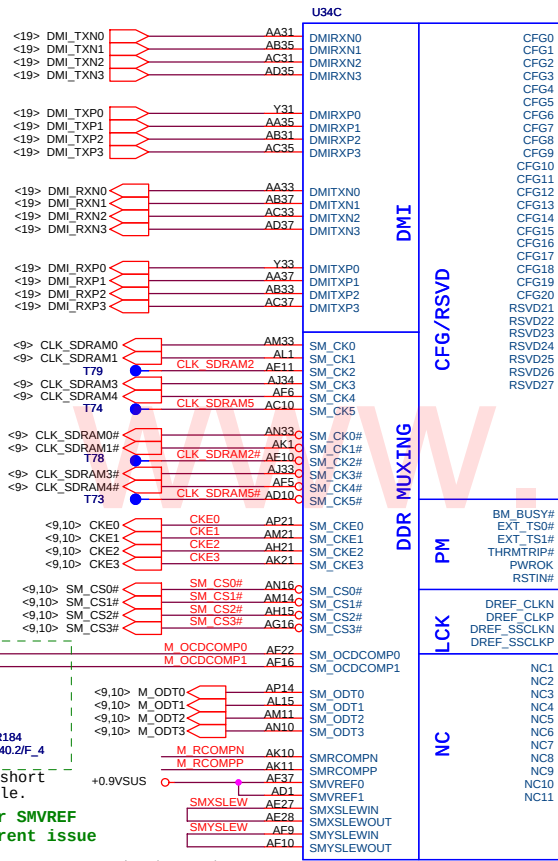
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ESR = 15m ohm/5 // 5m ohm/25 // 5m ohm/15



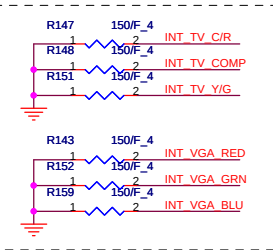
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H23	VSS137	Y37
AL22	VSS138	V37
AM22	VSS139	T37
J22	VSS140	P37
E22	VSS141	M37
D22	VSS142	K37
A22	VSS143	H37
AN21	VSS144	E37
AF21	VSS145	AN36
F21	VSS146	AL36
C21	VSS147	A36
AK20	VSS148	AE36
V20	VSS149	AC36
G20	VSS150	AD36
E20	VSS151	AC36
D20	VSS152	AB36
AD0	VSS153	AA36
AN19	VSS154	C36
AG19	VSS155	AE35
W19	VSS156	Y35
T19	VSS157	W35
J19	VSS158	U35
H19	VSS159	T35
C19	VSS160	R35
AL18	VSS161	P35
U18	VSS162	N35
A18	VSS163	M35
B18	VSS164	L35
AN17	VSS165	K35
AJ17	VSS166	J35
G17	VSS167	H35
G17	VSS168	G35
AL16	VSS169	F35
K16	VSS170	E35
D16	VSS171	D35
A16	VSS172	C35
K15	VSS173	B35
C15	VSS174	AN34
AN14	VSS175	AH34
AL14	VSS176	AD34
AG14	VSS177	AC34
J14	VSS178	AB34
B14	VSS179	AA34
A14	VSS180	PA34
Y13	VSS181	AL33
U13	VSS182	AE33
D13	VSS183	AC33
A13	VSS184	AD33
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AG12	VSS186	AB33
J12	VSS187	AA33
B12	VSS188	U33
C12	VSS189	W33
AN11	VSS190	T33
AL11	VSS191	R33
AG11	VSS192	P33
Y11	VSS193	N33
U11	VSS194	L33
A11	VSS195	K33
AN10	VSS196	J33
AG10	VSS197	H33
J10	VSS198	G33
B10	VSS199	E33
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AG08	VSS207	C32
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AG07	VSS212	R32
J07	VSS213	P32
B07	VSS214	N32
C07	VSS215	L32
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AG05	VSS222	AN31
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J02	VSS238	G31
B02	VSS239	E31
C02	VSS240	D31
AN01	VSS241	AN30
AG01	VSS242	AJ30
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AN00	VSS246	AA30
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AG97	VSS262	AN29
J97	VSS263	AJ29
B97	VSS264	AD29
C97	VSS265	AC29
AN96	VSS266	AB29
AG96	VSS267	AA29
J96	VSS268	C29
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B26		H27
C26		G27
AN25		E27
AG25		D27
J25		C27
B25		B27
C25		A27
AN24		Y27
AG24		W27

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CFG[0:2]=101 FOR FSB 400

CFG3 R168 1K_4
FOR DDR533



CFG[17:3] have internal pullup resistors.
CFG[19:18] have internal pulldown resistors



CFG5 Low=DMIx2
High=DMIx4
CFG6 Low=DDR2
High=DDR
CFG9 Low=REVERSE LANE
High=NORMAL
CFG11 FOR CPU533

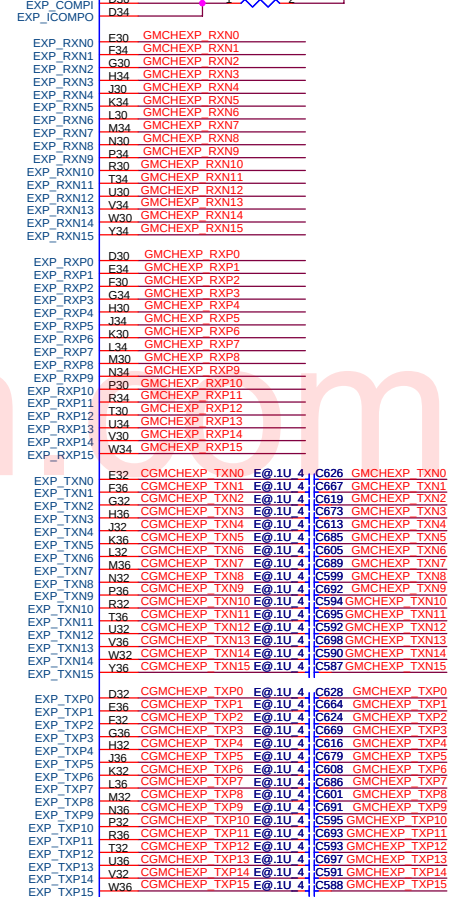
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<11> GMCHEXP_TXN[0..15] GMCHEXP_TXN[0..15]
<11,15> GMCHEXP_RXP[0..15] GMCHEXP_RXP[0..15]
<11,15> GMCHEXP_RXN[0..15] GMCHEXP_RXN[0..15]

TV

VGA

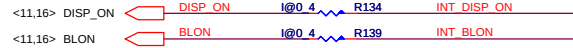
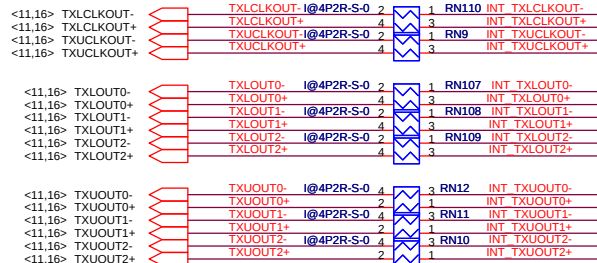
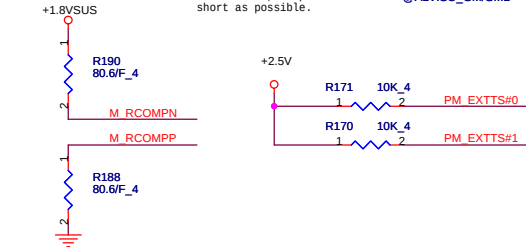
PCI-EXPRESS GRAPHICS

LVDS

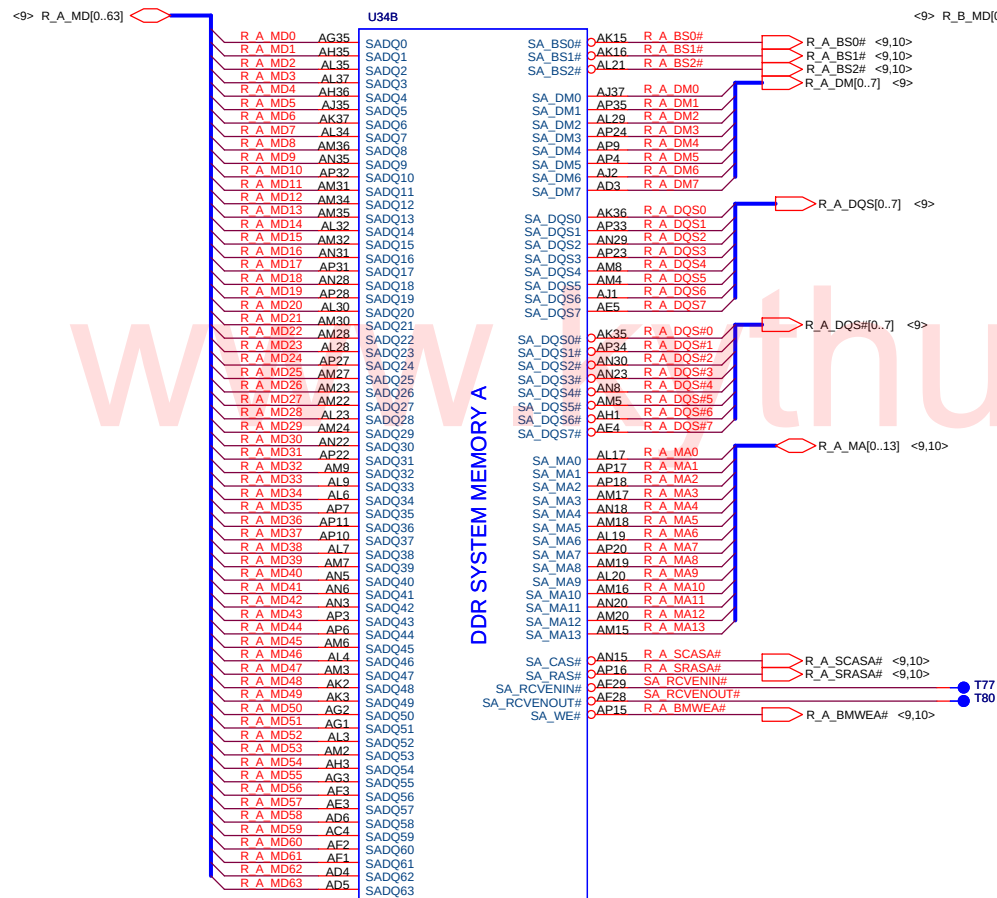


Route as short as possible.
Depop for SMVREF over current issue

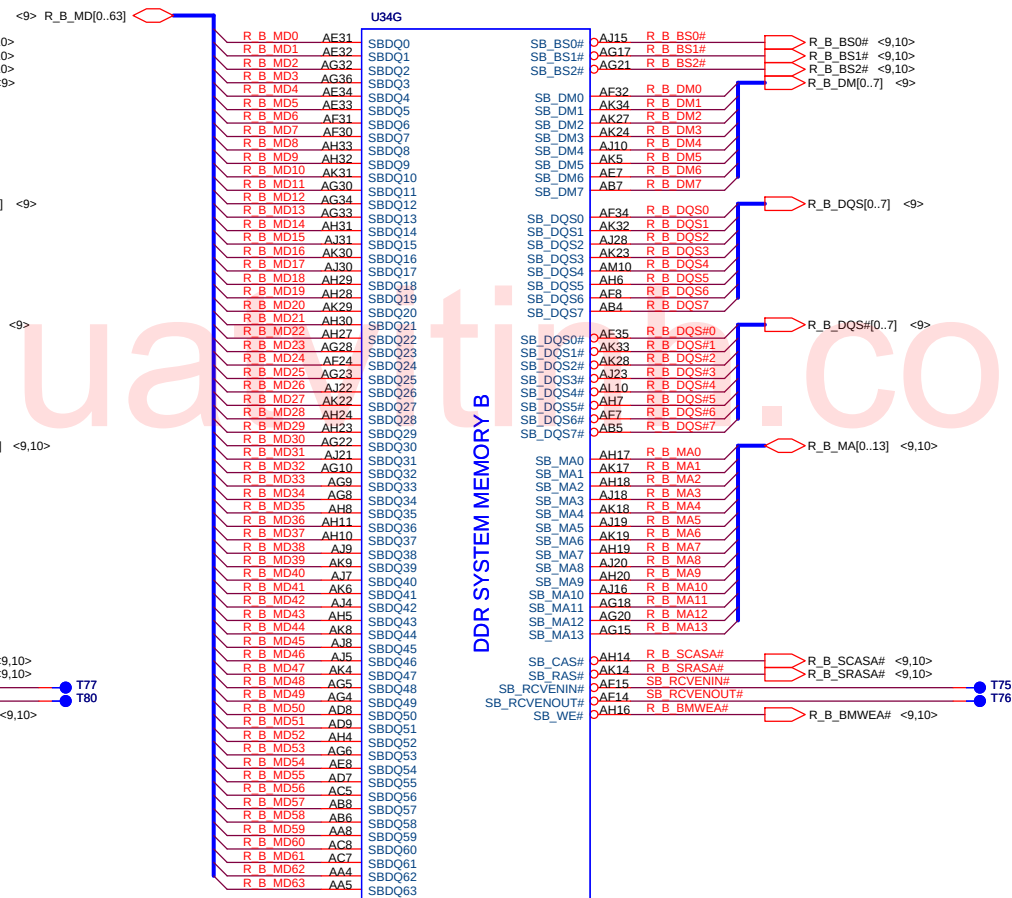
It's point to point, 55ohm trace, keep as short as possible.



Title		Alviso (VGA,DMI)	
Sheet		ZL7	
Date:		Thursday, June 23, 2005	
Sheet		6 of 40	

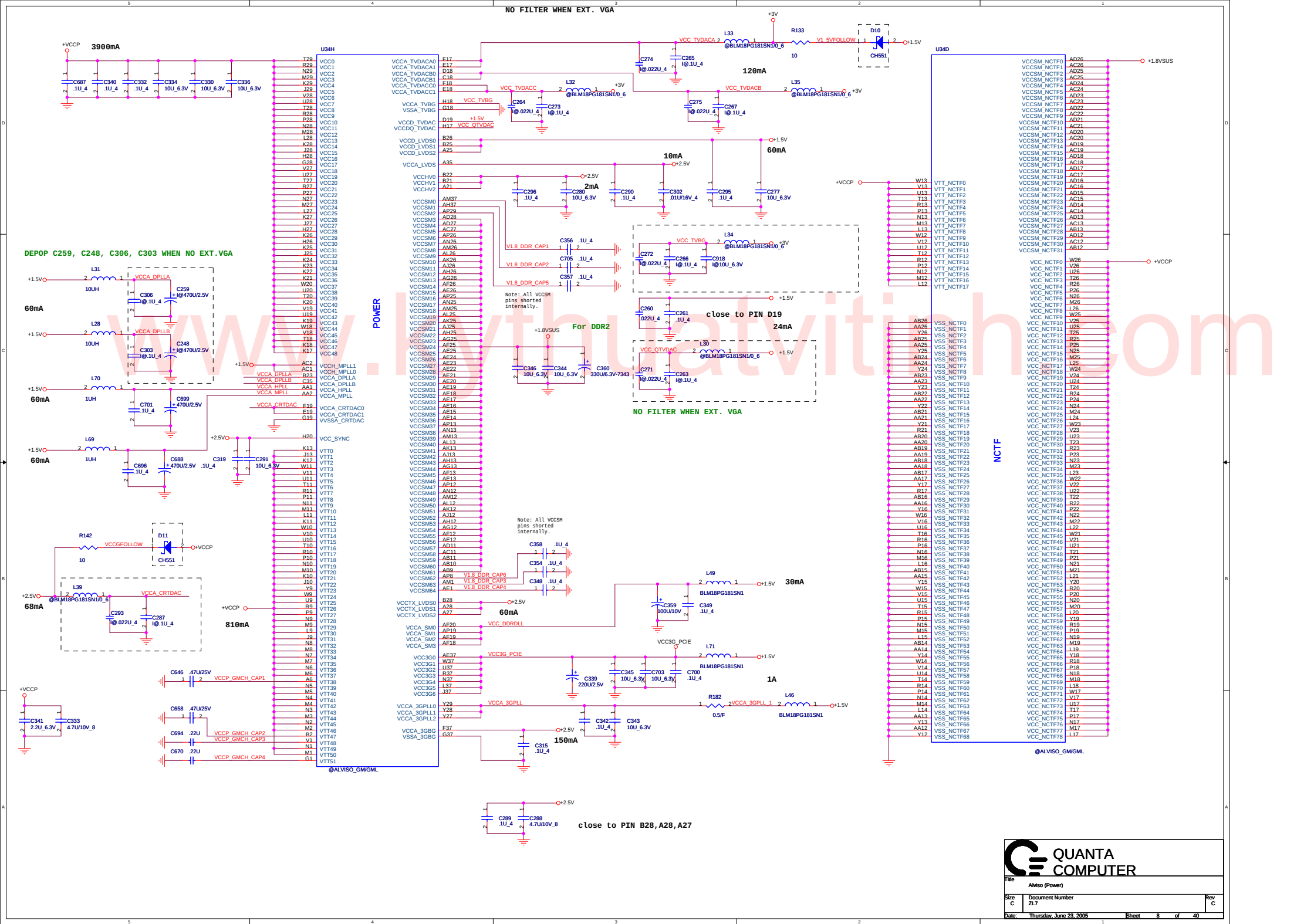


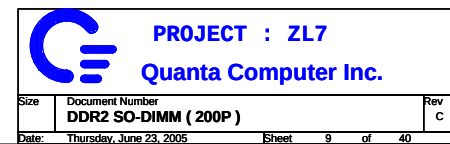
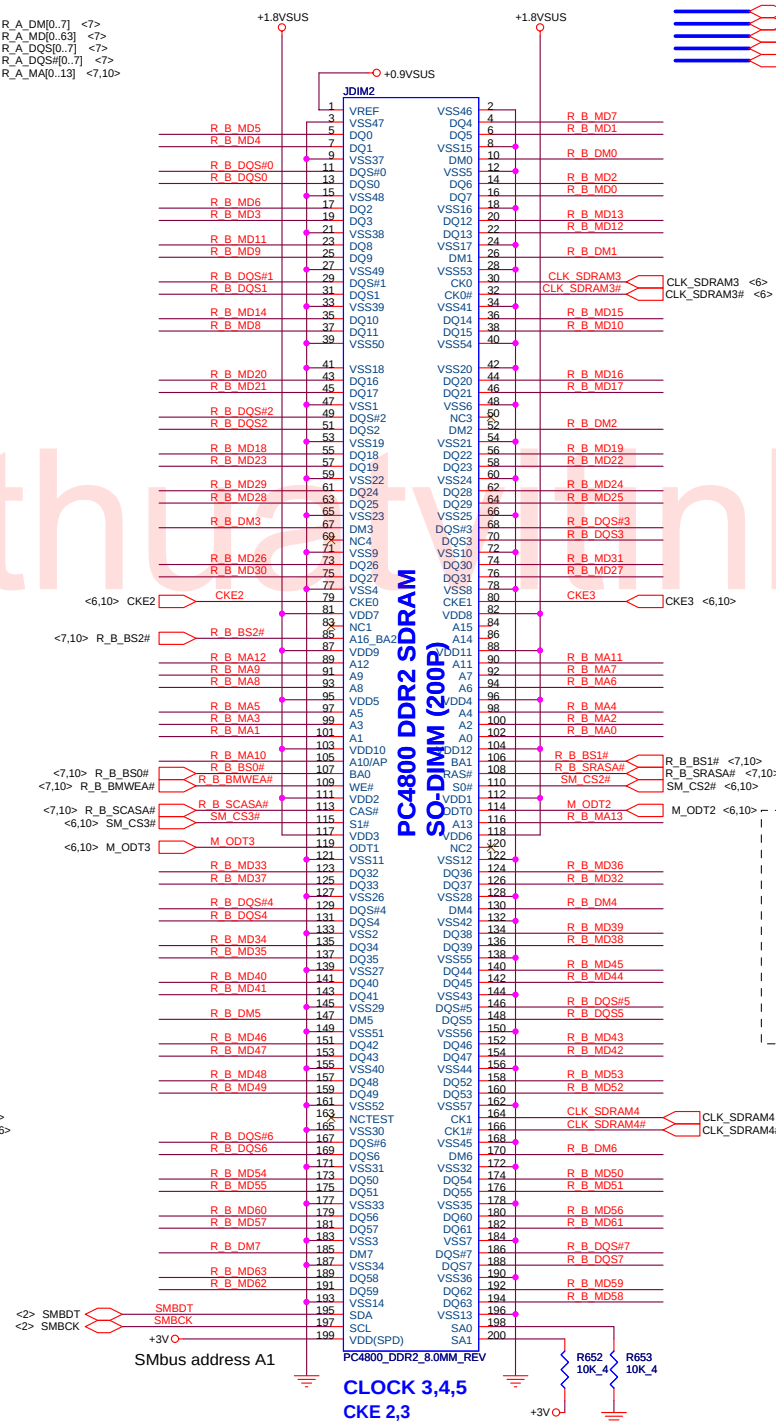
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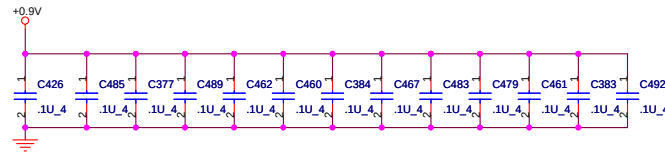


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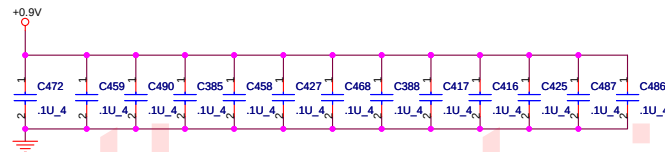




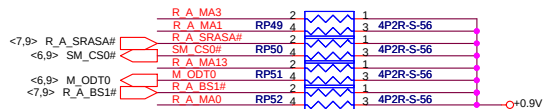
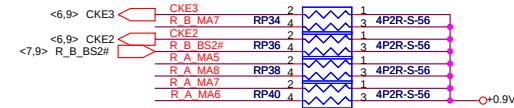
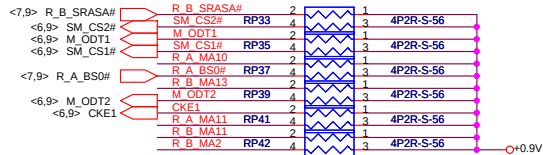
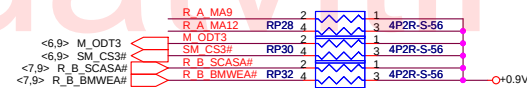
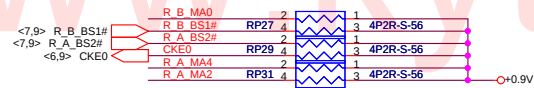


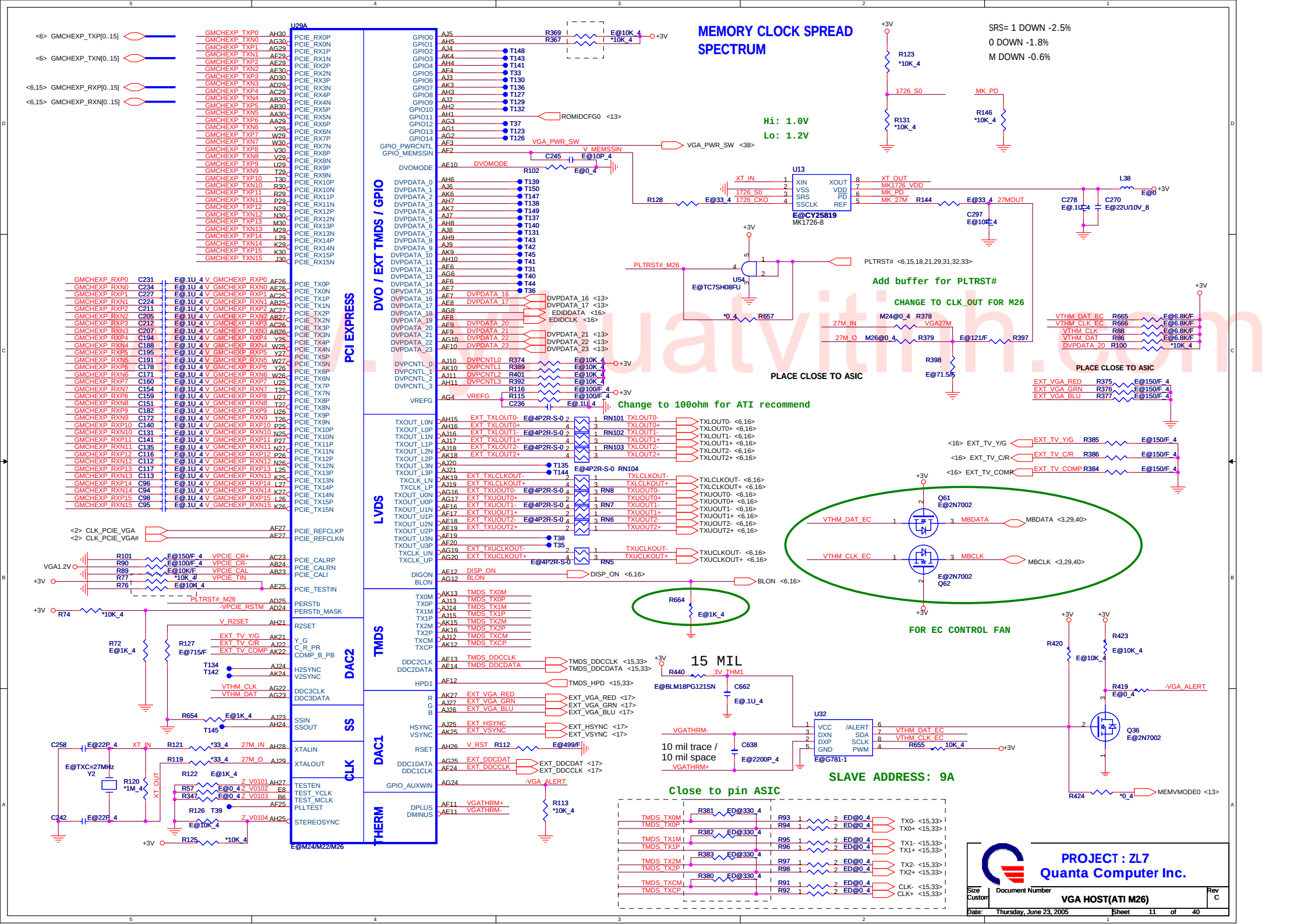


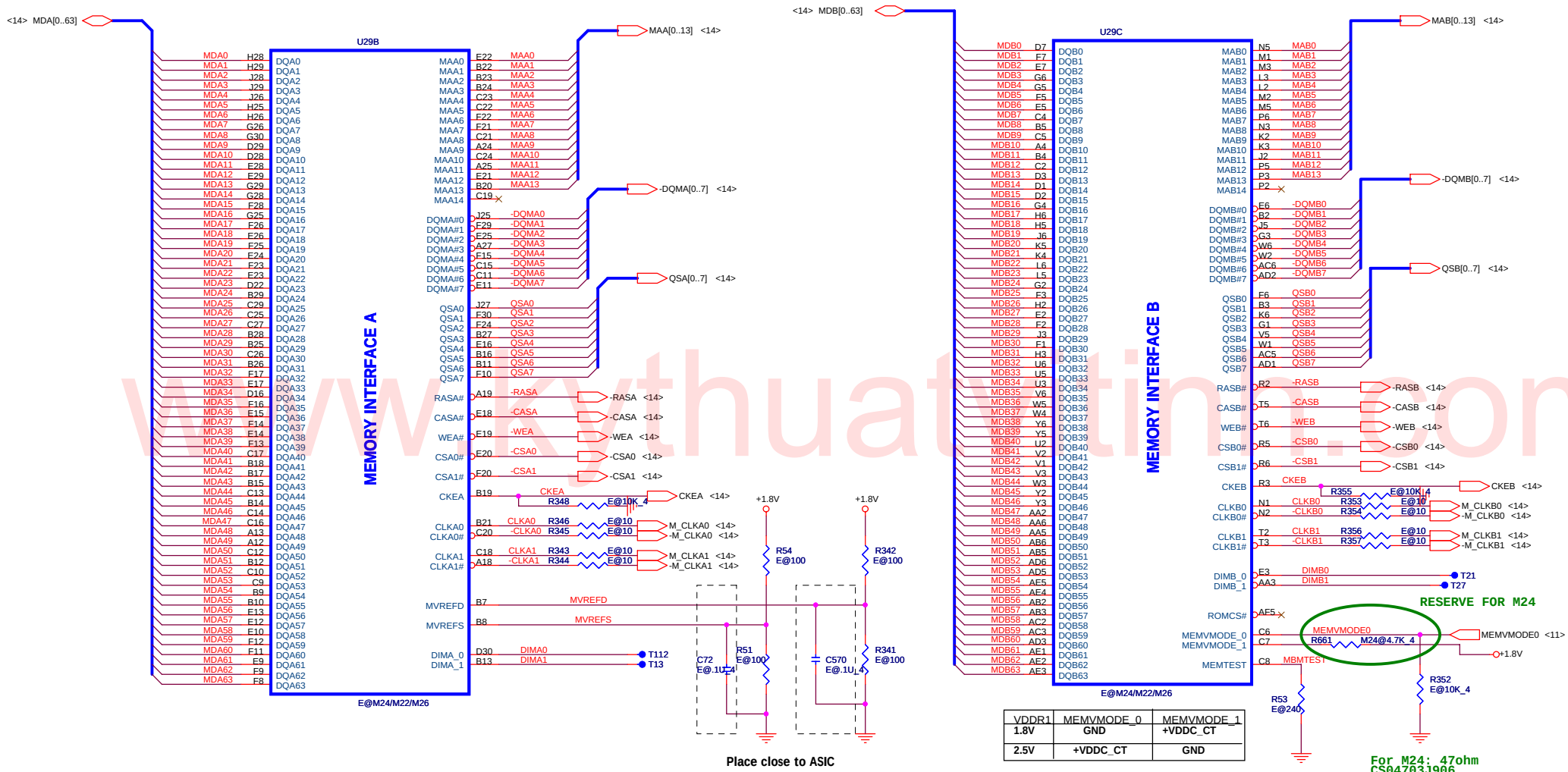
Layout note: Place one cap close to every 2 pullup resistors terminated to +0.9V



Layout note: Place one cap close to every 2 pullup resistors terminated to +0.9V







STRAPS PIN	
GPIO_0	PCI-Express Current Calibration Bandgap Backup 0: use reference voltage from Bandgap 1: use reference voltage from resistor divider
GPIO_1	PCI-Express PLL Calibration force enable 0: Disable PLL force calibration 1: Enable PLL force calibration
GPIO_(3,2)	00: PCI Express 1.0 mode 01: RESERVED 10: PCI Express 1.0 mode 11: RESERVED
GPIO_4	Turn off PCI-Express impedance / strength calibration 0: enable 1: disable
GPIO_5	Bypass PCI-Express PLL

STRAPS PIN	
GPIO_6	PCI-Express transmitter current compensation 0: Normal 1: Inject extra current for output buffer switching
GPIO_8	Strap to set the debug muxes to bring out DEBUG signals even if registers are inaccessible
GPIO(9,13:11) INT P/D	ROMIDCFG 0x0x: No ROM, CHG_ID=0 0x1x: No Rom, CHG_ID=1 1000: Parallel ROM, Chip ID's from ROM 1000: Parallel ROM, Chip ID's from ROM
DVPDATA_21-23	DVPDATA_21: 0=4Mx32 1=8Mx32 DVPDATA_22: 0=128M 1=64M DVPDATA_23: 0=Hynix 1=Samsung

FOR M26P ONLY
0: 128M
1: 256M

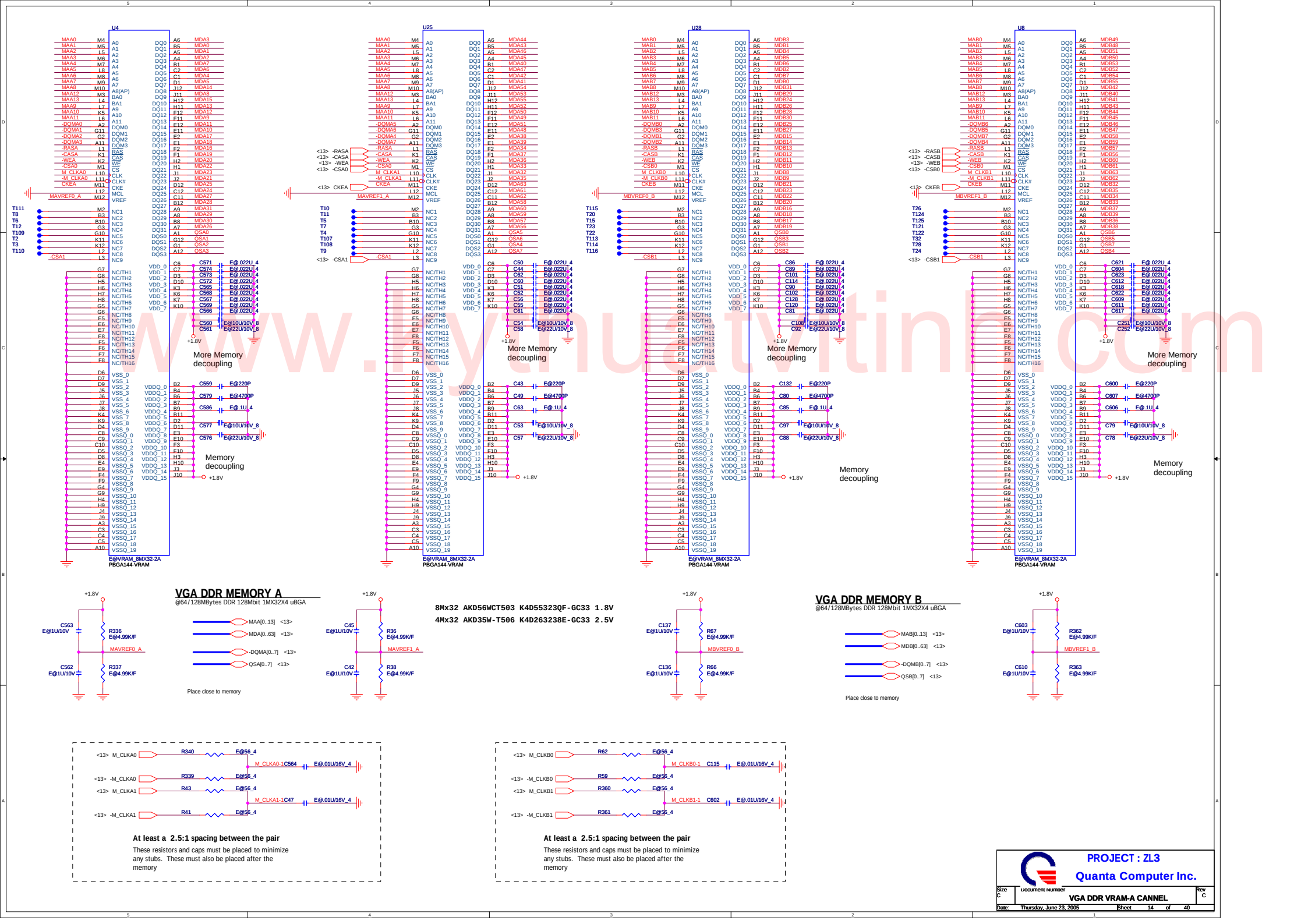
Add MEM_ID:
100 for 256M

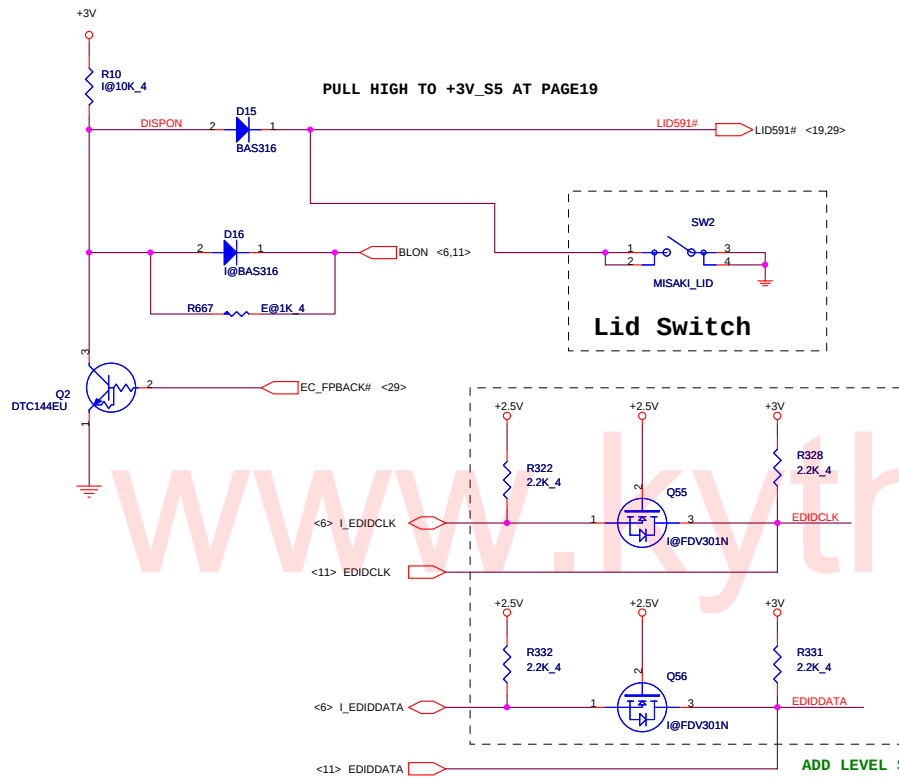
C: FOR HYNIX MEMORY

PROJECT :ZL3
Quanta Computer Inc.

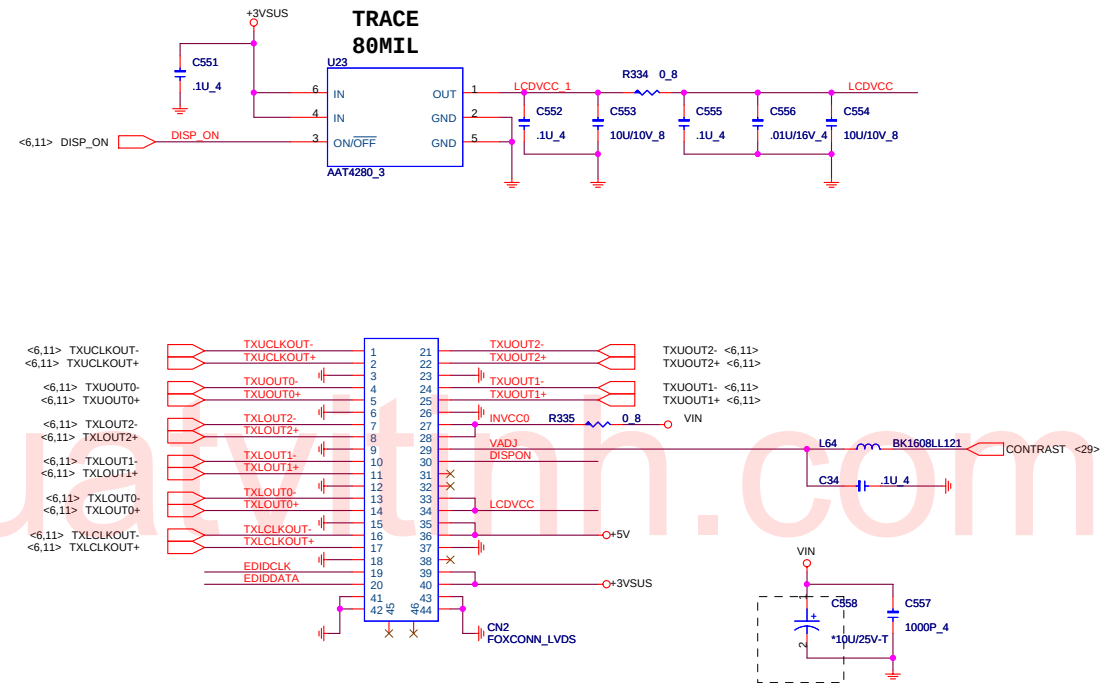
ATI M26 MEM/STRAPS PIN

Size Custom
Document number
Date: Thursday, June 23, 2005
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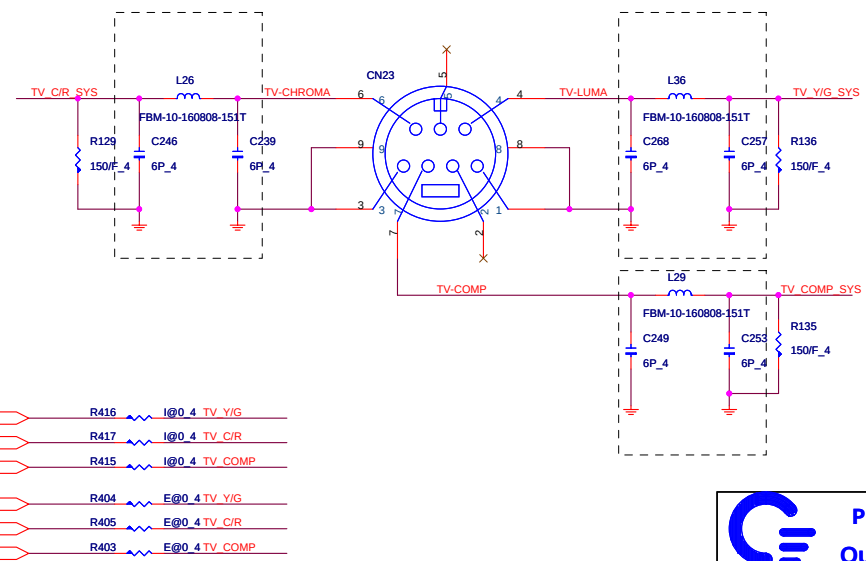




ADD LEVEL SHIFT FOR EDID
Change to FDS301 for Vgs issue

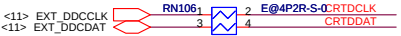
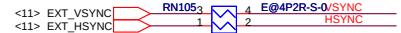
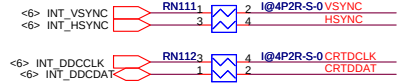


S-VIDEO

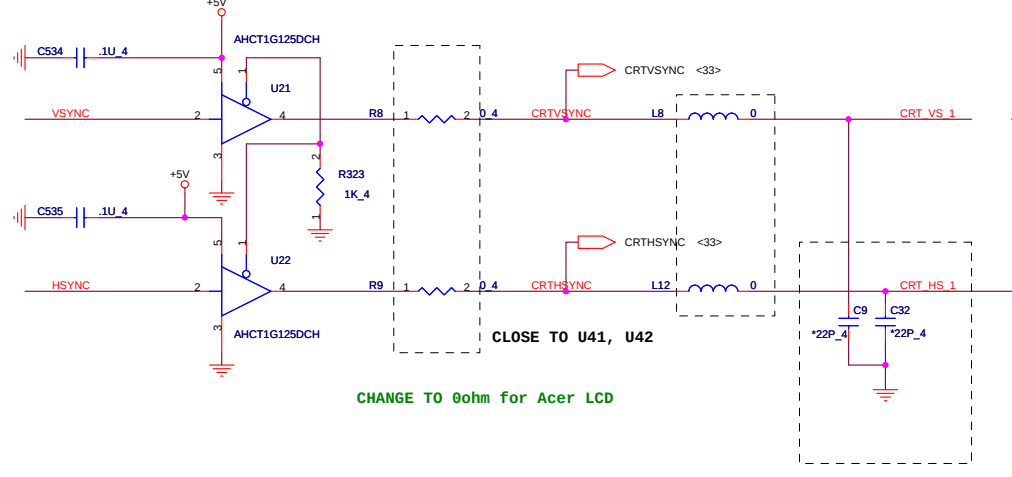
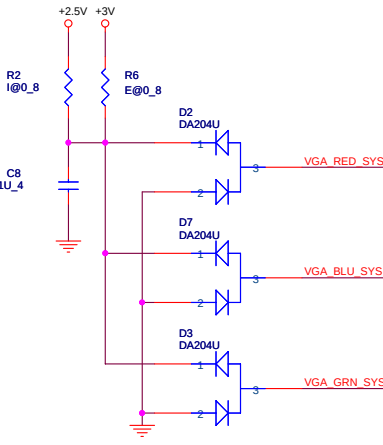
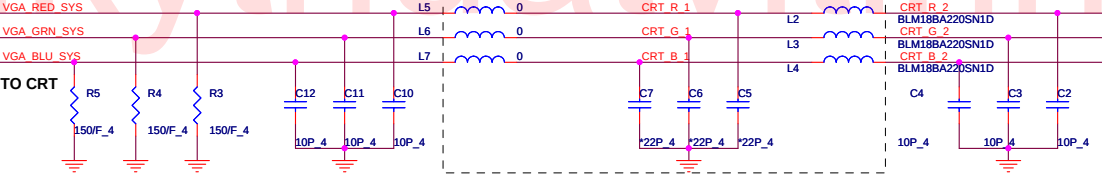
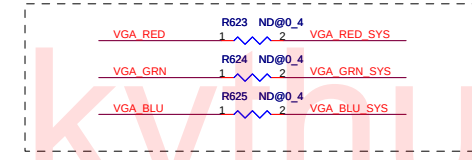
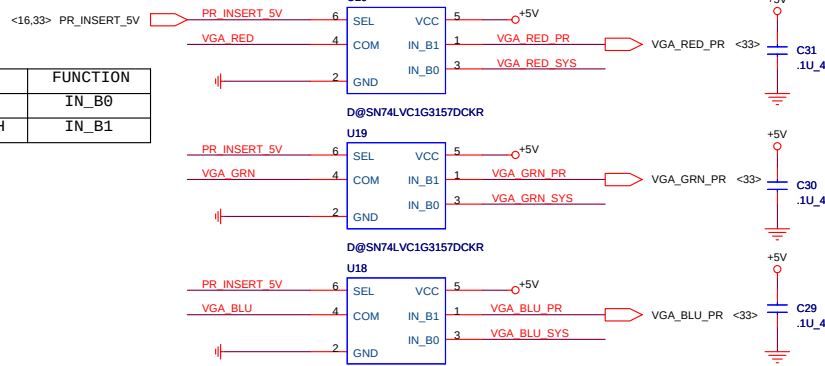


- <6> INT_TV_YIG R416 I@0.4 TV_YIG
- <6> INT_TV_C/R R417 I@0.4 TV_C/R
- <6> INT_TV_COMP R415 I@0.4 TV_COMP
- <11> EXT_TV_YIG R404 E@0.4 TV_YIG
- <11> EXT_TV_C/R R405 E@0.4 TV_C/R
- <11> EXT_TV_COMP R403 E@0.4 TV_COMP

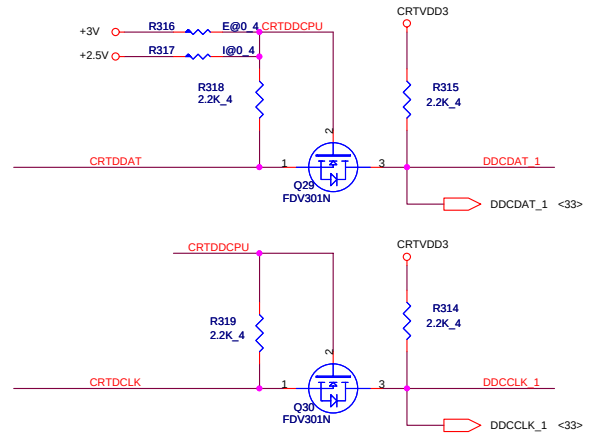
ADD CIRCUITS WHEN NO DOCKING



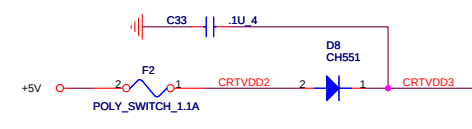
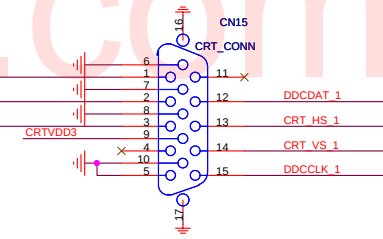
SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1

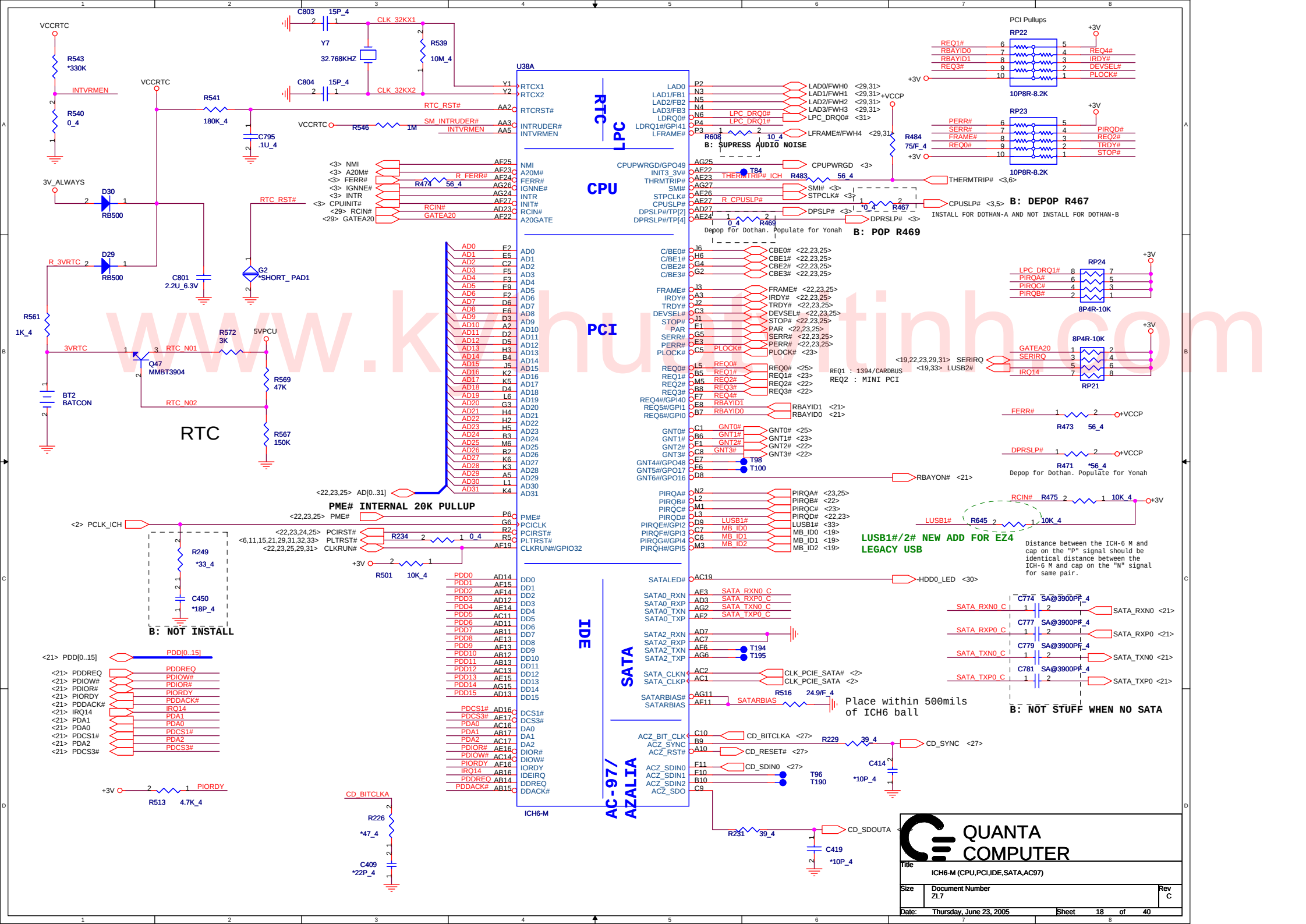


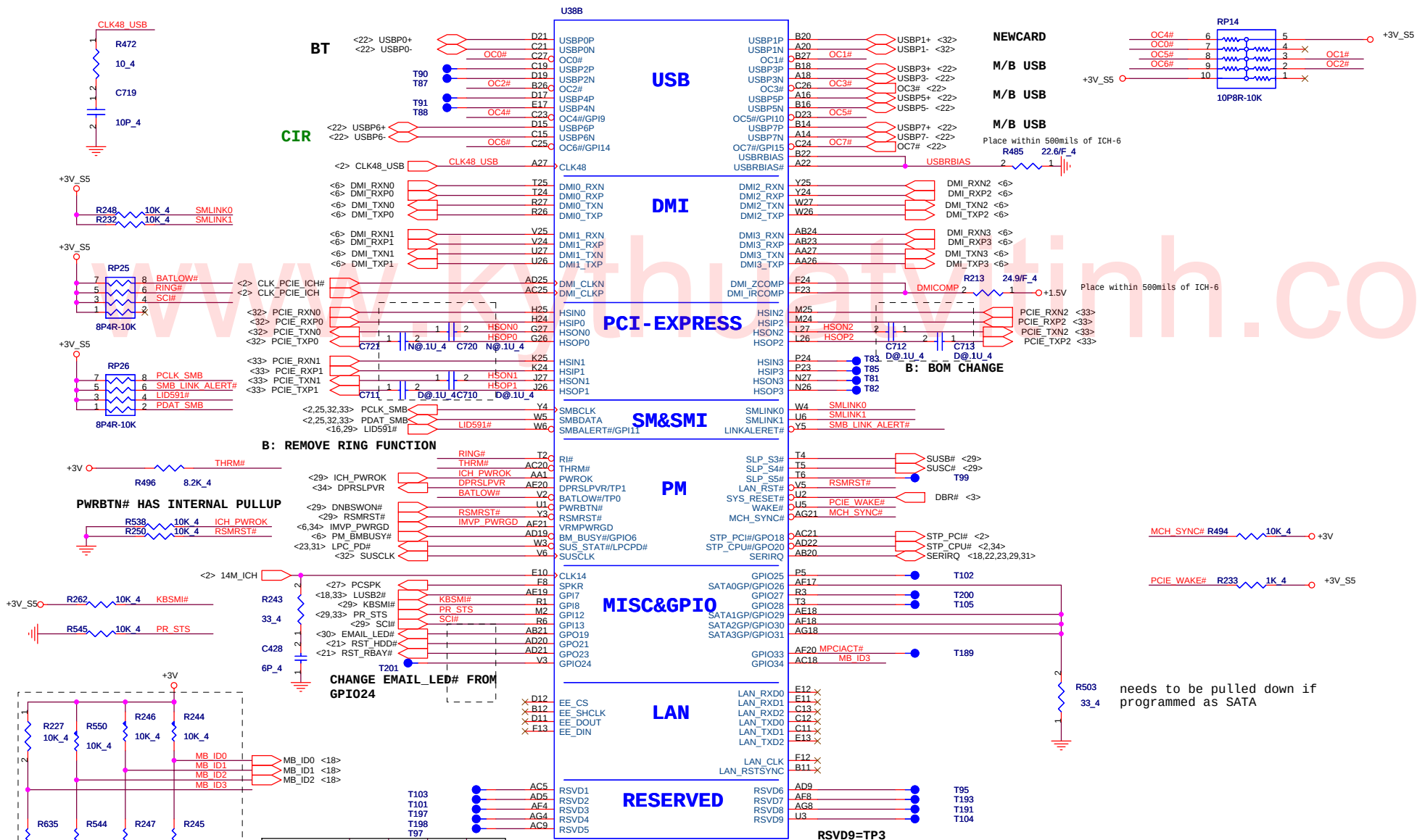
CHANGE TO 0ohm for Acer LCD



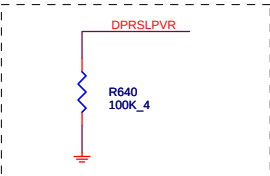
Change to FDV301N for Vgs issue.

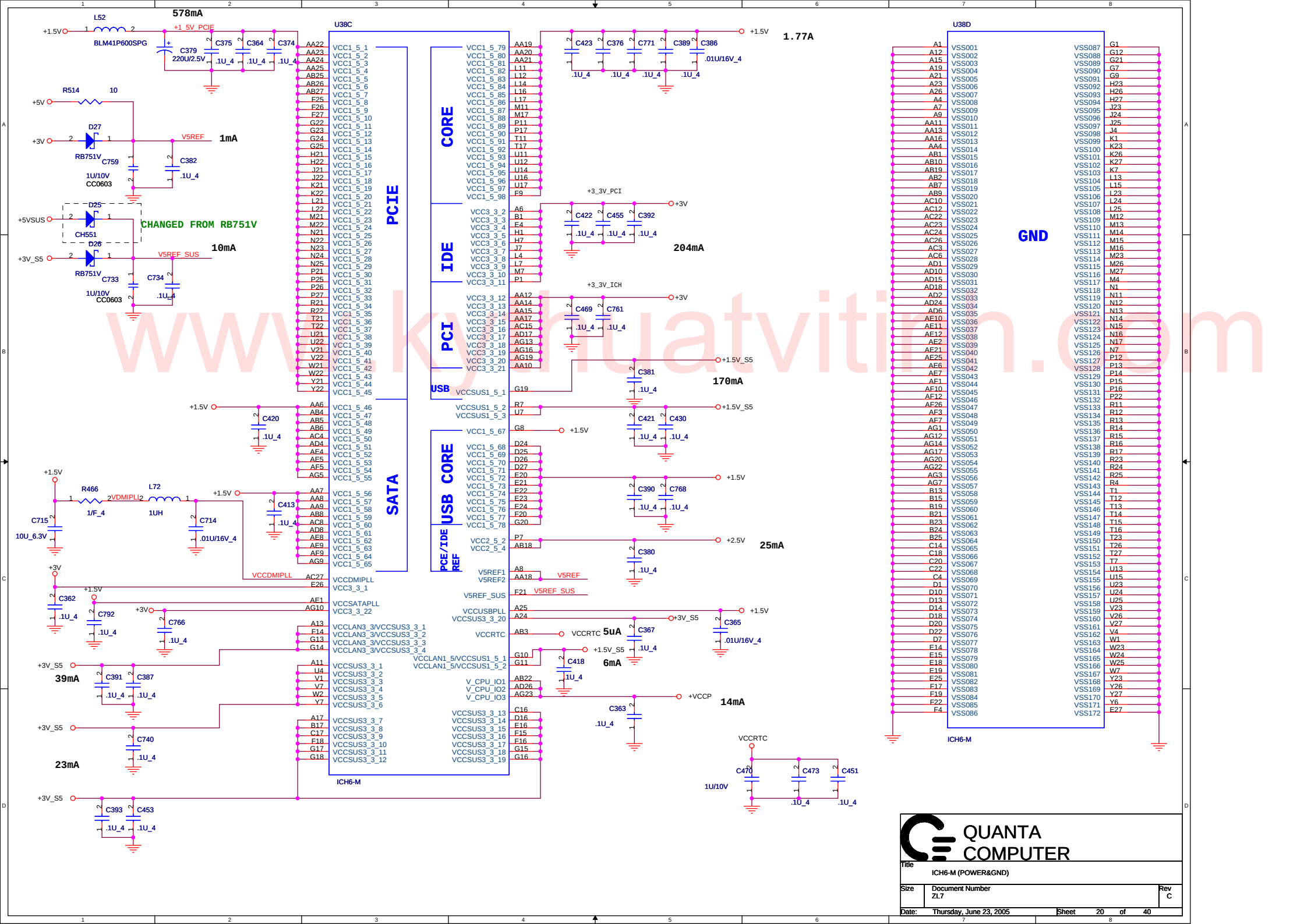


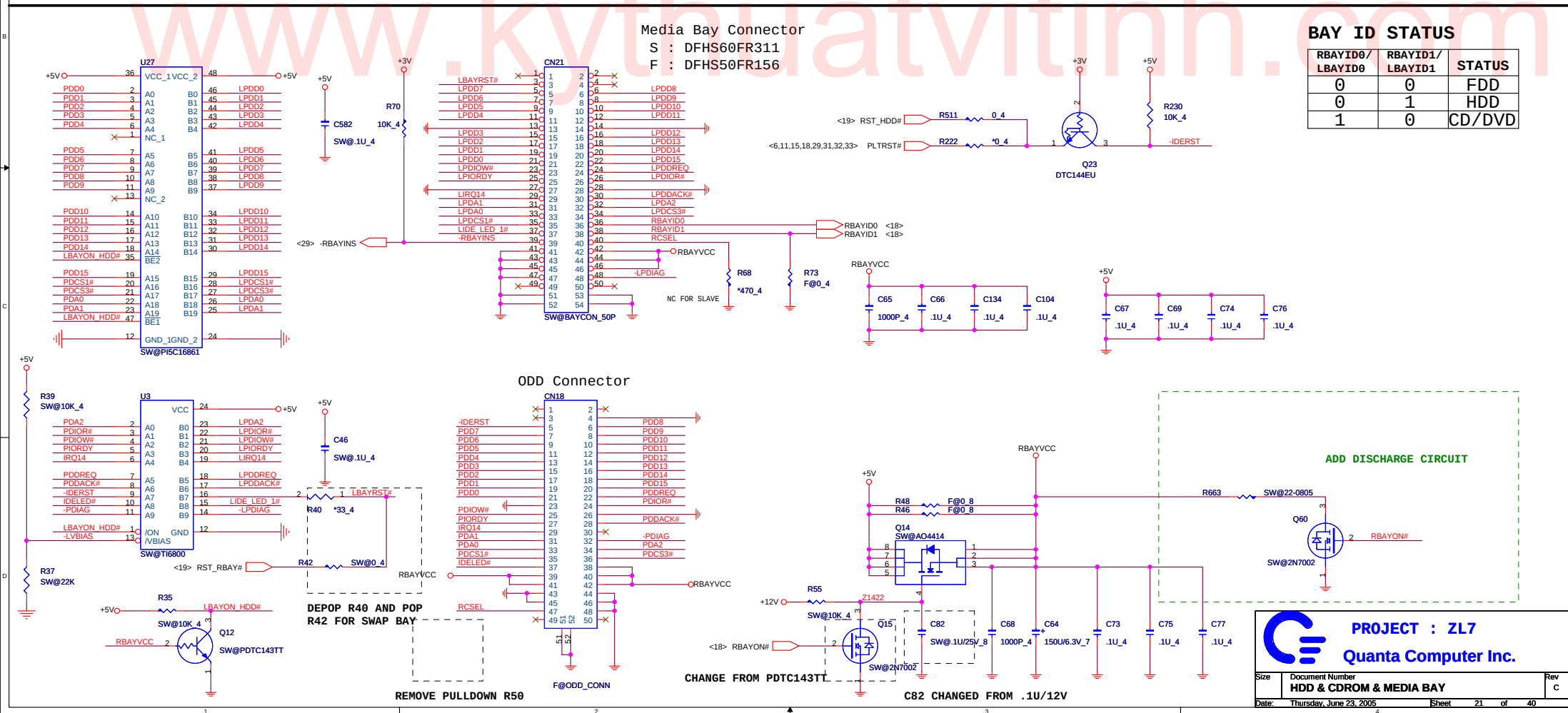


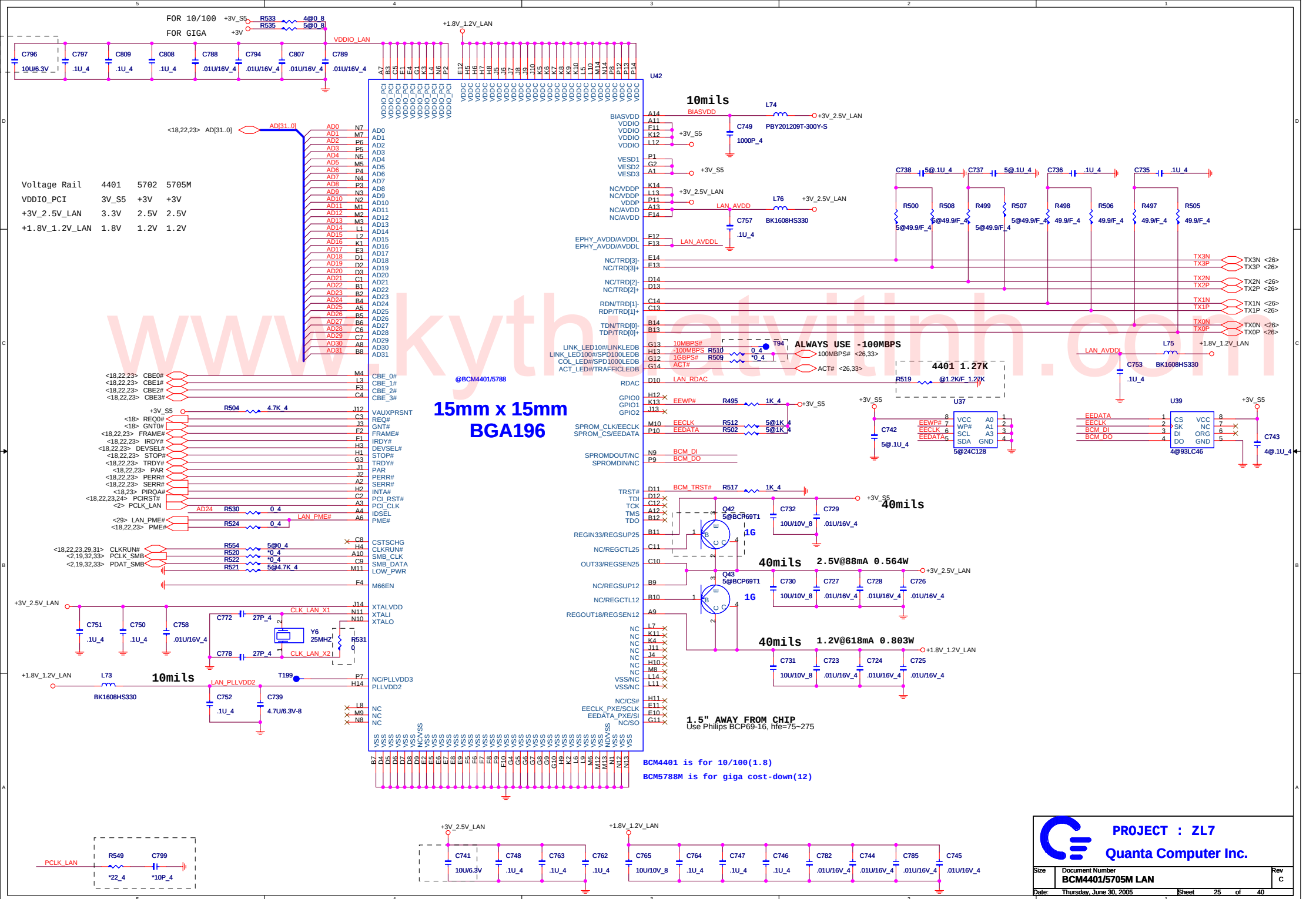


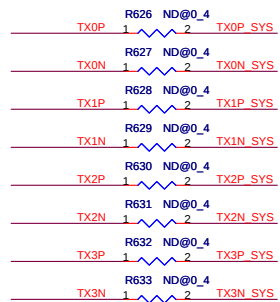
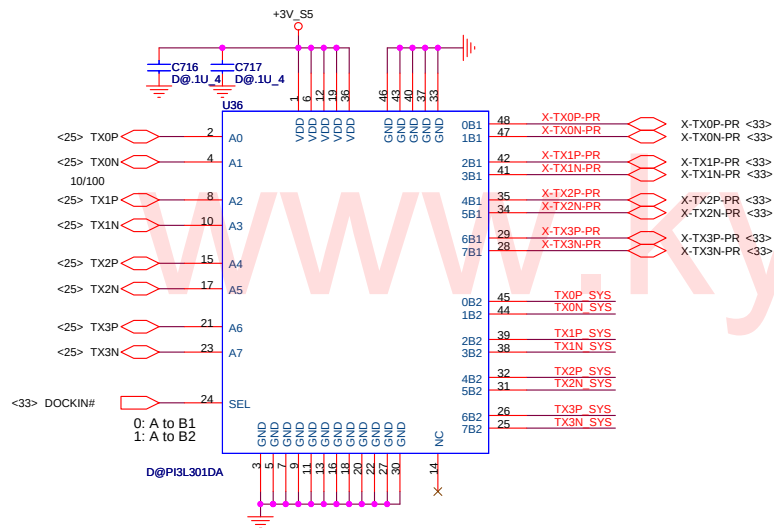
	ID3	ID2	ID1	ID0
ZL3/TM4600	0	0	0	0
ZL3B/TM4100	0	0	1	0
ZL3D/AS1690	0	1	0	0
ZL3F/AS3510	0	1	1	0
ZL3C/EX4100	1	0	1	1



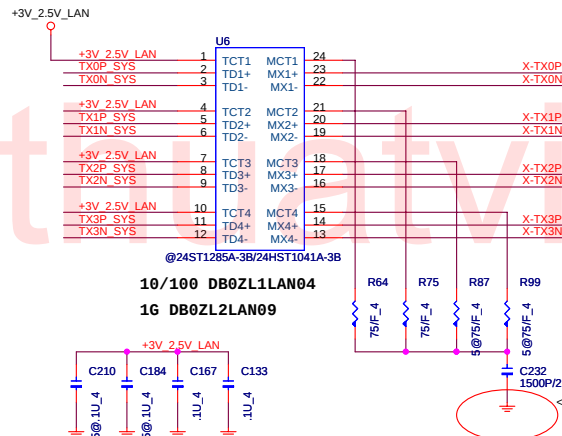




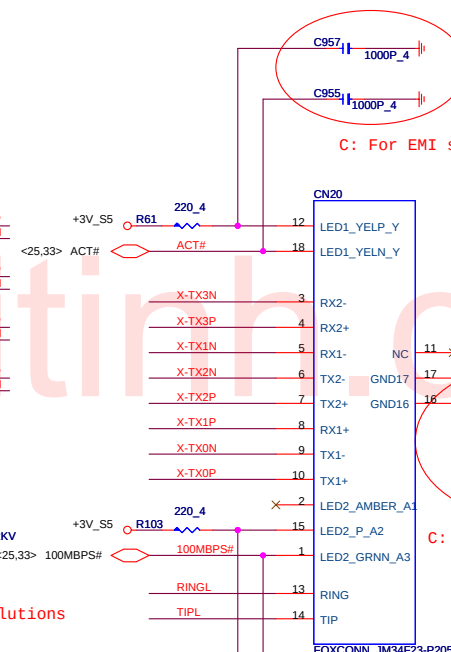
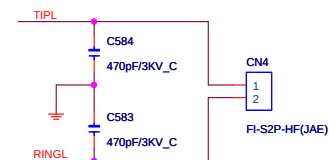




ADD CIRCUITS WHEN NO DOCKING



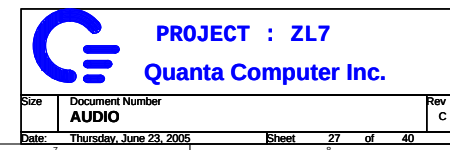
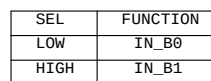
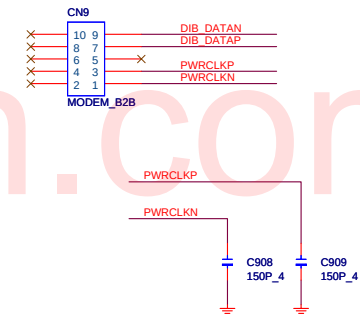
C: For EMI solutions



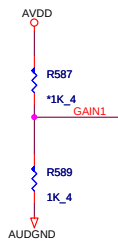
C: For EMI solutions

C: For EMI solutions

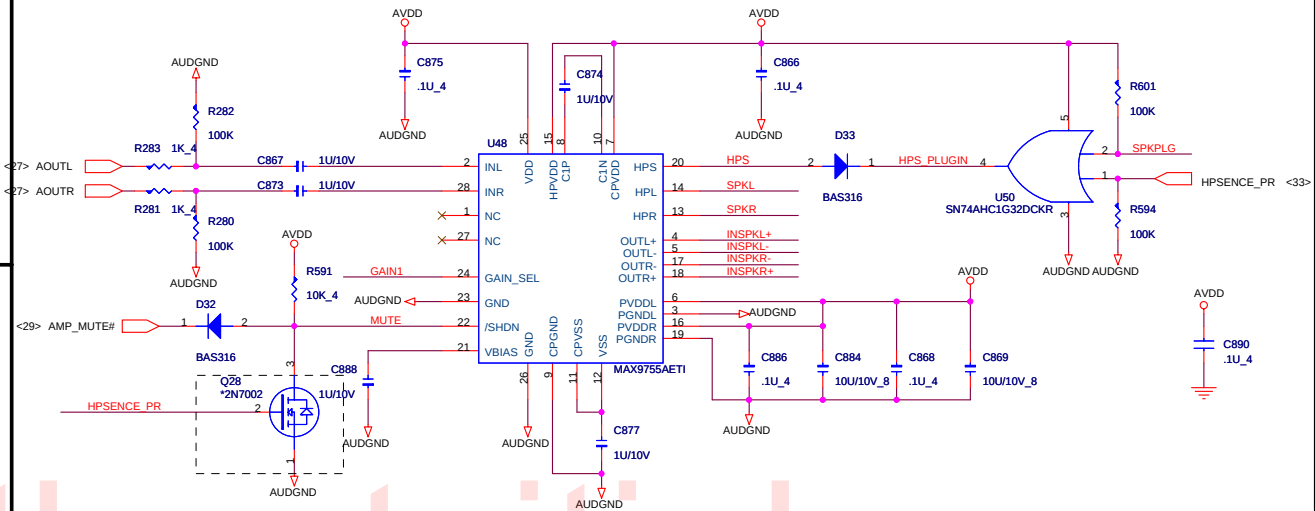
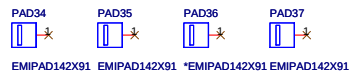
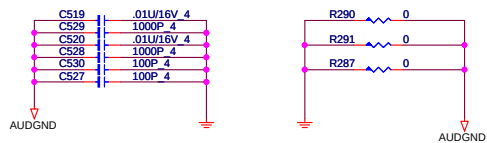
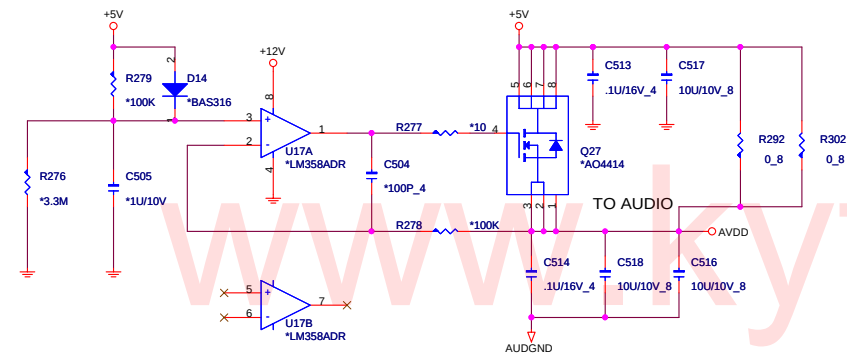
C: For EMI solutions



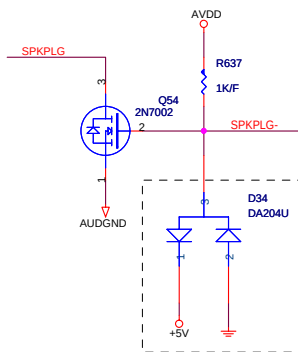
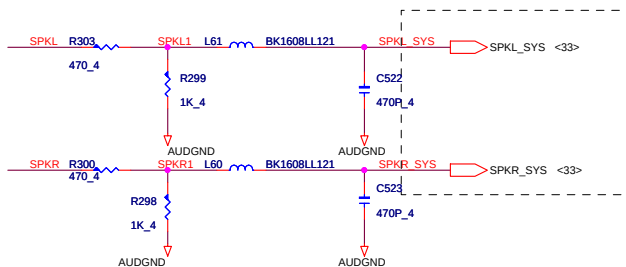
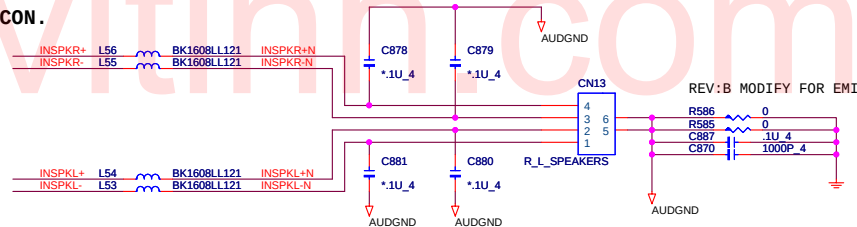
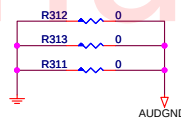
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



AMP POWER

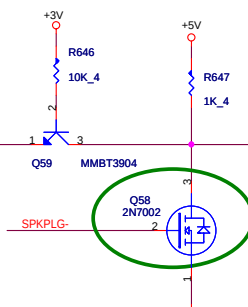
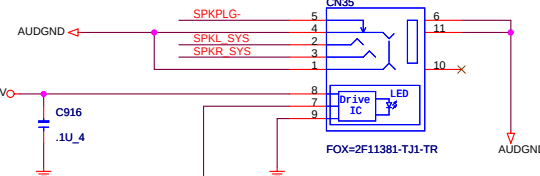


SPEAKER CON.



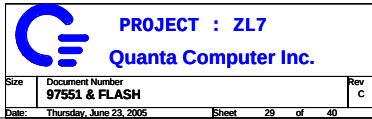
NEW ADD FOR ESD
CLOSE TO CN35

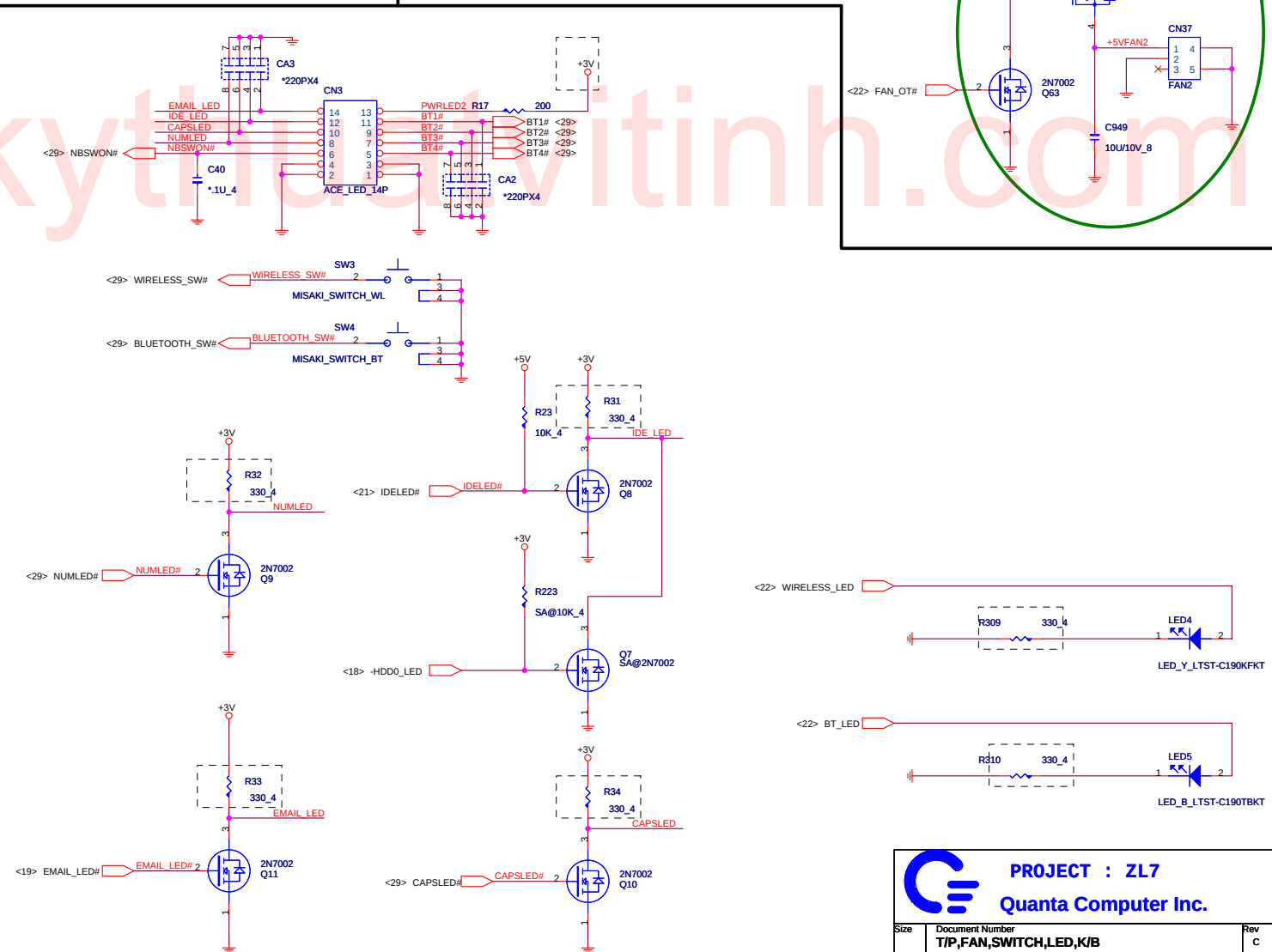
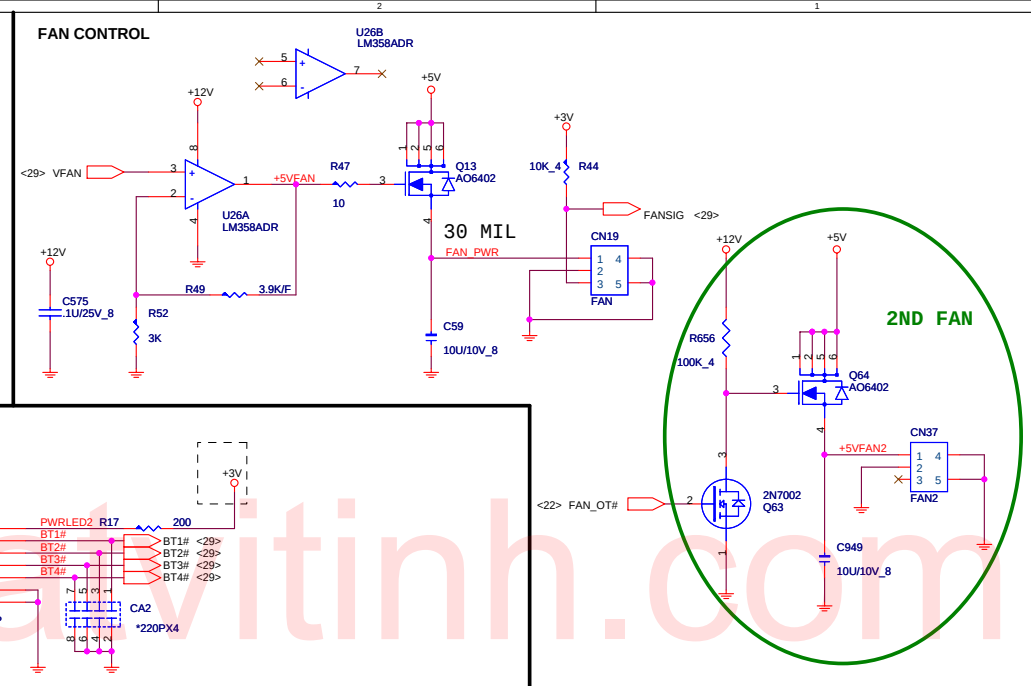
LINE OUT&SPDIF

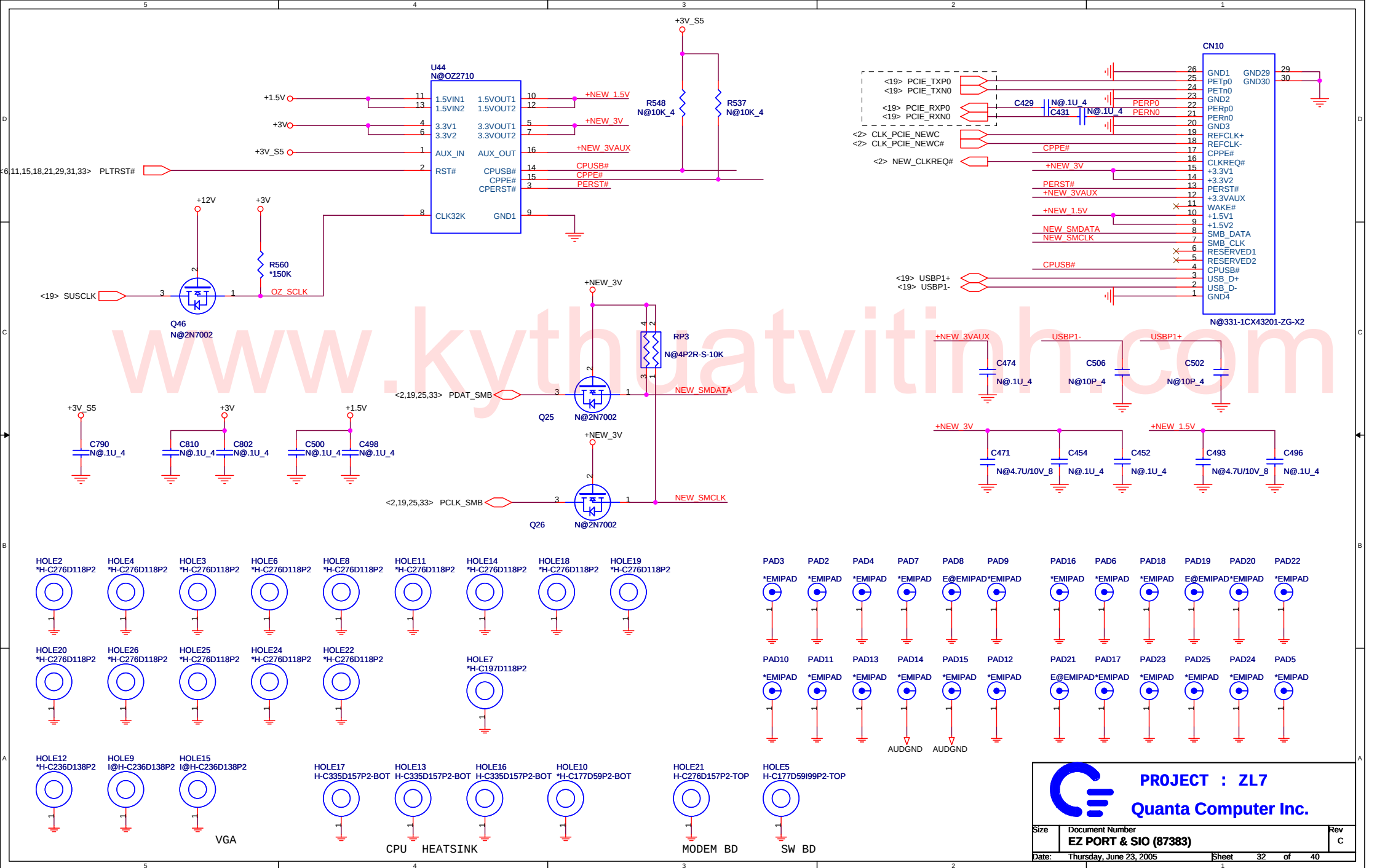


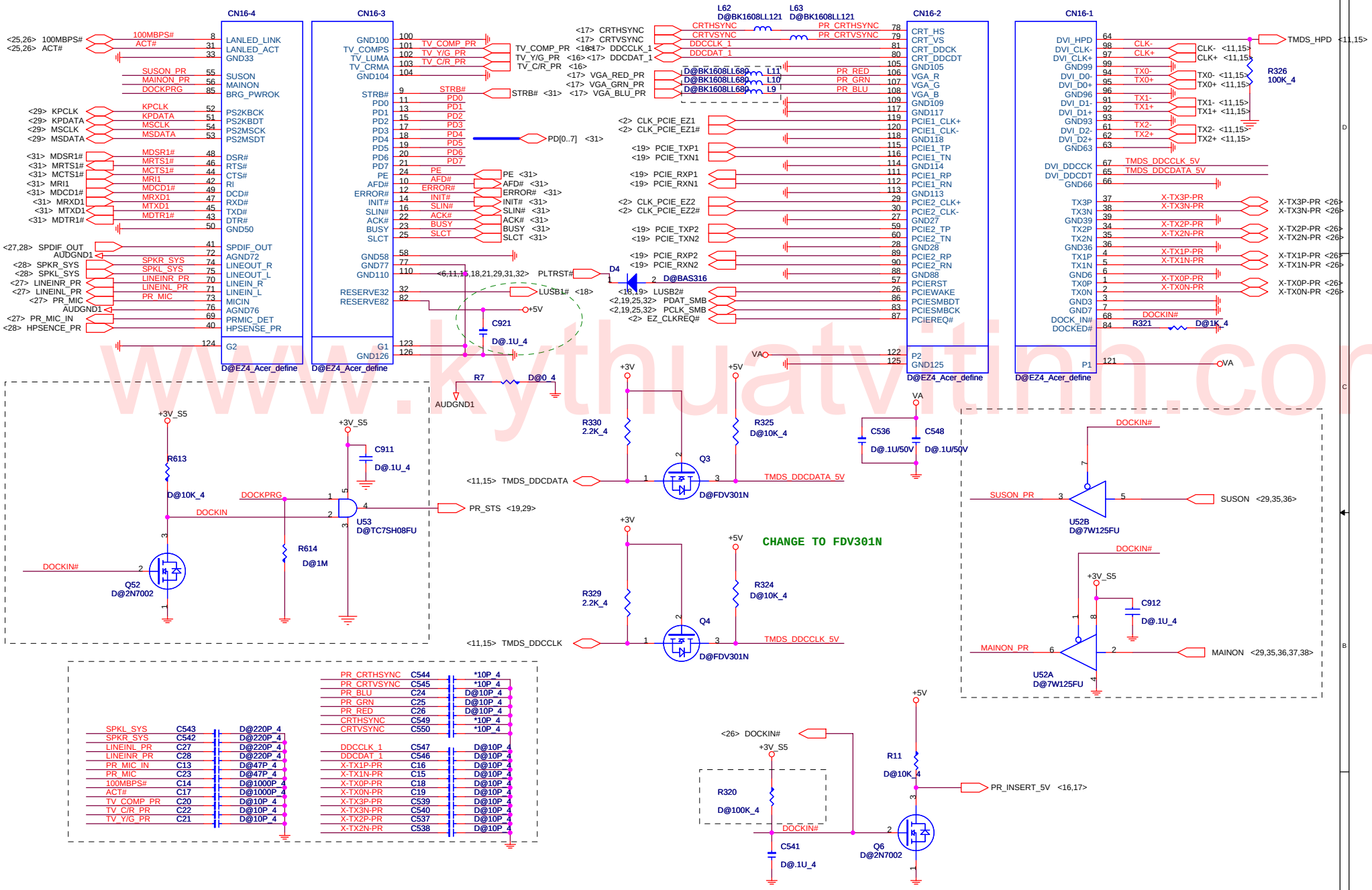
Change to 2N7002

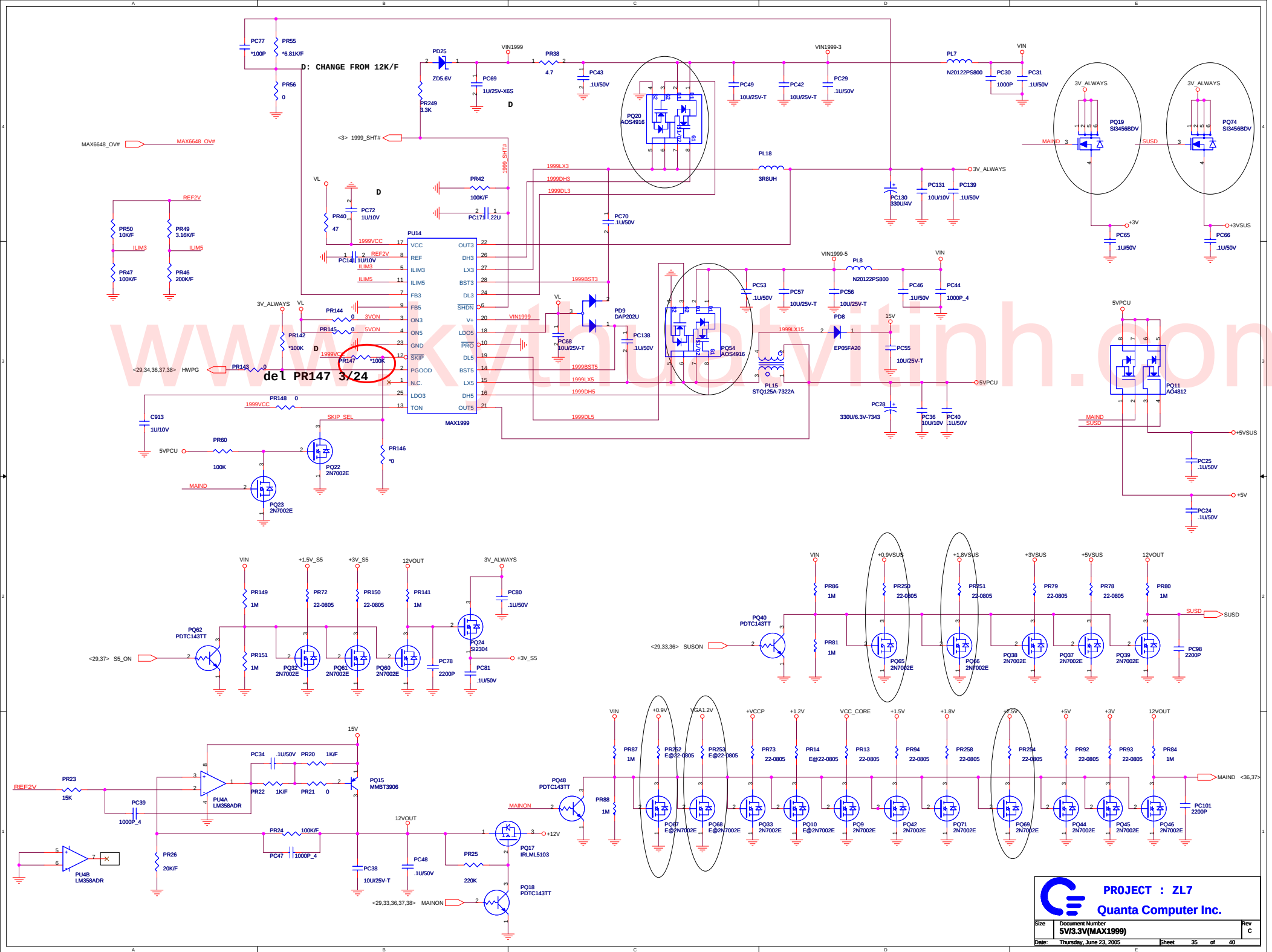
PROJECT : ZL7
Quanta Computer Inc.

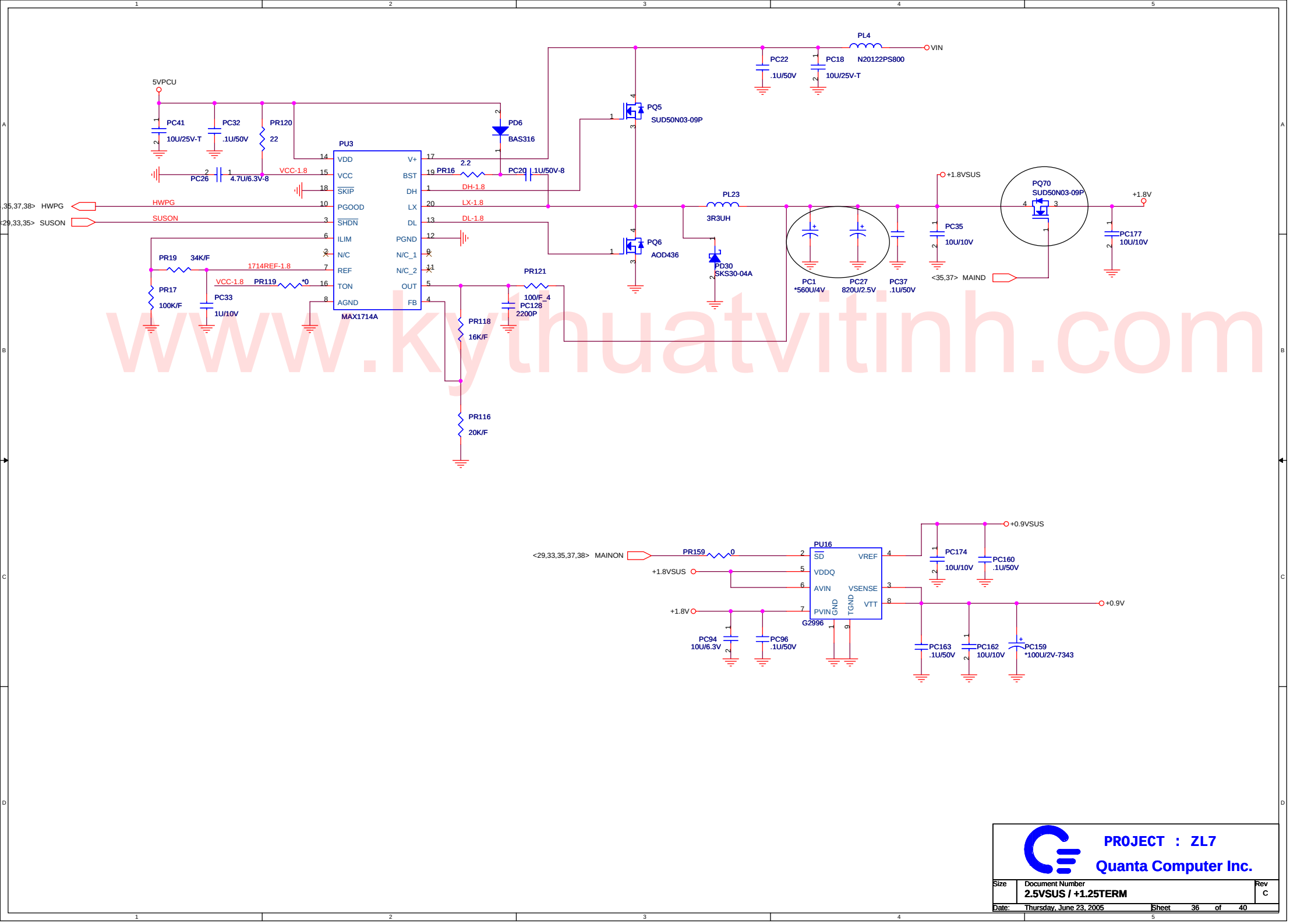




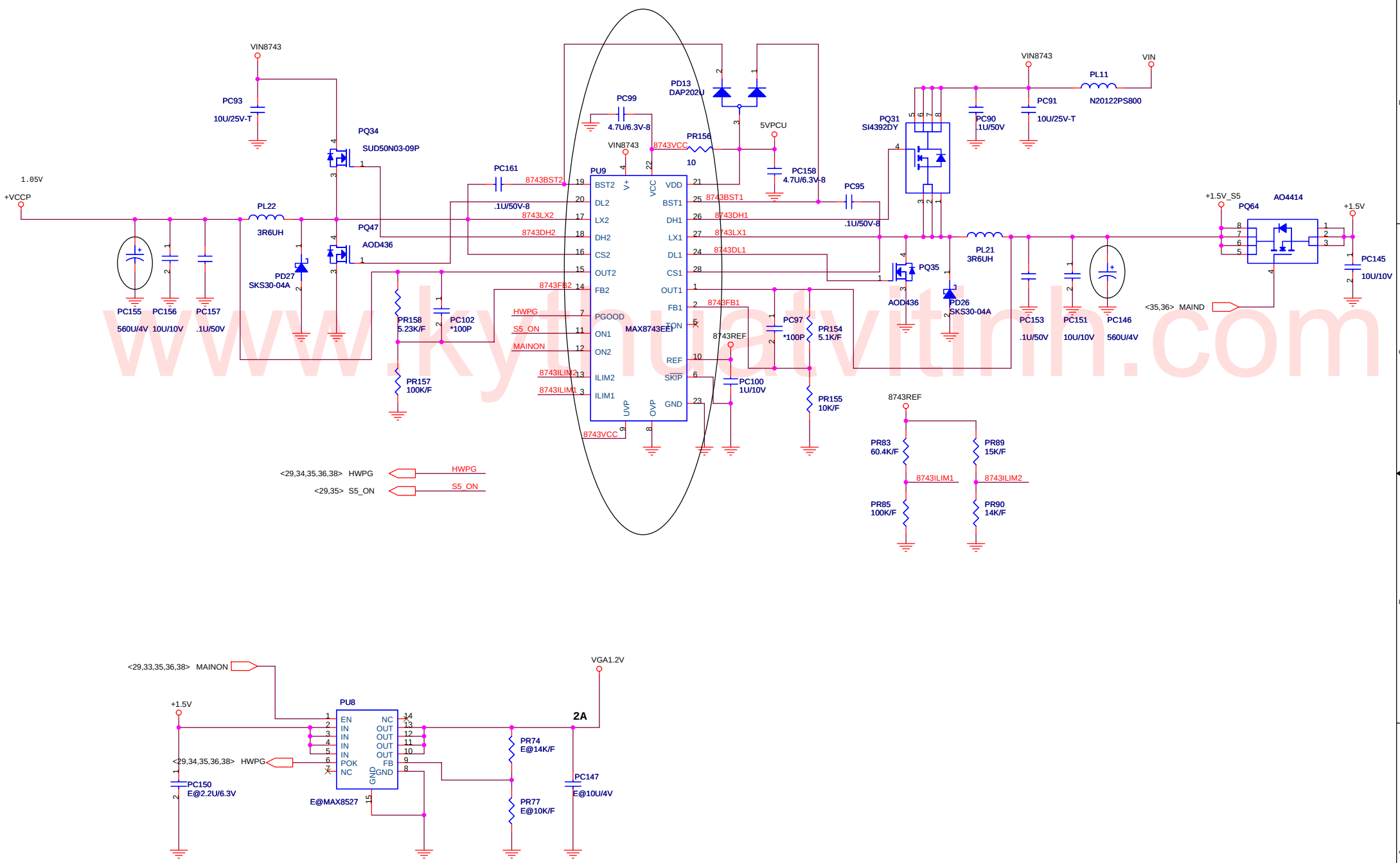


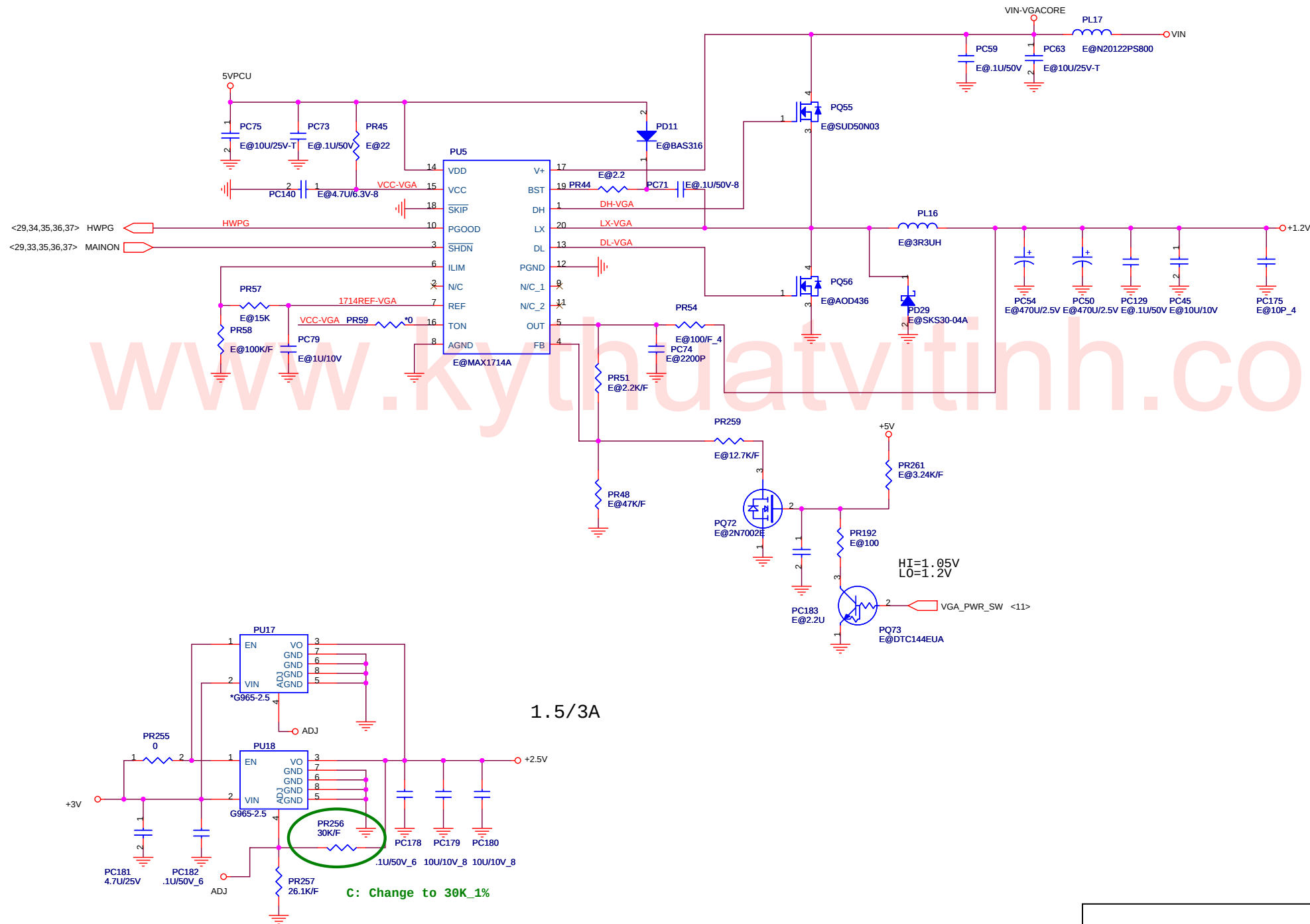




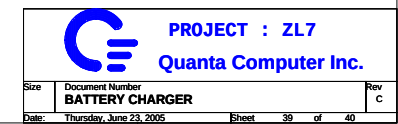


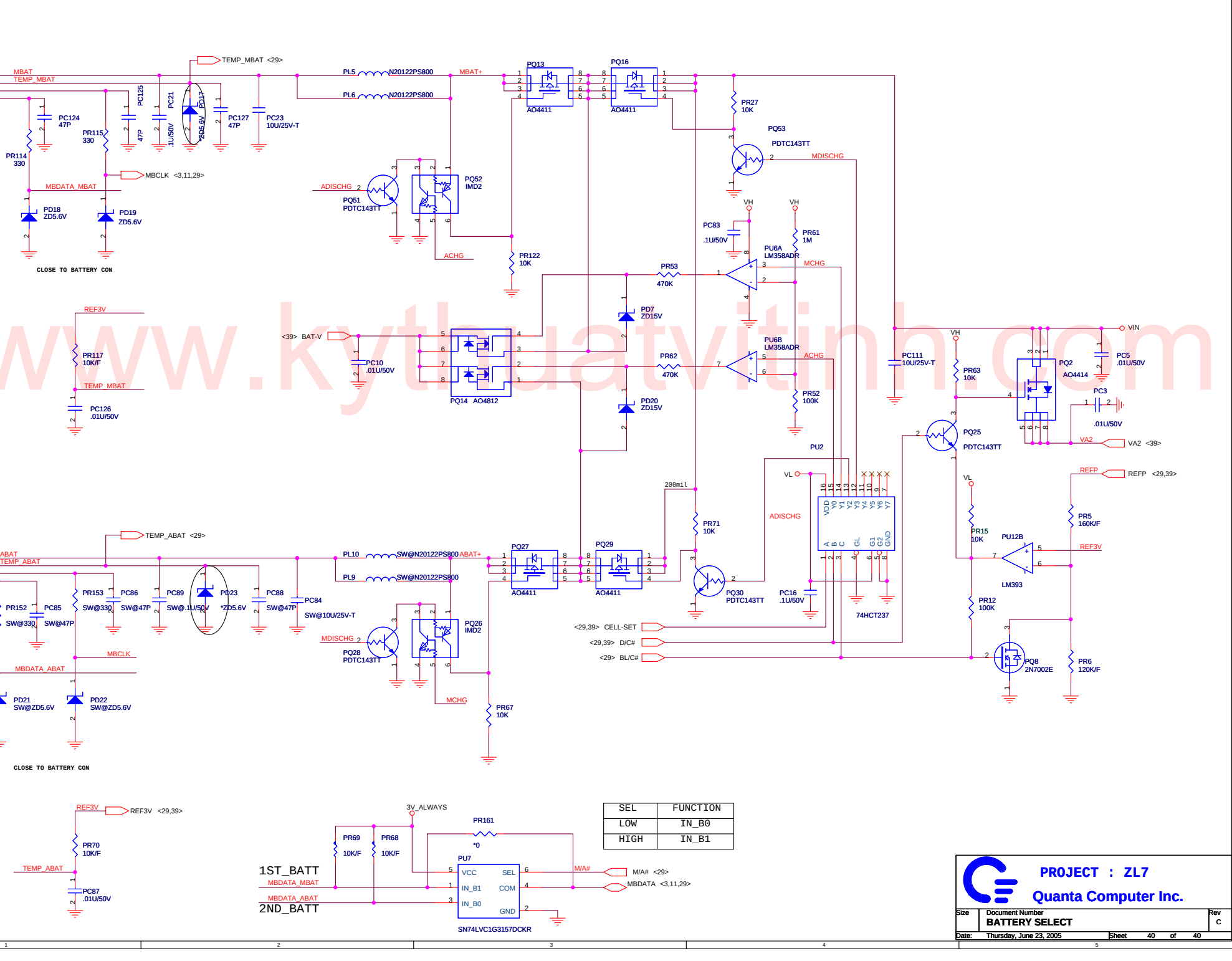
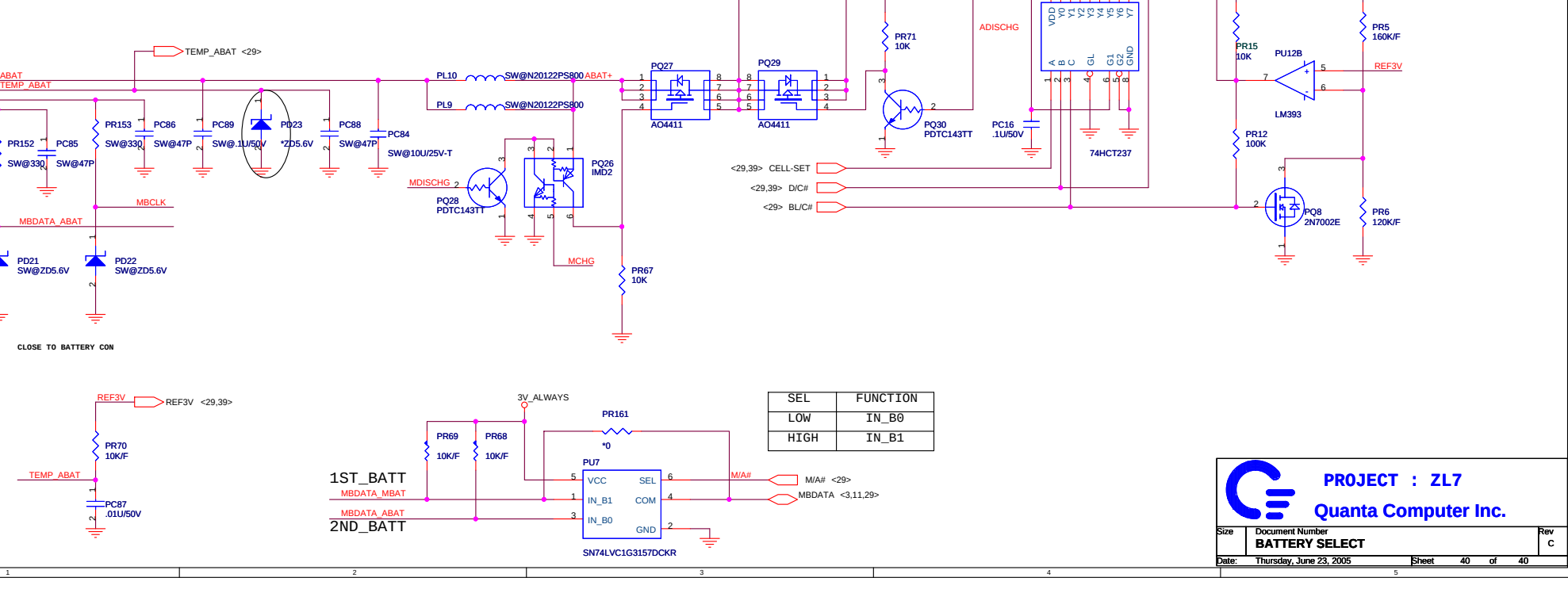
PROJECT : ZL7
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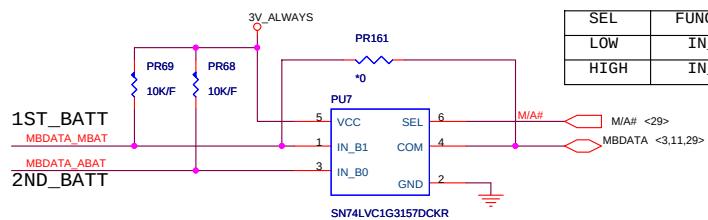
Size	Document Number	Rev
	+1.2V/+1.8V	C
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[illegible]

SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1

1ST_BATT
MBDATA_MBAT
MBDATA_ABAT
2ND_BATT



 PROJECT : ZL7
Quanta Computer Inc.

Size	Document Number BATTERY SELECT	Rev C
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