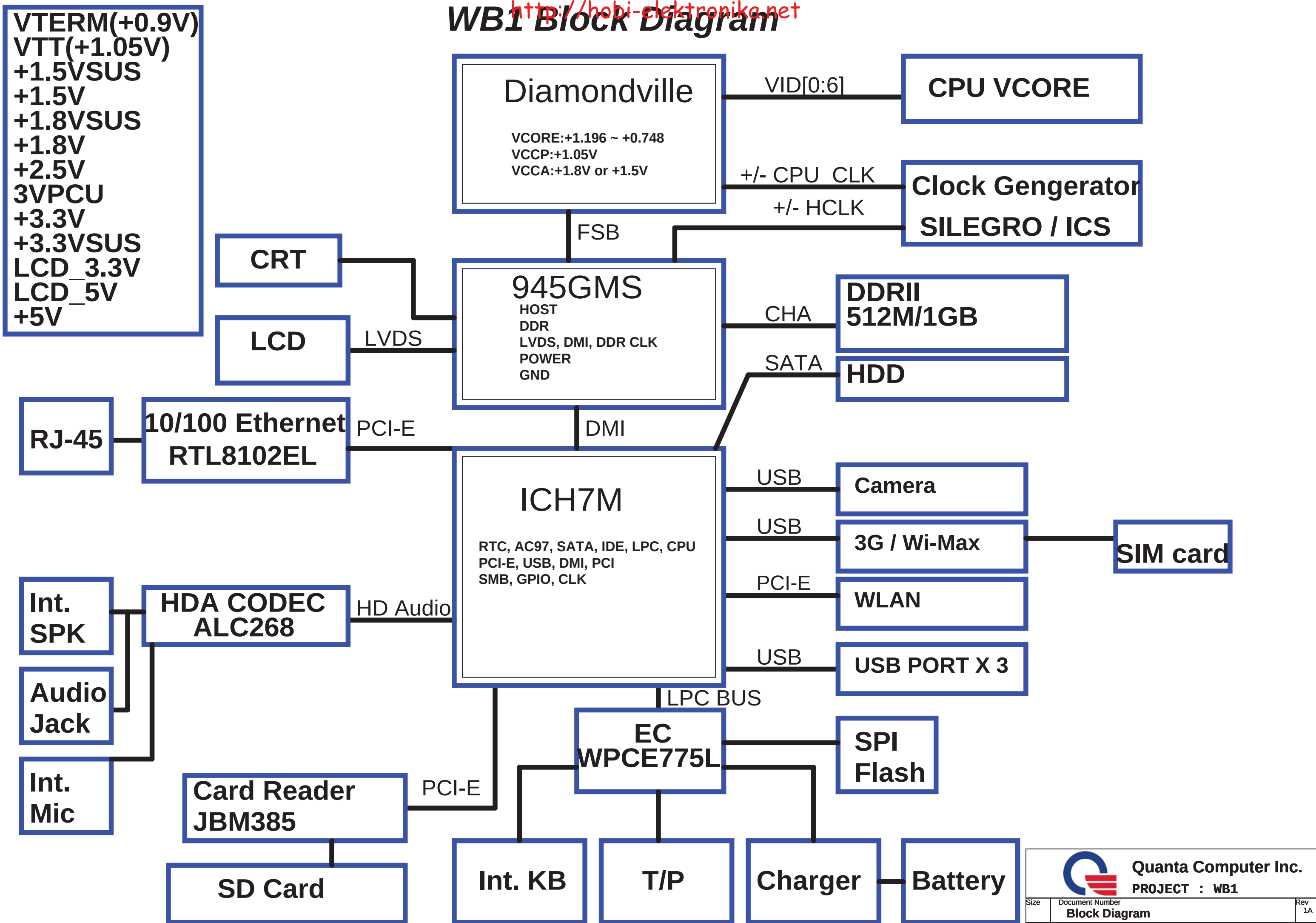


		5		4		3		2		1		
MODEL :		REV :	CHANGE LIST :							MODEL : WB1 MB		
WB1 MotherBoard		1A	FIRST RELEASE							PAGE	FROM	TO
		A2A	Page 10 --> 1.Add SATALED# 2. Add USB5 & USB7 Page 14 --> 1.Change CN8 pin 30 to GND 2. Change CN8 connector Page 16 --> 1.Change CN2 connector 2. Add USB Page 18 --> 1.Change CN19 connector 2. Add signal for 2nd FAN 3. Add CFLED# Page 19 --> 1.Add LED signals Page 21 --> 1.Change PR17 to 10K 2. Change Battery connector Page 22 --> 1.Add PR79 Page 23 --> 1.Change PR46 to 16.9K Page 25 --> 1.Change PR78 to VL							1	1A	
		2								1A		
		3								1A	C3A	
		4								1A		
		5								1A	C3A	
		B3A	Page 15 --> 1.Mount R219 R220(EMI) 2. Mount C36 (EMI) 3. change R20 to 0402 size Page 16 --> 1.Change CN10 connector BTB Type 2.Del R227 R228 R229 R230 3.Add RN35 RN36 3. Add C491 for EMI request Page 19 --> 1.Change U23 footprint to lgfp128-16x16-4-wb1 2. Del R327 R328 3.Add R329 R330 (KB_SEL) Page 24 --> 1.Change PU1 PN AL005601001(GMT) Page 25 --> 1.Mount PR55 PC53 (EMI) Page 26 --> 1.Mount PR44 PC40 (EMI)							6	1A	
		7								1A		
		8								1A		
		9								1A		
		10								1A	C3A	
		C3A	Page 03 --> 1.Remove 0 ohm Page 05 --> 1.Remove 1 ohm Page 10 --> 1.Add 3G Azalia interface Page 14 --> 1.Change Q4 2. Remove 0 ohm Page 15 --> 1.Change CN9 & CN12 Page 16 --> 1.Change CN11 Page 17 --> 1.Remove 0 ohm Page 18 --> 1.Change IDE Power bead , 2.Change CN13 Page 19 --> 1.Change CN7 & CN14 Page 20 --> 1.Remove 0 ohm Page 21 --> 1.Change CN5							11	1A	
		12								1A		
		13								1A		
		14								A2A	C3A	
		15								B3A	C3A	
		16								B3A	C3A	
		17								1A	C3A	
		18								A2A	C3A	
		19								B3A	C3A	
		20								1A	C3A	
		21								B2A	C3A	
		22								B3A		
		23								A2A		
		24								B3A		
		25								A2A		
26	B3A											

WB1 Block Diagram

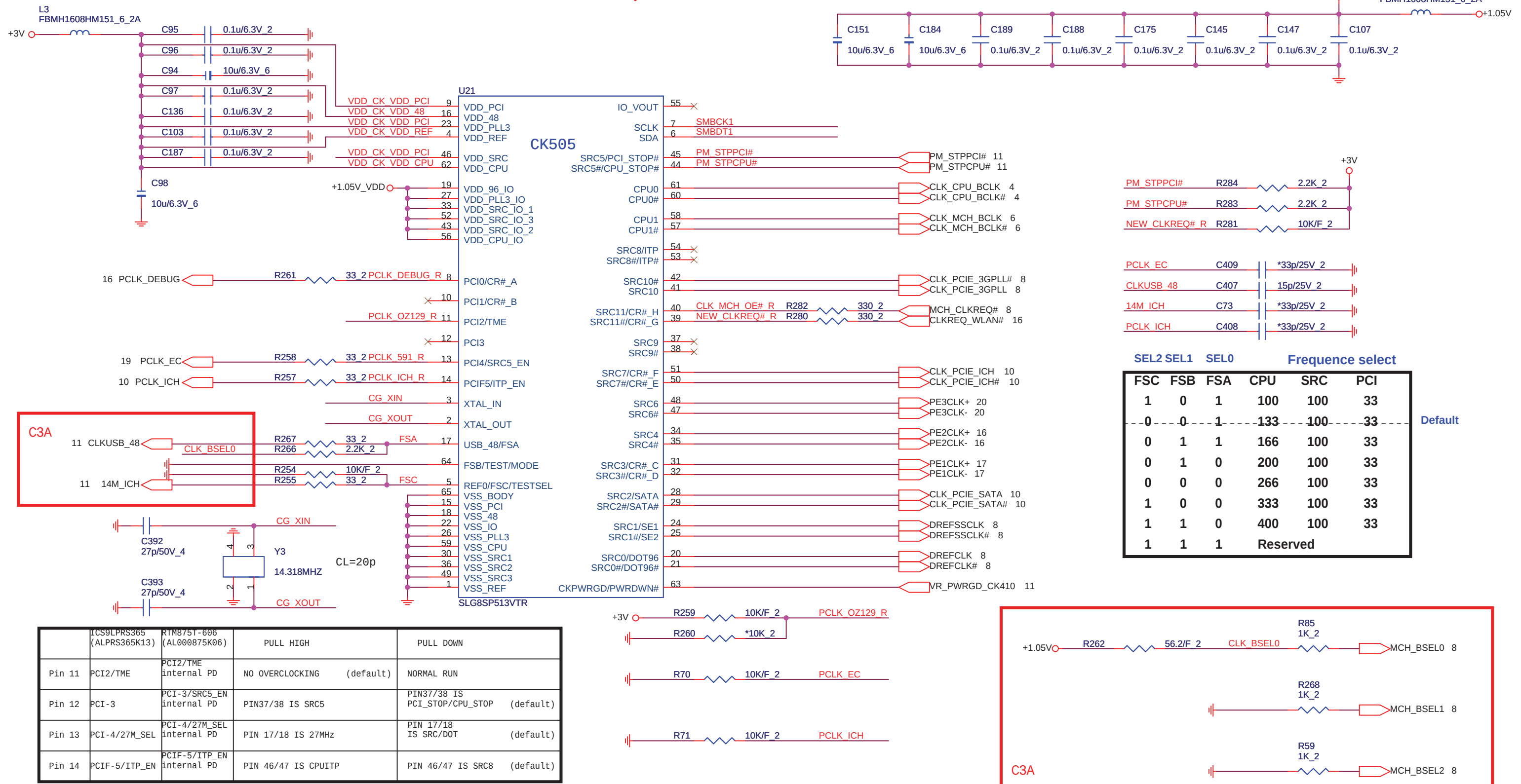
<http://hobi-elektronika.net>



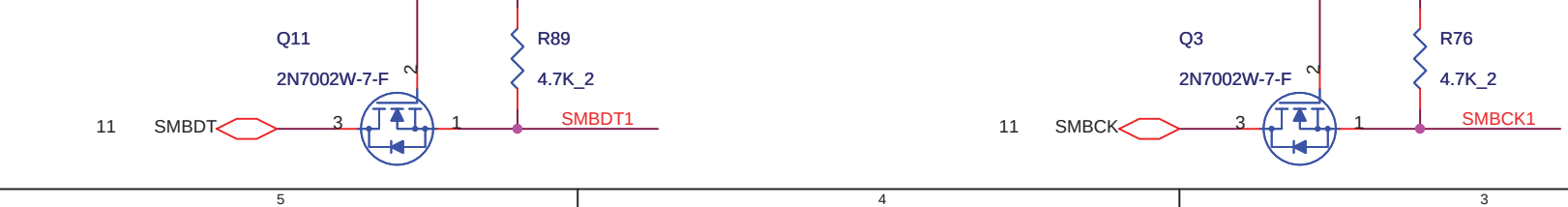
Quanta Computer Inc.
PROJECT : WB1

Clock Generator

http://hobi-elektronika.net



Clock Gen I2C



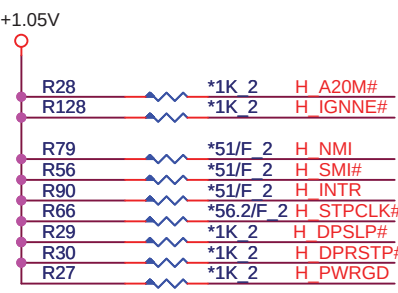
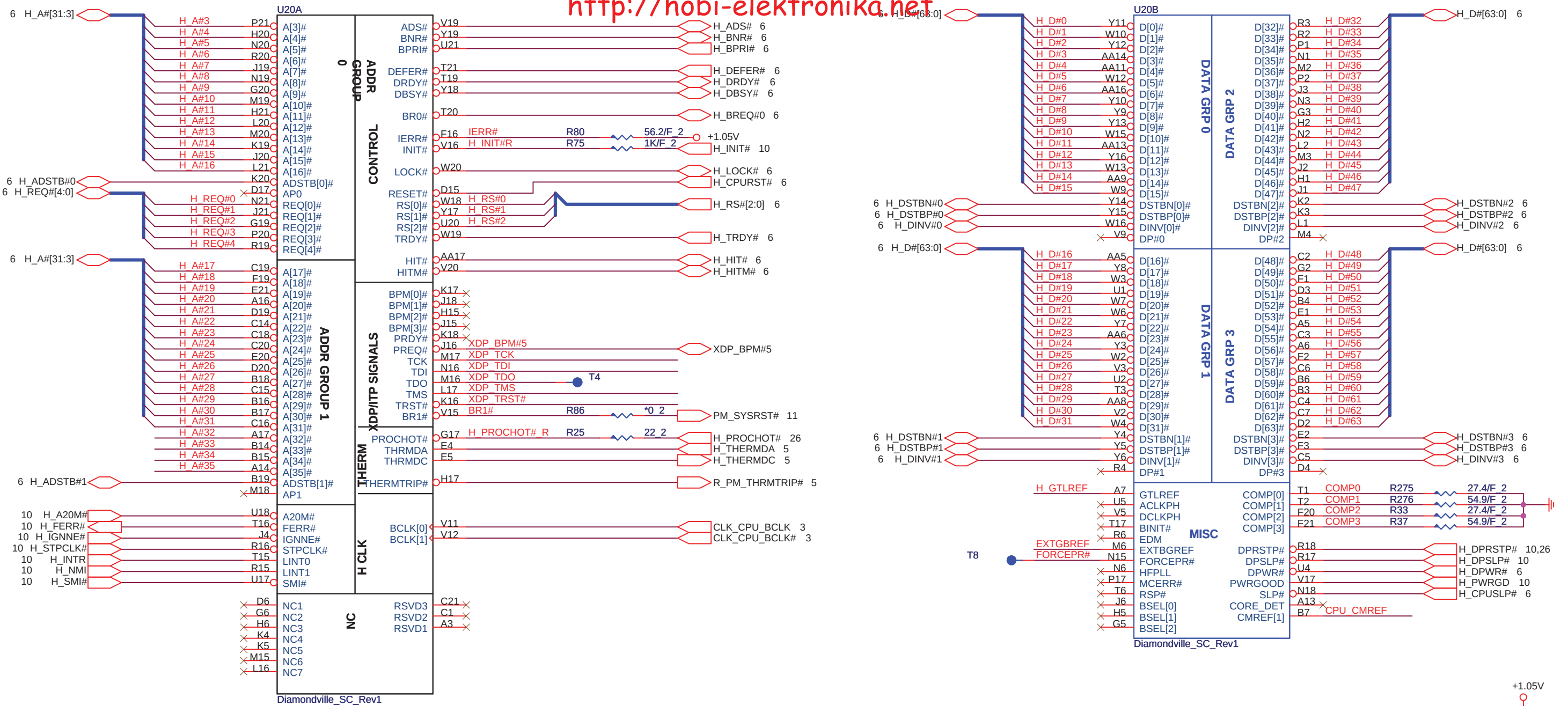
Quanta Computer Inc.

PROJECT : WB1

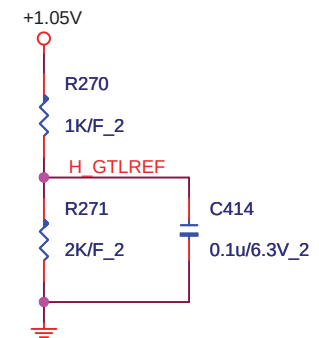
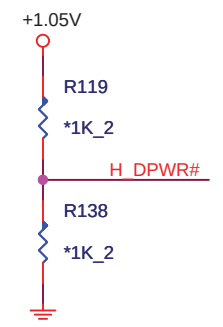
Size	Document Number	Rev
	CLOCK GENERATOR	C3A
Date:	Thursday, February 26, 2009	Sheet 3 of 26

CPU

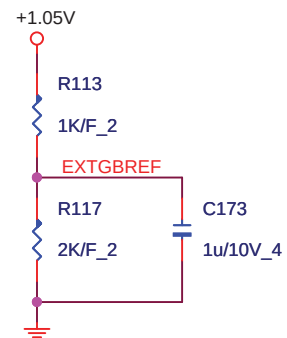
<http://hobi-elektronika.net>



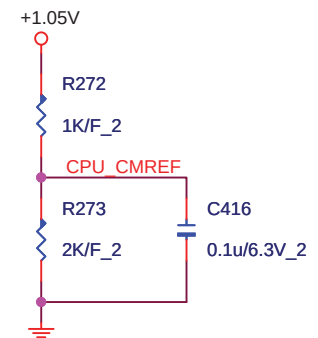
For defensive design reservation only in this initial release



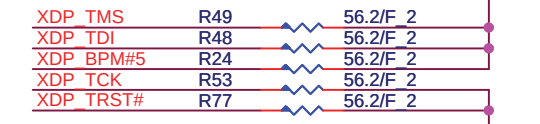
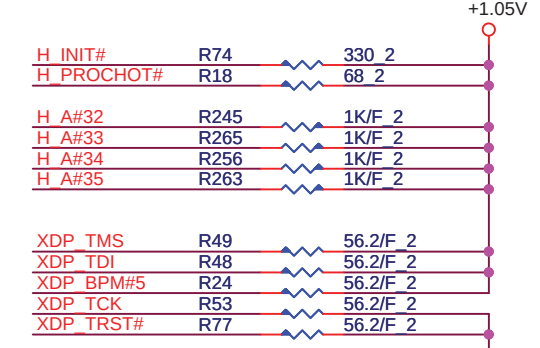
Layout note:
Zo=55ohm, 0.5" max
for GTLREF



Layout note:
Zo=55ohm, 0.5" max
max for EXTGBREF

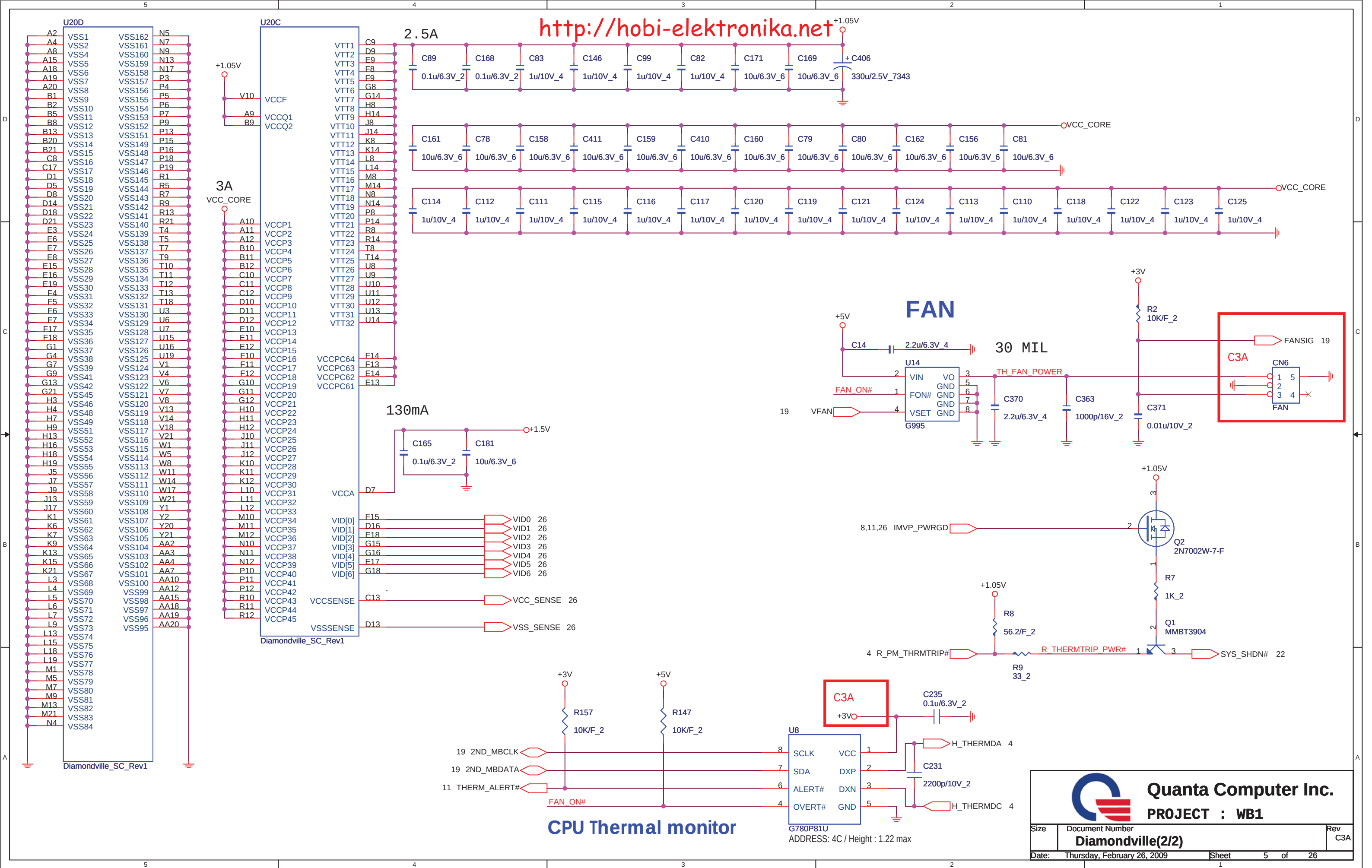


Layout note:
Zo=55ohm, 0.5" max
max for GTLREF



Quanta Computer Inc.
PROJECT : WB1

Size	Document Number	Rev
	Diamondville(1/2)	1A
Date:	Thursday, February 26, 2009	Sheet 4 of 26



<http://hobi-elektronika.net>

FAN

CPU Thermal monitor

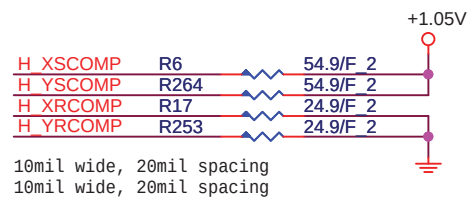
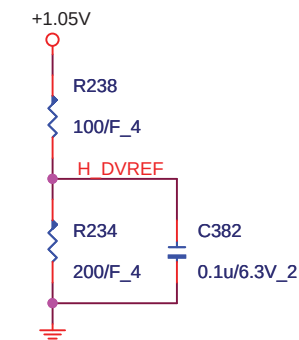
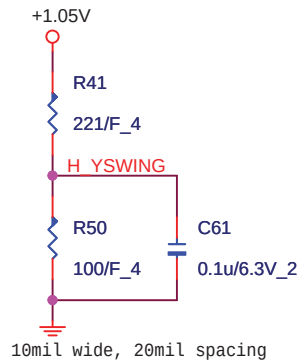
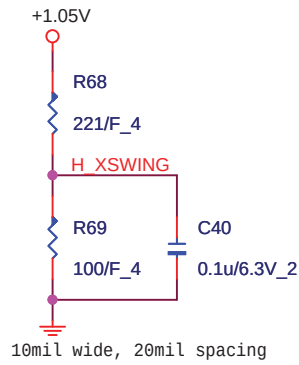
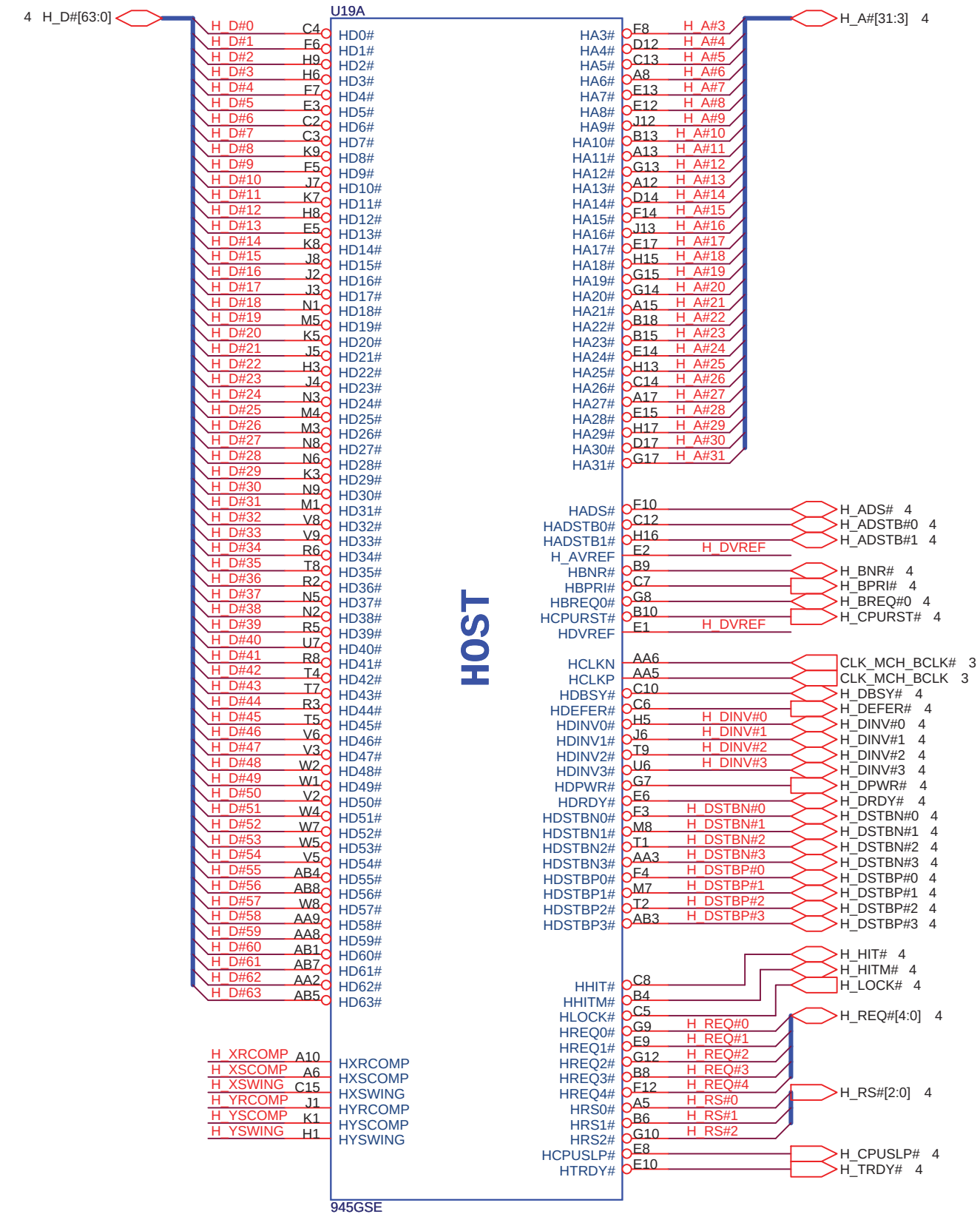


Quanta Computer Inc.
PROJECT : WB1

Size	Document Number	Rev
	Diamondville(2/2)	C3A
Date:	Thursday, February 26, 2009	Sheet 5 of 26

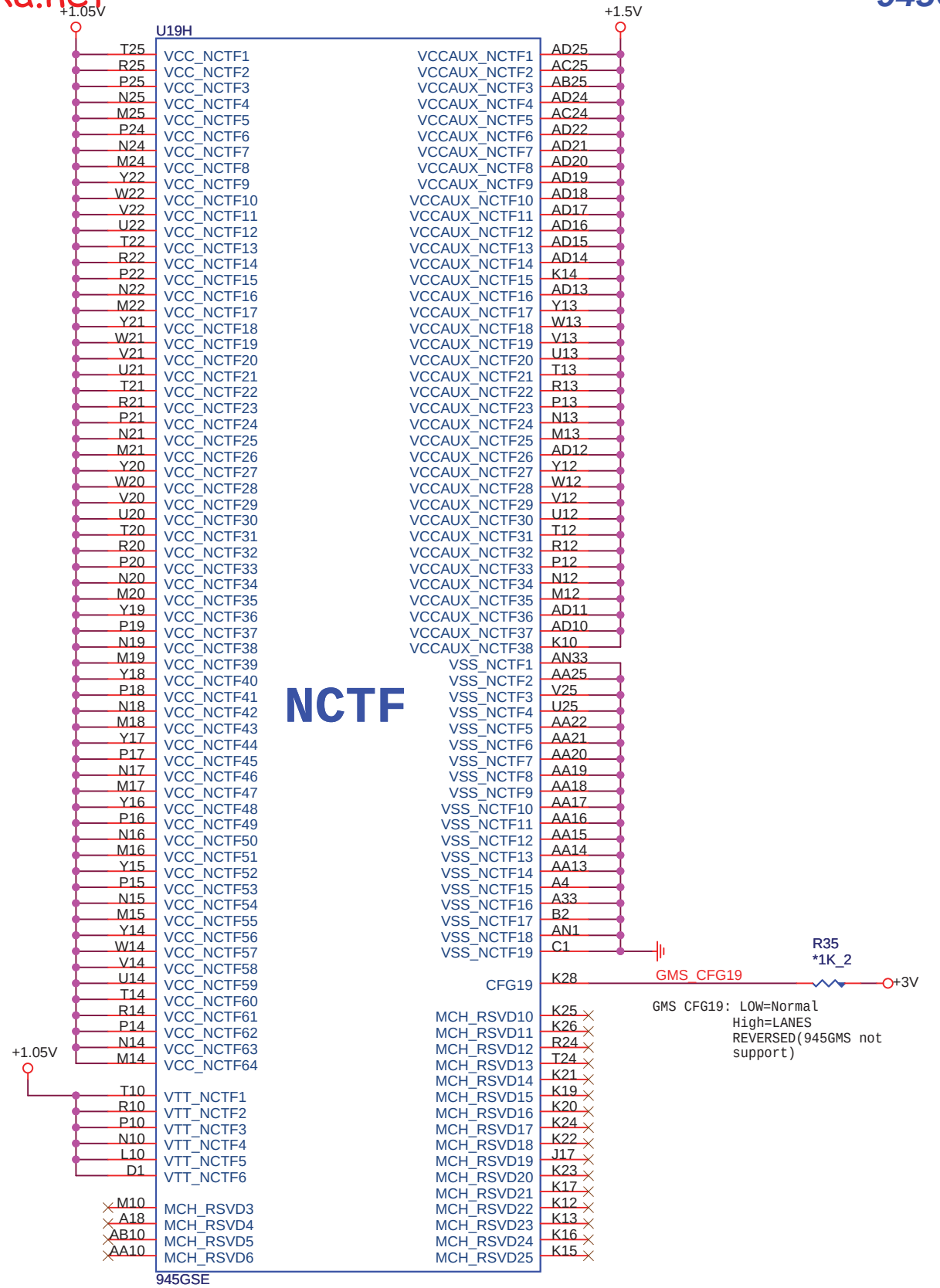
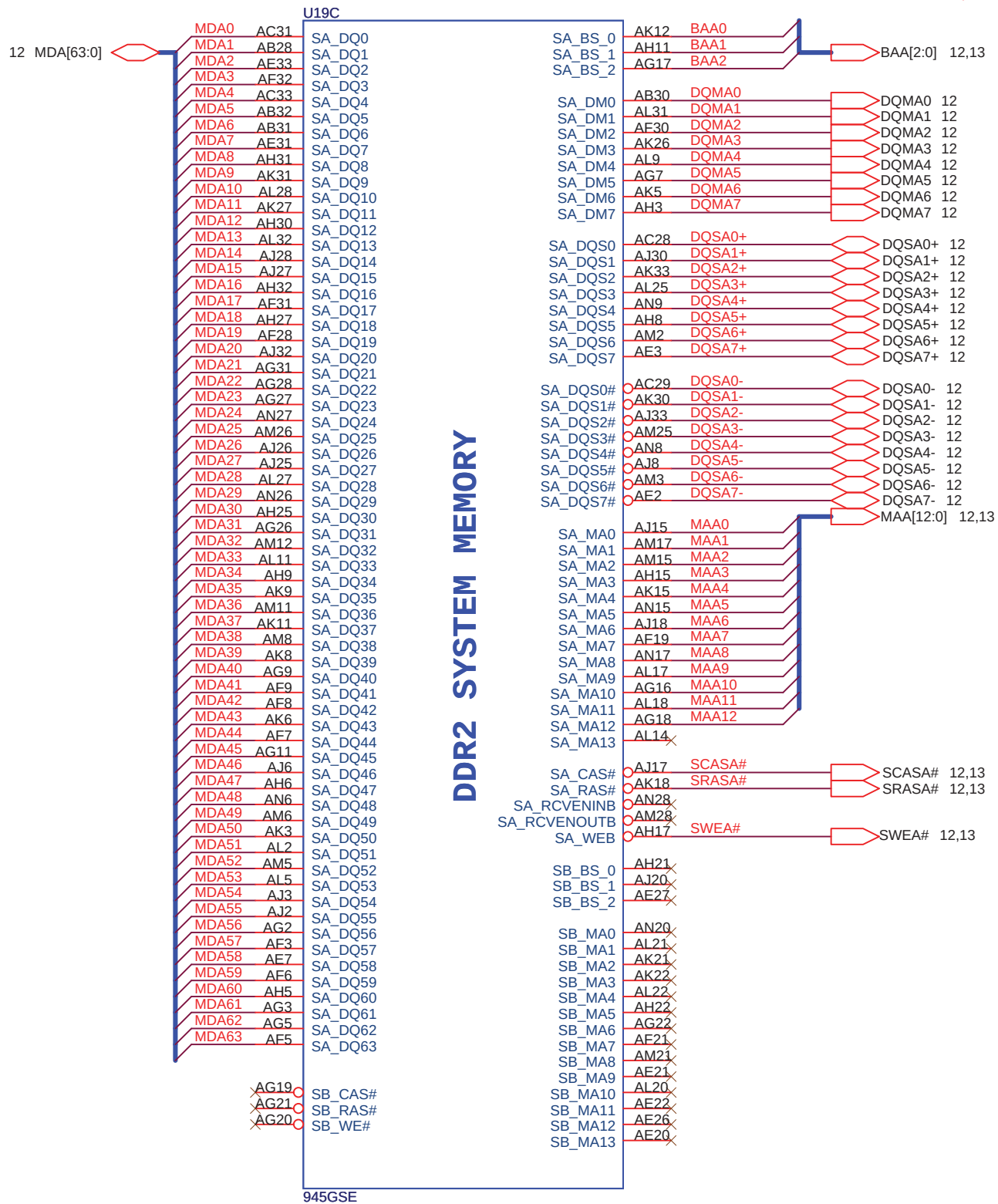
945GMS

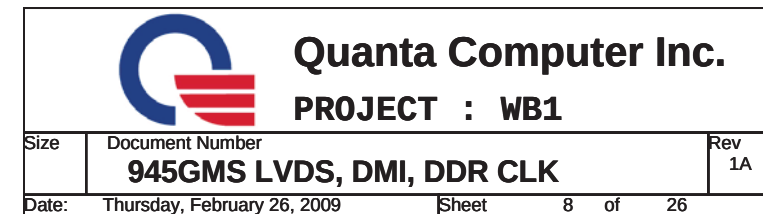
<http://hobi-elektronika.net>



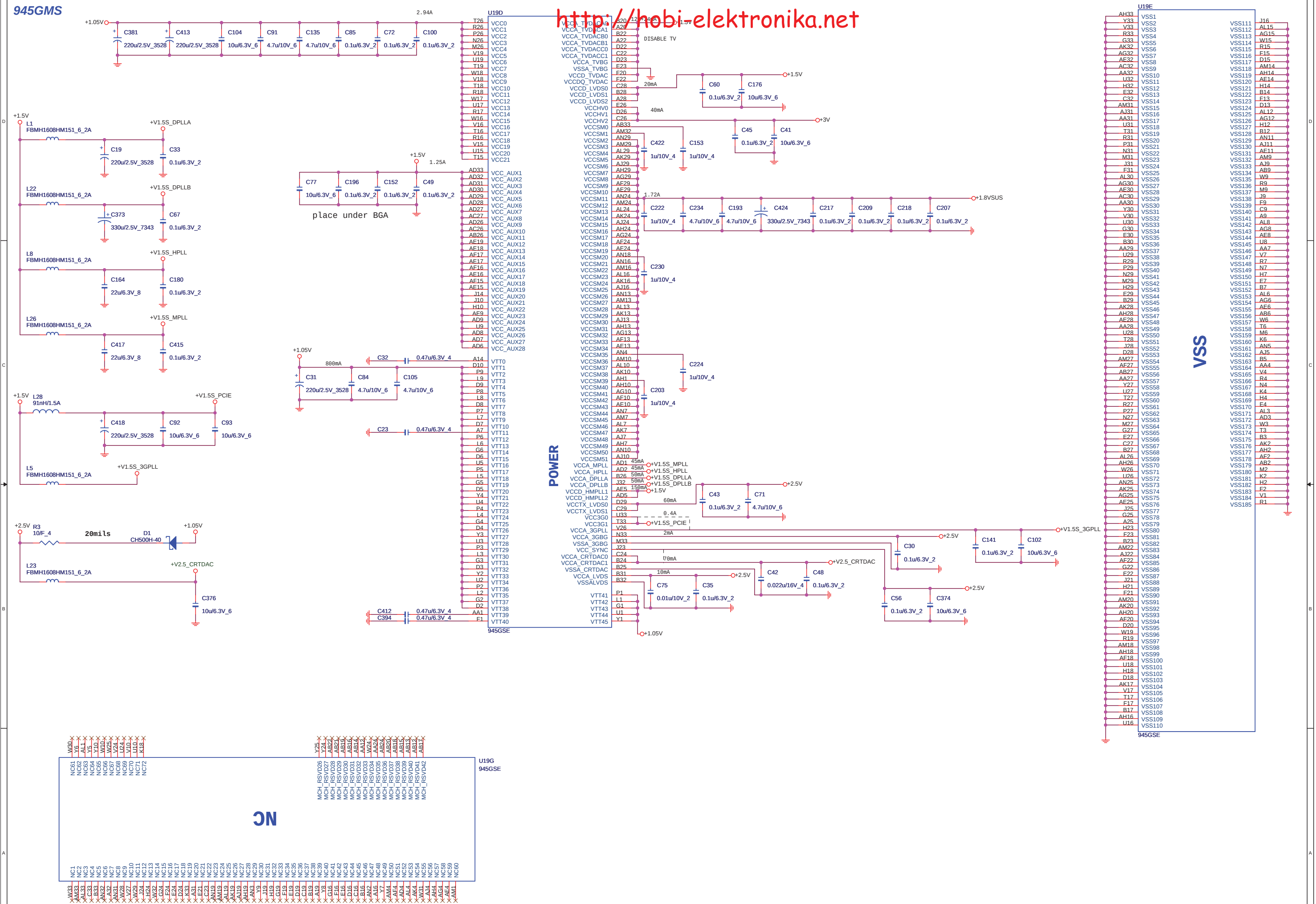
DDR2 SYSTEM MEMORY

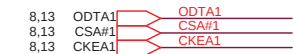
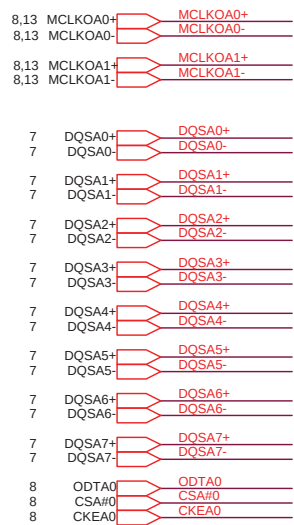
NCTF



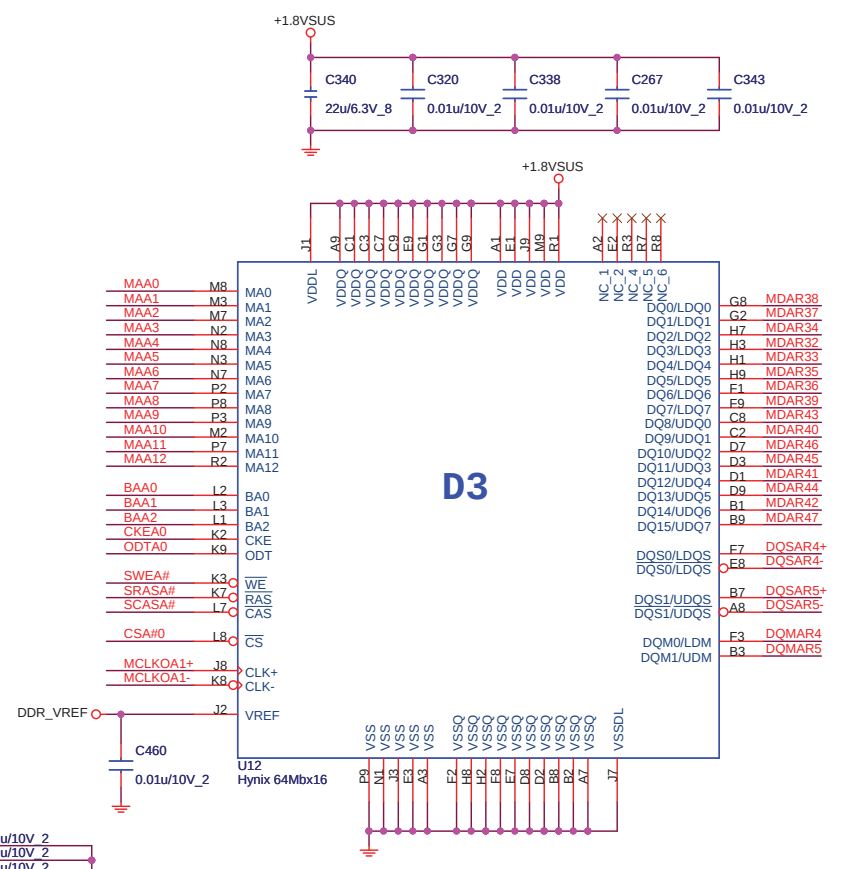
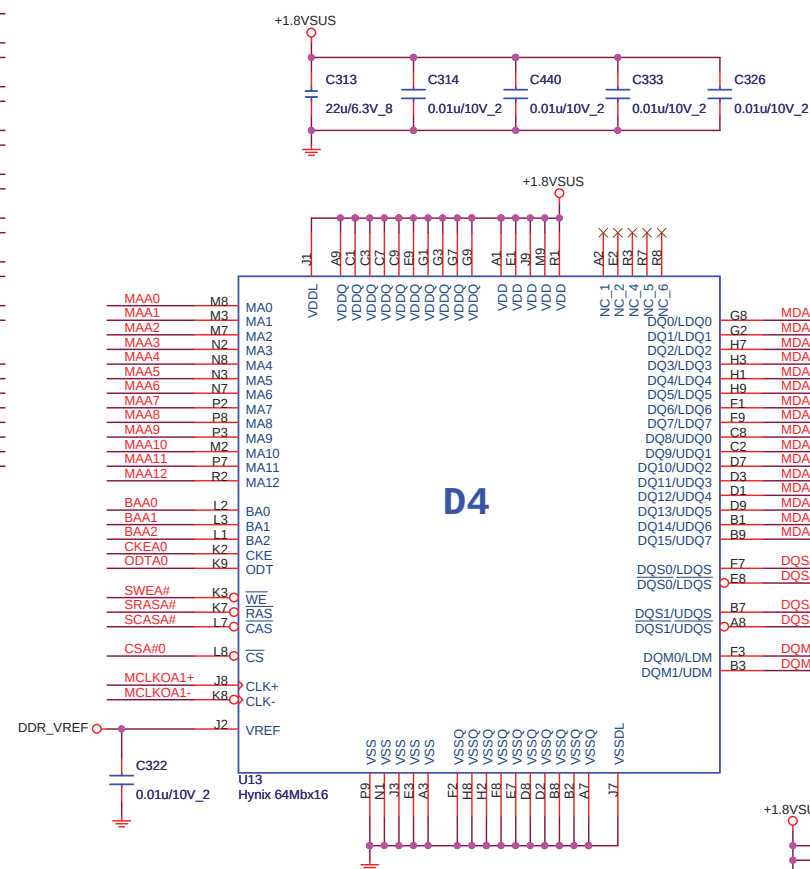
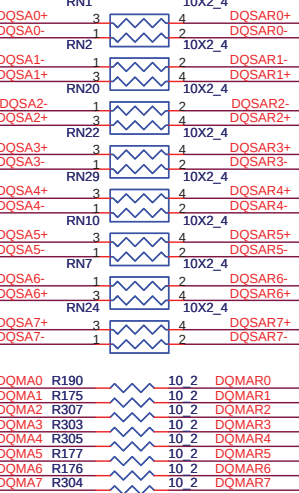
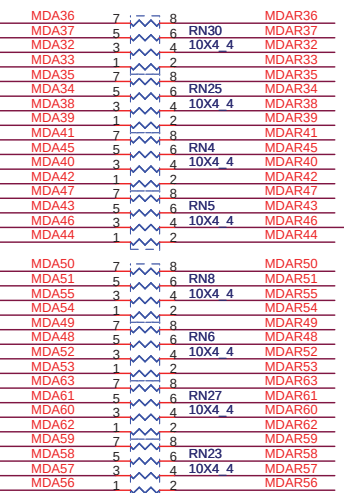
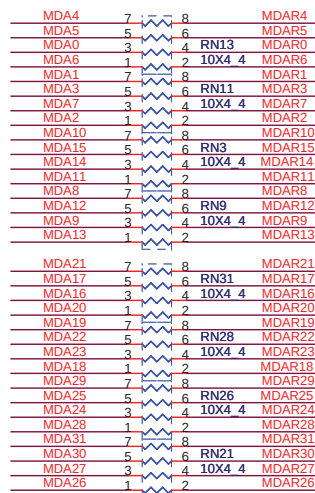
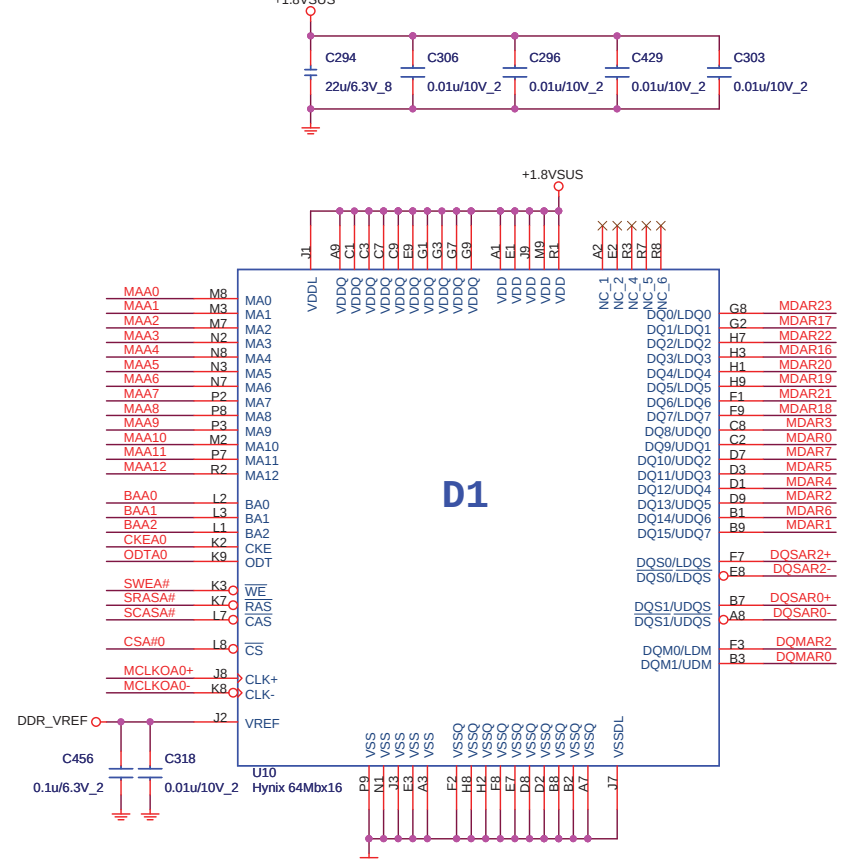
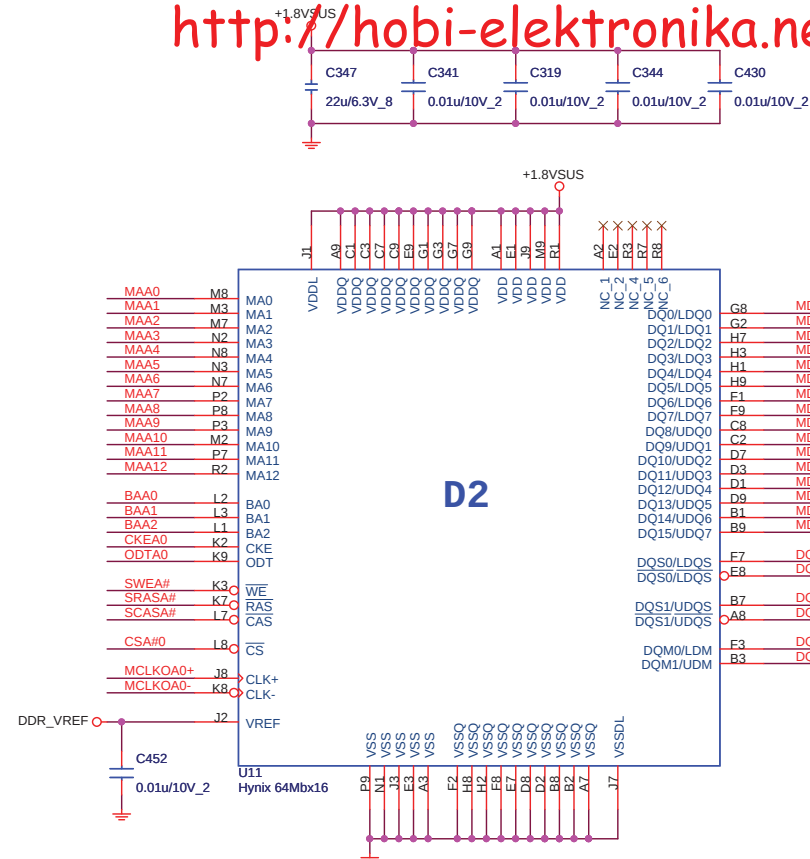
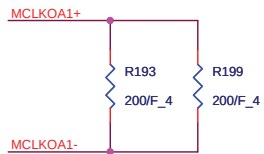
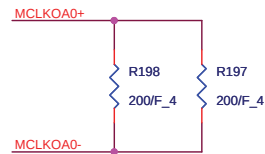


945GMS

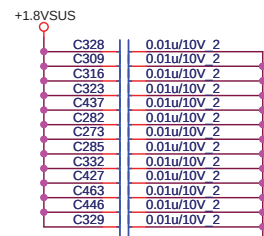
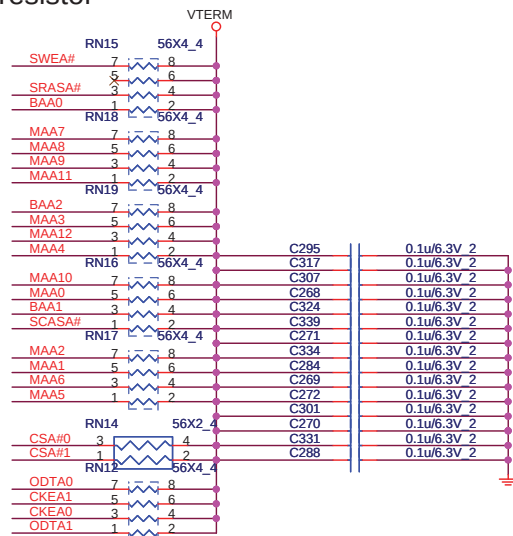




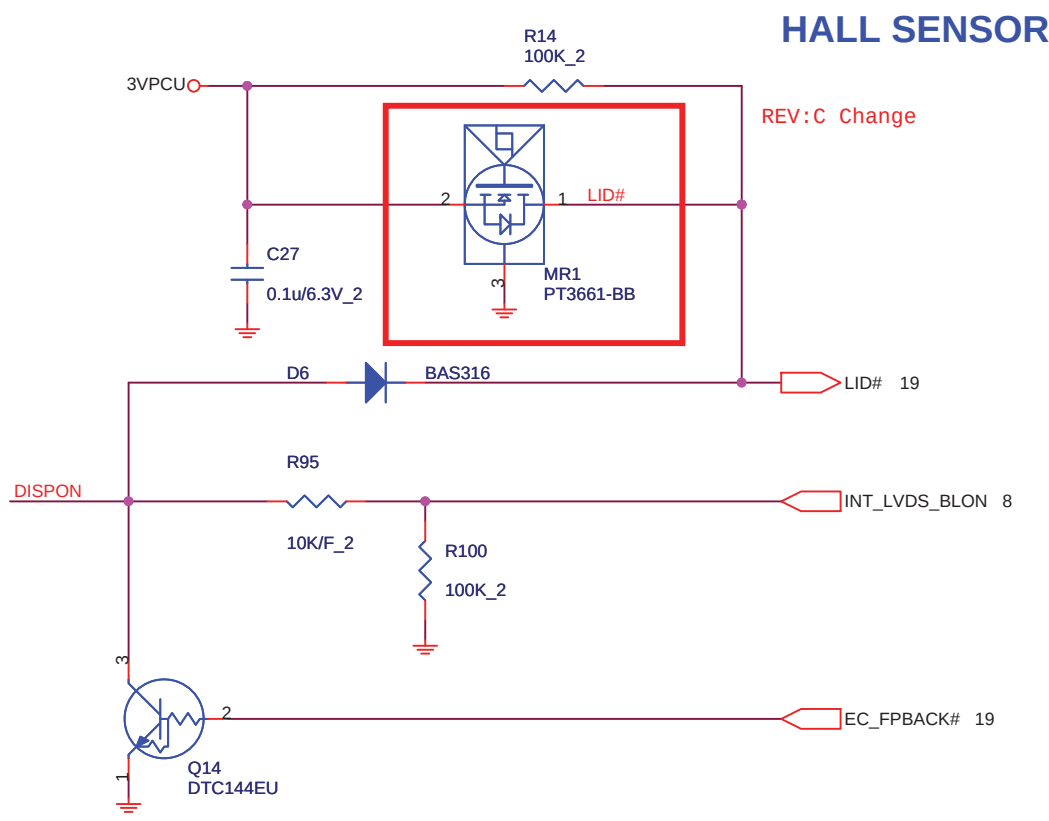
Layout Notes:
placement between



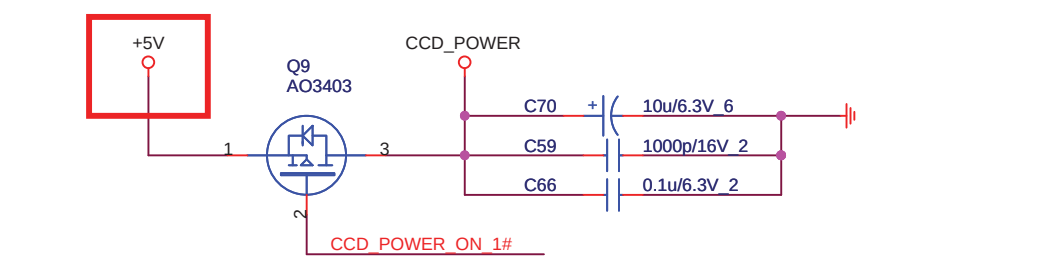
Termination resistor



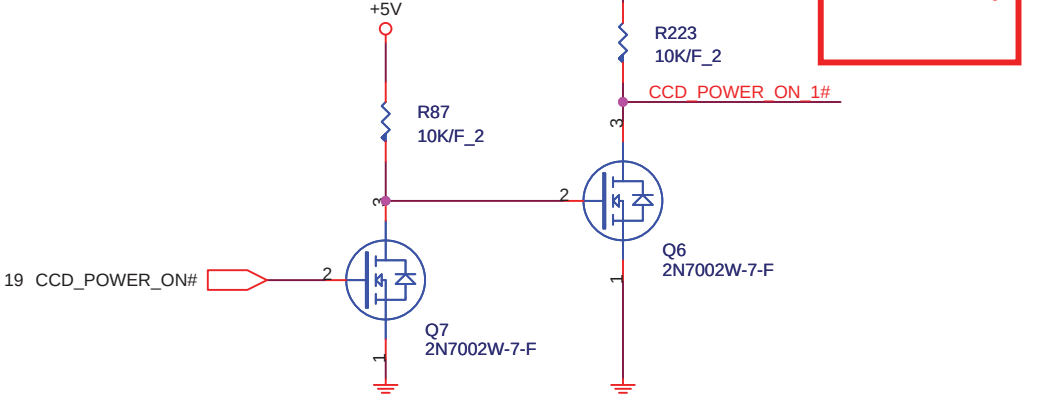




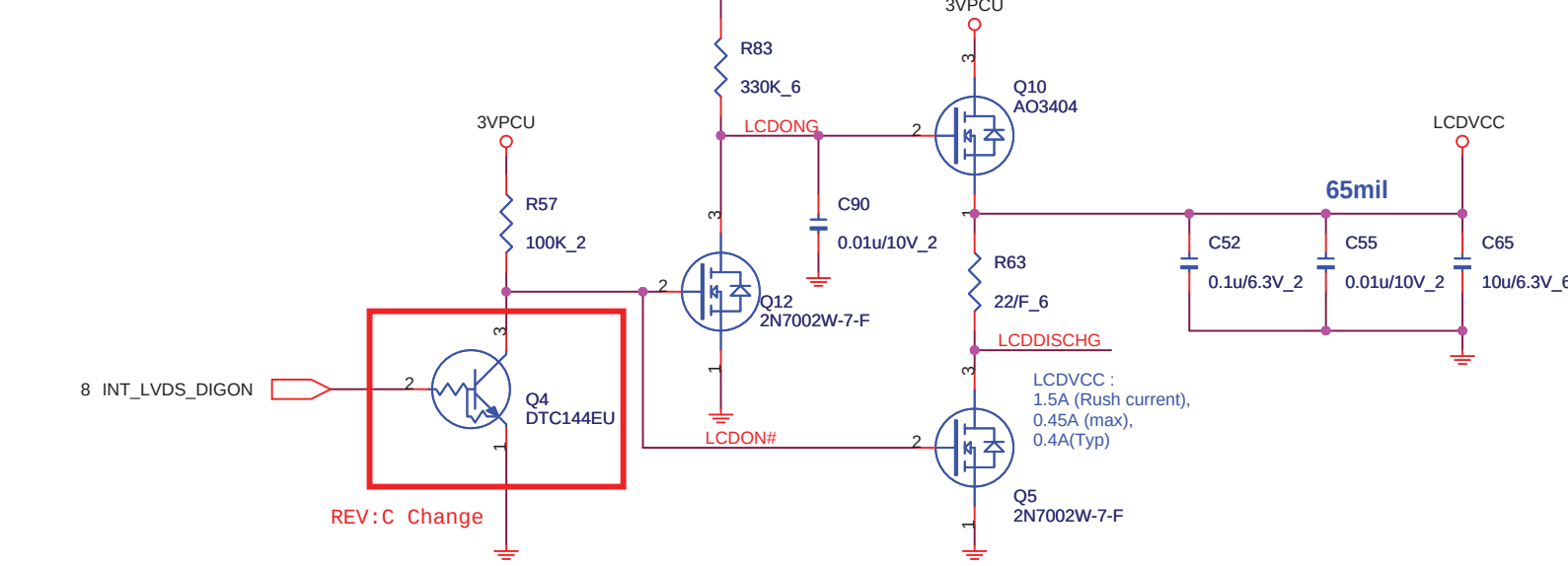
CAMERA POWER



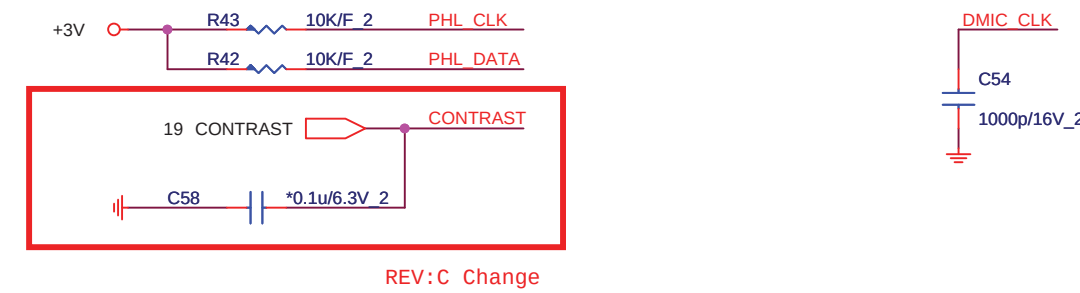
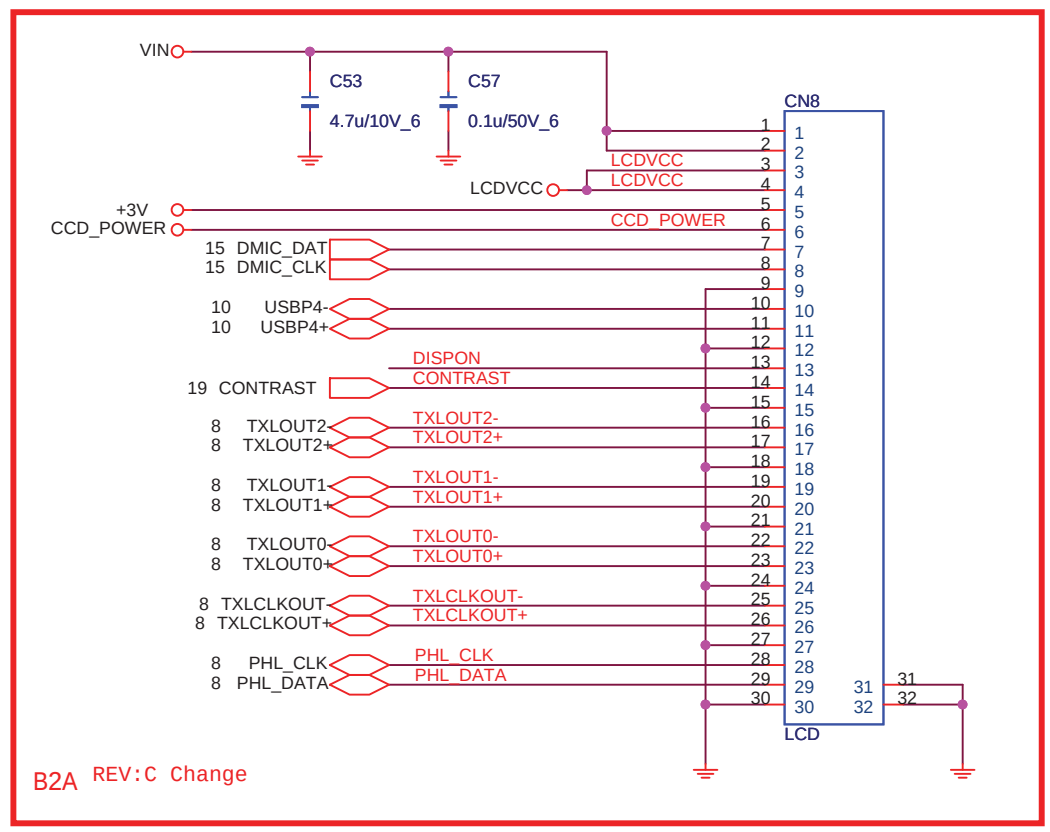
Pannel without DISPON circuit



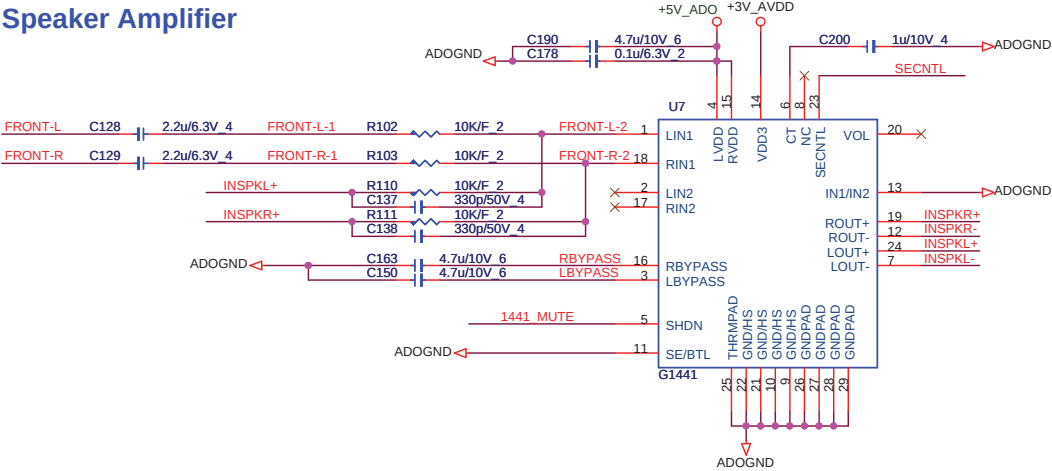
LCD POWER SWITCH



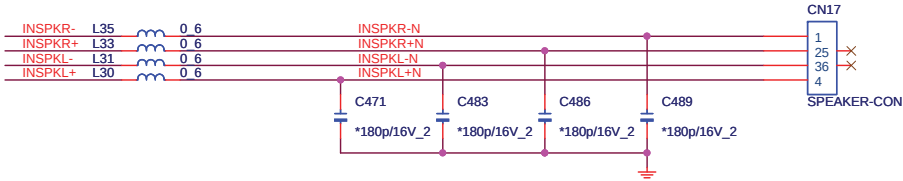
LCD MODULE



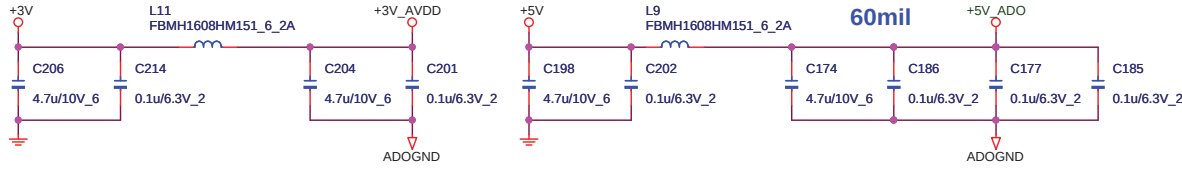
Speaker Amplifier



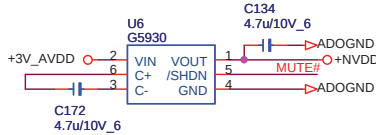
SPEAKER



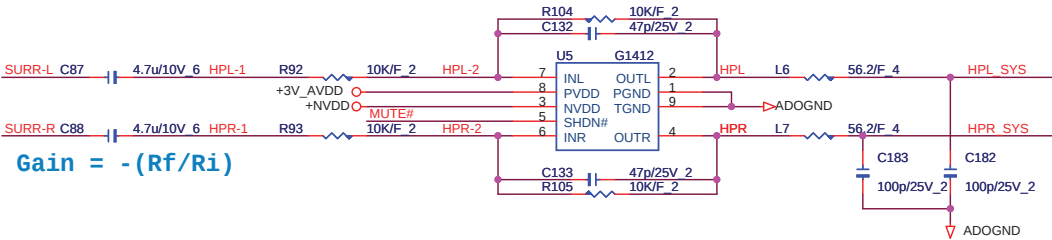
Amplifier POWER



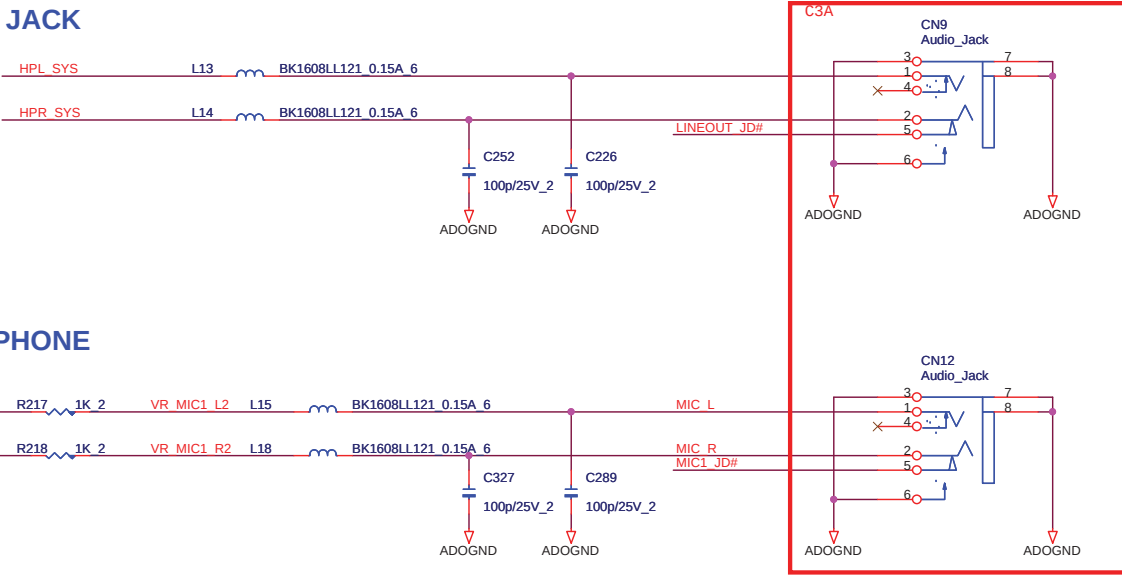
LINE OUT Amp Charge Pump



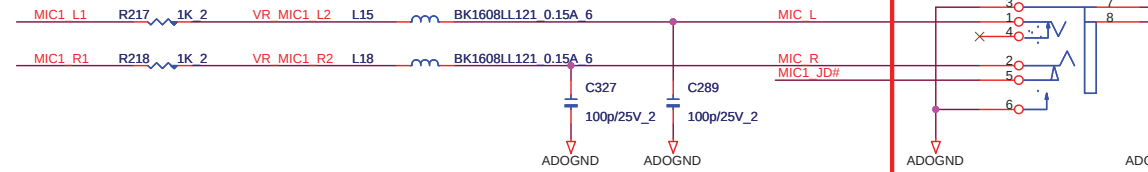
LINE OUT Amplifier



PHONE JACK

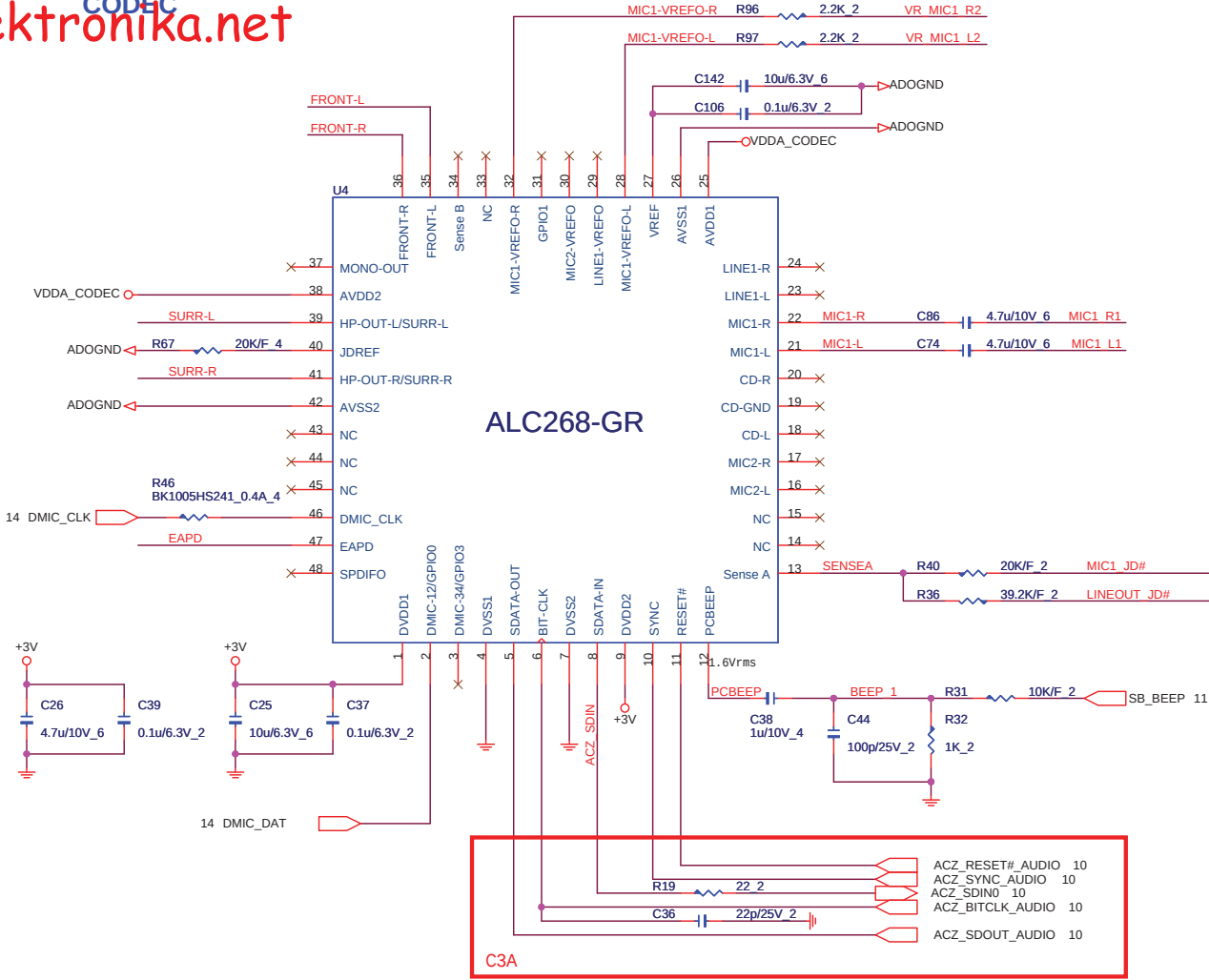


MICROPHONE

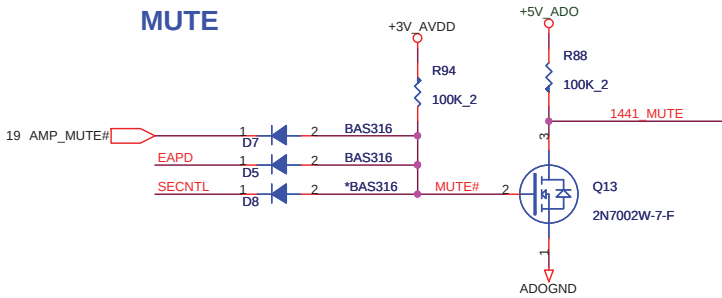


<http://hobi-elektronika.net>

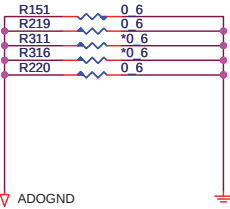
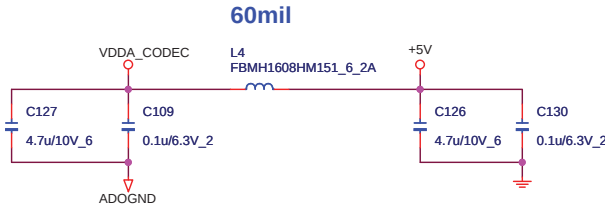
CODEC



MUTE



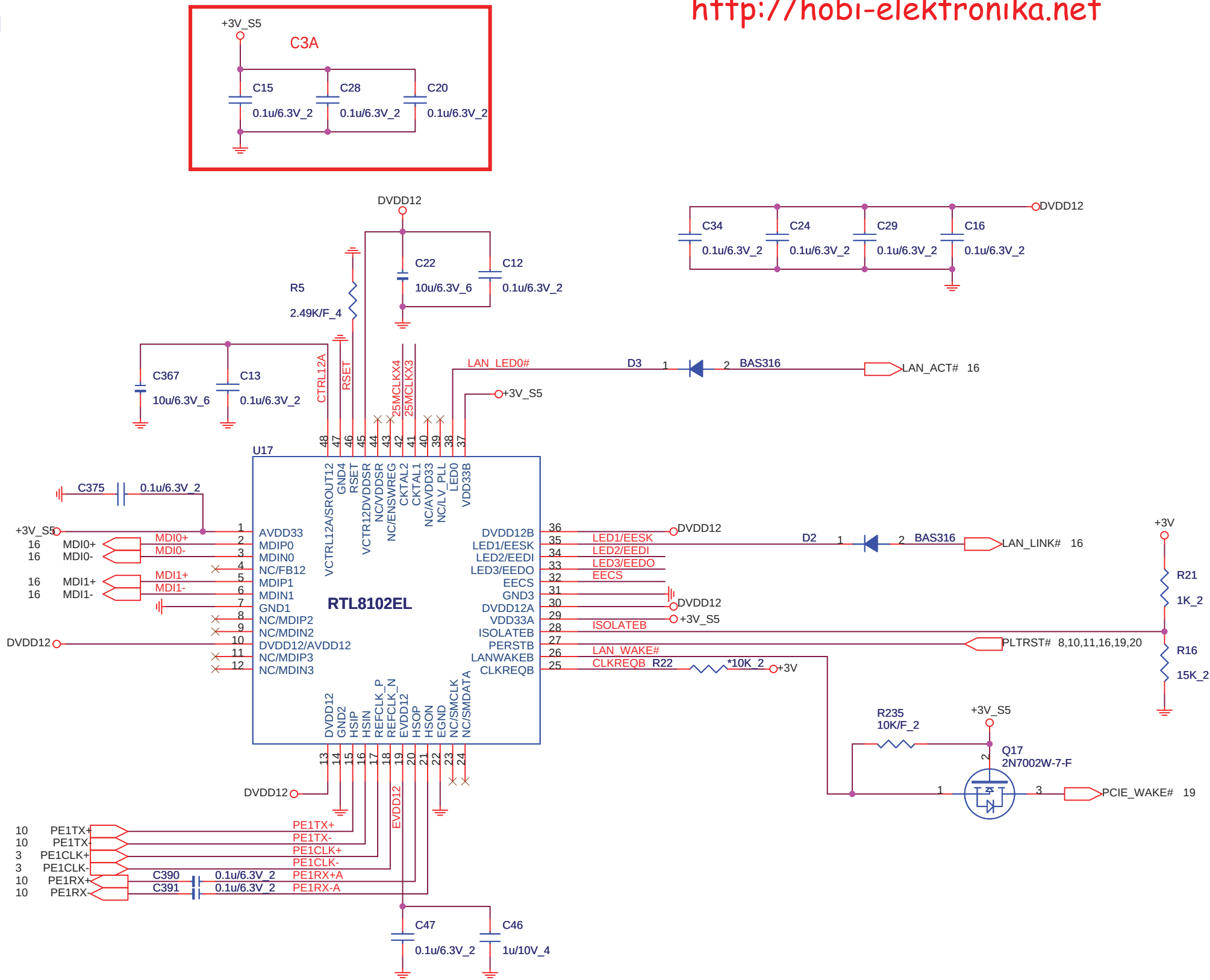
Codec Power



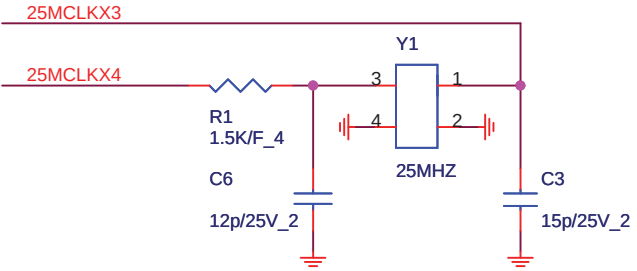
Tied at one point only under the codec or near the codec

LAN

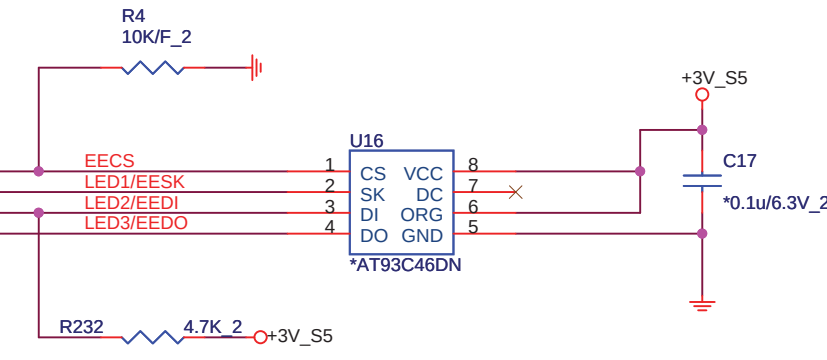
<http://hobi-elektronika.net>



X'tal 25MHz



EEPROM

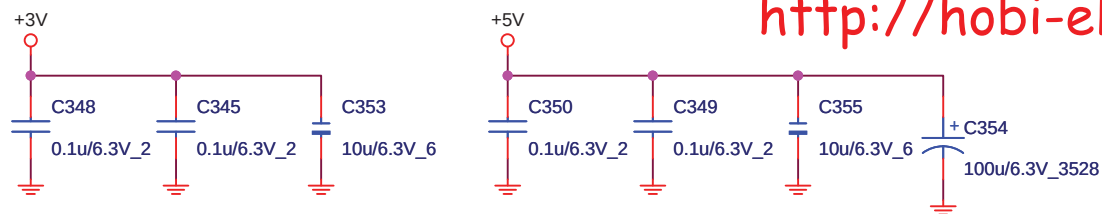


Quanta Computer Inc.
PROJECT : WB1

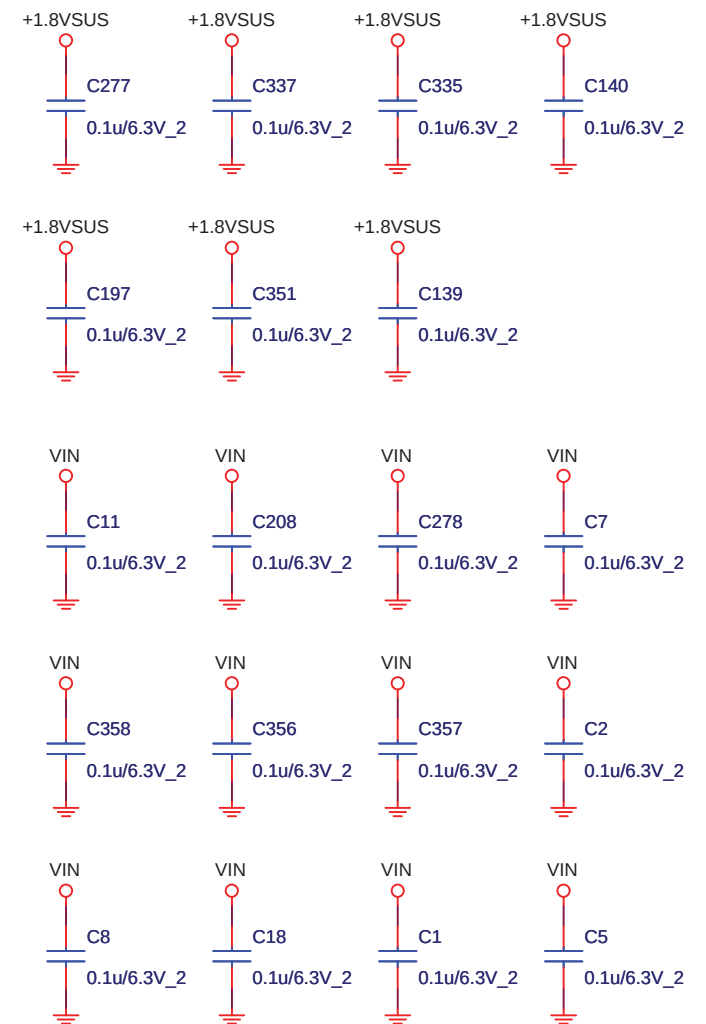
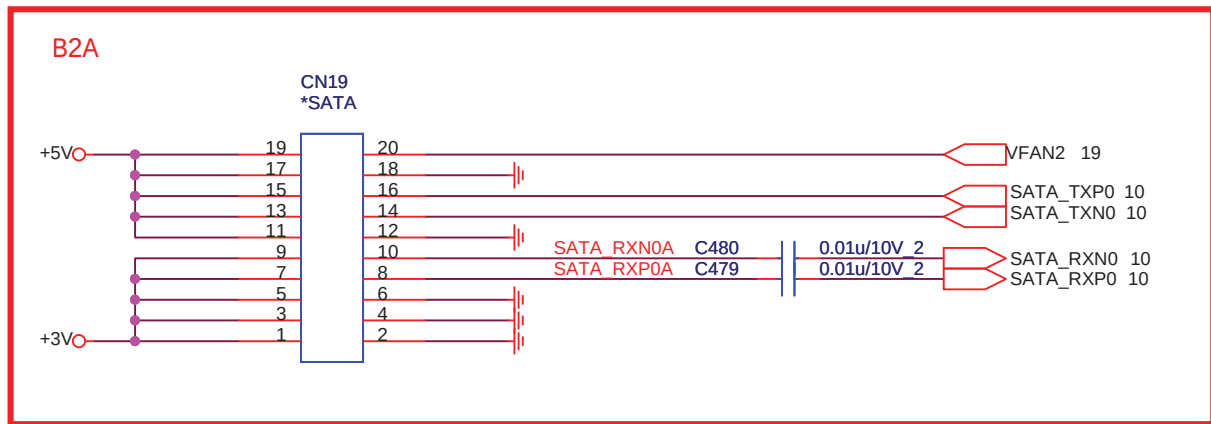
Size	Document Number	Rev
	LAN_RTL8102EL/RJ45	C3A

Date: Thursday, February 26, 2009 Sheet 17 of 26

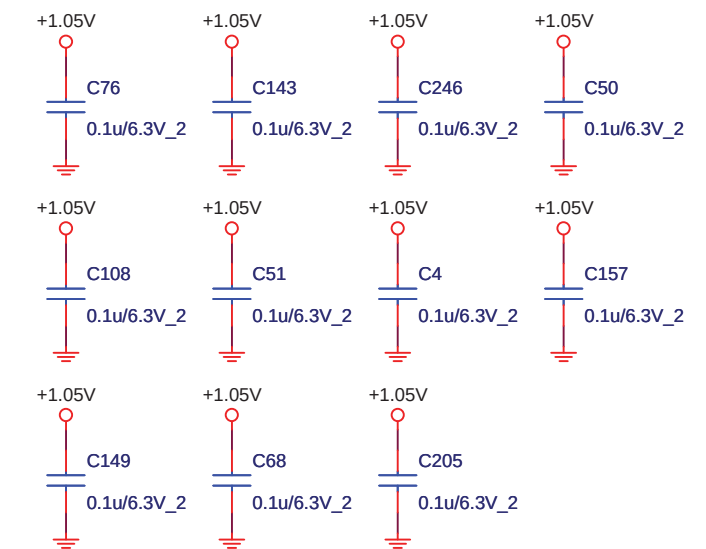
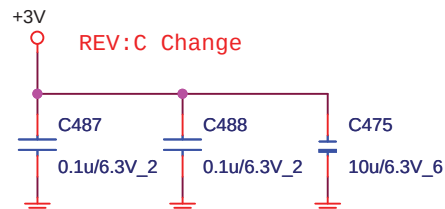
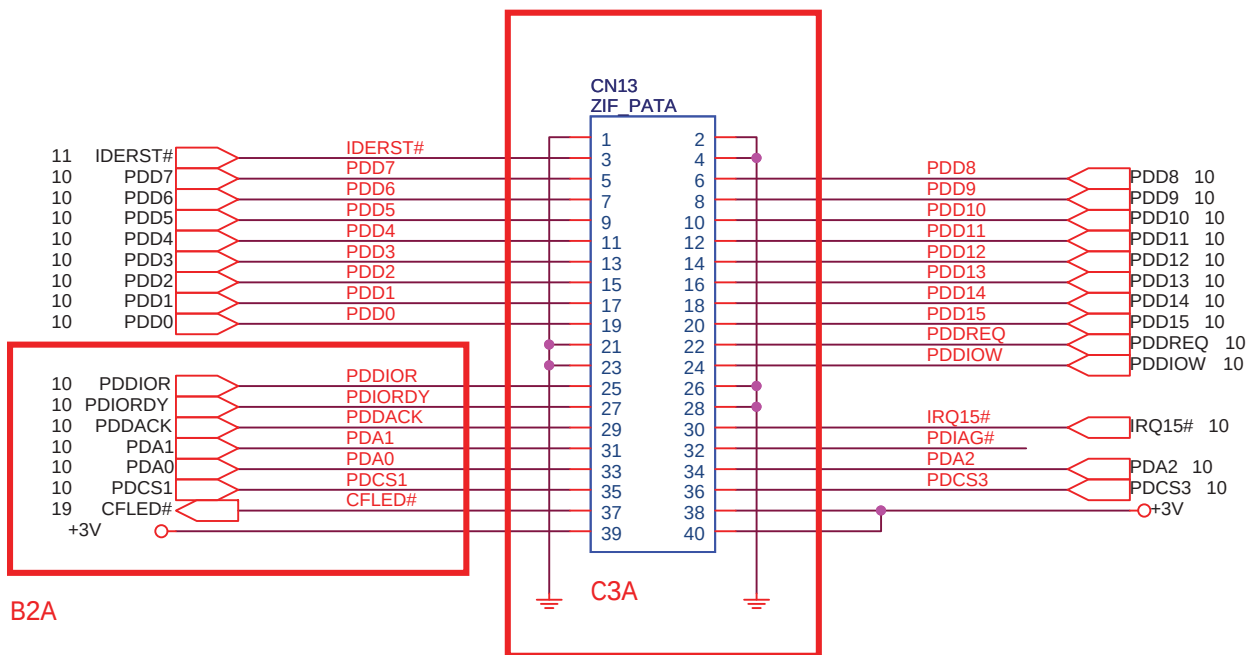
SATA HDD



<http://hobi-elektronika.net>



1.8" PATA HDD / ZIF SSD FPC CONNECTOR



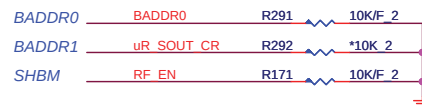
Quanta Computer Inc.

PROJECT : WB1

Size	Document Number	Rev
	HDD/SSD	C3A
Date:	Thursday, February 26, 2009	Sheet 18 of 26

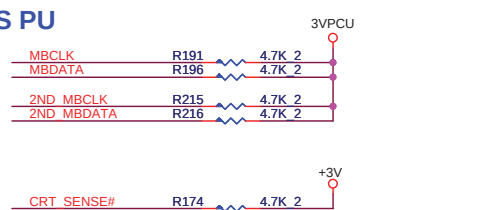
I/O Address		
BADDR1-0	Index	Data
0 0	XOR TREE TEST MODE	
0 1	CORE DEFINED	
1 0	2Eh	2Fh
1 1	164Eh	164Fh

SHBM=0: Enable shared memory with host BIOS

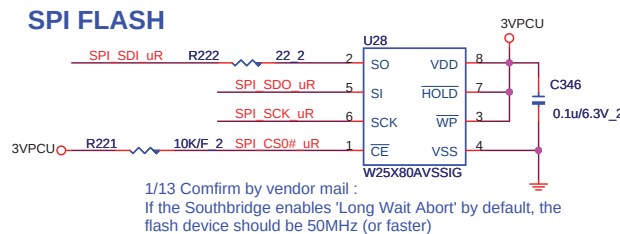


1/13 Confirm by vendor mail :
Disabled ('1') if using FWH device on LPC.
Enabled ('0') if using SPI flash for both system BIOS and EC firmware

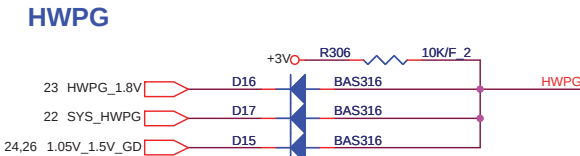
SM BUS PU



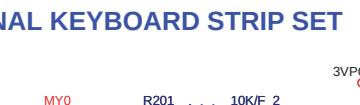
SPI FLASH



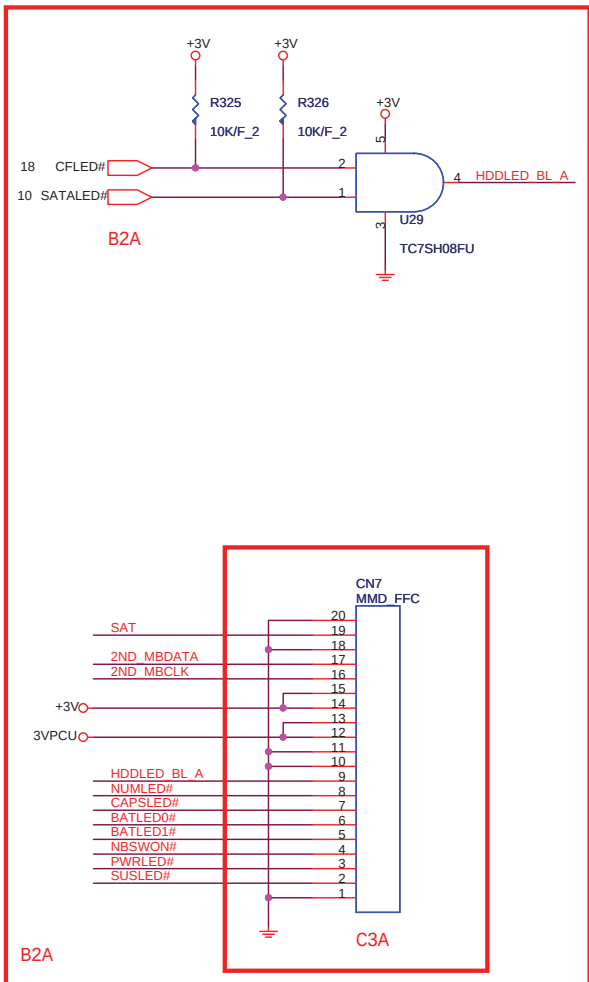
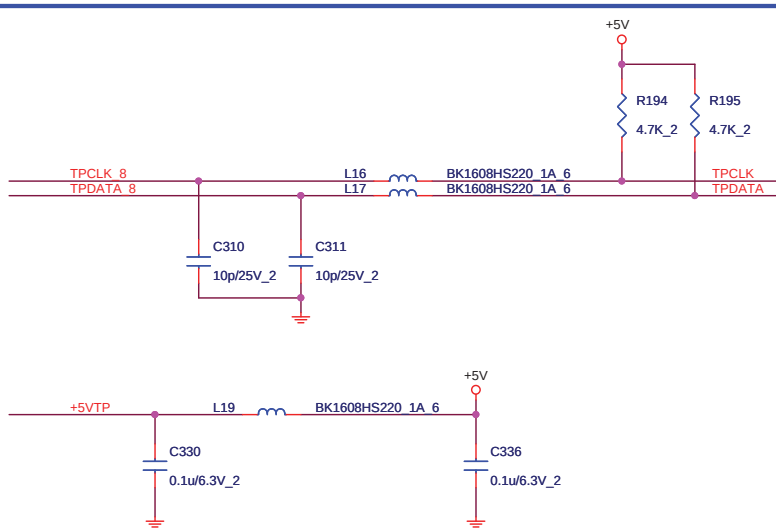
HWPG

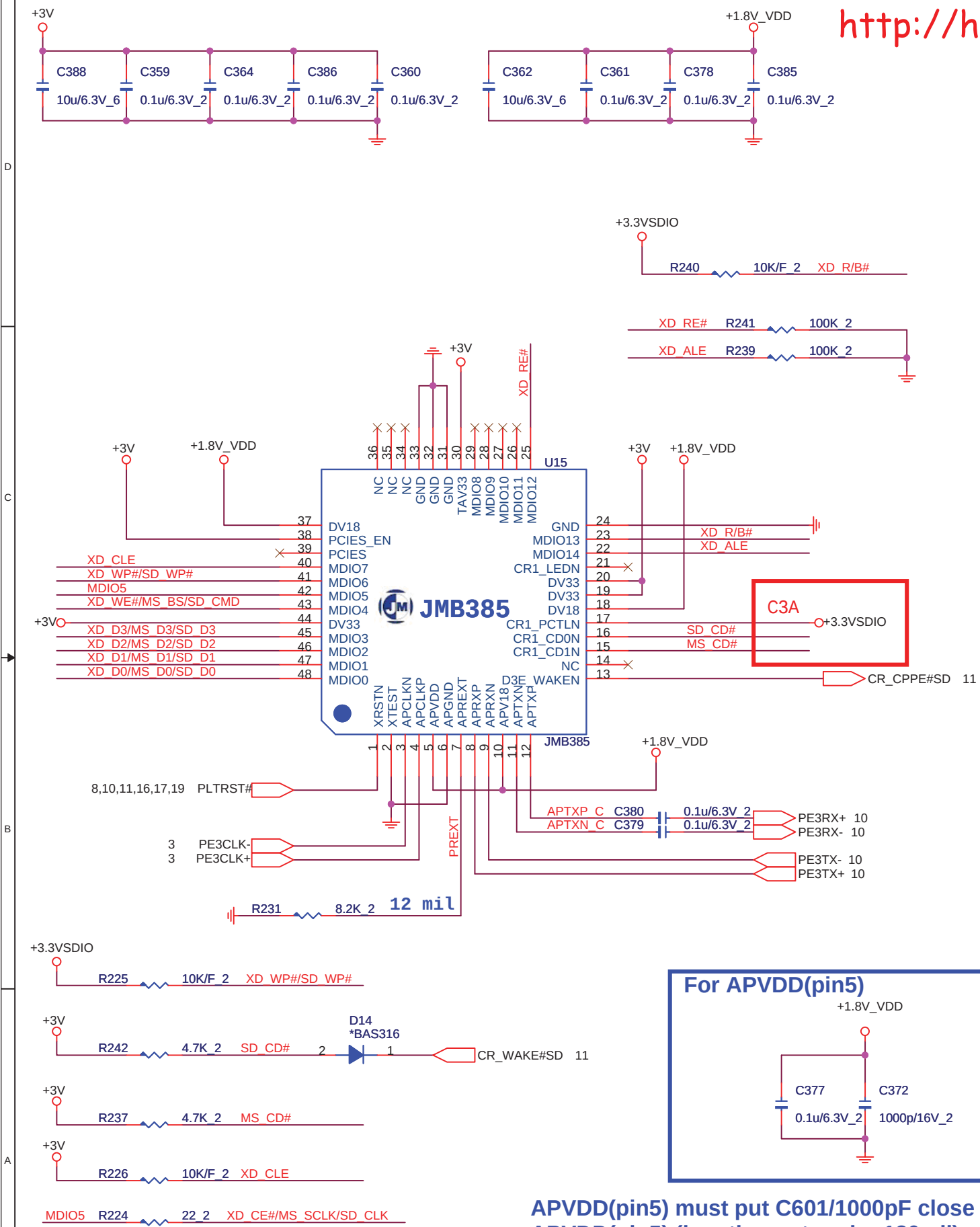


INTERNAL KEYBOARD STRIP SET



Keyboard & Touch Pad Connector



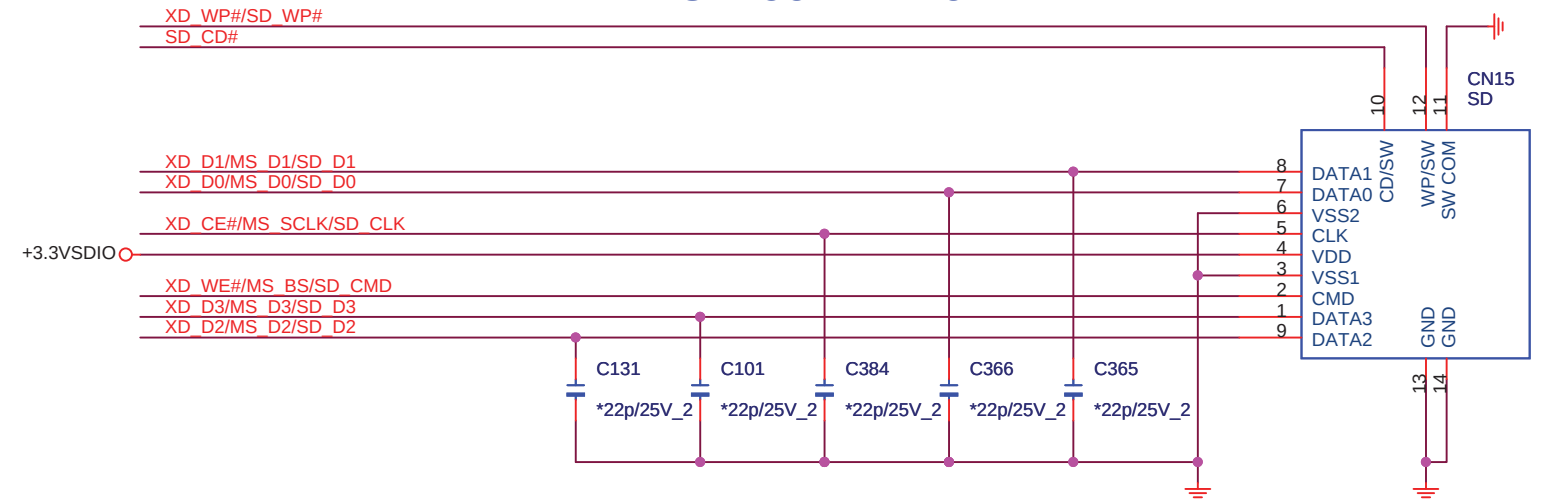


Memory Card Power Supply

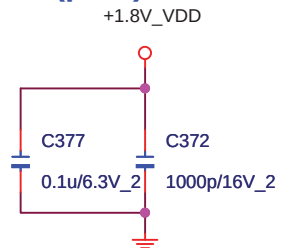
Use 0805 type and over 20 mils trace width on both side



SD CONNDETOR



For APVDD(pin5)



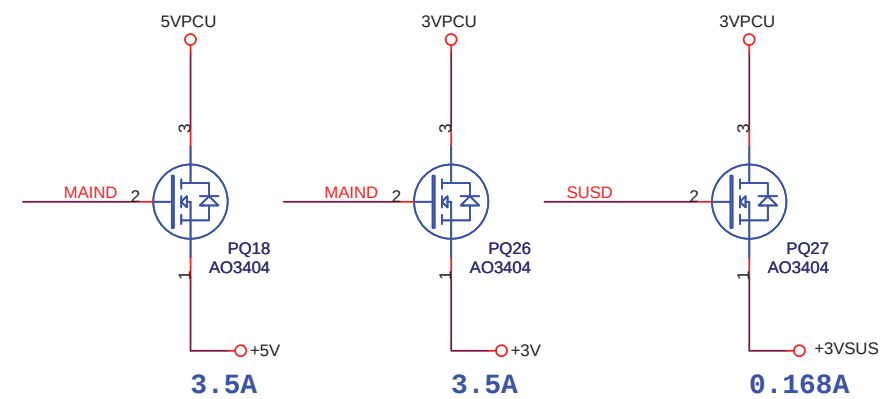
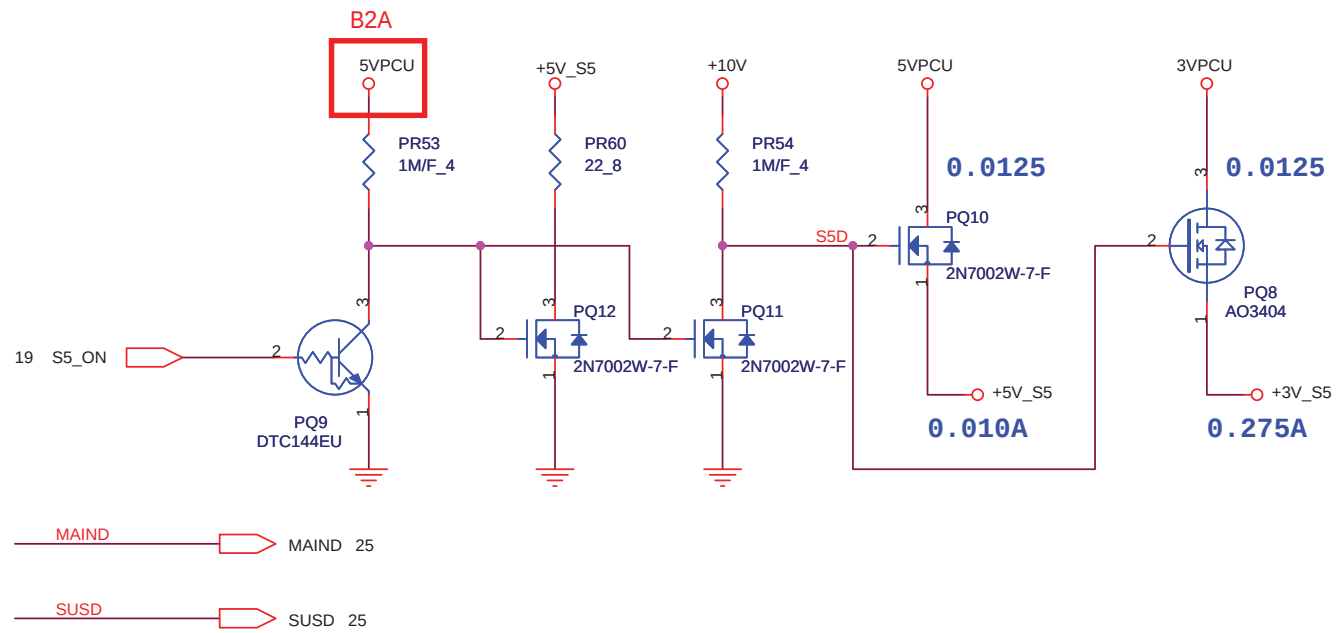
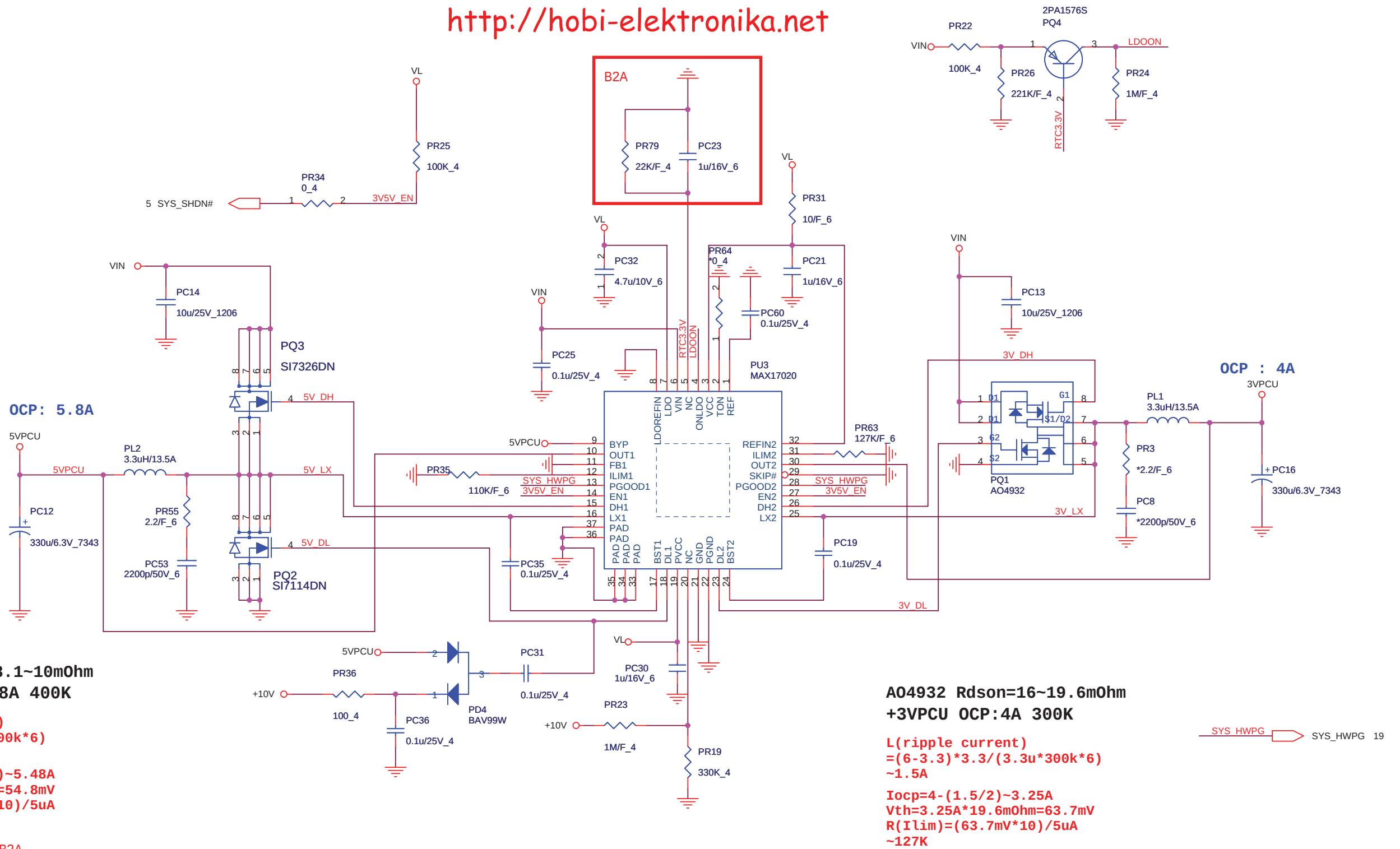
APVDD(pin5) must put C601/1000pF close to APVDD(pin5) (length must under 120mil) and trace width = 20mil, after C601, pls put one more 0.1uF for it.



Quanta Computer Inc.

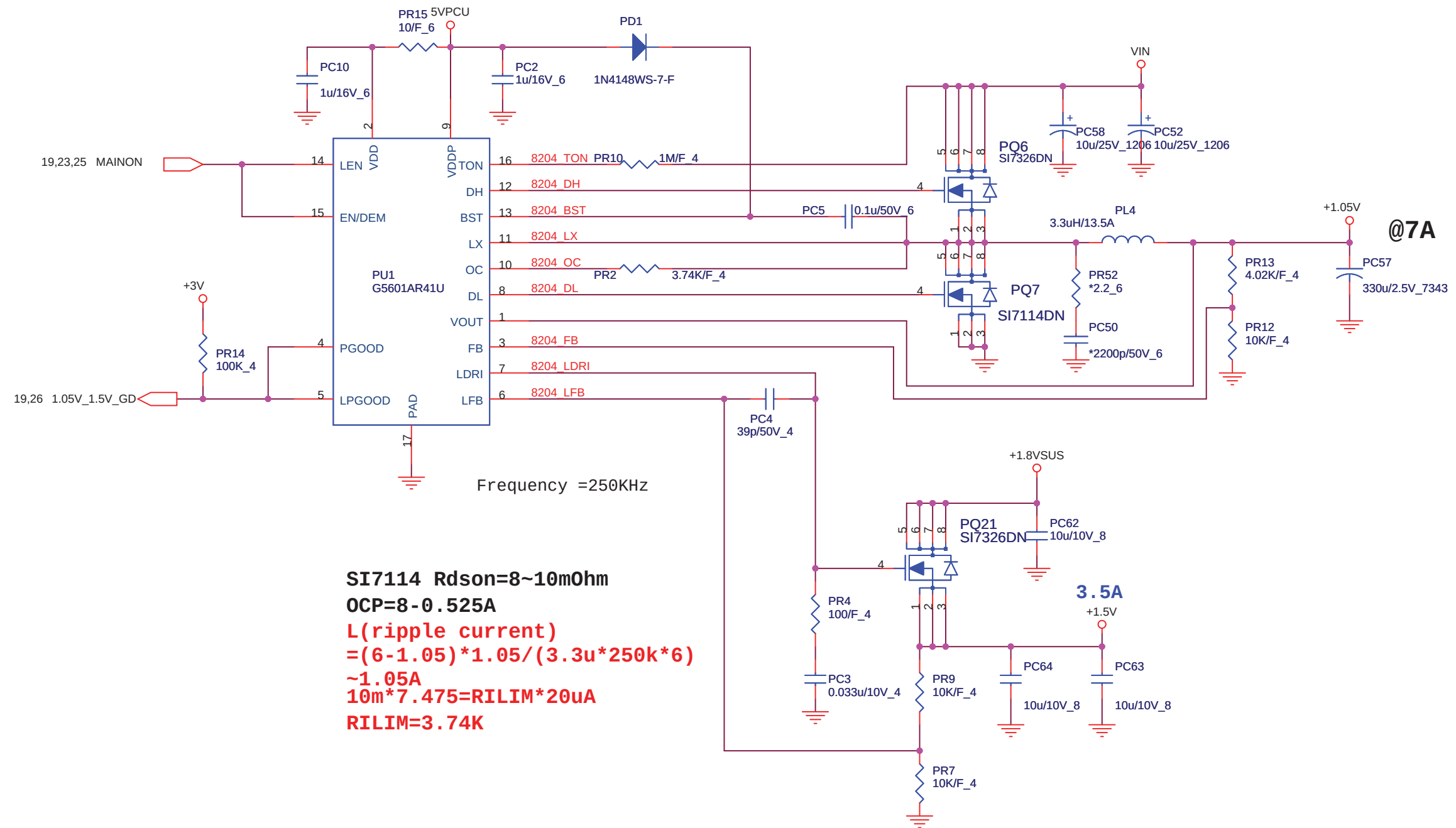
PROJECT : WB1

Size	Document Number JMB385 SDIO	Rev C3A
Date:	Thursday, February 26, 2009	Sheet 20 of 26





Sheet 23 of 26



Quanta Computer Inc.
PROJECT : WB1

Size	Document Number	Rev
	DC/DC 1.05V / 1.5V	1A
Date:	Thursday, February 26, 2009	Sheet 24 of 26

