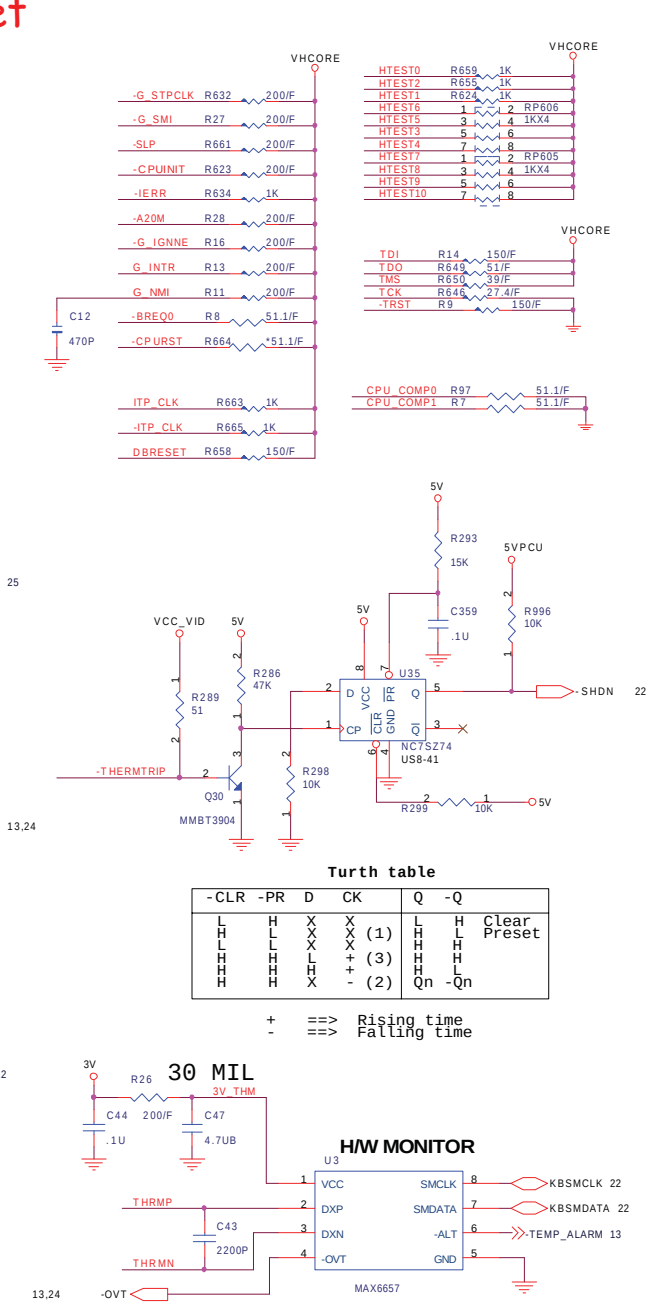
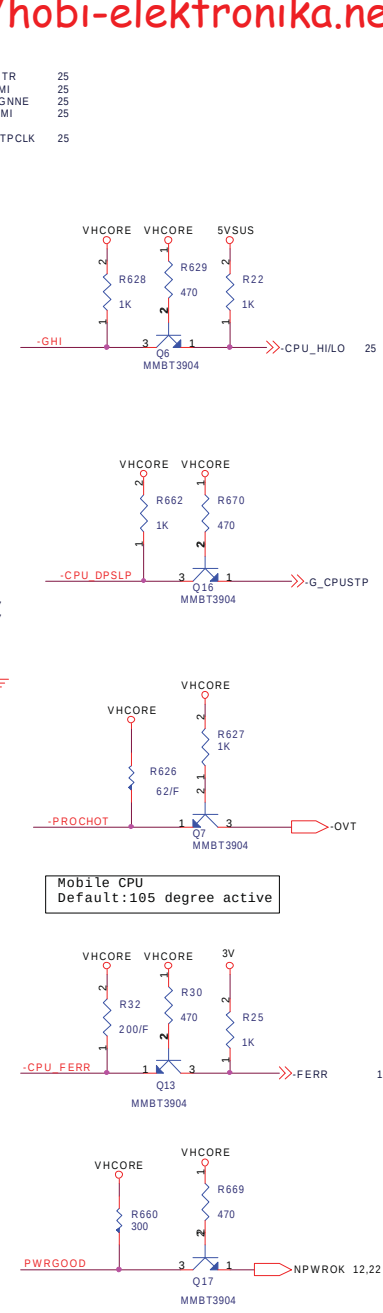
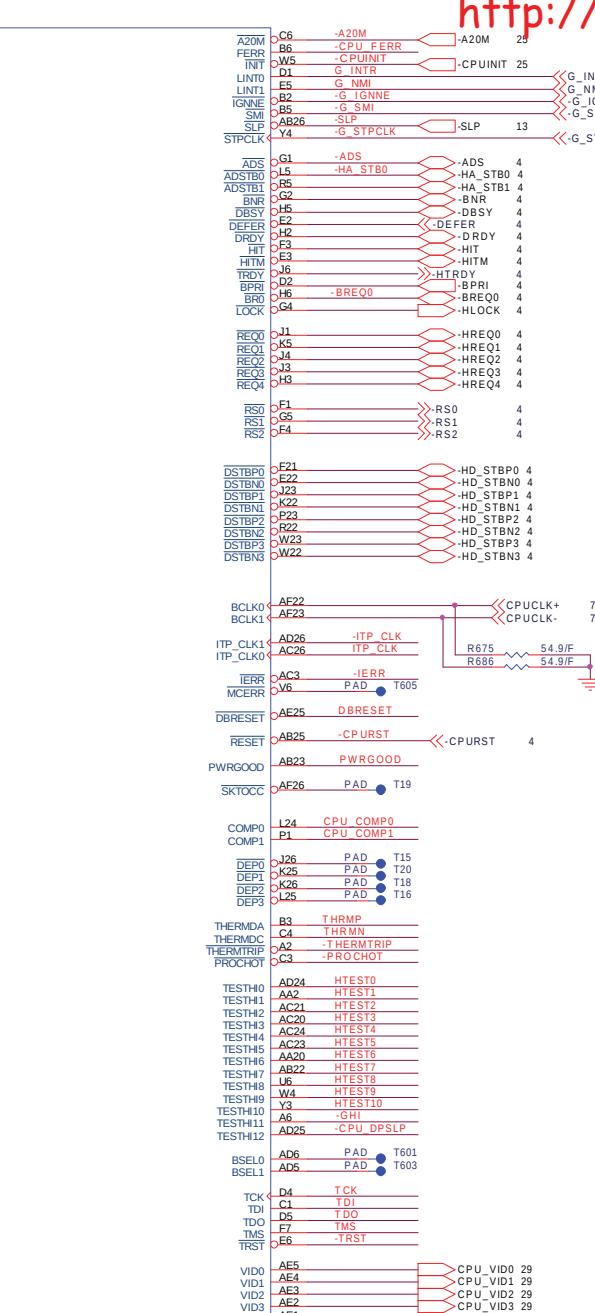


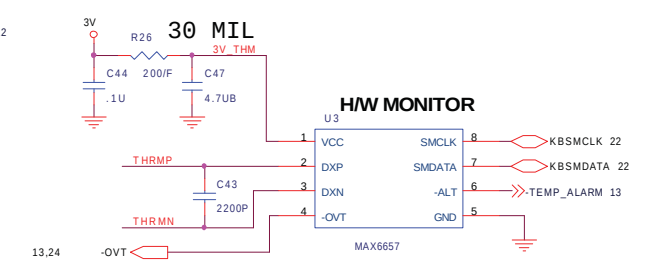


-CLR	-PR	D	CK	Q	-Q	
L	H	X	X	L	H	Clear Preset
H	L	X	X	H	L	
L	L	X	X	H	H	
H	H	+	+	H	L	
H	H	X	-	Qn	-Qn	

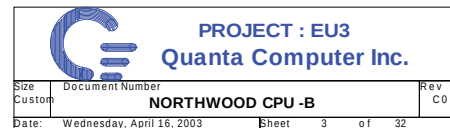
```

+  ==> Rising time
-  ==> Falling time

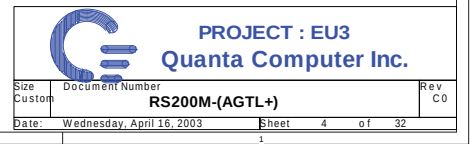
```



```
SM BUS ADDRESS : 98H
-TEMP_ALARM   : 70 degree
-OVT:         105 degree
```

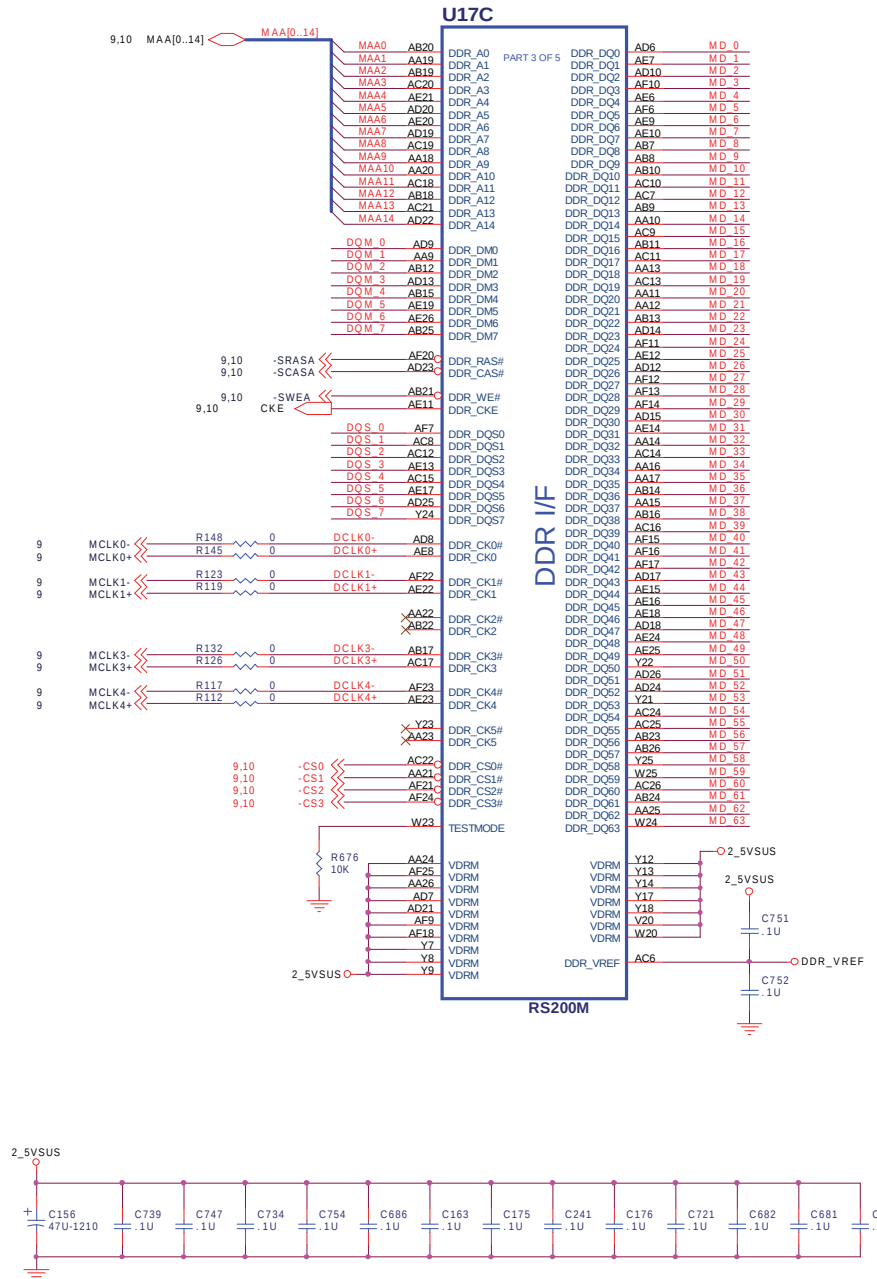


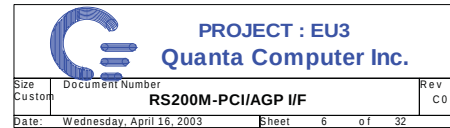
<http://hobi-elektronika.net>



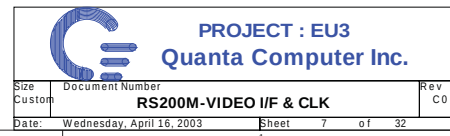
ATI_RS200M - 2 (DDR Interface)

<http://hobi-elektronika.net>

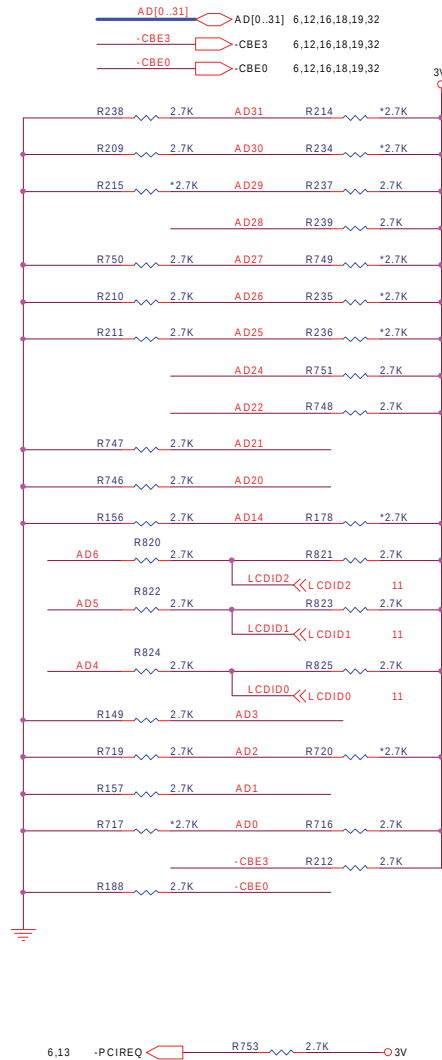




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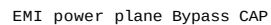


SYSTEM CONFIG. & N.B H/W STRAPPING



AD[30..31] : CLK SPEED	00: 100 MHZ	10: 166MHZ	AD15 : RESERVED
DEFAULT: 00	01: 133 MHZ	11: 66 MHZ	
AD29: RS200M FULL CONFIGURATION	0: FULL CONFIGURATION		AD14 : AGTL+ VDDQ 0: 1.2V MOBILE PART
DEFAULT: 0	1: USES DEFAULT VALUES		DEFAULT: 0 1: 1.7V DESKTOP PART
AD28: SPREAD SPECTRUM ENABLE	0: DISABLE		
DEFAULT: 1	1: ENABLE		
AD27: MCERR OBSERVATION	0: DISABLE		AD[12] : FLAT PANEL ID MSB
DEFAULT: 0	1: ENABLE		
AD26: QUICK START SELECT	0: DISABLE		
DEFAULT: 0	1: ENABLE		
AD25: INIT OBSERVATION	0: DISABLE		
DEFAULT: 0	1: ENABLE		
AD24: IOQ ENABLE	0: DISABLE		AD[7..4] : FLAT PANEL ID
DEFAULT: 1	1: ENABLE		
AD22: 27MHz/33 MHz CLOCK SELECTION	0: 33 MHz CLOCK OUTPUT		AD3: PC166 MODE 0: 33MHz
DEFAULT: 1	1: 27 MHz CLOCK INPUT		DEFAULT: 0 1: 66MHz
AD21: RESERVED	0: PLL Bypass mode disable(Normal mode)		AD2: CAL DEFAULTS FOR CPU 0: DISABLE
DEFAULT: 0	1: PLL Bypass mode enable(Debug only)		DEFAULT: 1 (FOR BRING UP) 1: ENABLE
AD20: PCICLK EXPANSION	1: PCI_REQ#3, PCI_GNT#3 USED AS PCICLK		AD1: FrcShortReset 0: DISABLE
DEFAULT: 0	0: PCI_REQ#3, PCI_GNT#3 USED AS REQ/GNT		DEFAULT: 0 1: ENABLE
CBE#3: PROD TEST	0: SHORT TIMERS		AD0: PCIIAGP CONFIG. 1: 66 MHz
DEFAULT: 1	1: NORMAL OPERATION		DEFAULT: 1 0: 33 MHz
			PCI_PREQACT#: INTERNAL CLOCK GEN. 0: DISABLE
			DEFAULT: 1 1: ENABLE

AD14 Use MOS to switch ? -->ATI 4/23:
 A14 chip will fix Auto Cal function,
 reserve Res is OK.(Ref AD2)

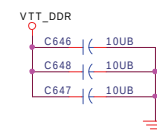


MD Terminator

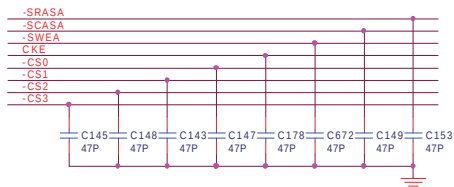
VTT_DDR

2_5VSUS

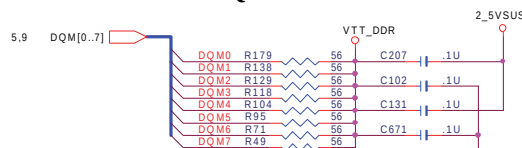
5.9 MD[0..63]  MD[0..63]
5.9 MAA[0..14]  MAA[0..14]



For Terminator



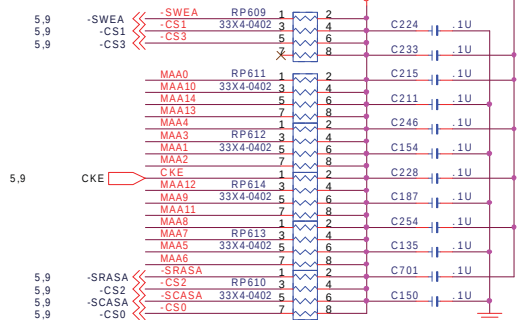
DQM Terminator



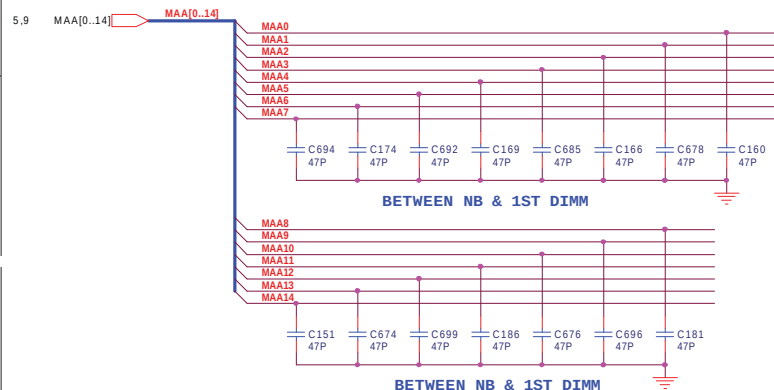
MA Terminator

VTT_DDR

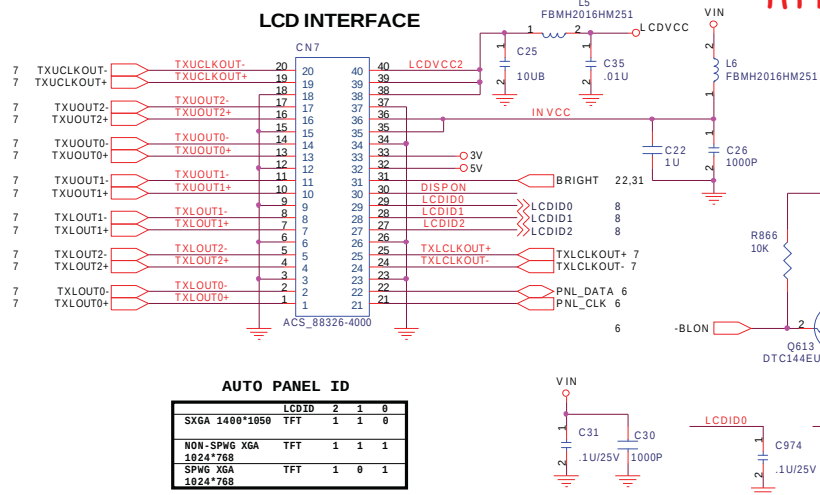
2_5VSUS



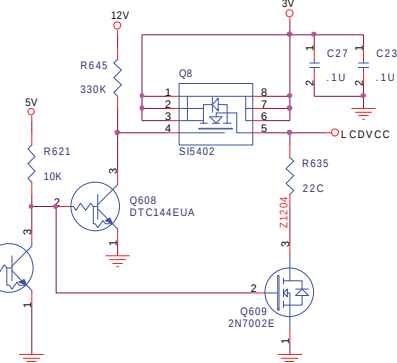
ATI's suggestion.
02_28



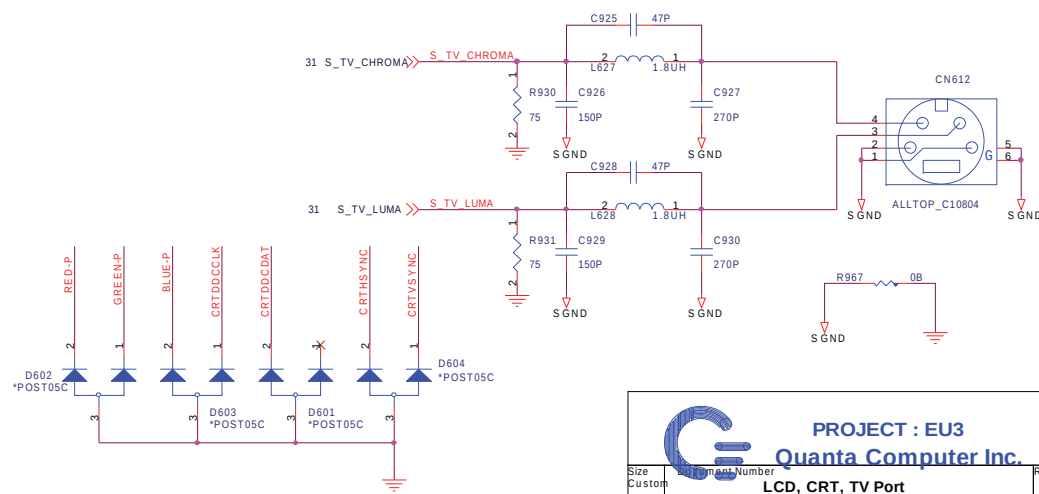
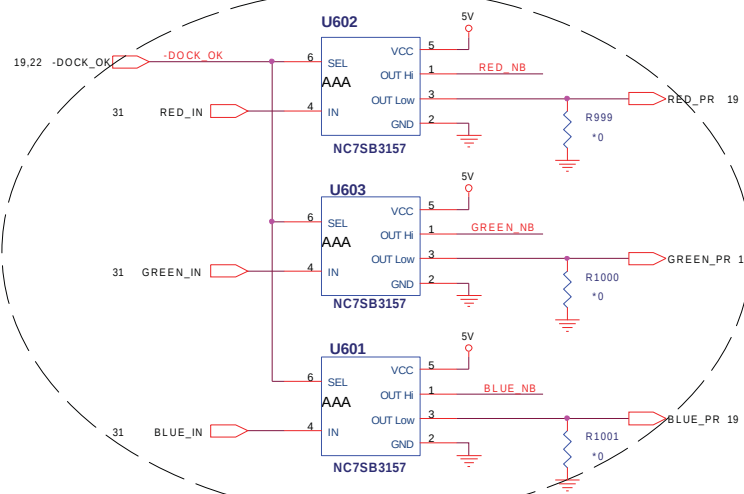
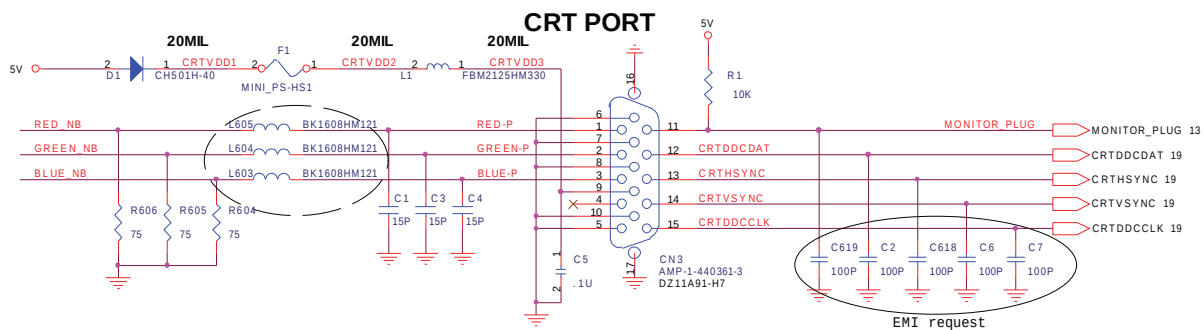
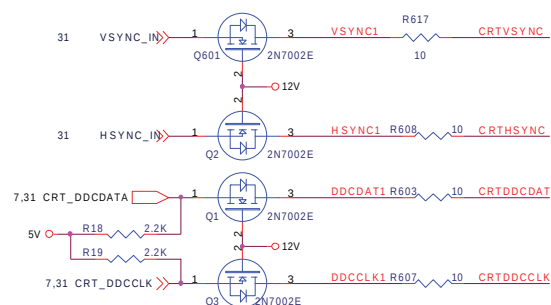
PROJECT : EU3
Quanta Computer Inc.

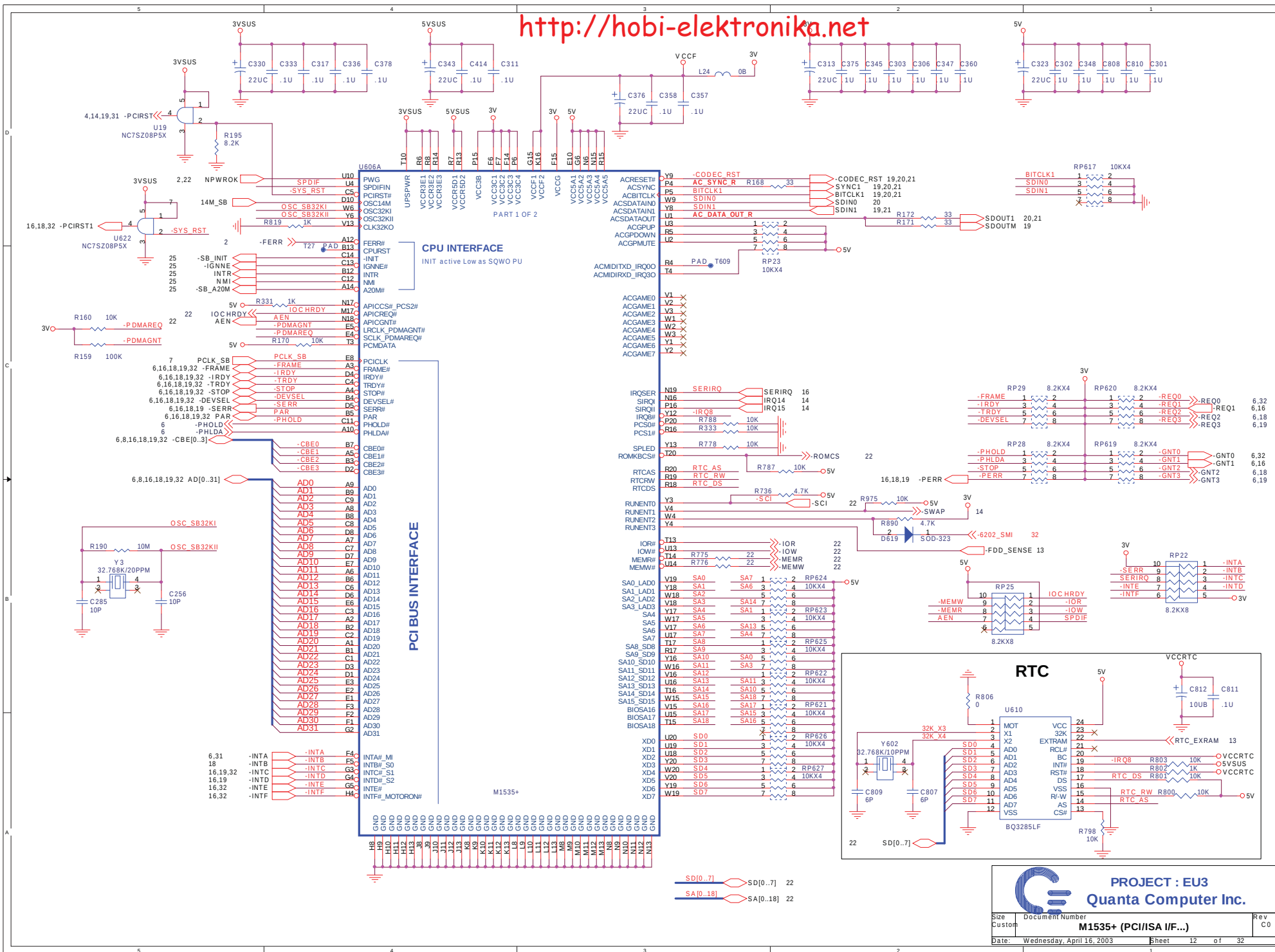


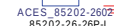
TRACE 80MIL
For 3V & LCDVCC



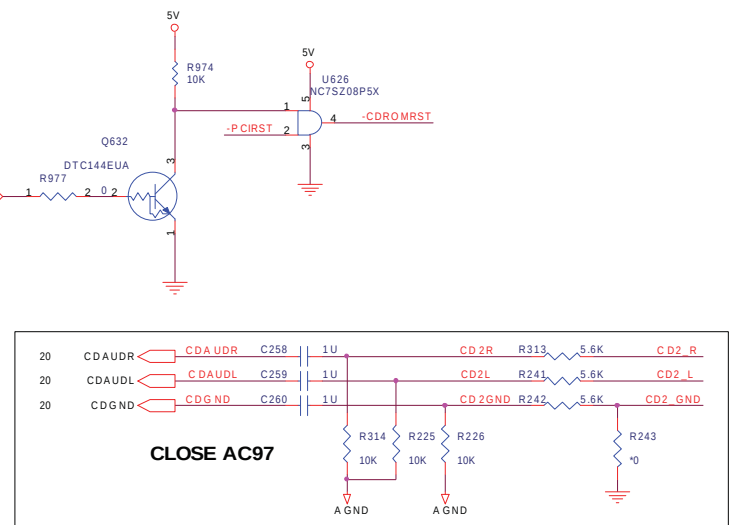
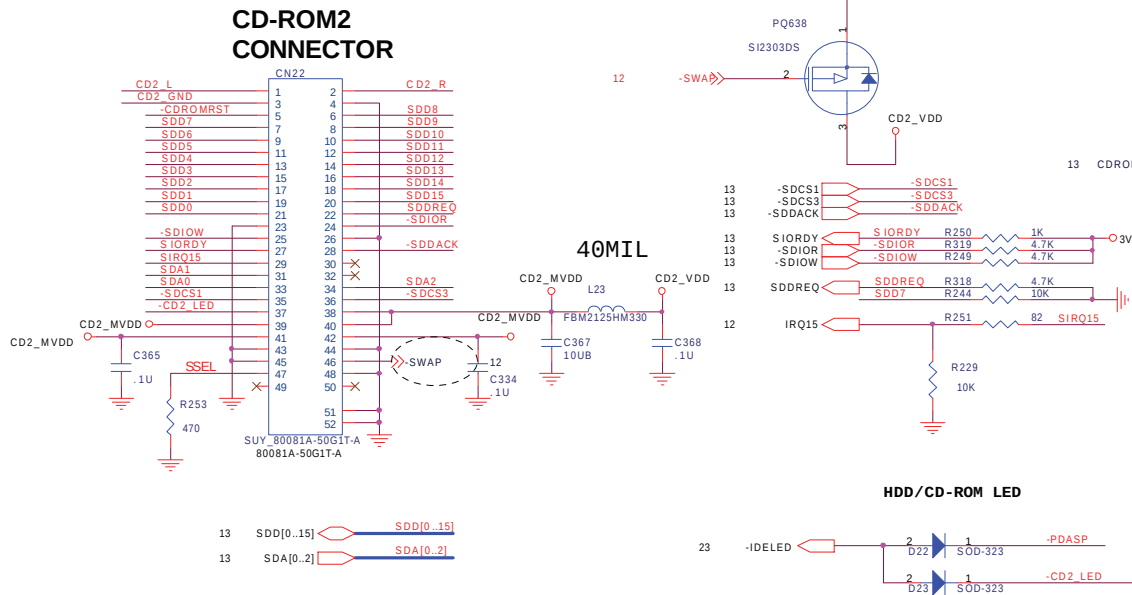
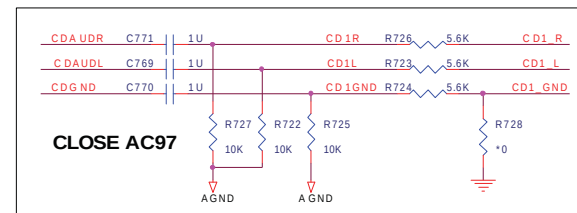
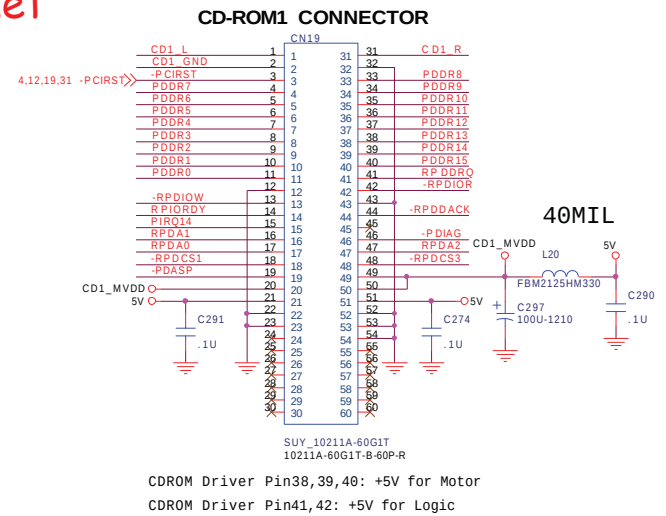
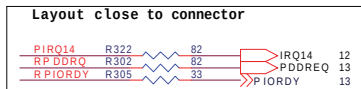
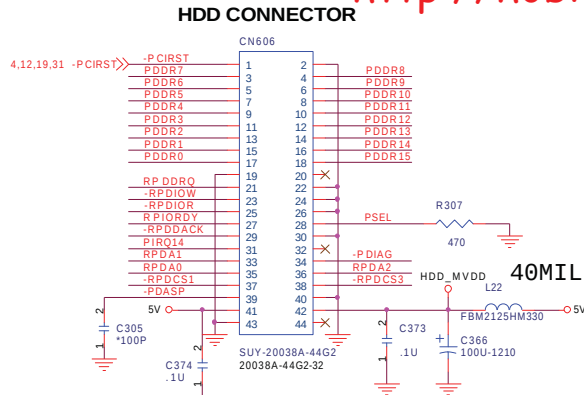
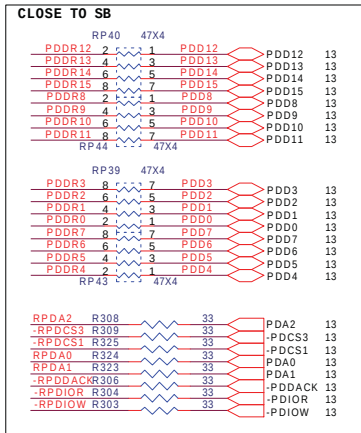
	LCDID	2	1	0
SXGA 1400*1050	TFT	1	1	0
NON-SPWG XGA 1024*768	TFT	1	1	1
SPWG XGA 1024*768	TFT	1	0	1



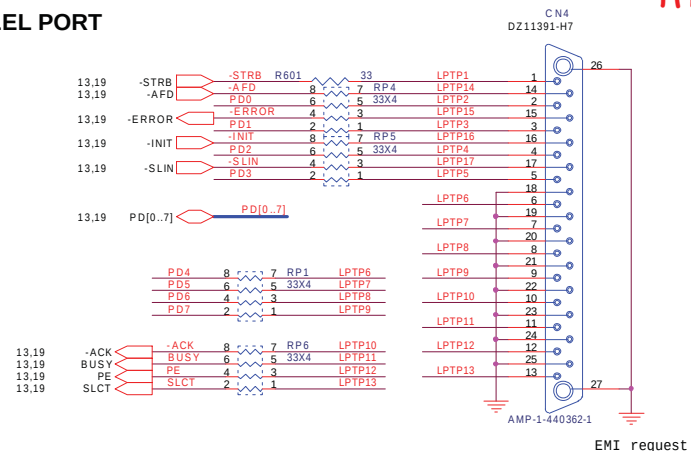




EMI Power plane Bypass CAP

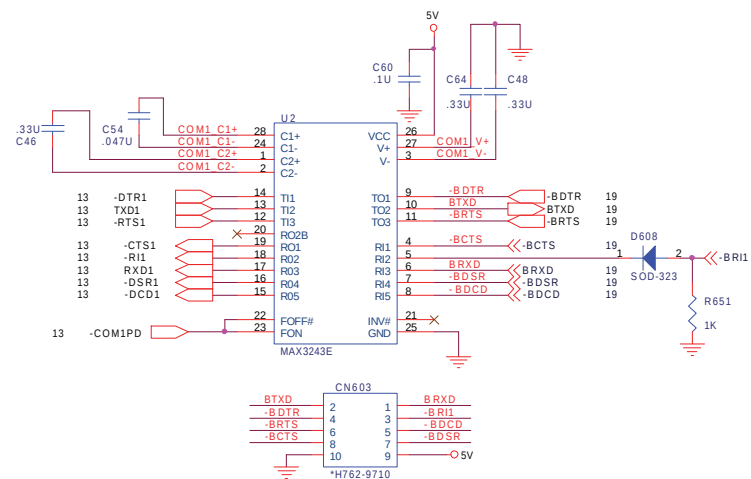


PARALLEL PORT



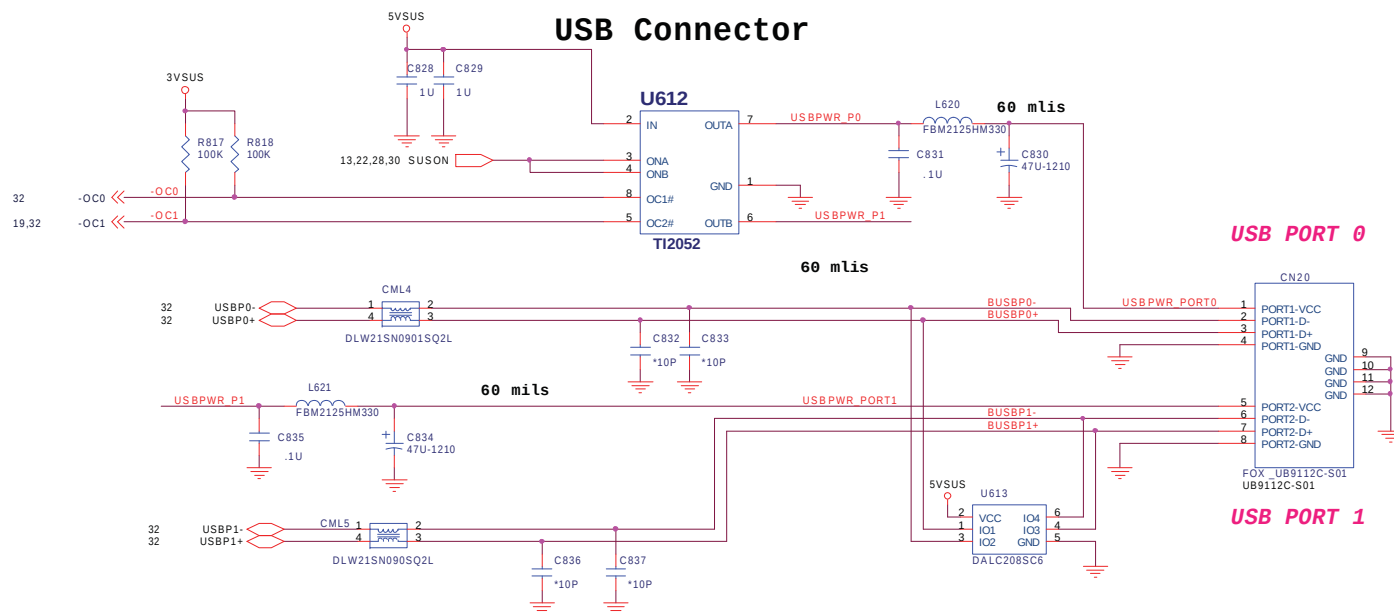
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SERIAL PORT

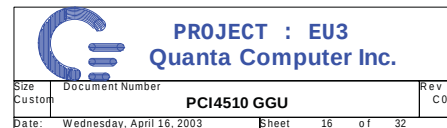


SERIAL PORT FOR DEBUG ONLY

USB Connector



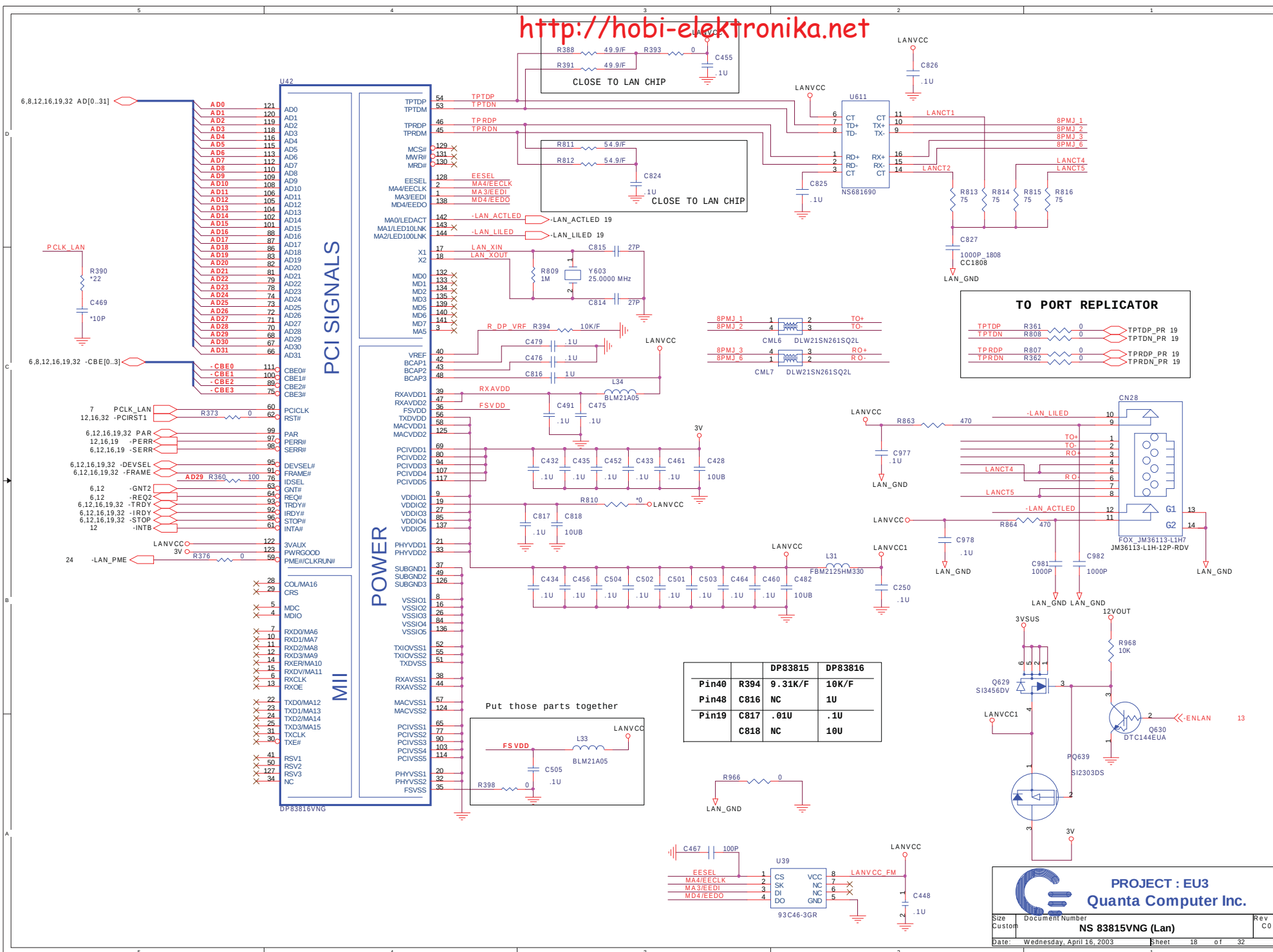
PROJECT : EU3
Quanta Computer Inc.

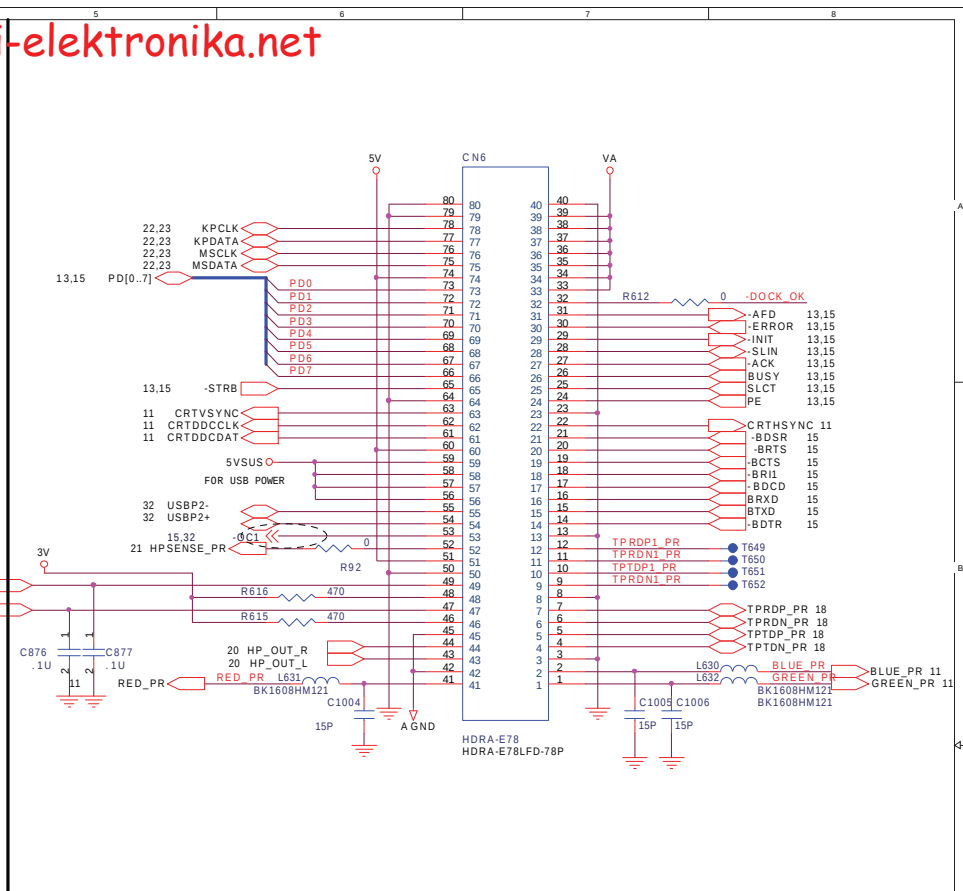


<http://hobi-elektronika.net>



Size	Document Number		
Custom	PCMCIA SOCKET & POWER(TPS2211)		
Date:	Wednesday, April 16, 2003	Sheet	17 of 32



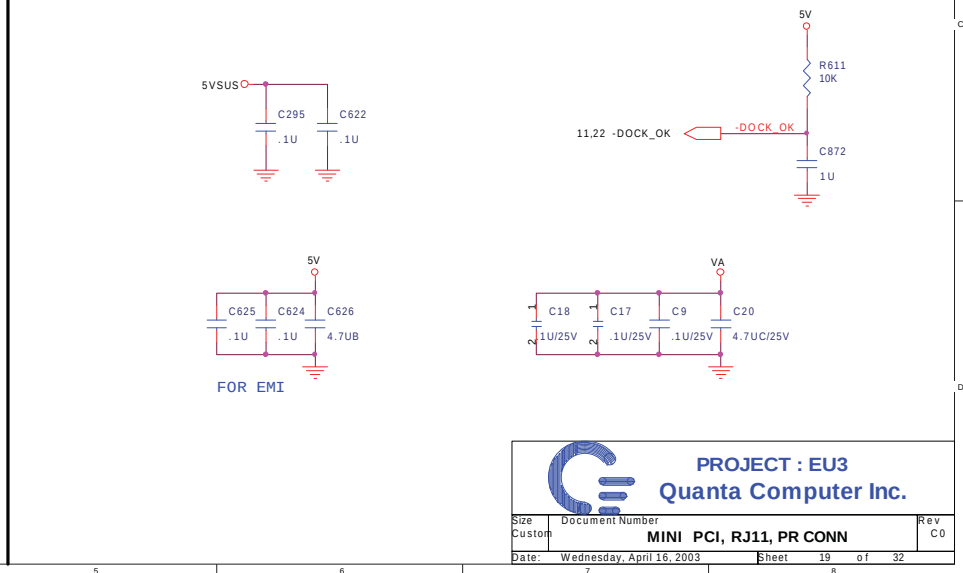


RJ11 CONNECTOR

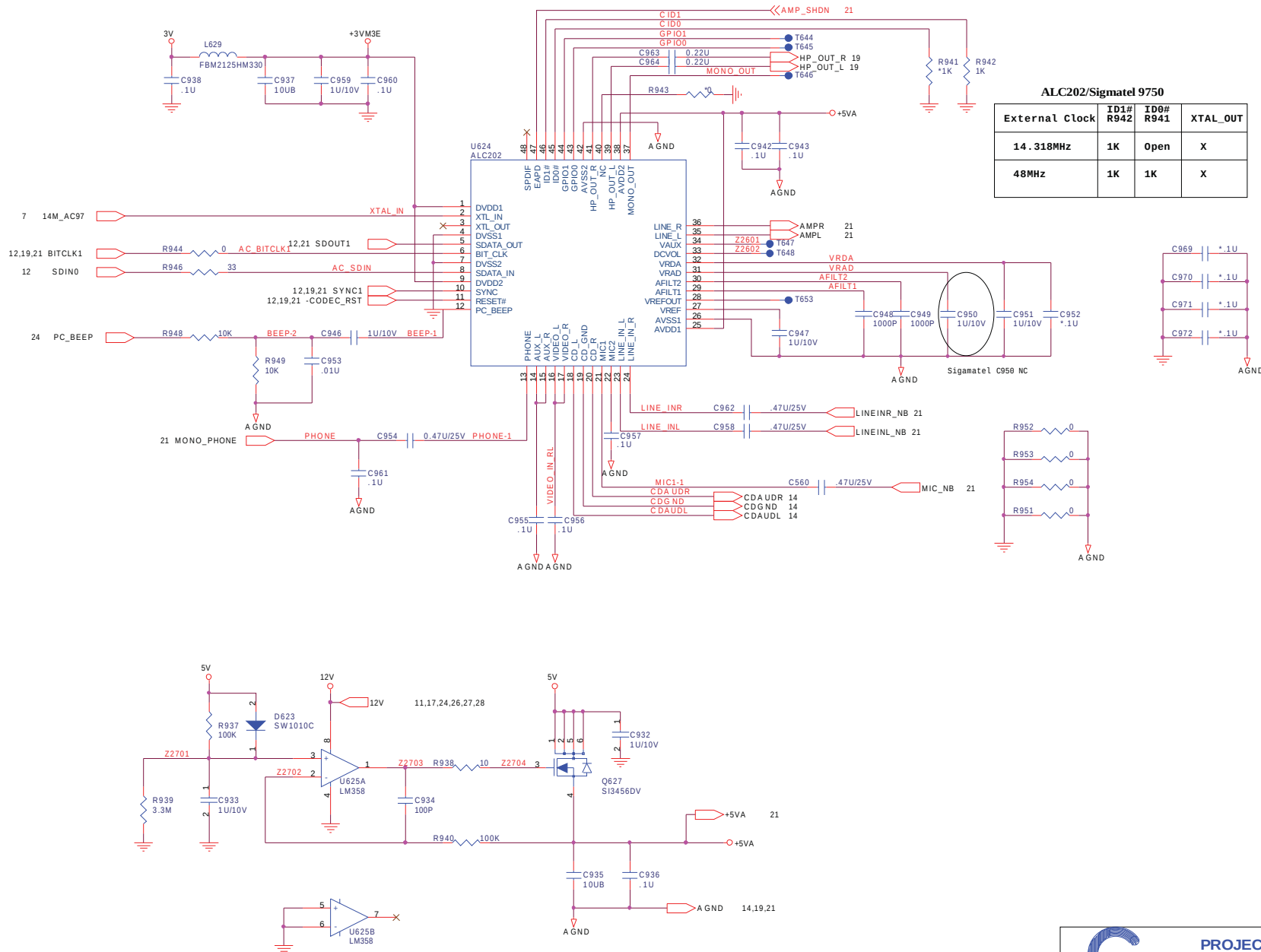
The diagram illustrates the internal wiring of an RJ11 connector. It features a 3-pin connector (CN5) on the left, with pins 3 and 4 connected to a 2-pin connector (CN602) on the right. The circuit includes two inductors (L601, L602) and two capacitors (C19, C620) connected to ground. The components are labeled with their part numbers: L601 (BK1608HS241), L602 (BK1608HS241), C19 (1000P_1808 CC1808), and C620 (1000P_1808). The circuit is powered by a 3800-2P-RH source.

1

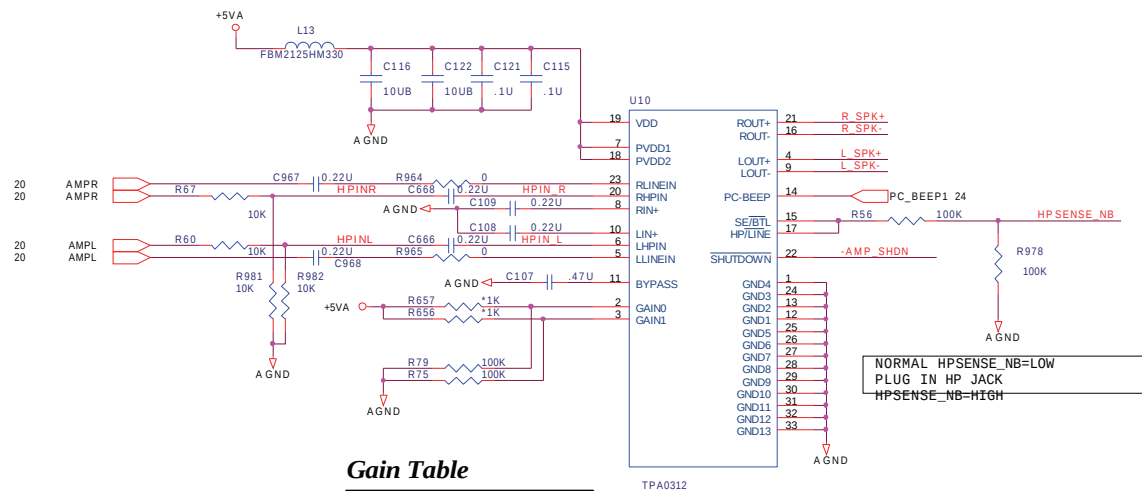
2



Pre AMP IN 39,41 pin

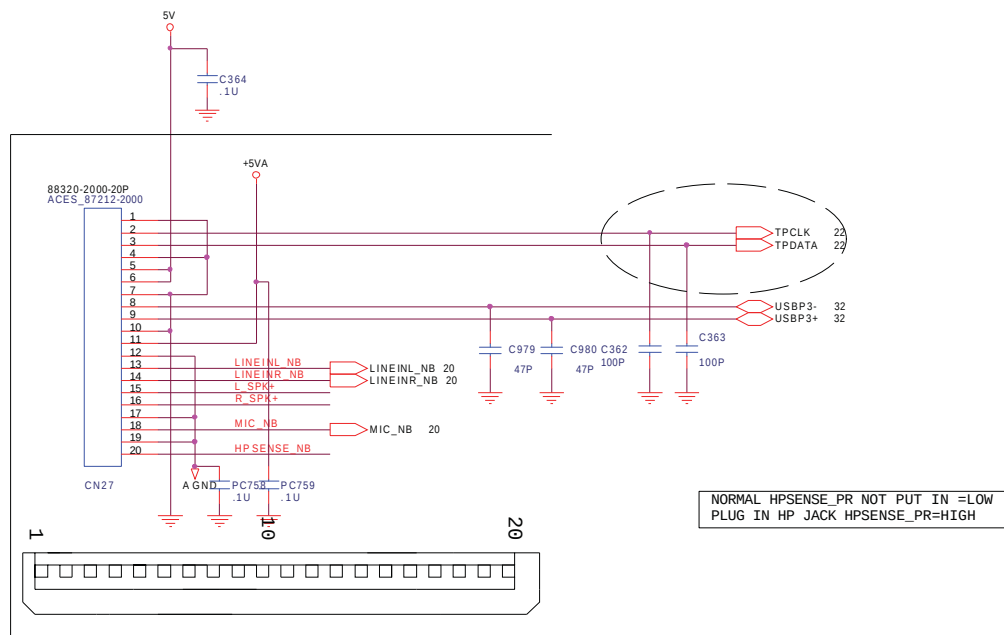
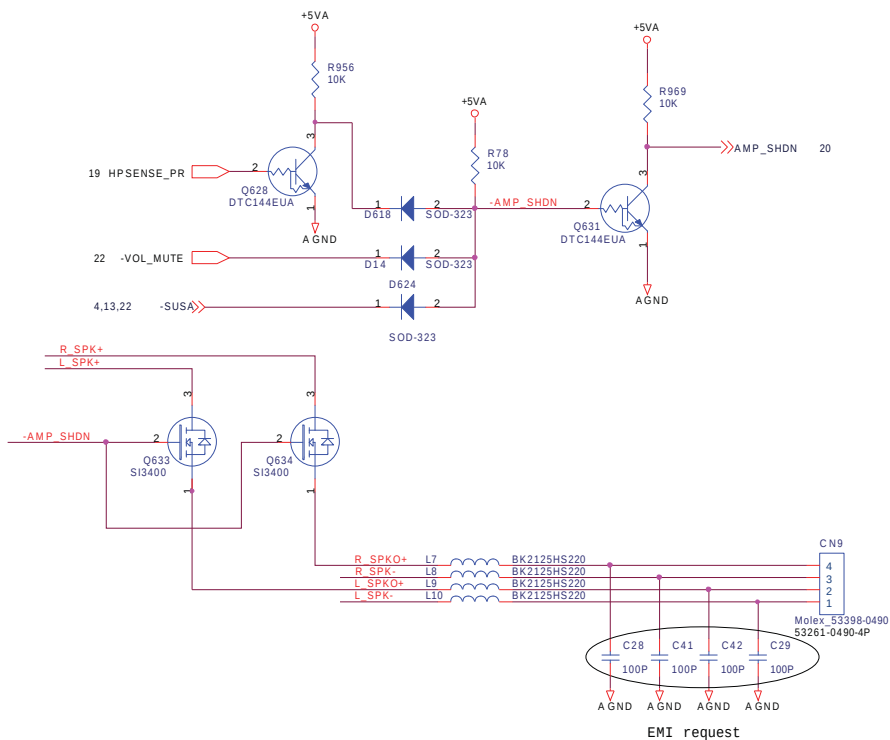
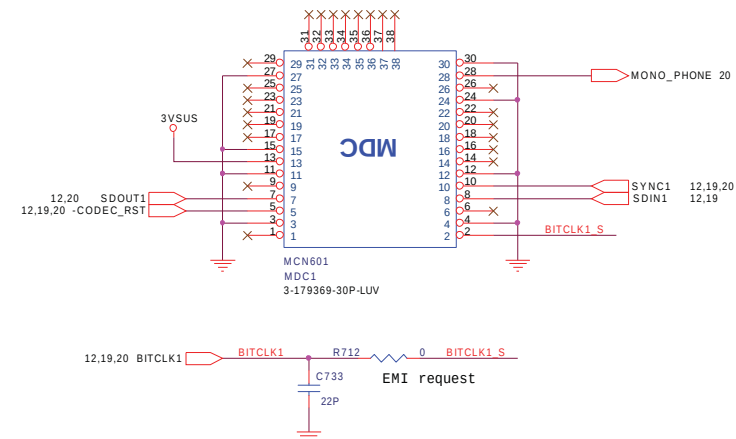


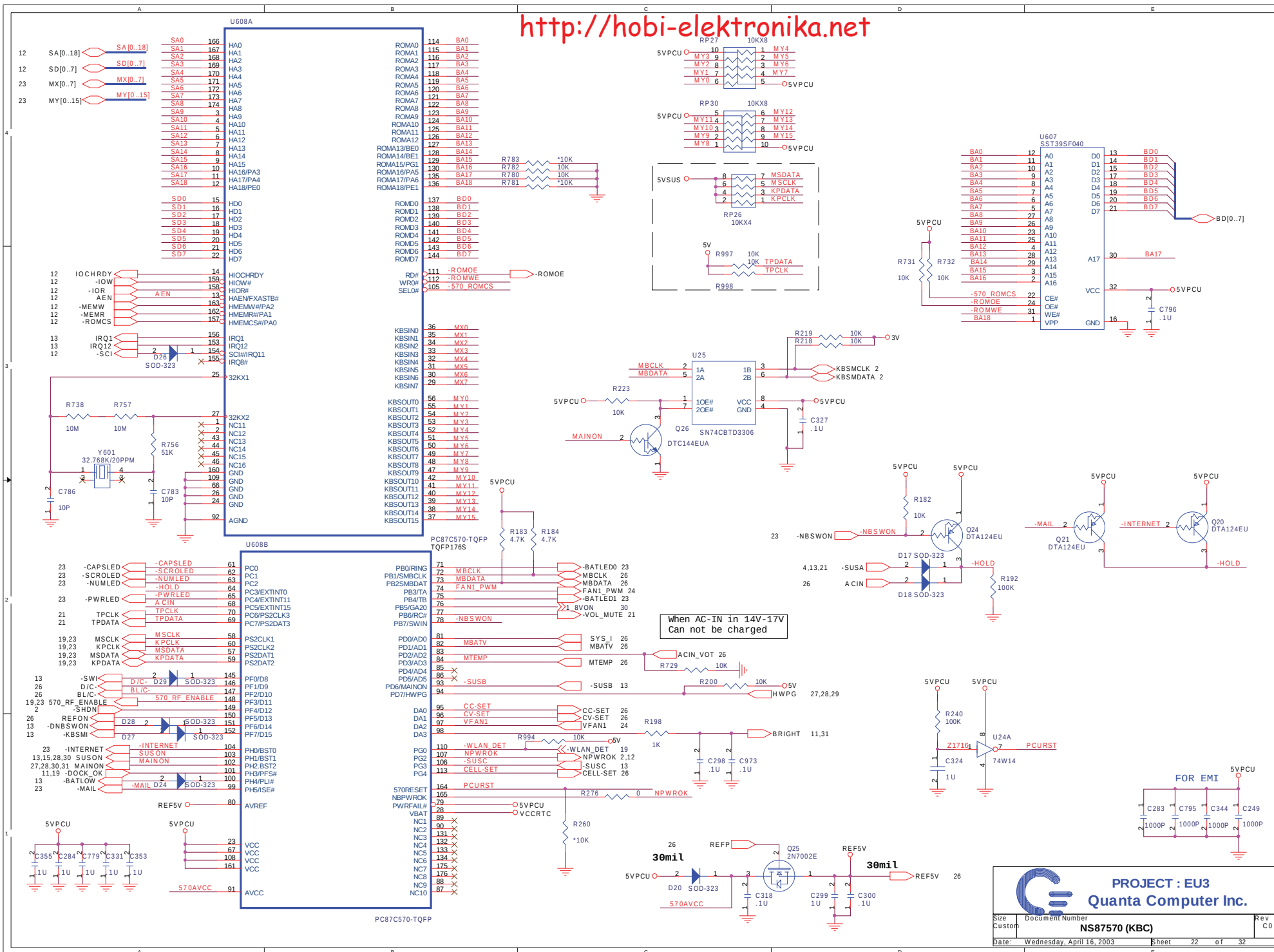
MDC CONN.



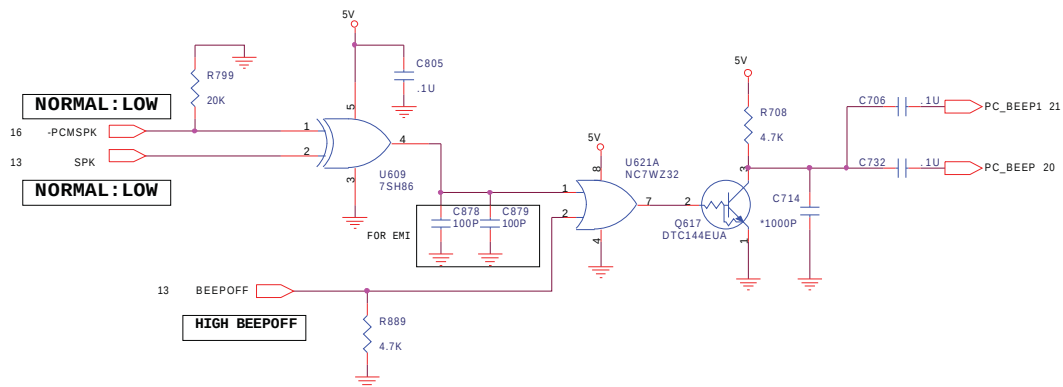
Gain Table

Gain0	Gain1	Av
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB

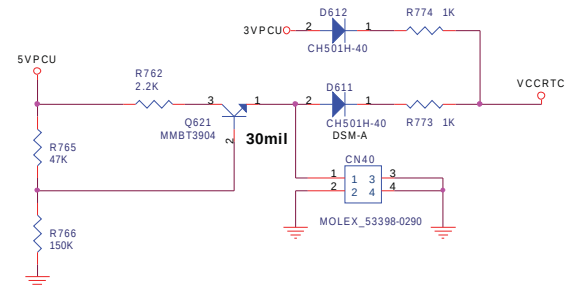




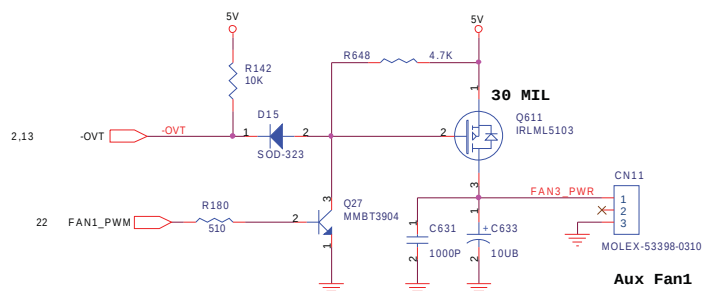
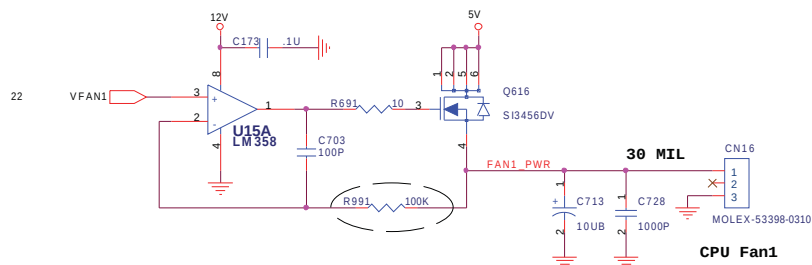
PC SPEAKER



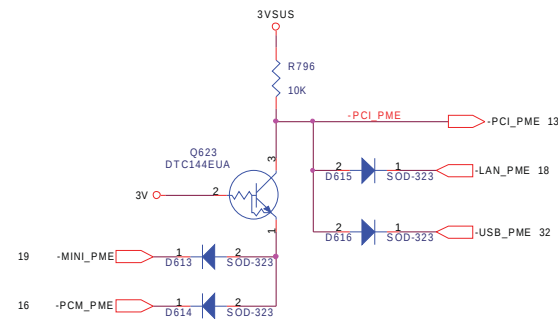
RTCVCC



FAN CONTROL



PME CIRCUIT

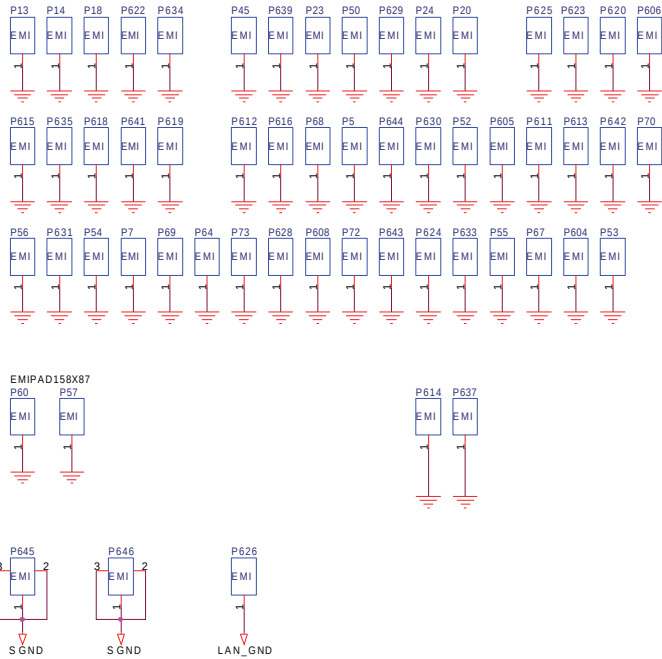


PROJECT : EU3
Quanta Computer Inc.

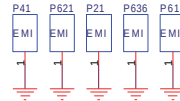
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Custom	FAN, Beep, Other	C0
Date:	Wednesday, April 16, 2003	Sheet 24 of 32

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EMI FENSES

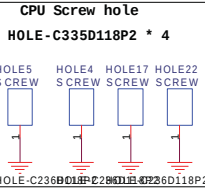
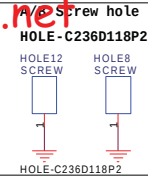
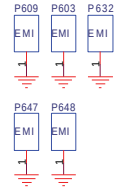


ANTENNA WIRE SPRINGS

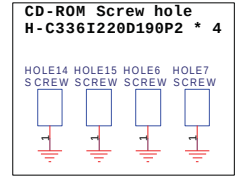
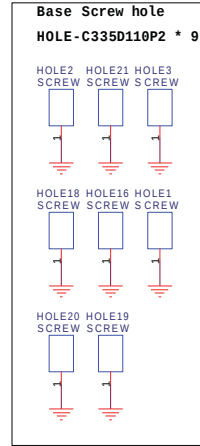


H : 7.5MM

MODEM WIRE SPRINGS



SCREW HOLES



Geyserville Function (Use ALI_M1535+ Internal GCL)

