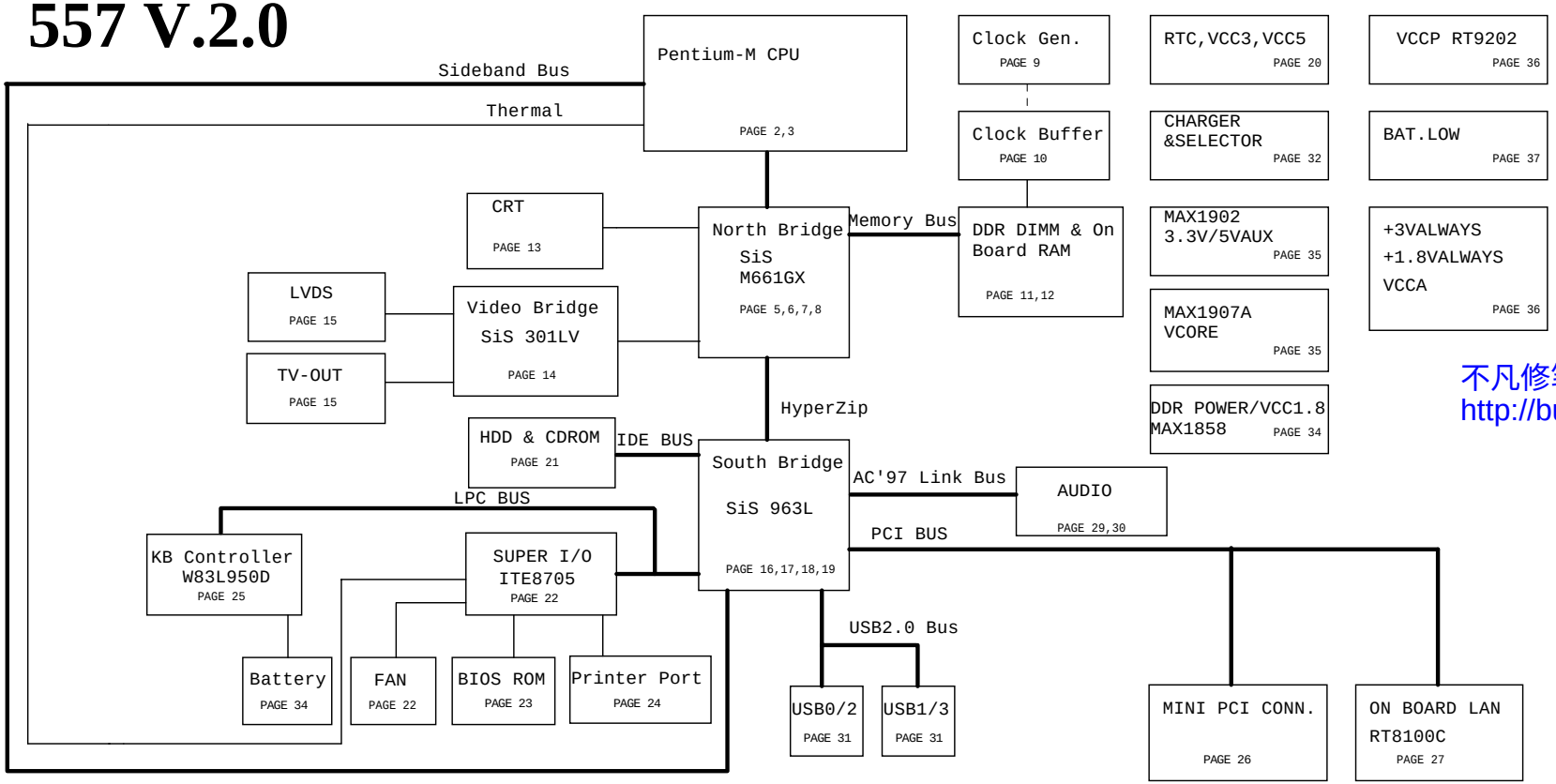
557 V.2.0



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PAGE	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
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DATE	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26

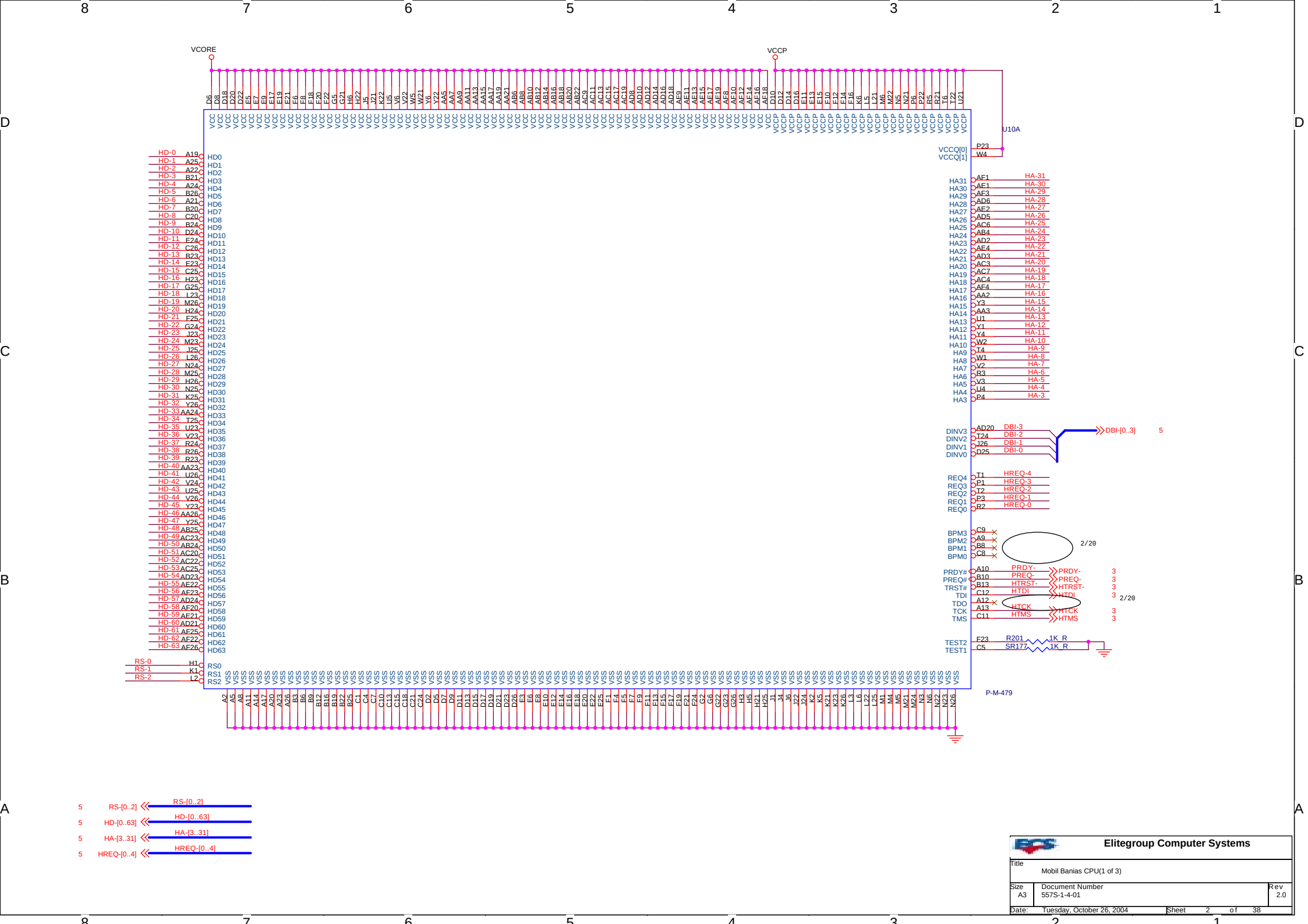
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DATE	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26	2004/10/26

Elitegroup Computer Systems

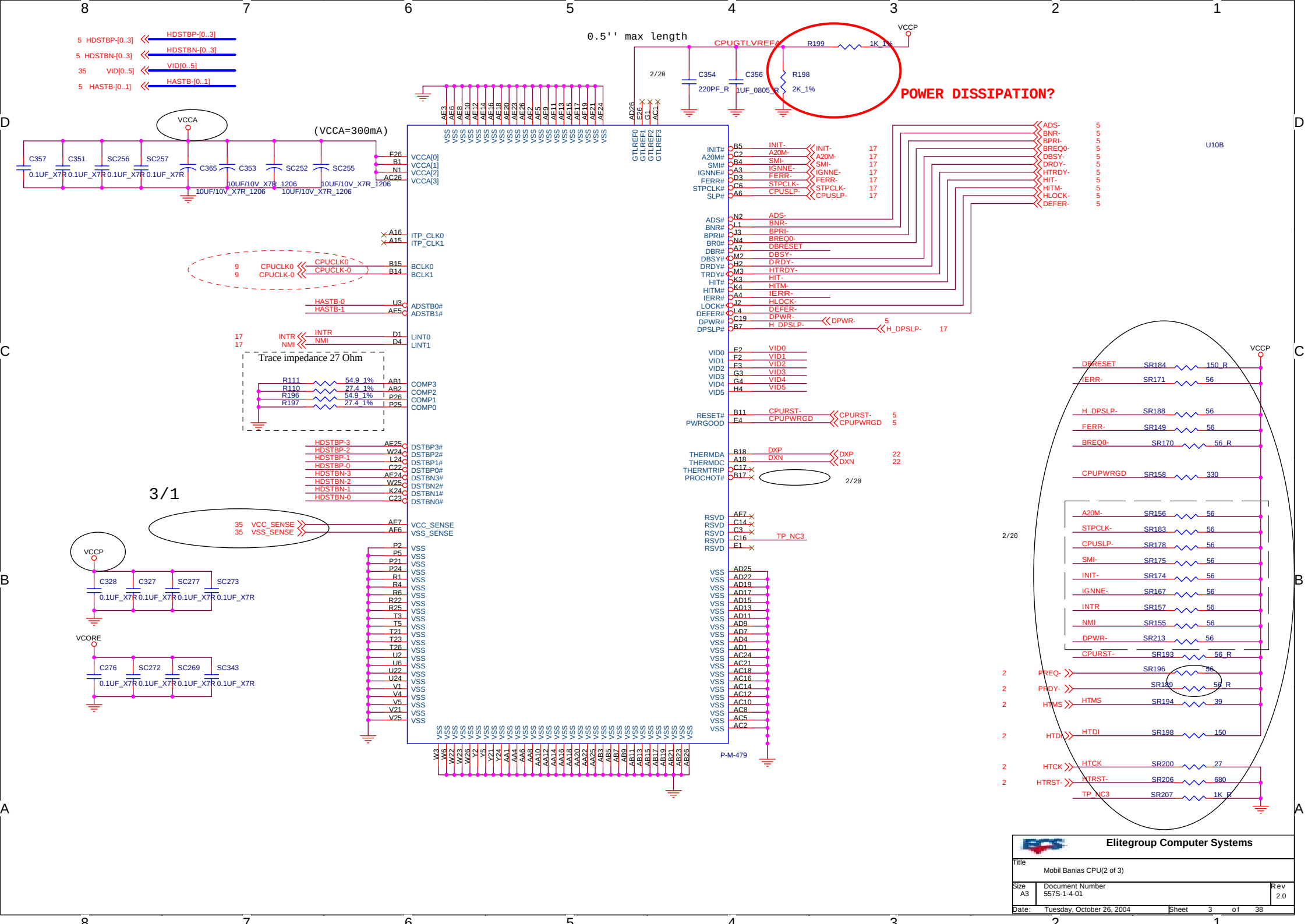
Tit 557S-1-4-01

Size B | Document Number 557S-1-4-01 | Rev 2.0

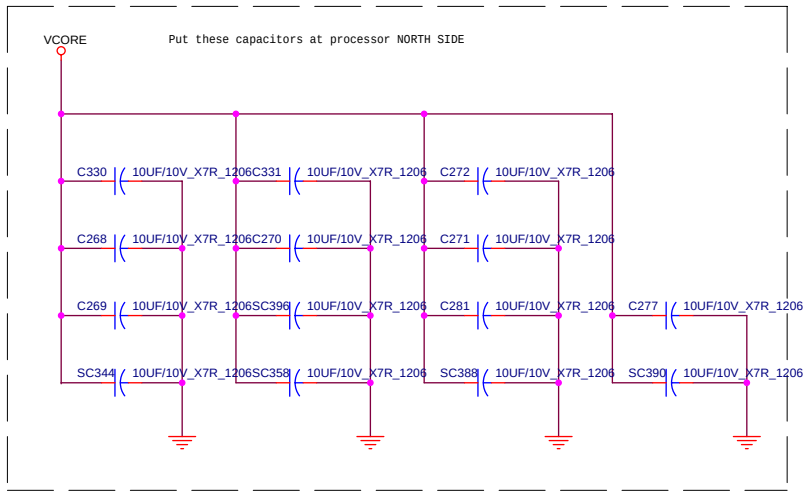
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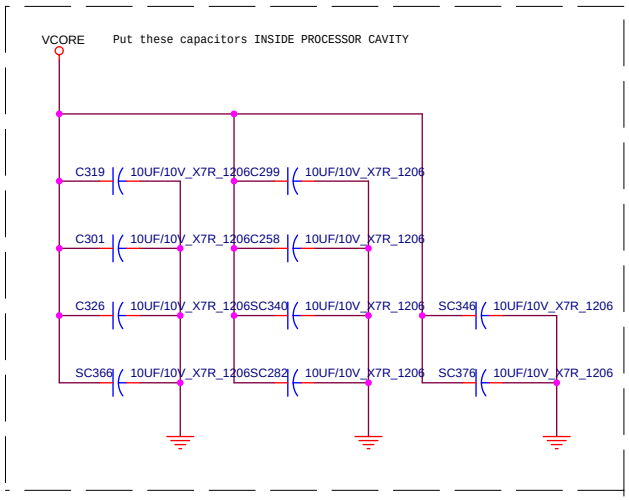


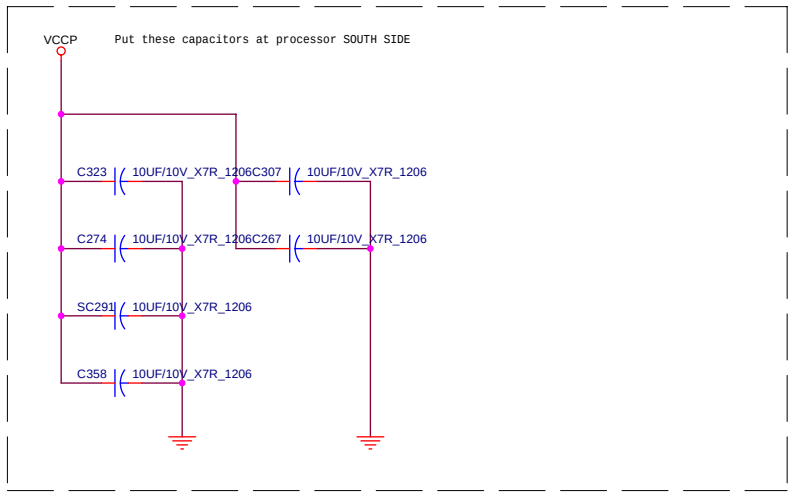
Elitegroup Computer Systems	
Title	Mobil Banias CPU(1 of 3)
Size A3	Document Number 557S-1-4-01
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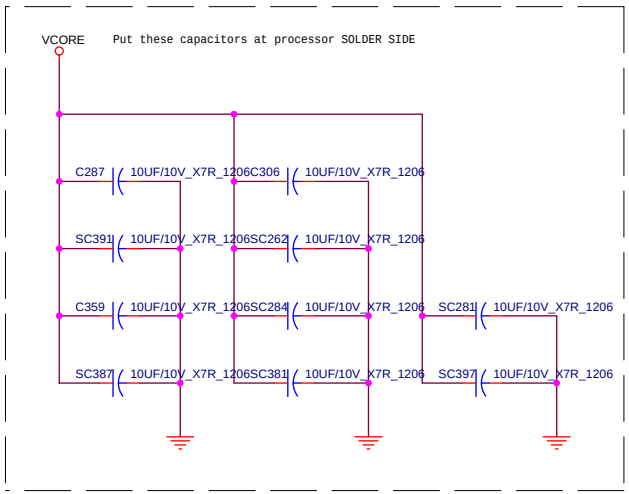

0.5'' max length
POWER DISSIPATION?
(VCCA=300mA)
Trace impedance 27 Ohm
3/1
U10B
P-M-479
Elitegroup Computer Systems
Title
Mobil Banias CPU(2 of 3)
Size
A3
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38

Bus Signal	Data Bus Signals	Associated Strobe
DINV[0]#	D[0..15]#	DSTBN[0]# DSTBP[0]#
DINV[1]#	D[16..31]#	DSTBN[1]# DSTBP[1]#
DINV[2]#	D[32..47]#	DSTBN[2]# DSTBP[2]#
DINV[3]#	D[48..63]#	DSTBN[3]# DSTBP[3]#









P.S. Choose X7R/X5R components instead of Y5V for all 10uF_1206 capacitors on this page.

Elitegroup Computer Systems

Title: Mobil Banias CPU(3 of 3)

Size: A3 | Document Number: 557S-1-4-01 | Rev: 2.0

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LVDS/AGP

M661MX-1

HOST

SU13A

M661MX

C30~C34 is noise issue(Optional Use)

AGP3.0 = 50 ohm

Rds-on(n) = 10 ohm
HNCVERF = 1/3 VCCP

Rds-on(p) = 56 ohm
HPCVERF = 2/3 VCCP

POWER DISSIPATION?

POWER DISSIPATION?

place this capacitor under 661MX solder side

2/27

Elitegroup Computer Systems	
Title	M661MX (HOST/AGP)
Size: Custom	Document Number: 557S-1-4-01
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DQM[0..7] → DQM[0..7] 11,12
DQS[0..7] → DQS[0..7] 11,12
MD[0..63] → MD[0..63] 11,12
MAA[0..15] → MAA[0..15] 11,12

SU13B

M661MX-2

MD0	AN35	MD0	
MD1	AP36	MD1	
MD2	AK33	MD2	
MD3	AM33	MD3	
MD4	AN34	MD4	
MD5	AK32	MD5	
MD6	AR34	MD6	
MD7	AN33	MD7	
DQM0	AR35	DQM0	
DQS0	AP34	DQS0/CSB0#	
MD8	AM32	MD8	
MD9	AL31	MD9	
MD10	AR31	MD10	
MD11	AL30	MD11	
MD12	AN32	MD12	
MD13	AR33	MD13	
MD14	AN31	MD14	
MD15	AM31	MD15	
DQM1	AR32	DQM1	
DQS1	AP32	DQS1/CSB1#	
MD16	AP30	MD16	
MD17	AR30	MD17	
MD18	AM29	MD18	
MD19	AL27	MD19	
MD20	AN30	MD20	
MD21	AN29	MD21	
MD22	AL28	MD22	
MD23	AN28	MD23	
DQM2	AL29	DQM2	
DQS2	AR29	DQS2/CSB2#	
MD24	AP26	MD24	
MD25	AN25	MD25	
MD26	AR24	MD26	
MD27	AL24	MD27	
MD28	AL25	MD28	
MD29	AR26	MD29	
MD30	AM25	MD30	
MD31	AN24	MD31	
DQM3	AP24	DQM3	
DQS3	AR25	DQS3/CSB3#	
MD32	AN21	MD32	
MD33	AP20	MD33	
MD34	AN20	MD34	
MD35	AL18	MD35	
MD36	AM21	MD36	
MD37	AR21	MD37	
MD38	AL19	MD38	
MD39	AM19	MD39	
DQM4	AL20	DQM4	
DQS4	AR20	DQS4/CSB4#	
MD40	AL15	MD40	
MD41	AL14	MD41	
MD42	AN15	MD42	
MD43	AR15	MD43	
MD44	AN16	MD44	
MD45	AM15	MD45	
MD46	AN14	MD46	
MD47	AL13	MD47	
DQM5	AP16	DQM5	
DQS5	AR16	DQS5/CSB5#	
MD48	AM13	MD48	
MD49	AL12	MD49	
MD50	AL11	MD50	
MD51	AR12	MD51	
MD52	AP14	MD52	
MD53	AR14	MD53	
MD54	AN13	MD54	
MD55	AP12	MD55	
DQM6	AN12	DQM6	
DQS6	AR13	DQS6/CSB6#	
MD56	AL10	MD56	
MD57	AR11	MD57	
MD58	AM9	MD58	
MD59	AR9	MD59	
MD60	AM11	MD60	
MD61	AN11	MD61	
MD62	AP10	MD62	
MD63	AN9	MD63	
DQM7	AN10	DQM7	
DQS7	AR10	DQS7/CSB7#	

MA0	AR23	MAA0
MA1	AN23	MAA1
MA2	AN22	MAA2
MA3	AM23	MAA3
MA4	AL23	MAA4
MA5	AL26	MAA5
MA6	AN26	MAA6
MA7	AN27	MAA7
MA8	AR27	MAA8
MA9	AR28	MAA9
MA10	AP22	MAA10
MA11	AN18	MAA11
MA12	AR22	MAA12
MA13	AP28	MAA13
MA14	AM27	MAA14
MA15	AT14	MAA15
SRAS#	AL17	/RRASA-
SCAS#	AR19	/RCASA-
SWE#	AN19	/RMWA-
CS0#	AM17	/RCSA-0
CS1#	AL16	/RCSA-1
CS2#	AN17	/RCSA-2
CS3#	AR17	/RCSA-3
CS4#	AP18	
CS5#	AR18	
CKE0	AP4	CKE0 11,12
CKE1	AT3	CKE1 11,12
CKE2	AR3	CKE2 11,12
CKE3	AP3	CKE3 11,12
CKE4	AR2	
CKE5	AN4	
S3AUXSW#	AP2	TP1
FWDSDCLKO	AL21	FWDCLK
SDRCLKI	AL22	
DLLAVDD	AL35	DLLAVDD
DLLAVSS	AL34	DLLAVSS
DDRAVDD	AM35	DDRAVDD
DDRAVSS	AN36	DDRAVSS
DDRVREFB	AF16	DDRVREFA
DDRVREFA	AF23	DDRVREFB
DRAM_SEL	AP1	
DDRCOMP_P	AR8	DDRCOMP
DDRCOMP_N	AP8	DDRCOMN

M661MX

RASA-	SR69	0	/RRASA-	
MWA-	R62	0	/RMWA-	
CASA-	R61	0	/RCASA-	
CSA-0	R63	0	/RCSA-0	
CSA-1	SR70	0	/RCSA-1	
CSA-2	R64	0	/RCSA-2	
CSA-3	SR71	0	/RCSA-3	

MWA- → MWA- 11,12
RASA- → RASA- 11,12
CASA- → CASA- 11,12
CSA-[0..3] → CSA-[0..3] 11,12

VCC_DIMM
DDRCOMN R77 40.2 1%
DDRCOMP R78 40.2 1%

Put near 741GX chip.
FWDCLK R76 22 FWDSDCLKO 10
R241 10K
5/10

POWER DISSIPATION?

VCC_DIMM
DDRVREFA
(10-mil trace)
25-mil clearance
or shielded by
VSS trace and
VDD trace
C149 0.01UF
R79 150_1%
R80 150_1%
C150 0.01UF
JP7 SHORT

VCC_DIMM
DDRVREFB
(10-mil trace)
25-mil clearance
or shielded by
VSS trace and
VDD trace
C171 0.01UF
R84 150_1%
R85 150_1%
C172 0.01UF
JP9 SHORT

DDRAVDD
L9 FCM2012V-121RC10_0805
VCC3
C152 0.1UF
C147 0.01UF
C136 10UF/10V_1206
JP6 SHORT
DDRAVSS

DLLAVDD
L8 FCM2012V-121RC10_0805
VCC3
C151 0.1UF
C146 0.01UF
C135 10UF/10V_1206
JP5 SHORT
DLLAVSS

Elitegroup Computer Systems

Title	M661MX-2 (MEMORY for DDR)	
Size B	Document Number 557S-1-4-01	Rev 2.0
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NB Hardware Trap Table

SW2 Pin		Description	ON/1	OFF/0	Default
S1	DLLEN-	CPUCLK SDCLK PLL/DLL Circuit Enable	DISABLE	ENABLE	OFF
S2	TRAP0	Debug Mode Select	ENABLE	DISABLE	OFF
S3	TMODE1	MuTIOL (ASL) Initialize Series Mode	DISABLE	ENABLE	OFF
S4	TMODE2	NB ASL DBI REGISTER MODE	DISABLE	ENABLE	OFF
S5	TRAP1	MuTIOL Type (Verision 1 , 2)	Ver. 1	Ver. 2	OFF

NB Hardware Trap internal pull-low

16 ZAD[0..16]

ZAD[0..16]

POWER DISSIPATION?

VCC1.8V R74 150_1% C137 0.1UF ZVREF R75 49.9_1% C138 0.1UF

SU13C

HyperZip

M661MX-3

VGA

M661MX

		Enable	Disable
RSYNC	VGA Interrupt	1	0
LSYNC	TBD	1	0
CSYNC	TBD	1	0

VCC3 RSYNC R121 4.7K LSYNC R119 4.7K CSYNC R115 4.7K ENTEST R124 4.7K

VCC3 FCM2012V-121RC10_0805 SL4 Z1XAVDD SC160 10UF/10V_1206 SJP2 SC183 0.1UF SC187 0.01UF Z1XAVSS SHORT

VCC3 FCM2012V-121RC10_0805 SL5 Z4XAVDD SC161 10UF/10V_1206 SJP3 SC201 0.1UF SC205 0.01UF Z4XAVSS SHORT

VCC1.8V FCM2012V-121RC10_0805 L16 R89 56_1% ZCMP_N C224 10UF/10V_1206 JP12 C214 0.1UF C211 0.01UF R90 56_1% ZCMP_P SHORT

14,15,16,21,22,26,27 PCIRST-
17,20,30 PWRGD
17,20 AUXOK

SC171 0.1UF_R C154 0.1UF C144 0.1UF

FOR DAC I/O DRIVER

VVBWN SC287 0.1UF VCOMP SC288 0.1UF DACAVDD DACAVSS FCM2012V-121RC10_0805 L21 VCC1.8V VRSET R141 130_1% C278 0.1UF C284 1UF_0805 C303 10UF/10V_1206 JP15 SHORT

FOR GUI CLK PLL

DCLKAVDD DCLKAVSS SL12 FCM2012V-121RC10_0805 VCC3 SC293 0.01UF SC285 0.1UF SJP6 SC312 10UF/10V_1206 SHORT

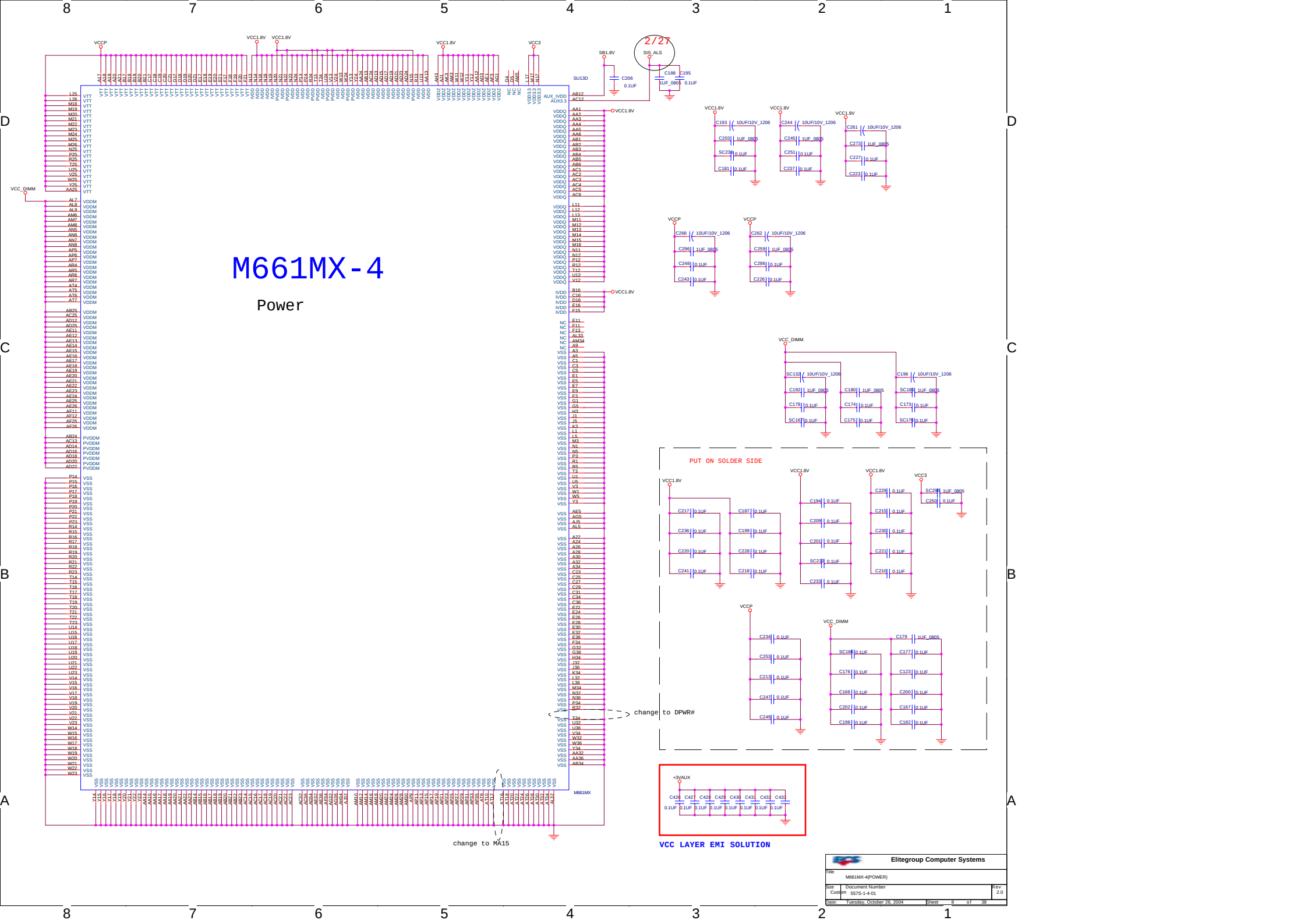
FOR GUI CLK PLL

ECLKAVDD ECLKAVSS L20 FCM2012V-121RC10_0805 VCC3 C279 0.01UF C283 0.1UF JP14 C302 10UF/10V_1206 SHORT

Title	Elitegroup Computer Systems
	M661MX-3 (Hyper Zip/VGA)
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M661MX-4

Power



Elitegroup Computer Systems	
Title	M661MX-4(POWER)
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Main Clock Generator

(5 OPTIONS)
1: (ICS)
2: (Cypress)
3. (Hitachi)
4: ()
5: ()

VCC3
L10
FCM2012V-121RC10_0805

SU10
ICS952013

C158 0.1UF_X7R
C142 0.1UF_X7R
C139 0.1UF_X7R
C160 0.1UF_X7R
C153 22UF/6.3V_B
C159 0.1UF_X7R
C140 0.1UF_X7R
C141 0.1UF_X7R
C161 0.1UF_X7R

1 VDDREF
11 VDDZ
13 VDDPCI
19 VDDPCI
28 VDD48
29 VDDAGP
42 VDDCPU
48 VDDSD
12 PCI_STOP#
5 VSSREF
8 VSSZ
18 VSSPCI
24 VSSPCI
25 VSS48
32 VSSAGP
41 VSSCPU
46 VSSSD

VCC3
SR134 10K
17,35 CPUSTP- CPUSTP-
45 CPU_STOP#
SQ27 2N7002
33 PD#/VTT_PWRGD
SC226 0.1UF_R
R86 475_1%
38 IREF
35 CLKEN-
5/10

VCC3
FCM2012V-121RC10_0805
SL9
36 VDDA
SC229 0.1UF_X7R
SC230 0.1UF_X7R
SC221 0.001UF
37 VSSA

VCC3
C132 22UF/6.3V_B
SC237 0.1UF_X7R
SC213 0.001UF

X/N 6
X/OUT 7
SY1 14.318MHz
SC166 10PF
SC217 10PF

Damping Resistors
Place near to the
Clock Outputs

CPUCLK0	40	SR125	33	CPUCLK0	CPUCLK0	3
CPUCLK#0	39	SR124	33	CPUCLK-0	CPUCLK-0	3
CPUCLK1	44	SR127	33	CPUCLK1	CPUCLK1	5
CPUCLK#1	43	SR126	33	CPUCLK-1	CPUCLK-1	5
SDCLK	47					
AGPCLK0	31	SR123	22	AGPCLK0	AGPCLK0	5
AGPCLK1	30					
ZCLK0	9	SR99	22	ZCLK1	ZCLK1	16
ZCLK1	10	SR98	22	ZCLK0	ZCLK0	7
PCICLK_F0/FS3	14	FS3 SR97	33	96XPCLK	96XPCLK	16
PCICLK_F1/FS4	15	FS4				
PCICLK0	16	SR96	33	SIOPCLK	SIOPCLK	22
PCICLK1	17	SR95	33	8100CLK	8100CLK	27
PCICLK2	20	SR94	33	MINI2CLK	MINI2CLK	26
PCICLK3	21	SR93	33	MINICLK	MINICLK	26
PCICLK4	22	SR92	33	KBCLK	KBCLK	25
PCICLK5	23					
REF0/FS0	2	FS0 SR102	33	OSCI	OSCI	17
REF1/FS1	3	FS1 SR101	33	VOSCI	VOSCI	7
REF2/FS2	4	FS2 SR100	33	VVBRCLK	VVBRCLK	14
48M	27	SR122	22	UCLK48M	UCLK48M	18
24_48M/MULTISEL	26	MULTISEL SR121	22	SIO48M	SIO48M	22
SCLK	35			SMBCLK	SMBCLK	10,11,17
SDATA	34			SMBDAT	SMBDAT	10,11,17

SELECT 66MHz ZCLK
SELECT 48MHz SIOCLK

VCC3
FS4 MULTISEL R73 SR120 10K_R 10K

By-Pass Capacitors
Place near to the Clock Outputs

CPUCLK0	SR136	49.9_1%
CPUCLK-0	SR135	49.9_1%
CPUCLK1	SR138	49.9_1%
CPUCLK-1	SR137	49.9_1%
AGPCLK0	SC235	10PF
ZCLK0	SC154	10PF
ZCLK1	SC155	10PF
96XPCLK	SC153	10PF
SIOPCLK	SC152	10PF
KBCLK	SC148	10PF
MINICLK	SC149	10PF
MINI2CLK	SC150	10PF
OSCI	SC158	10PF
VOSCI	SC157	10PF
VVBRCLK	SC156	10PF
UCLK48M	SC234	10PF
SIO48M	SC233	10PF
8100CLK	SC151	10PF

Different clock generator with different frequency define table

ICS952013

(FS4)	(FS3)	(FS2)	(FS1)	(FS0)	CPU (MHz)	ZCLK (MHz)	AGPCLK (MHz)	PCI (MHz)	SDRAM (MHz)
0	0	0	0	0	100.00	133.33	66.66	33.33	100.00
0	0	0	0	1	100.00	133.33	66.66	33.33	133.33
0	0	0	1	0	100.00	133.33	66.66	33.33	200.00
0	0	0	1	1	100.00	133.33	66.66	33.33	160.00
0	0	1	0	0	133.33	133.33	66.66	33.33	100.00
0	0	1	0	1	133.33	133.33	66.66	33.33	133.33
0	0	1	1	0	133.33	133.33	66.66	33.33	200.00
0	0	1	1	1	133.33	133.33	66.66	33.33	166.66
0	1	0	0	0	200.00	133.33	66.66	33.33	100.00
0	1	0	0	1	200.00	133.33	66.66	33.33	133.33
0	1	0	1	0	200.00	133.33	66.66	33.33	200.00
0	1	0	1	1	200.00	133.33	66.66	33.33	160.00
0	1	1	0	0	160.00	133.33	66.66	33.33	100.00
0	1	1	0	1	166.66	133.33	66.66	33.33	133.33
0	1	1	1	0	160.00	133.33	66.66	33.33	200.00
0	1	1	1	1	166.66	133.33	66.66	33.33	166.66

ICS952013

(FS4)	(FS3)	(FS2)	(FS1)	(FS0)	CPU (MHz)	ZCLK (MHz)	AGPCLK (MHz)	PCI (MHz)	SDRAM (MHz)
1	0	0	0	0	100.00	66.66	66.66	33.33	100.00
1	0	0	0	1	100.00	66.66	66.66	33.33	133.33
1	0	0	1	0	100.00	66.66	66.66	33.33	200.00
1	0	0	1	1	100.00	66.66	66.66	33.33	166.66
1	0	1	0	0	133.33	66.66	66.66	33.33	100.00
1	0	1	0	1	133.33	66.66	66.66	33.33	133.33
1	0	1	1	0	133.33	66.66	66.66	33.33	200.00
1	0	1	1	1	133.33	66.66	66.66	33.33	166.66
1	1	0	0	0	100.99	67.33	67.33	33.66	100.99
1	1	0	0	1	100.99	67.33	67.33	33.66	134.65
1	1	0	1	0	100.99	67.33	67.33	33.66	201.98
1	1	0	1	1	100.99	67.33	67.33	33.66	168.31
1	1	1	0	0	134.65	67.33	67.33	33.66	100.00
1	1	1	0	1	134.65	67.33	67.33	33.66	134.65
1	1	1	1	0	134.65	67.33	67.33	33.66	201.98
1	1	1	1	1	134.65	67.33	67.33	33.66	168.31

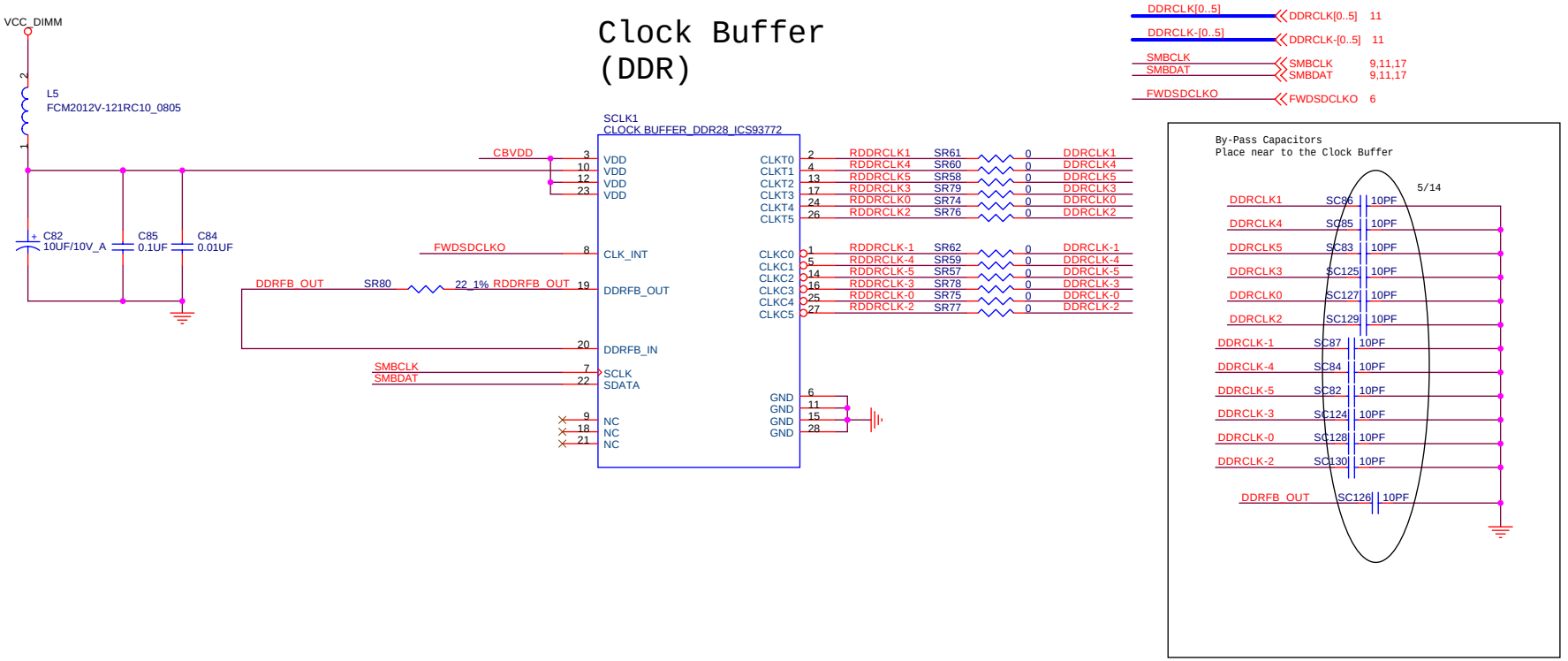
Elitegroup Computer SystemS

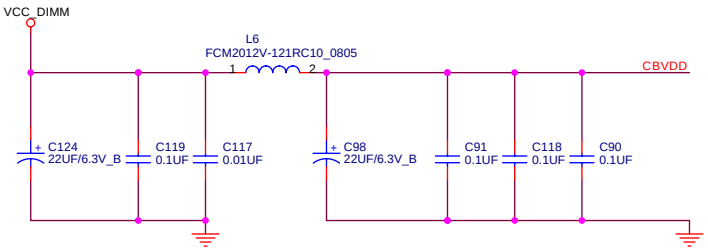
Title: CLOCK GENERATOR

Size: B | Document Number: 557S-1-4-01 | Rev: 2.0

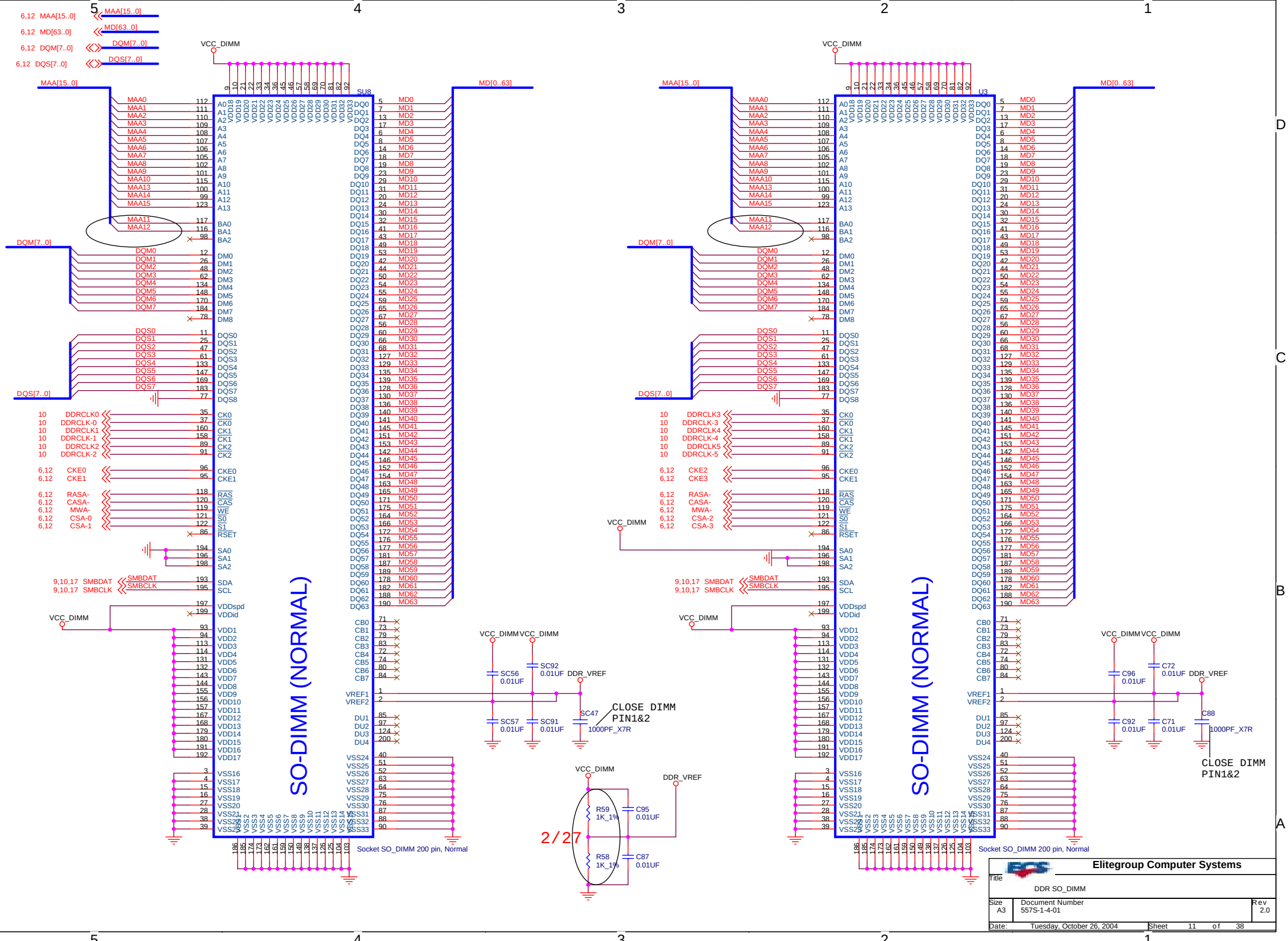
Date: Tuesday, October 26, 2004 | Sheet 9 of 38

Clock Buffer (DDR)





Elitegroup Computer Systems		
Title: CLOCK BUFFER FOR DDR		
Size: B	Document Number: 557S-1-4-01	Rev: 2.0
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6,12 MAA[15..0]
MAA[15..0]
6,12 MD[63..0]
MD[63..0]
6,12 DQM[7..0]
DQM[7..0]
6,12 DQS[7..0]
DQS[7..0]
VCC_DIMM
SU8
U3
MD[0..63]
SO-DIMM (NORMAL)
DDRCLK0
DDRCLK-0
DDRCLK1
DDRCLK-1
DDRCLK2
DDRCLK-2
DDRCLK3
DDRCLK-3
DDRCLK4
DDRCLK-4
DDRCLK5
DDRCLK-5
CKE0
CKE1
CKE2
CKE3
RASA-
CASA-
MWA-
CSA-0
CSA-1
CSA-2
CSA-3
RSET
SA0
SA1
SA2
9,10,17 SMBDAT
9,10,17 SMBCLK
SMBDAT
SMBCLK
SDA
SCL
VDDspd
VDDid
VDD1
VDD2
VDD3
VDD4
VDD5
VDD6
VDD7
VDD8
VDD9
VDD10
VDD11
VDD12
VDD13
VDD14
VDD15
VDD16
VDD17
VSS16
VSS17
VSS18
VSS19
VSS20
VSS21
VSS22
VSS23
VSS24
VSS25
VSS26
VSS27
VSS28
VSS29
VSS30
VSS31
VSS32
VSS33
CB0
CB1
CB2
CB3
CB4
CB5
CB6
CB7
VREF1
VREF2
DU1
DU2
DU3
DU4
SC56 0.01UF
SC92 0.01UF
SC57 0.01UF
SC91 0.01UF
DDR_VREF
SC47 1000PF_X7R
CLOSE DIMM PIN1&2
R59 1K_1%
R58 1K_1%
C95 0.01UF
C87 0.01UF
2/27
C96 0.01UF
C72 0.01UF
C92 0.01UF
C71 0.01UF
C88 1000PF_X7R
Socket SO_DIMM 200 pin, Normal
Elitegroup Computer Systems
DDR SO_DIMM
Size A3
Document Number 557S-1-4-01
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Sheet 11 of 38

SO-DIMM no "WP" pin

SSTL-2 Termination Resistors

	SDR	Rs	DDR	Rs	Rtt
MD/DQM(/DQS)	LV-CMOS	0/10/-	SSTL-2	10	33
MA/Control	LV-CMOS	10	SSTL-2	0	33
CS	LV-CMOS	0	SSTL-2	0	47
CKE	DD 3.3V		DD 2.5V		

Control signals can NOT be placed within the same RPs as data , strobe , or command signals

2/27

MD[0..63] MD[0..63] 6,11
DQM[0..7] DQM[0..7] 6,11
DQS[0..7] DQS[0..7] 6,11
MAA[0..15] MAA[0..15] 6,11

VCC_DIMM
RP14 470X4_R
CKE3 6,11
CKE2 6,11
CKE1 6,11
CKE0 6,11

DDR_VTT

SRP9: MD4 MD5 DQM0 MD6
RP1: MD2 DQS0 MD1 MD0
RP3: MD17 MD16 MD11 MD10
SRP10: MD7 MD12 MD13 DQM1
RP2: DQS1 MD9 MD8 MD3
RP4: MD24 MD19 MD18 DQS2
SRP11: MD14 MD15 MD20 MD21
RP5: MD27 MD26 DQS3 MD25
SRP12: MD29 DQM3 MD30 MD31
SRP20: DQM2 MD22 MD23 MD28
SRP16: MD36 MD37 DQM4 MD38
RP13: DQS5 MD41 MD40 MD35
RP9: MD34 DQS4 MD33 MD32
SRP21: MD39 MD44 MD45 DQM5
RP10: MD49 MD48 MD43 MD42
SRP18: DQM6 MD54 MD55 MD60
RP11: MD56 MD51 MD50 DQS6
SRP17: MD46 MD47 MD52 MD53
RP12: MD59 MD58 DQS7 MD57
SRP19: MD61 DQM7 MD62 MD63
(33X4_0402)

SRP14: MAA2 MAA0 MAA12 RASA- → RASA- 6,11
RP6: MAA5 MAA7 MAA9 MAA14
RP7: MAA11 MAA10 MAA1 MAA3
SRP13: MAA13 MAA8 MAA6 MAA4
SRP15: CSA-0 CSA-1 CSA-2 CSA-3 MAA15
RP8: CASA- MWA-
(47X4_0402 / 33X4_0402)

ADD 13 PCS
0.1UF

DECOUPLING CAPACITOR FOR SSTL-2 END TERMIANTION VTT ISLAND
0603 Package placed within 300mils of VTT Termination R-packs

SC34 SC35 SC36 SC51 SC26 SC48 SC37 SC50 C36 C38 SC31 SC30 C34 SC46 SC49 C37 C53 SC29 C33 C29 C50 C52 SC25 C43 C47 SC28 C30 C49 SC52 C48 C51 C42 C32 SC55 SC24 C62 SC58 C60 SC23 C46 C39 SC32 C31 C40 C35 C54 C41 SC27 SC53 SC54 SC33 C55 (0.1UF_X7R)

every one "RPs" put two CAPs

0.1UF X 39 pcs

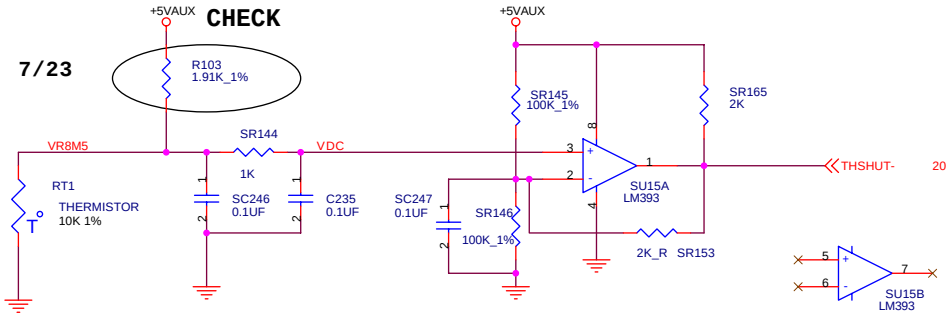
DIMM DECOUPLING ; CLOSE TO DIMM

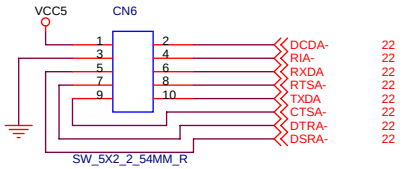
SC59 C110 C112 SC61 C59 C109 SC101 C103 SC104 SC100 C102 SC99 C116 C57 C61 SC60 C100 SC107 SC102 C111 SC106 C108 SC105 C104 SC65 SC63 C107 C101 SC103 C115 (0.1UF_X7R) C58 C106 C114 C99 (0.1UF_X7R) 1000PF_X7R 10UF/10V_1206 SC108 SC109 (0.1UF_X7R)

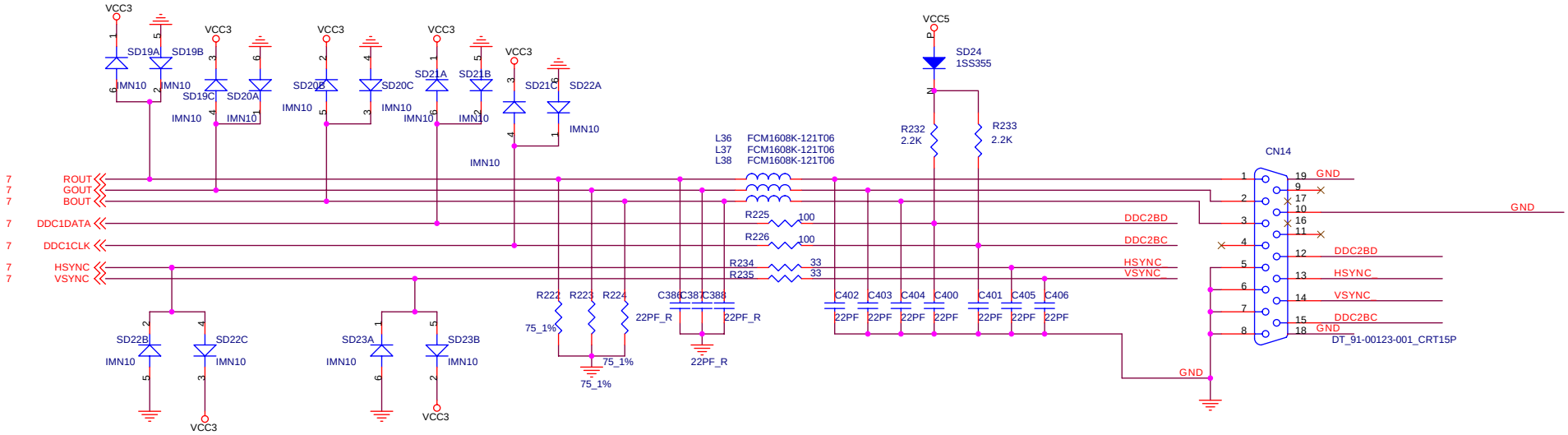
Add island plane

SC40 220UF/10V_D C27 220UF/10V_D

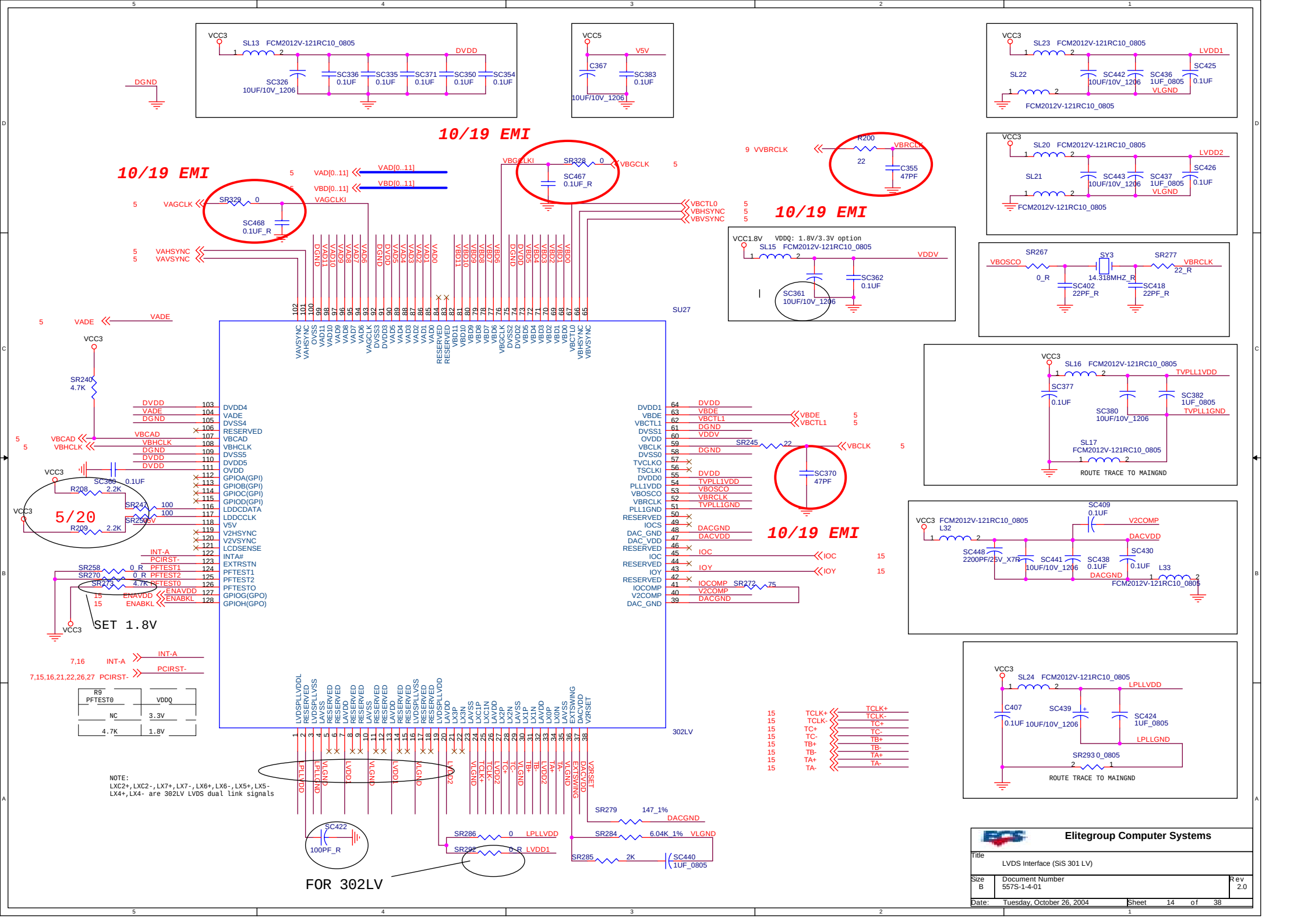
Elitegroup Computer Systems		
Title: DDR TERMINATION & DECOUPLING		
Size: B	Document Number: 557S-1-4-01	Rev: 2.0
Date: Tuesday, October 26, 2004	Sheet 12 of 38	



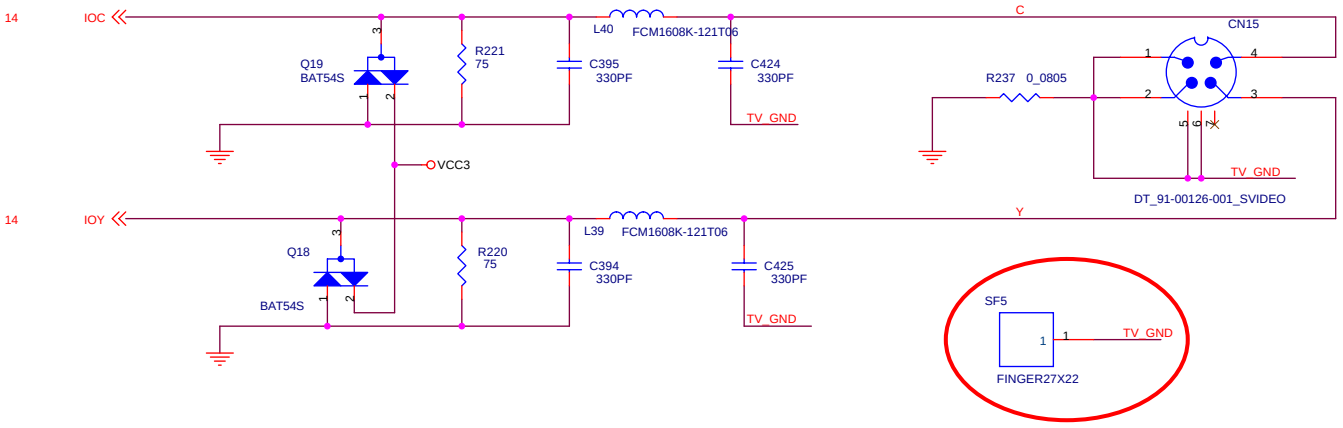




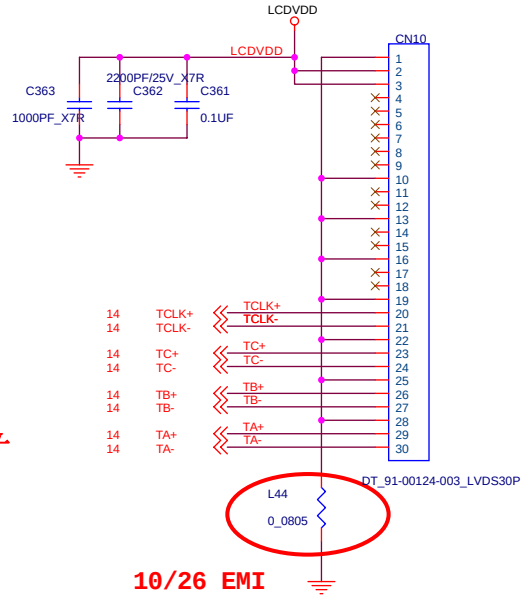
Elitegroup Computer Systems		
Title: VGA & CRT CONN.		
Size: B	Document Number: 557S-1-4-01	Rev: 2.0
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10/19 EMI
10/19 EMI
10/19 EMI
10/19 EMI
5/20
SET 1.8V
FOR 302LV
SU27
302LV
VCC3
VCC5
DGND
SL13 FCM2012V-121RC10_0805
SL23 FCM2012V-121RC10_0805
SL22 FCM2012V-121RC10_0805
SL20 FCM2012V-121RC10_0805
SL21 FCM2012V-121RC10_0805
SL15 FCM2012V-121RC10_0805
SL16 FCM2012V-121RC10_0805
SL17 FCM2012V-121RC10_0805
SL24 FCM2012V-121RC10_0805
L32 FCM2012V-121RC10_0805
DVDD
LVDD1
LVDD2
LPLLVDD
TVPLL1VDD
V2COMP
DACVDD
VLGND
LPLLGND
TVPLL1GND
VBRCLK
VDDV
VBCLK
VBDE
VBCTL0
VBCTL1
VBHSYNC
VBVSYNC
VAVSYNC
VAHSYNC
VAGCLK
VBGCLK
VADE
VBCAD
VBHCLK
VAD[0..11]
VBD[0..11]
VVBRCLK
VBOSCO
IOC
IOY
IOCOMP
DACGND
TCLK+
TCLK-
TC+
TC-
TB+
TB-
TA+
TA-
INT-A
PCIRST-
ENAVDD
ENABKL
PFTEST0
PFTEST1
PFTEST2
PFTESTO
V5V
R200 22
C355 47PF
SR328 0
SC467 0.1UF_R
SR329 0
SC468 0.1UF_R
SR240 4.7K
SC360 0.1UF
R208 2.2K
R209 2.2K
SR247 100
SR250 100
SR258 0_R
SR270 0_R
SR273 4.7K
SR245 22
SC370 47PF
SR272 75
SC361 10UF/10V_1206
SC362 0.1UF
SR267 0_R
SY3 14.318MHZ_R
SR277 22_R
SC402 22PF_R
SC418 22PF_R
SC409 0.1UF
SC448 2200PF/25V_X7R
SC441 10UF/10V_1206
SC438 0.1UF
SC430 0.1UF
L33
SC422 100PF_R
SR286 0
SR292 0_R
SR279 147_1%
SR284 6.04K_1%
SR285 2K
SC440 1UF_0805
R9 PFTEST0 NC 4.7K
VDDQ 3.3V 1.8V
NOTE:
LXC2+,LXC2-,LX7+,LX7-,LX6+,LX6-,LX5+,LX5-
LX4+,LX4- are 302LV LVDS dual link signals
Elitegroup Computer Systems
LVDS Interface (SiS 301 LV)
Size B
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Sheet 14 of 38

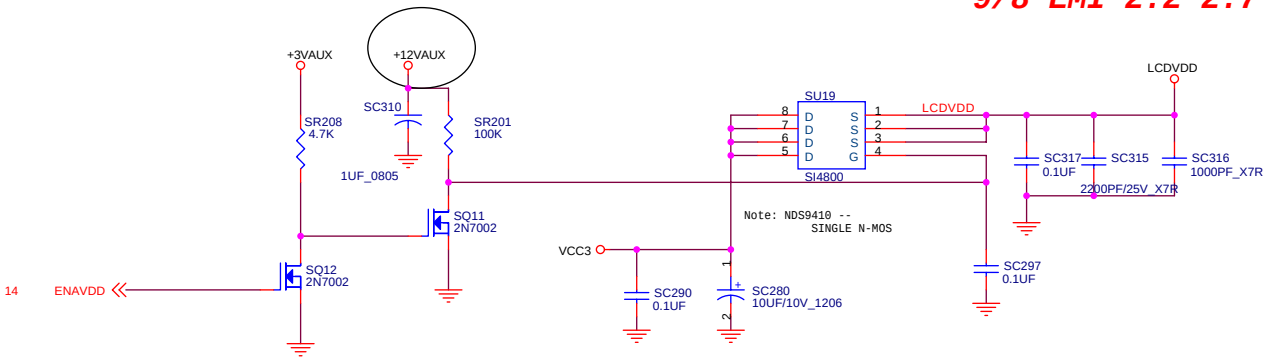
5/20

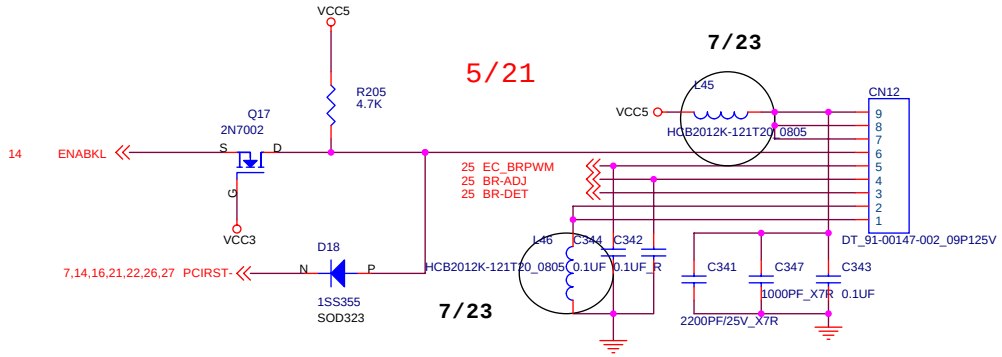


9/8 EMI 2.2*2.7 彈片



10/26 EMI





Elitegroup Computer Systems		
Title: LCD/TV CONN.		
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963L-1

PCI

IDE

MuTIOL

SWAP 2.3

RP17 8.2KX4 — INT-D, INT-B, INT-C, INT-A

RP15 4.7KX4 — PGNT-4, PGNT-2, PGNT-3, PGNT-1, PGNT-0; R81 4.7K

PCI Bus Configuation

Dev.	INT.	REQ.	GNT.	IDSEL.
MINIPCI	C, D	2	2	16
RTL8110S	B	0	0	18
M661MX	A			11, 12
SIS301LV	A			
963L				13, 14, 15

POWER DISSIPATION?

Analog Power supplies of Transzip function for 961 Chip.

SR83 0; 10UF/10V_1206_R SC123; SC136 0.1UF; SC143 0.1UF; SJP1 SHORT; SZ1XAVDD; SZ1XAVSS

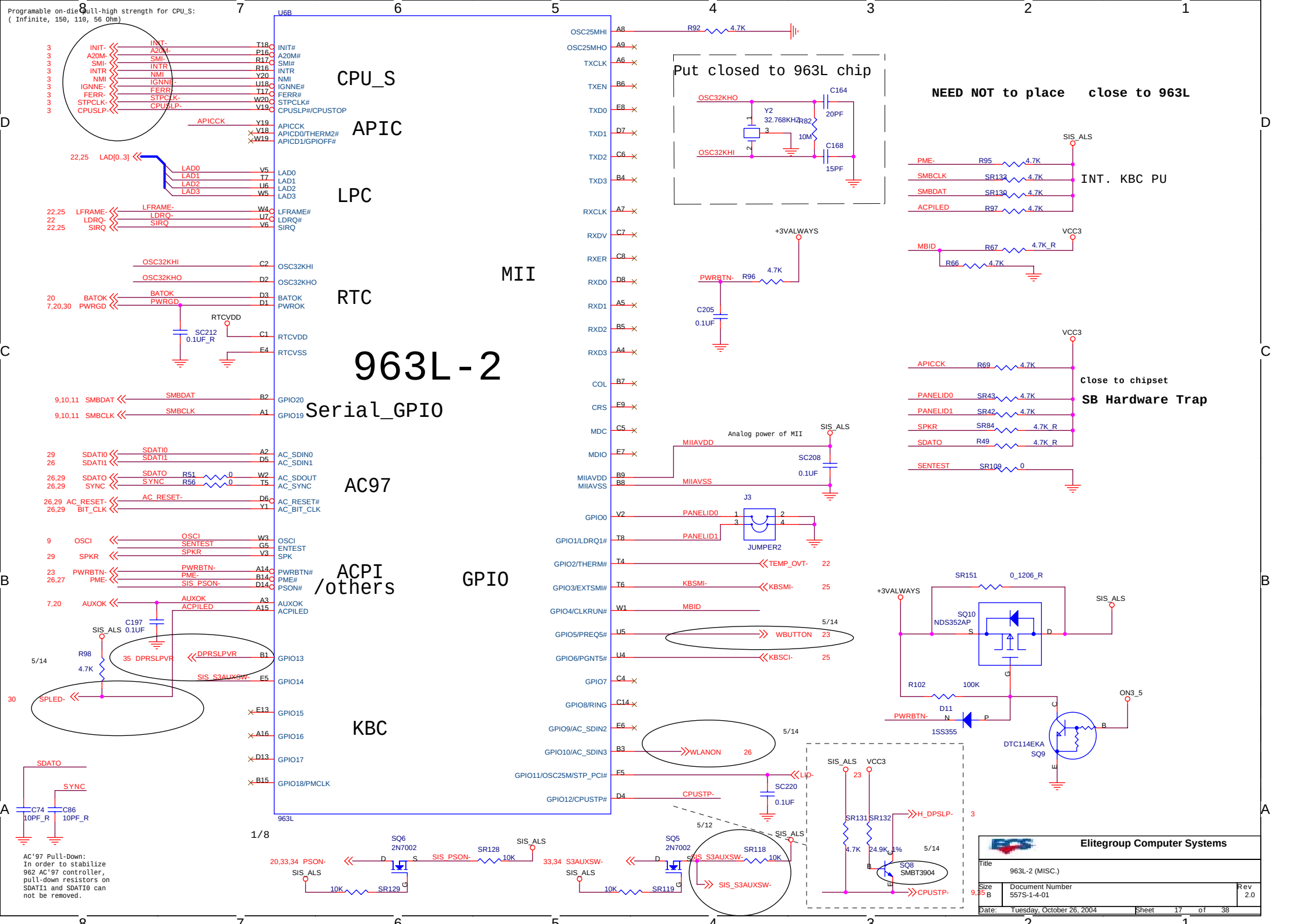
L7 HCB2012K-121T20; 10UF/10V_1206 C120; C126 0.1UF; C127 0.1UF; JP3 SHORT; SZ4XAVDD; SZ4XAVSS

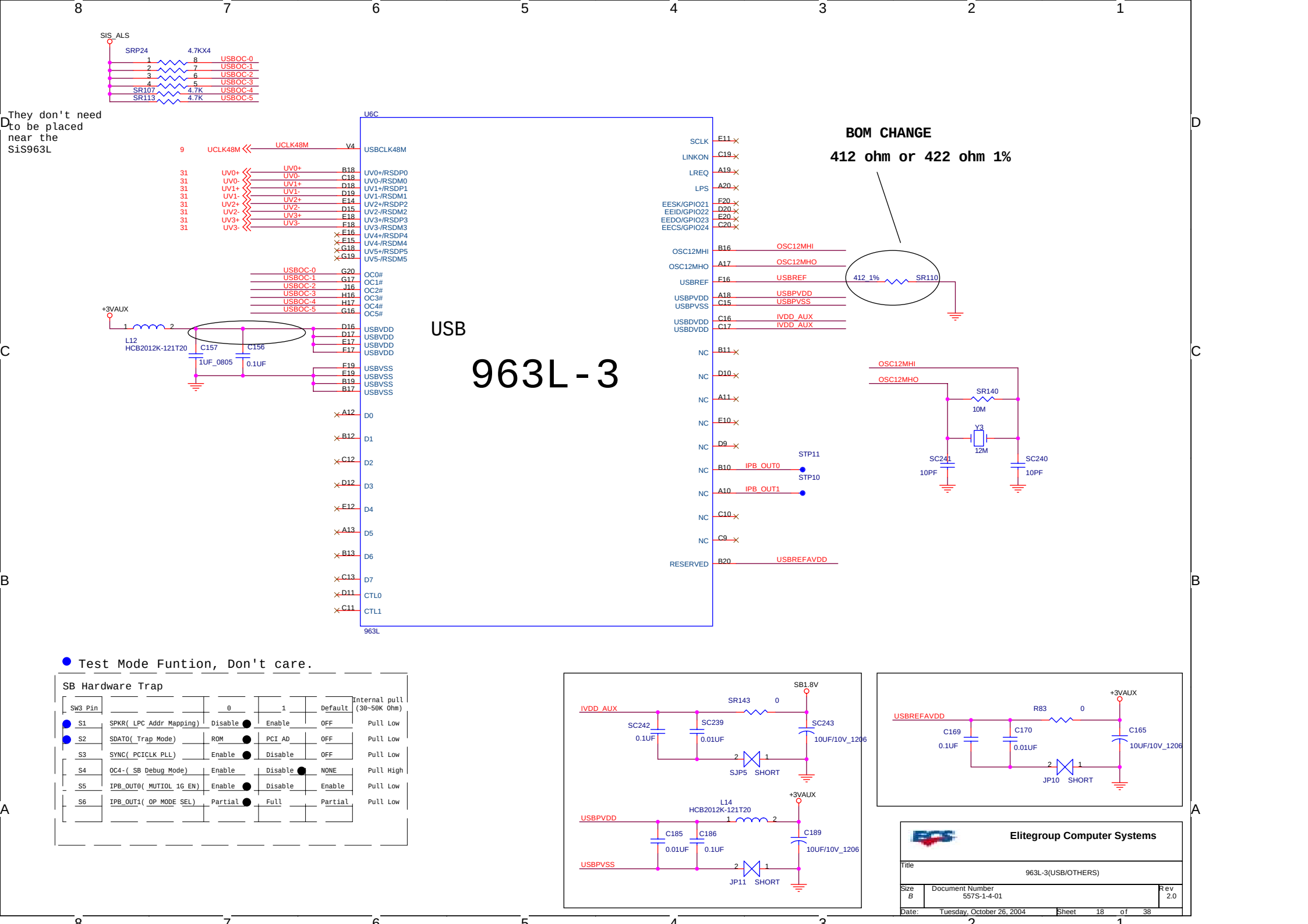
R72 0; R71 150_1%; R70 49.9_1%; C129 0.1UF; SR90 56; C131 0.1UF; C128 0.1UF; SR89 56; JP4 SHORT; SVDDZCMP; SZCMP_N; SZVREF; SZCMP_P; SVSSZCMP

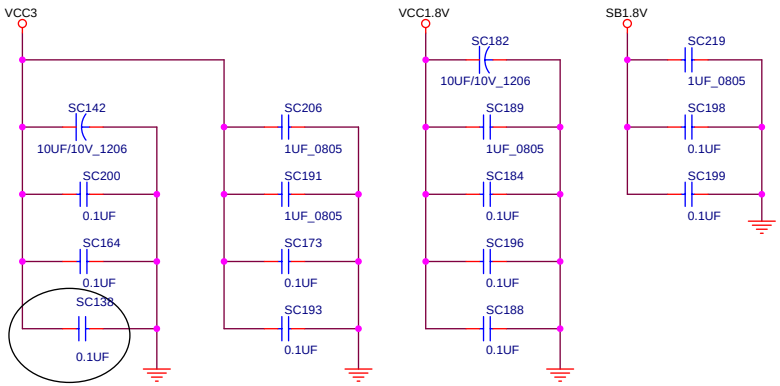
150 Ohm for MuTIOL 1.0/66MHz
49.9 Ohm for MuTIOL 2.0/133Mhz

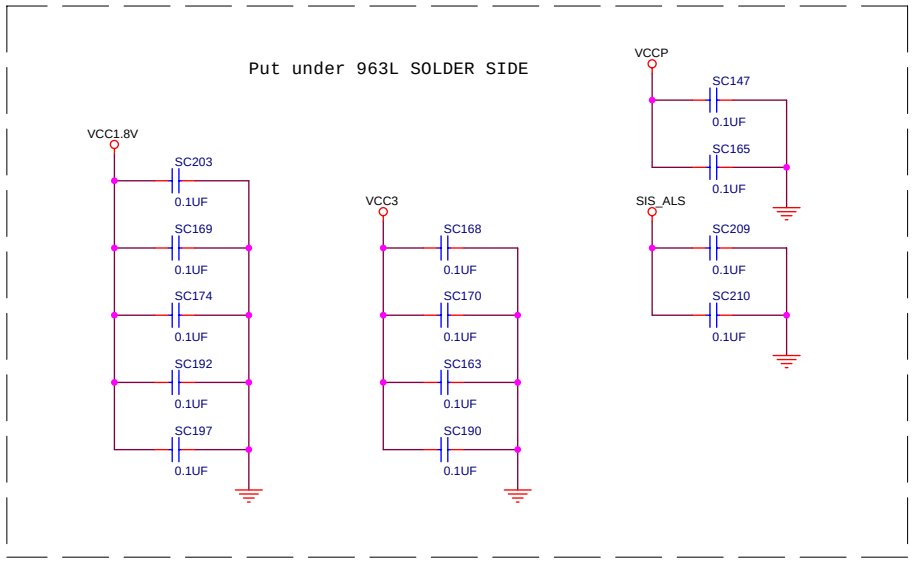
R68 0; C125 0.01UF; C122 0.1UF; JR2 SHORT; SC118 0.1UF

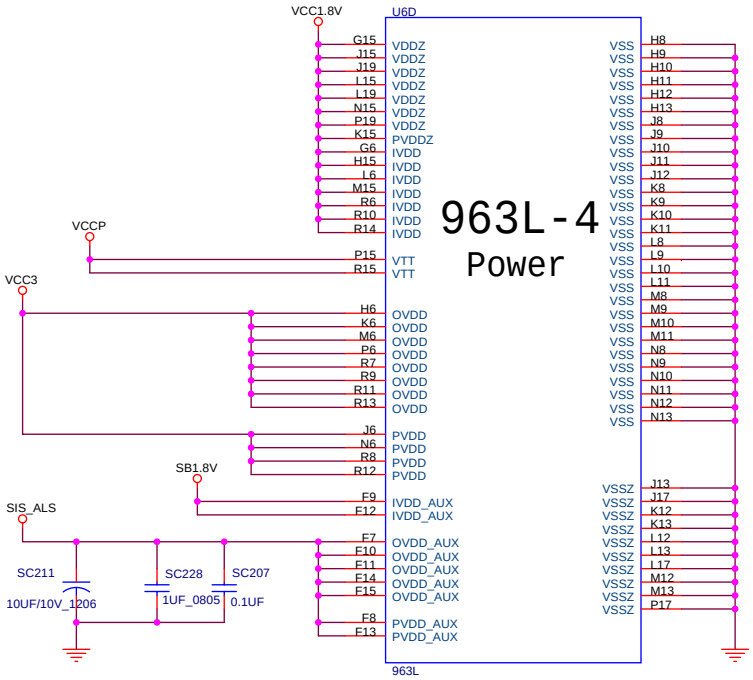
Elitegroup Computer Systems	
Title	963L-1 (PCI/IDE/HyperZip)
Size B	Document Number 557S-1-4-01
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Date: Tuesday, October 26, 2004	Sheet 16 of 38


Programable on-die pull-high strength for CPU_S:
(Infinite, 150, 110, 56 Ohm)
U6B
CPU_S
APIC
LPC
RTC
MII
963L-2
Serial_GPIO
AC97
ACPI
/others
GPIO
KBC
963L
Put closed to 963L chip
NEED NOT to place close to 963L
INT. KBC PU
Close to chipset
SB Hardware Trap
Analog power of MII
JUMPER2
AC'97 Pull-Down:
In order to stabilize
962 AC'97 controller,
pull-down resistors on
SDATI1 and SDATI0 can
not be removed.
Elitegroup Computer Systems
963L-2 (MISC.)
Document Number
557S-1-4-01
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2.0
Tuesday, October 26, 2004
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SIS_ALS
SRP24
4.7KX4
USBOC-0
USBOC-1
USBOC-2
USBOC-3
USBOC-4
USBOC-5
SR107
SR113
4.7K
They don't need to be placed near the SiS963L
U6C
USB
963L-3
963L
UCLK48M
USBCLK48M
UV0+/RSDP0
UV0-/RSDM0
UV1+/RSDP1
UV1-/RSDM1
UV2+/RSDP2
UV2-/RSDM2
UV3+/RSDP3
UV3-/RSDM3
UV4+/RSDP4
UV4-/RSDM4
UV5+/RSDP5
UV5-/RSDM5
OC0#
OC1#
OC2#
OC3#
OC4#
OC5#
USBVDD
USBVSS
D0
D1
D2
D3
D4
D5
D6
D7
CTL0
CTL1
+3VAUX
L12
HCB2012K-121T20
C157
1UF_0805
C156
0.1UF
SCLK
LINKON
LREQ
LPS
EESK/GPIO21
EEID/GPIO22
EEDO/GPIO23
EECS/GPIO24
OSC12MHI
OSC12MHO
USBREF
USBPVDD
USBPVSS
USBDVDD
IVDD_AUX
NC
RESERVED
USBREFAVDD
STP11
STP10
IPB_OUT0
IPB_OUT1
BOM CHANGE
412 ohm or 422 ohm 1%
412_1%
SR110
SR140
10M
Y3
12M
SC241
10PF
SC240
10PF
Test Mode Funtion, Don't care.
SB Hardware Trap
SW3 Pin
Default
Internal pull (30~50K Ohm)
S1 SPKR(LPC Addr Mapping) Disable Enable OFF Pull Low
S2 SDATO(Trap Mode) ROM PCI AD OFF Pull Low
S3 SYNC(PCICLK PLL) Enable Disable OFF Pull Low
S4 OC4-(SB Debug Mode) Enable Disable NONE Pull High
S5 IPB_OUT0(MUTIOL 1G EN) Enable Disable Enable Pull Low
S6 IPB_OUT1(OP MODE SEL) Partial Full Partial Pull Low
SB1.8V
SR143
SC242
0.1UF
SC239
0.01UF
SC243
10UF/10V_1206
SJP5 SHORT
L14
HCB2012K-121T20
C185
0.01UF
C186
0.1UF
C189
10UF/10V_1206
JP11 SHORT
R83
C169
0.1UF
C170
0.01UF
C165
10UF/10V_1206
JP10 SHORT
Elitegroup Computer Systems
Title
963L-3(USB/OTHERS)
Size B
Document Number 557S-1-4-01
Rev 2.0
Date: Tuesday, October 26, 2004
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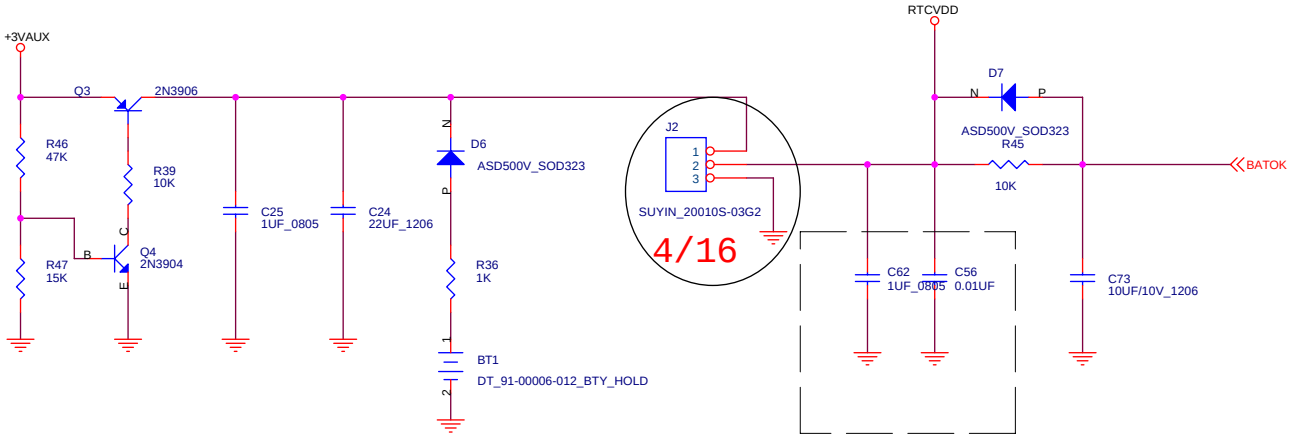


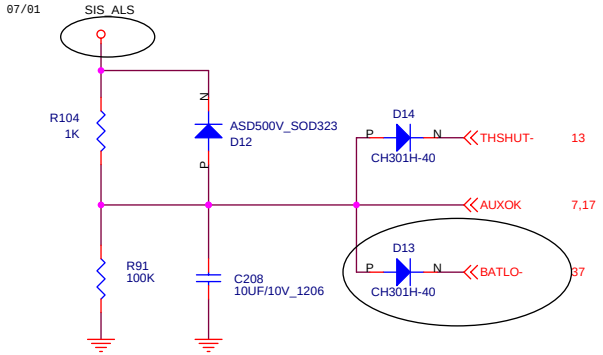


Elitegroup Computer Systems		
Title: 963L-4 (Powers)		
Size: B	Document Number: 557S-1-4-01	Rev: 2.0
Date: Tuesday, October 26, 2004	Sheet 19 of 38	

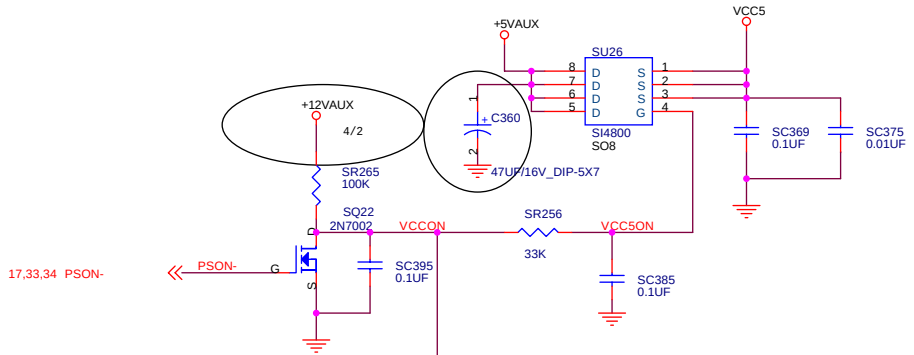
RTC

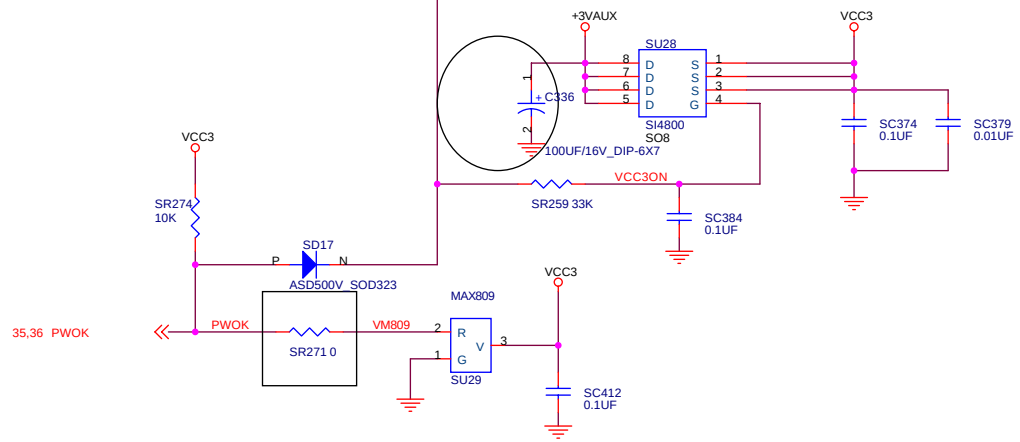
NOTE!
1.The RTCVDD is 3V
2.Decoupling capacitor must be close to 963L RTCVDD pin.
3.RTC circuit must strictly follow SiS's recommended design
  SiS is not responsible for RTC problems from foreign designs.



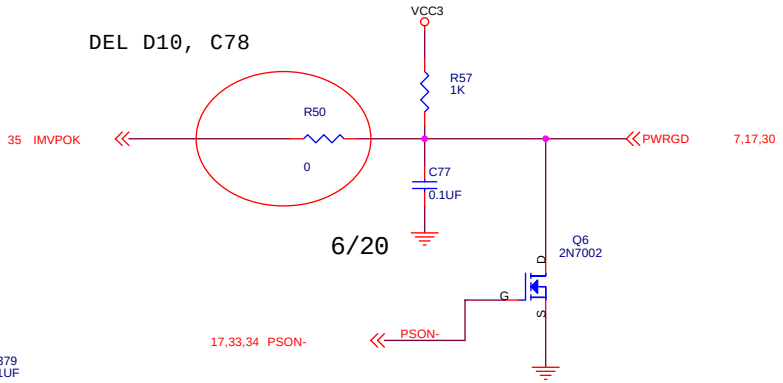


CONN---> LONGTRUMP_BH-8009G_RTC Place close to 963L



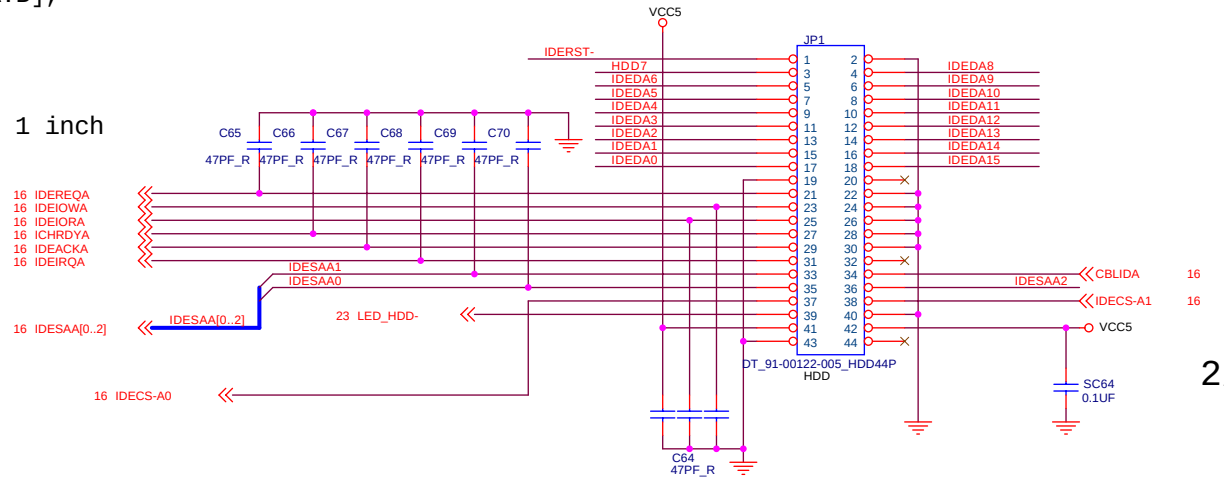


DEL D10, C78

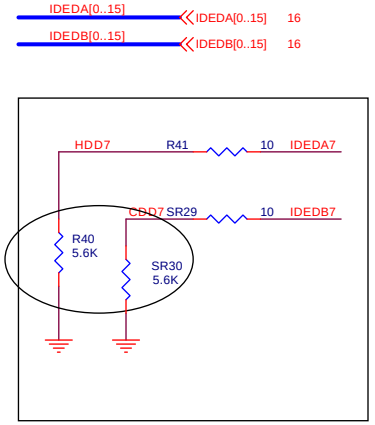


Elitegroup Computer Systems		
Title: RTC & VCC3 & VCC5		
Size: B	Document Number: 557S-1-4-01	Rev: 2.0
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Place the damping resistors for IDEA[2:0][A:B], IDECS[1:0][A:B], IDEIOR[A:B]#, IDEIOW[A:B]#, and IDACK[A:B]# near SiS962L
The length must be less than 1 inch



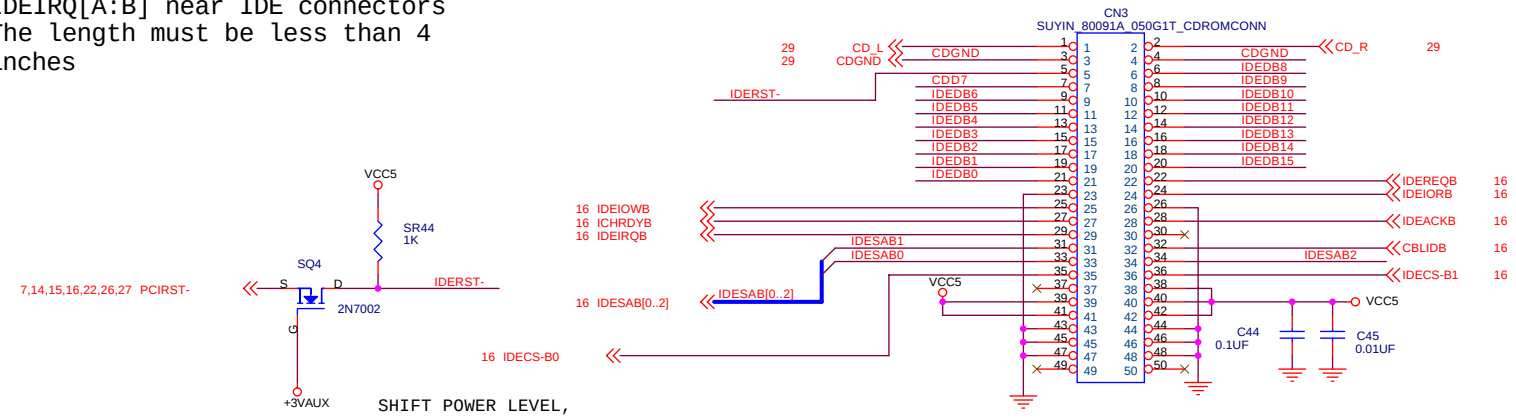
CLOSE TO HDD



CLOSE TO HDD CONN.

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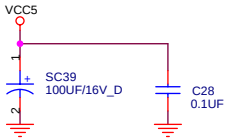
Place the damping resistors for IDED[15:0][A:B], IDREQ[A:B], ICHRDY[A:B], and IDEIRQ[A:B] near IDE connectors
The length must be less than 4 inches



SHIFT POWER LEVEL, 3.3V TO 5V

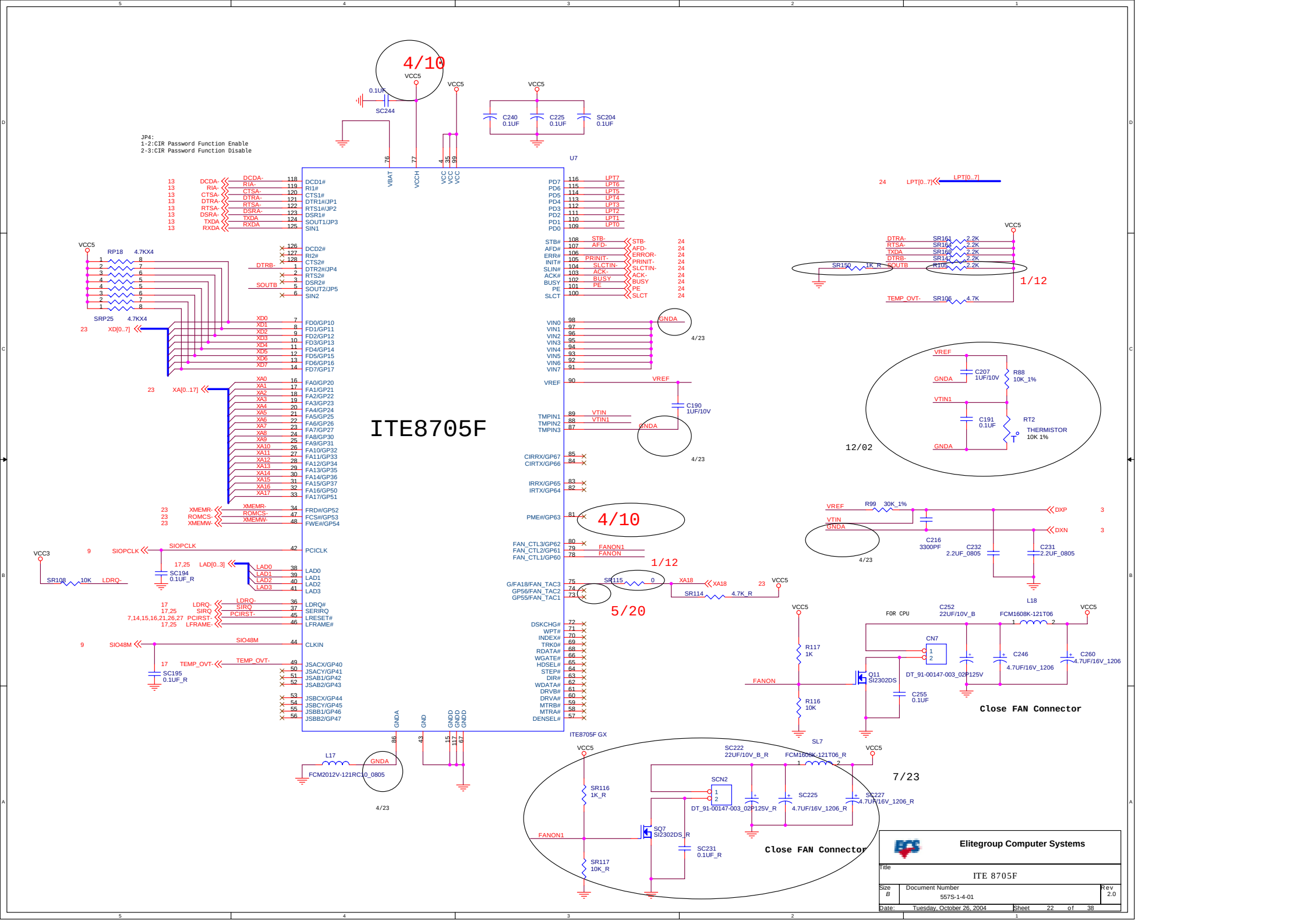
CSEL : "NC" ; "Hi" --> Master(CDROM)/Slave(DVDROM)

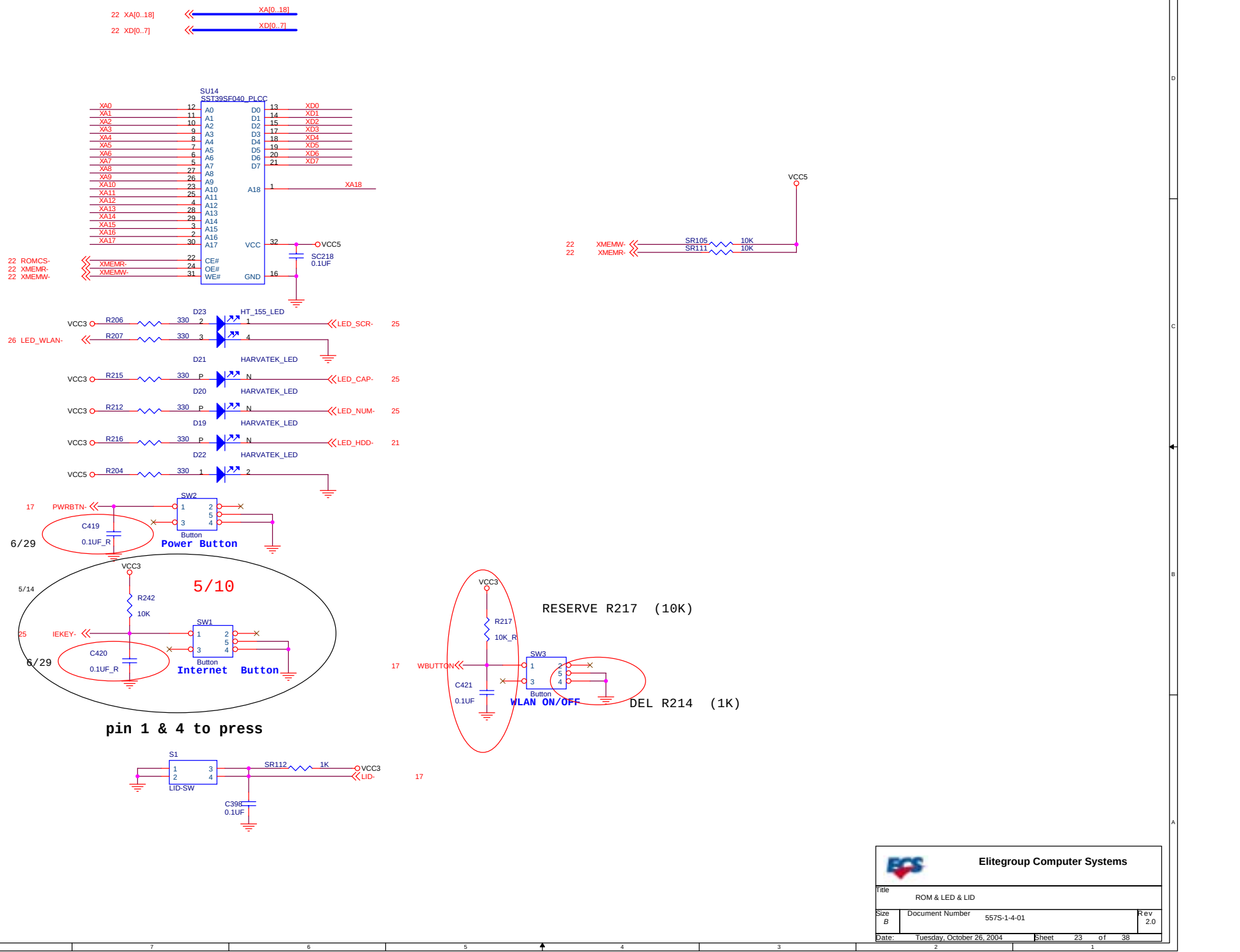
CSEL : "Low --> Slave(CDROM)/Master(DVDROM)



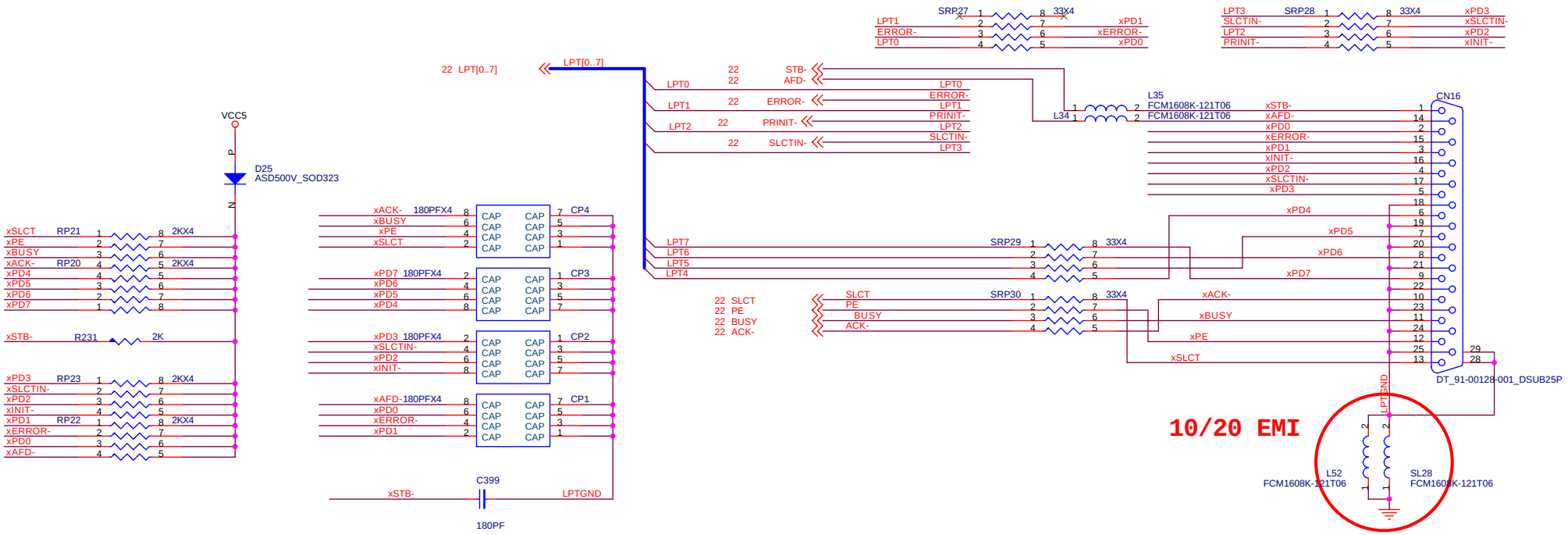
CLOSE TO CDROM/DVDROM

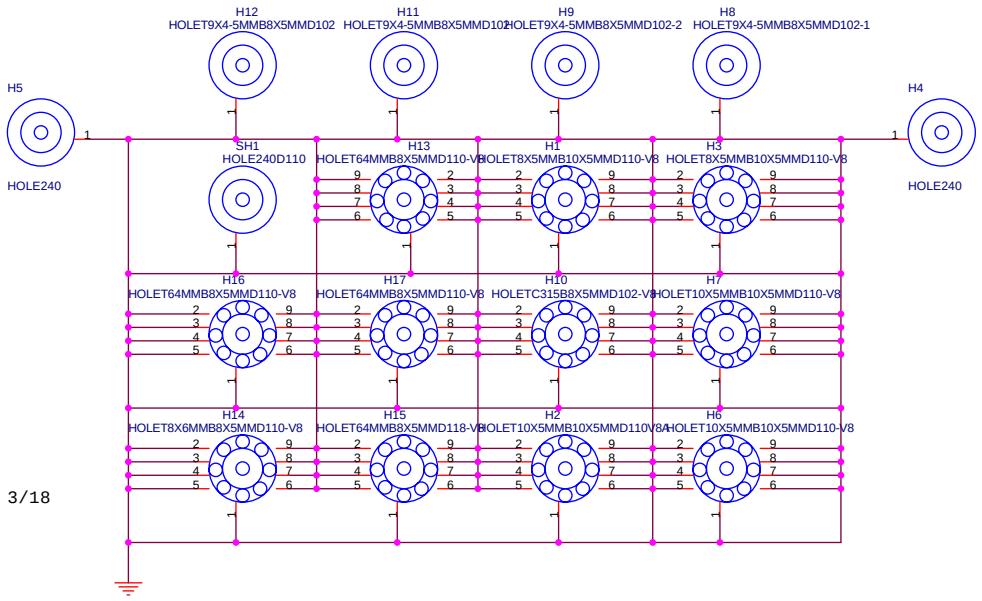
Elitegroup Computer Systems		
Title: HDD & CDROM CONN.		
Size: B	Document Number: 557S-1-4-01	Rev: 2.0
Date: Tuesday, October 26, 2004	Sheet 21 of 38	


4/10
JP4:
1-2:CIR Password Function Enable
2-3:CIR Password Function Disable
U7
ITE8705F
ITE8705F GX
VCC5
0.1UF
SC244
C240 0.1UF
C225 0.1UF
SC204 0.1UF
VBAT
VCCH
VCC
GNDA
GND
RP18 4.7KX4
SRP25 4.7KX4
DCDA-
RIA-
CTSA-
DTRA-
RTSA-
DSRA-
TXDA
RXDA
DTRB-
SOUTB
XD[0..7]
XA[0..17]
XMEMR-
ROMCS-
XMEMW-
SIOPCLK
LAD[0..3]
LDRQ-
SIRQ
PCIRST-
LFRAME-
SIO48M
TEMP_OVT-
SC194 0.1UF_R
SC195 0.1UF_R
LPT[0..7]
STB-
AFD-
ERROR-
PRINIT-
SLCTIN-
ACK-
BUSY
PE
SLCT
GNDA
VREF
VTIN
VTIN1
C190 1UF/10V
4/23
CIRRX/GP67
CIRTX/GP66
IRRX/GP65
IRTX/GP64
PME#/GP63
FANON1
FANON
1/12
SR115 0
XA18
SR114 4.7K_R
5/20
DSKCHG#
WPT#
INDEX#
TRK0#
RDATA#
WGATE#
HDSEL#
STEP#
DIR#
WDATA#
DRV#
DRVA#
MTRB#
MTRA#
DENSEL#
L17
FCM2012V-121RC10_0805
SR161 2.2K
SR162 2.2K
SR160 2.2K
SR147 2.2K
SR105 2.2K
SR150 1K_R
SR106 4.7K
TEMP_OVT-
1/12
12/02
C207 1UF/10V
R88 10K_1%
C191 0.1UF
RT2
THERMISTOR 10K 1%
R99 30K_1%
C216 3300PF
C232 2.2UF_0805
C231 2.2UF_0805
DXP
DXN
L18
FOR CPU
C252 22UF/10V_B
FCM1608K-121T06
CN7
DT_91-00147-003_02P125V
C246 4.7UF/16V_1206
C260 4.7UF/16V_1206
Q11 SI2302DS
R117 1K
R116 10K
C255 0.1UF
Close FAN Connector
SL7
SC222 22UF/10V_B_R
FCM1608K-121T06_R
SCN2
DT_91-00147-003_02P125V_R
SC225 4.7UF/16V_1206_R
SC227 4.7UF/16V_1206_R
SR116 1K_R
SR117 10K_R
SQ7 SI2302DS_R
SC231 0.1UF_R
7/23
Elitegroup Computer Systems
Title
ITE 8705F
Size B
Document Number
557S-1-4-01
Rev 2.0
Date: Tuesday, October 26, 2004
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Elitegroup Computer Systems		
Title: ROM & LED & LID		
Size: B	Document Number: 557S-1-4-01	Rev: 2.0
Date: Tuesday, October 26, 2004	Sheet 23 of 38	





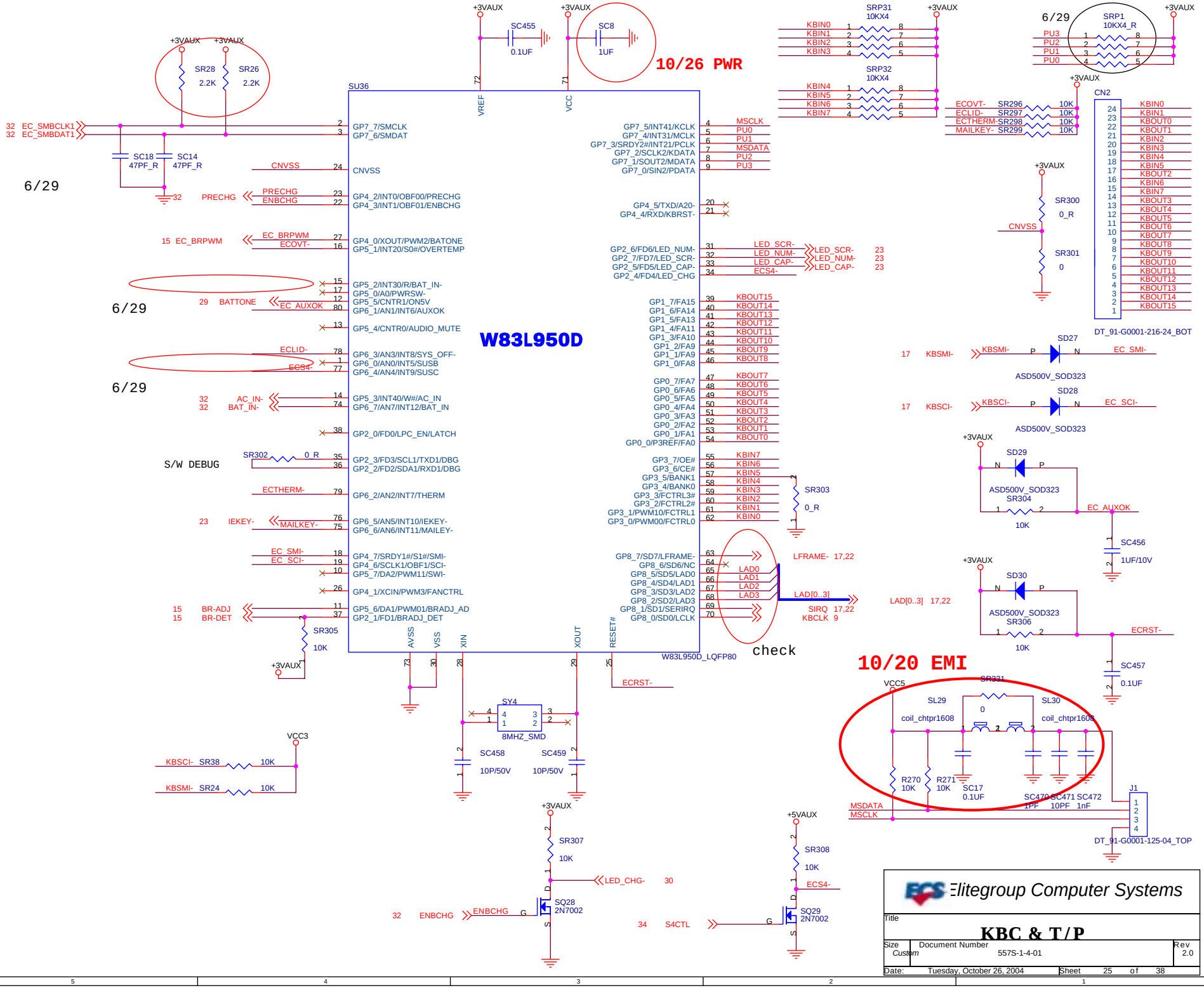
3/18

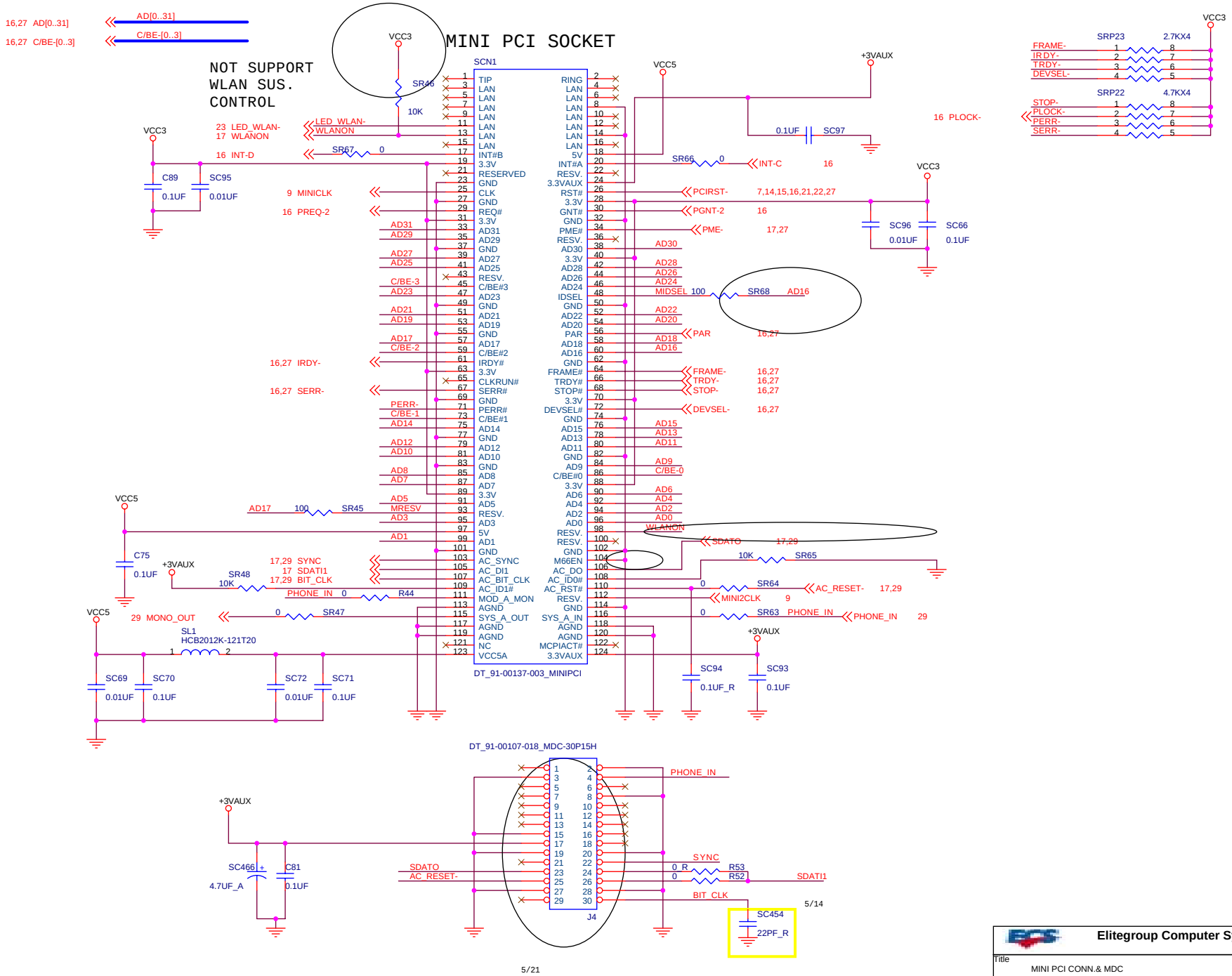
Elitegroup Computer Systems

Title: LPT PORT

Size: B | Document Number: 557S-1-4-01 | Rev: 2.0

Date: Tuesday, October 26, 2004 | Sheet 24 of 38


10/26 PWR
W83L950D
W83L950D_LQFP80
SU36
6/29
6/29
6/29
S/W DEBUG
check
10/20 EMI
+3VAUX
SC455
0.1UF
SC8
1UF
SR28
2.2K
SR26
2.2K
SC18
47PF_R
SC14
47PF_R
CNVSS
PRECHG
ENBCHG
EC_BRPWM
ECOVT-
BATTONE
EC_AUXOK
ECLID-
ECS4-
AC_IN-
BAT_IN-
SR302
0_R
ECTHERM-
IEKEY-
MAILKEY-
EC_SMI-
EC_SCI-
BR-ADJ
BR-DET
SR305
10K
SY4
8MHZ_SMD
SC458
10P/50V
SC459
10P/50V
VCC3
KBSCI- SR38 10K
KBSMI- SR24 10K
SR307
10K
LED_CHG-
SQ28
2N7002
ENBCHG
SR308
10K
ECS4-
SQ29
2N7002
S4CTL
+5VAUX
SRP31
10KX4
SRP32
10KX4
SRP1
10KX4_R
KBIN0
KBIN1
KBIN2
KBIN3
KBIN4
KBIN5
KBIN6
KBIN7
PU3
PU2
PU1
PU0
ECOVT- SR296 10K
ECLID- SR297 10K
ECTHERM- SR298 10K
MAILKEY- SR299 10K
CN2
DT_91-G0001-216-24_BOT
SR300
0_R
SR301
0
SD27
SD28
SD29
SD30
ASD500V_SOD323
SR304
SR306
SC456
1UF/10V
SC457
0.1UF
SR331
SL29
SL30
coil_chtpr1608
R270
10K
R271
10K
SC17
0.1UF
SC470
1PF
SC471
10PF
SC472
1nF
J1
DT_91-G0001-125-04_TOP
MSDATA
MSCLK
LFRAME- 17,22
LAD[0..3]
SIRQ 17,22
KBCLK 9
ECRST-
Elitegroup Computer Systems
KBC & T/P
Size Custom
Document Number 557S-1-4-01
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Sheet 25 of 38



Elitegroup Computer Systems	
Title	MINI PCI CONN.& MDC
Size B	Document Number 557S-1-4-01
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8100C USE VDD25
8110SB USE AVDD33
8110S USE NC

8100C USE AVDD33
8110S(B) USE AVDD25

8100C USE VDD25
8110S(B) USE VDD18

8100C USE NC
8110S(B) USE AVDD33

RTL RECOMMEND

KEEP SR87 at 93C56

R54 is used only at RTL8110S(B) application and only at 93C56 is used.

9/8

10/20 1G SQ

7/23

8100C USE 5.6K
8110S(B) USE 2.49K

KEEP SR73
FOR RTL8110SB AP

1.8V/1.25V Regulater

For RTL8110SB
U2 change to G1117,
R246 change to 1.2K_1%

PUT CLOSE PIN.3,7,16,20

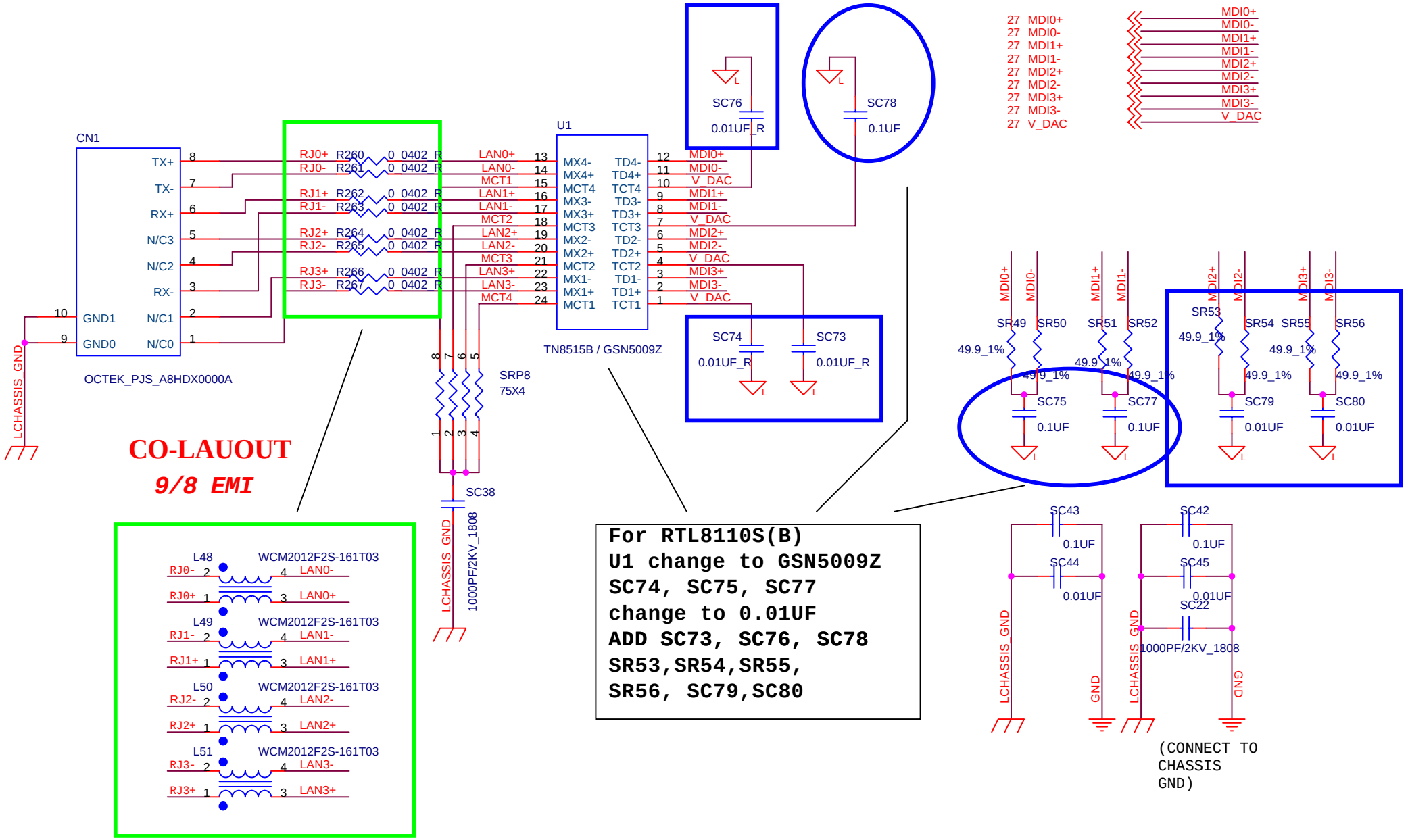
PUT CLOSE PIN.26,41,56,71,84,94,107

PUT CLOSE PIN.10,120

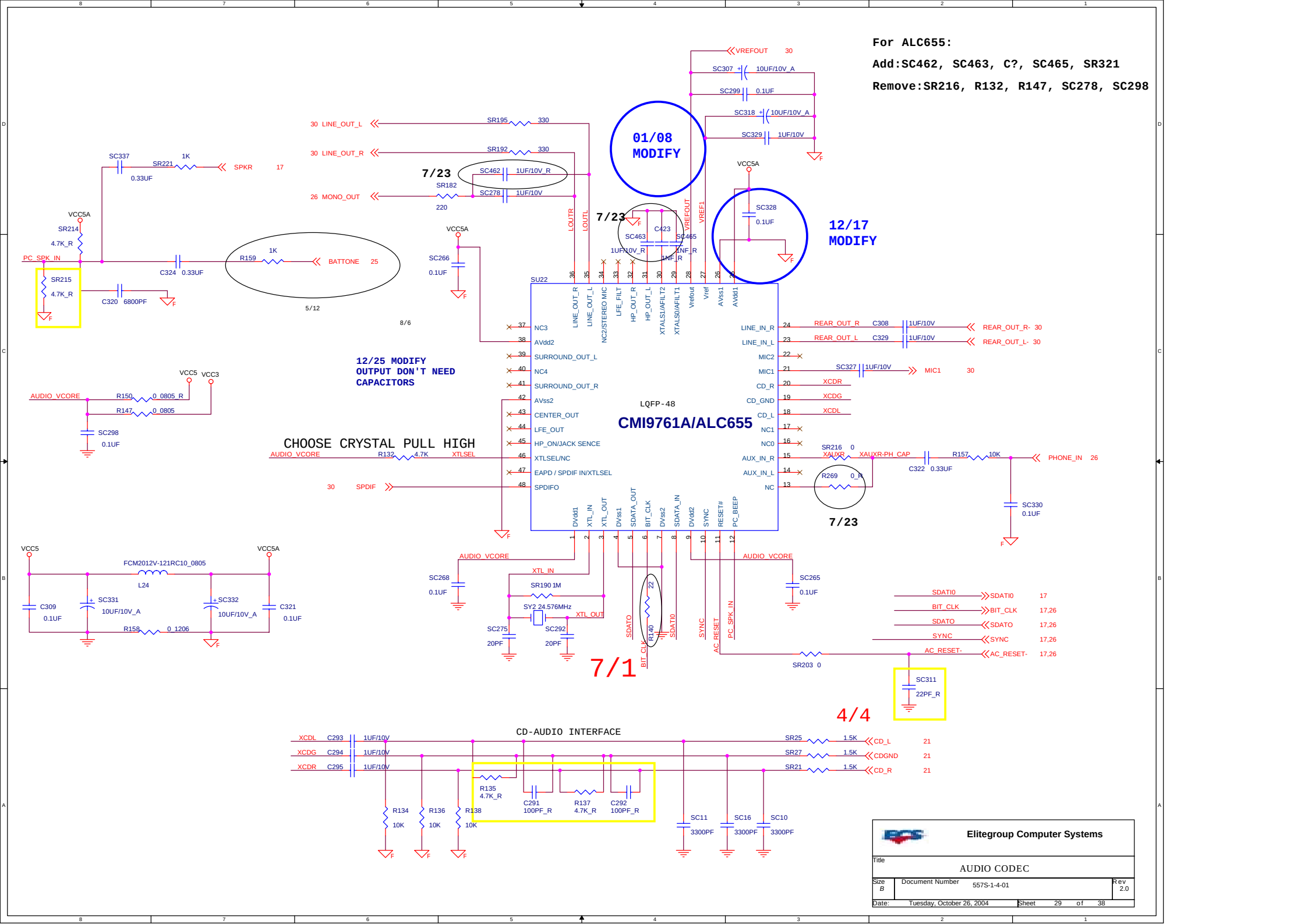
PUT CLOSE PIN.24,32,45,54,64,78,99,110,116,126

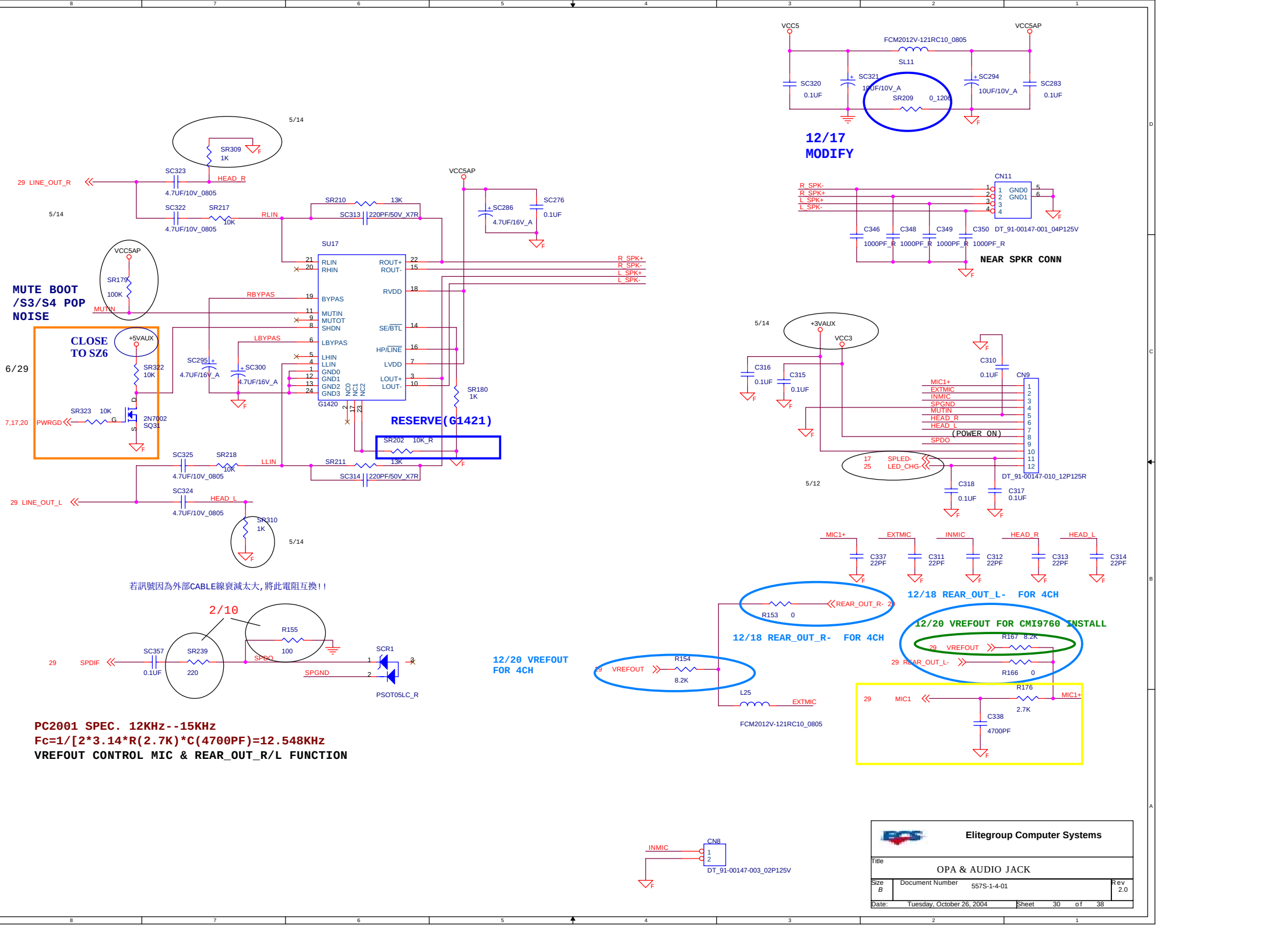
RTL8110S(B)

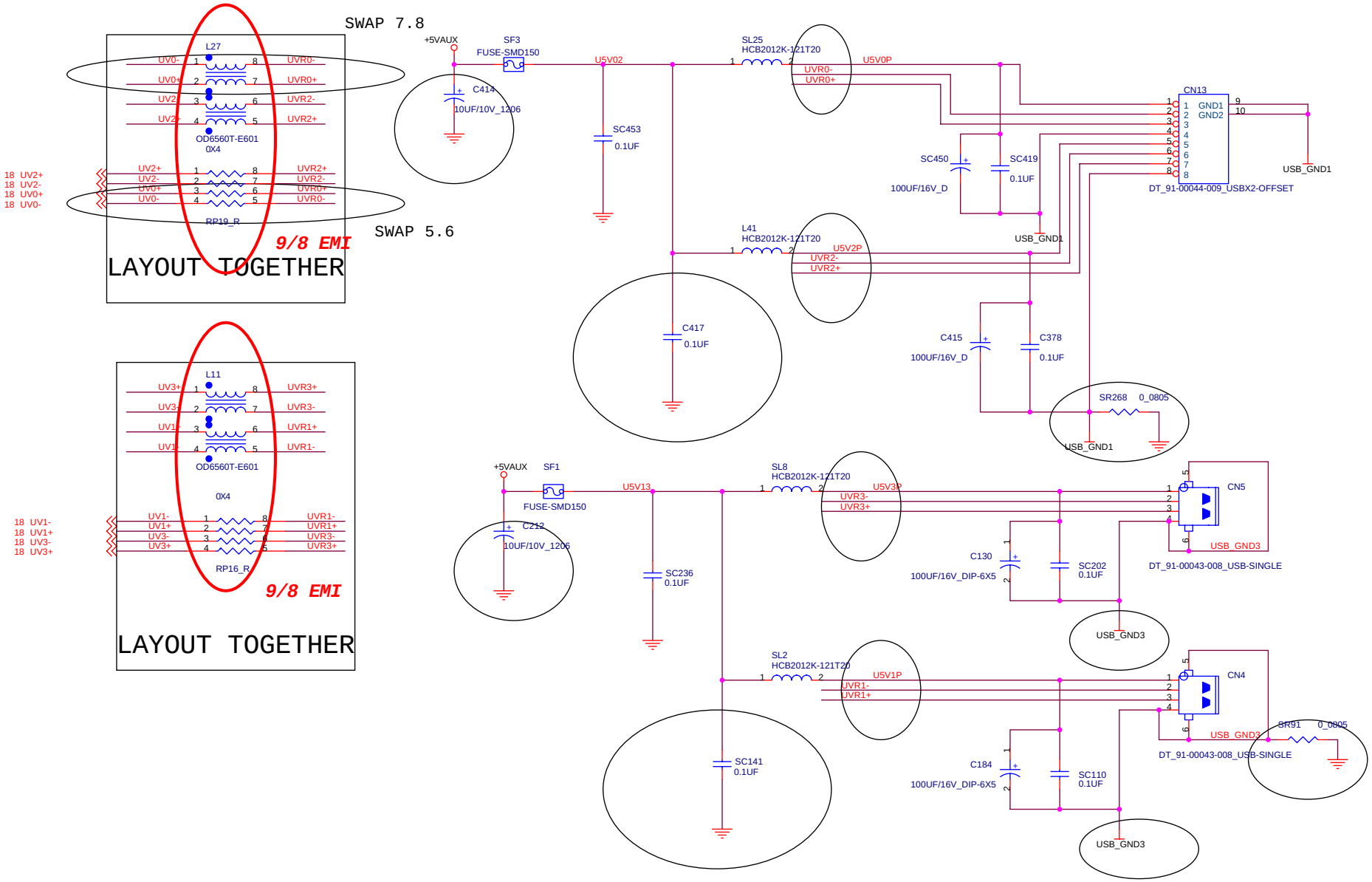
Elitegroup Computer Systems			
Title: RTL8110S(B)			
Size: B	Document Number: 557S-1-4-01		Rev: 2.0
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Elitegroup Computer Systems			
Title: RJ45 & HW THERMAL PROTECT			
Size: A4	Document Number: 557S-1-4-01		Rev: 2.0
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For ALC655:
Add:SC462, SC463, C?, SC465, SR321
Remove:SR216, R132, R147, SC278, SC298
01/08 MODIFY
12/17 MODIFY
12/25 MODIFY
OUTPUT DON'T NEED CAPACITORS
CHOOSE CRYSTAL PULL HIGH
CMI9761A/ALC655
LQFP-48
CD-AUDIO INTERFACE
7/23
7/1
4/4
5/12
8/6
VREFOUT 30
SC307 10UF/10V_A
SC299 0.1UF
SC318 10UF/10V_A
SC329 1UF/10V
30 LINE_OUT_L
30 LINE_OUT_R
26 MONO_OUT
SR195 330
SR192 330
SR182 220
SC462 1UF/10V_R
SC278 1UF/10V
SC337 0.33UF
SR221 1K
SPKR 17
VCC5A
SR214 4.7K_R
PC_SPK_IN
SR215 4.7K_R
C324 0.33UF
R159 1K
BATTONE 25
C320 6800PF
SC266 0.1UF
SC328 0.1UF
C423
SC463 1UF/10V_R
SC465 1NF_R
LOUTR
LOUTL
VREFOUT
VREF1
SU22
NC3
AVdd2
SURROUND_OUT_L
NC4
SURROUND_OUT_R
AVss2
CENTER_OUT
LFE_OUT
HP_ON/JACK SENCE
XTLSEL/NC
EAPD / SPDIF IN/XTLSEL
SPDIFO
LINE_OUT_R
LINE_OUT_L
NC2/STEREO MIC
LFE_FILT
HP_OUT_R
HP_OUT_L
XTALS1/APFILT2
XTALS0/APFILT1
Vrefout
Vref
AVss1
AVdd1
LINE_IN_R
LINE_IN_L
MIC2
MIC1
CD_R
CD_GND
CD_L
NC1
NC0
AUX_IN_R
AUX_IN_L
NC
REAR_OUT_R C308 1UF/10V
REAR_OUT_L C329 1UF/10V
REAR_OUT_R- 30
REAR_OUT_L- 30
SC327 1UF/10V
MIC1 30
XCDR
XCDG
XCDL
AUDIO_VCORE
R150 0_0805_R
R147 0_0805
SC298 0.1UF
R132 4.7K
XTLSEL
30 SPDIF
SR216 0
XAUXR
XAUXR-PH_CAP
R269 0_R
R157 10K
C322 0.33UF
PHONE_IN 26
SC330 0.1UF
DVdd1
XTL_IN
XTL_OUT
DVss1
SDATA_OUT
BIT_CLK
DVss2
SDATA_IN
DVdd2
SYNC
RESET#
PC_BEEP
SC268 0.1UF
SR190 1M
SY2 24.576MHz
SC275 20PF
SC292 20PF
R140 22
SC265 0.1UF
SDATI0
BIT_CLK
SDATO
SYNC
AC_RESET-
PC_SPK_IN
SR203 0
SC311 22PF_R
VCC5
VCC3
VCC5A
FCM2012V-121RC10_0805
L24
C309 0.1UF
SC331 10UF/10V_A
SC332 10UF/10V_A
C321 0.1UF
R158 0_1206
XCDL C293 1UF/10V
XCDG C294 1UF/10V
XCDR C295 1UF/10V
R135 4.7K_R
C291 100PF_R
R137 4.7K_R
C292 100PF_R
R134 10K
R136 10K
R138 10K
SR25 1.5K
SR27 1.5K
SR21 1.5K
CD_L 21
CDGND 21
CD_R 21
SC11 3300PF
SC16 3300PF
SC10 3300PF
Elitegroup Computer Systems
Title
AUDIO CODEC
Size B
Document Number 557S-1-4-01
Rev 2.0
Date: Tuesday, October 26, 2004
Sheet 29 of 38

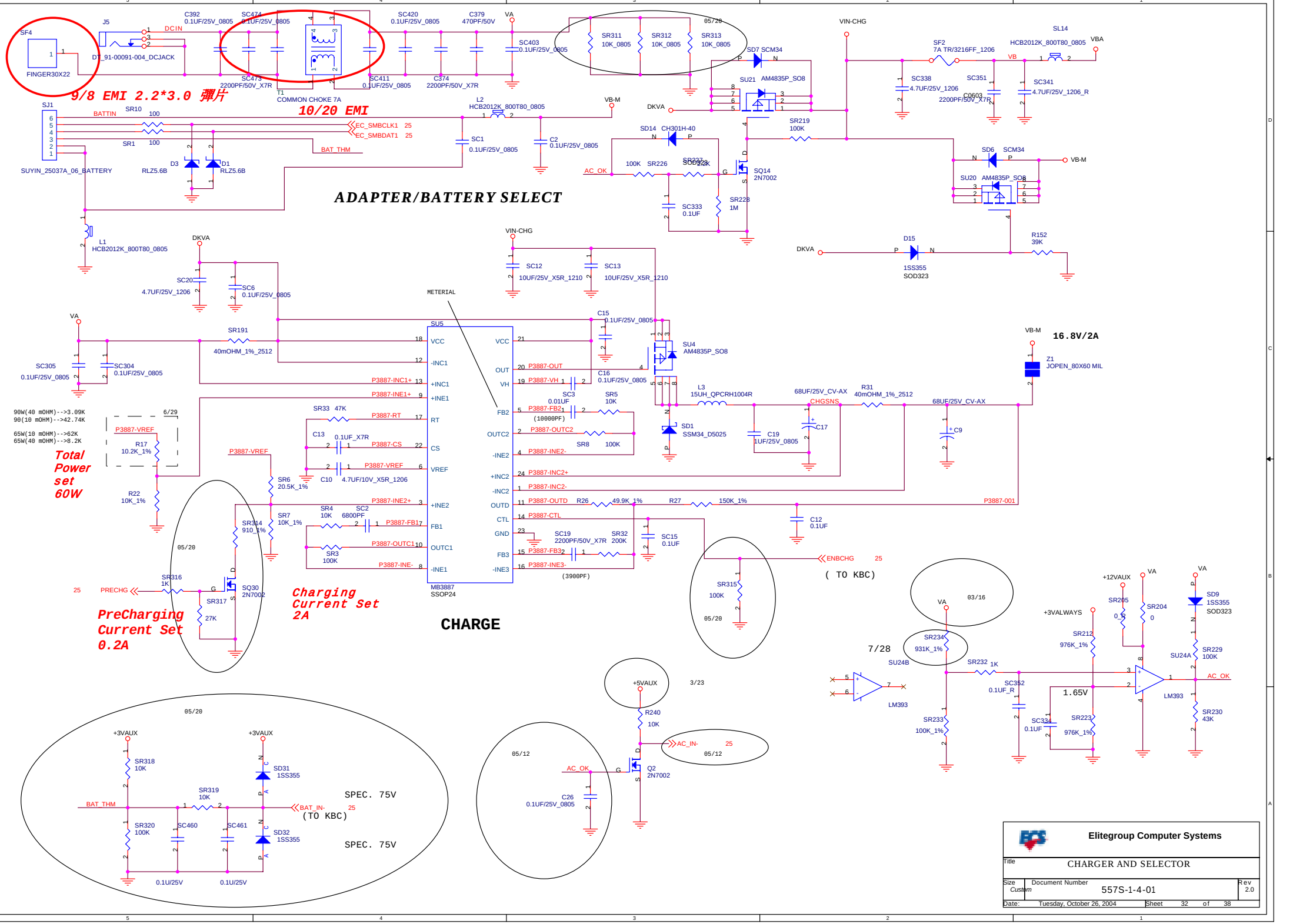

MUTE BOOT /S3/S4 POP NOISE
CLOSE TO SZ6
RESERVE(G1421)
12/17 MODIFY
NEAR SPKR CONN
12/18 REAR_OUT_L- FOR 4CH
12/18 REAR_OUT_R- FOR 4CH
12/20 VREFOUT FOR 4CH
12/20 VREFOUT FOR CMI9760 INSTALL
(POWER ON)
若訊號因為外部CABLE線衰減太大,將此電阻互換!!
PC2001 SPEC. 12KHz--15KHz
Fc=1/[2*3.14*R(2.7K)*C(4700PF)=12.548KHz
VREFOUT CONTROL MIC & REAR_OUT_R/L FUNCTION
SU17
G1420
FCM2012V-121RC10_0805
PSOT05LC_R
DT_91-00147-001_04P125V
DT_91-00147-010_12P125R
DT_91-00147-003_02P125V
Elitegroup Computer Systems
OPA & AUDIO JACK
557S-1-4-01
Tuesday, October 26, 2004

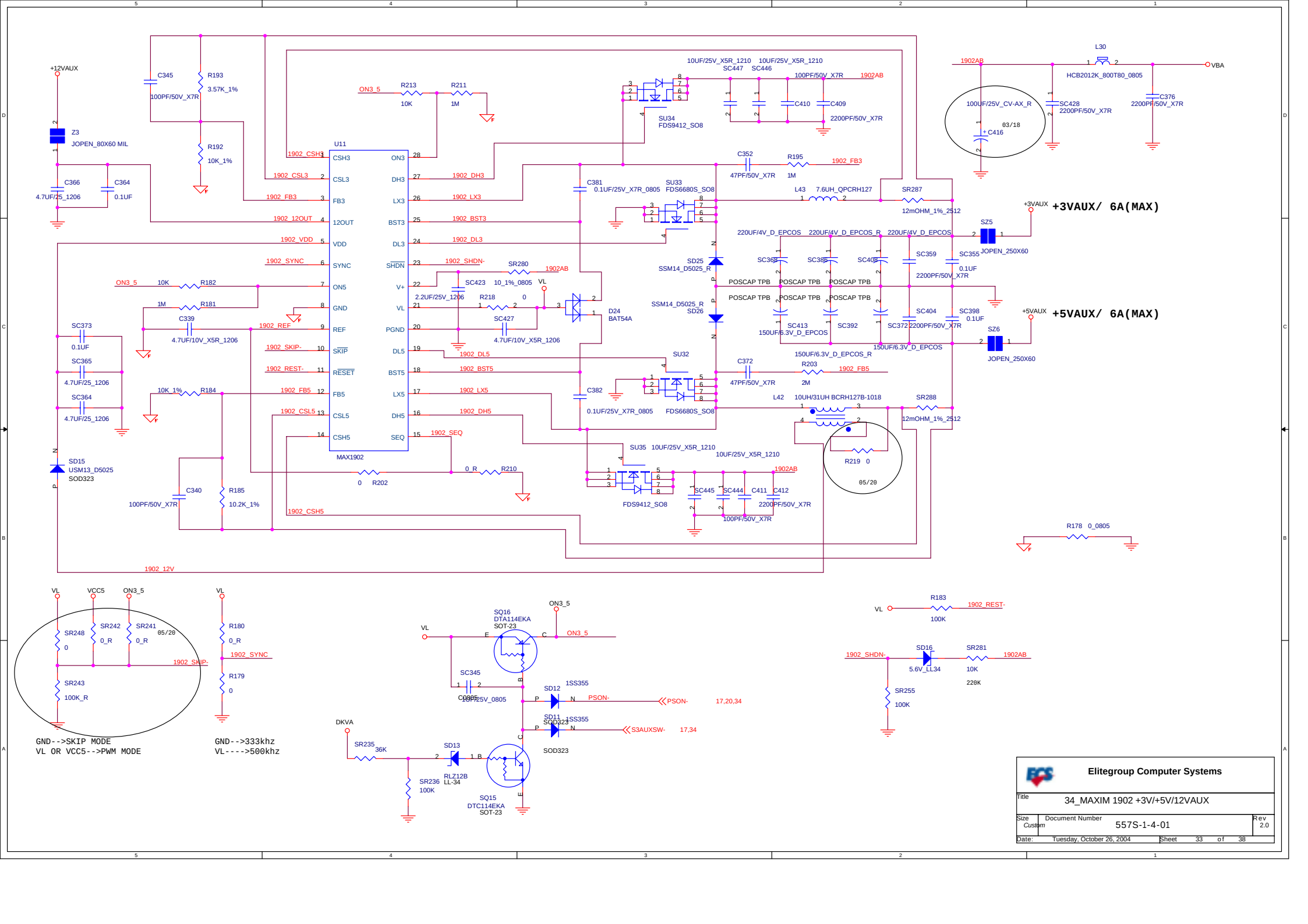


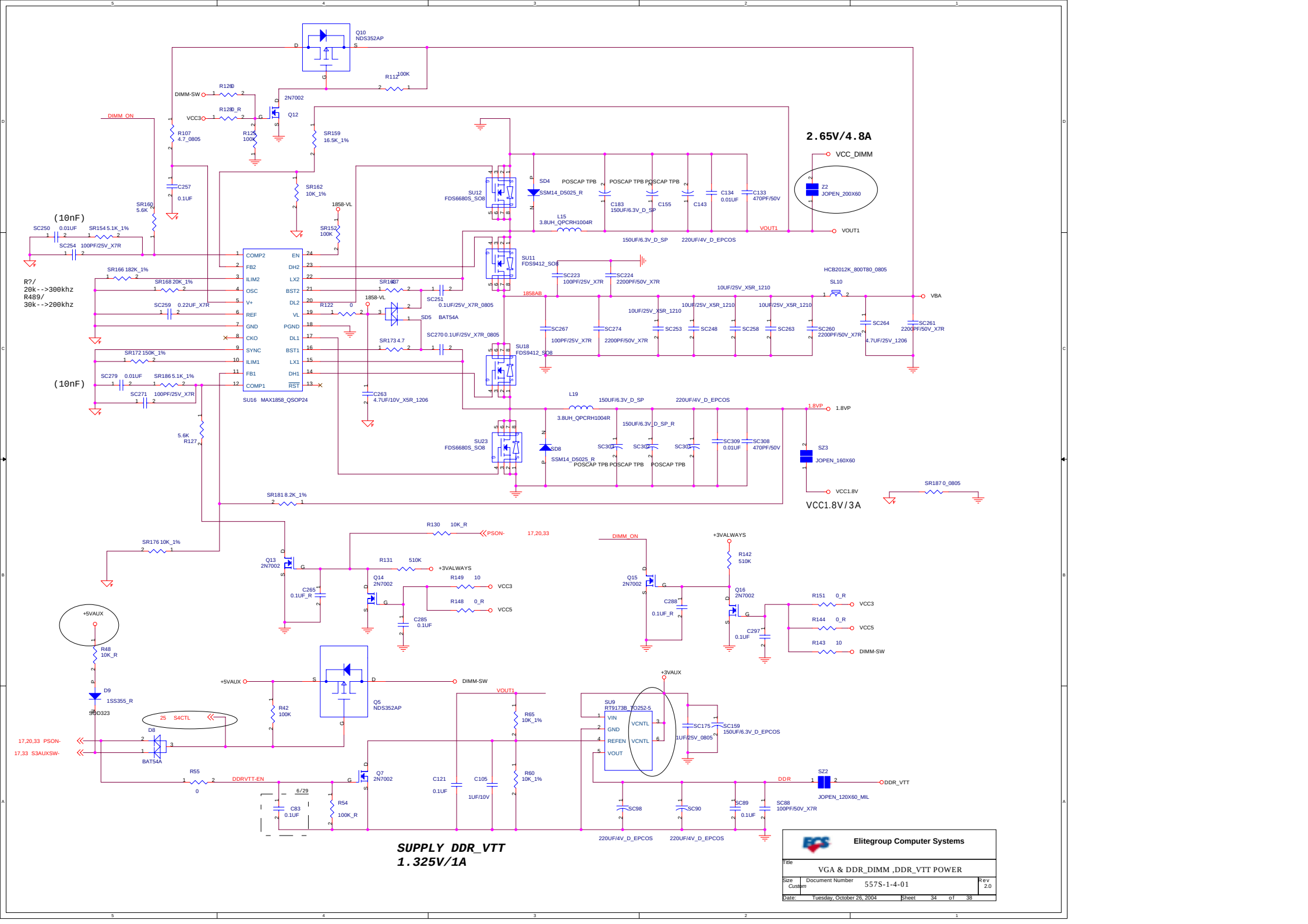
ALL THIS PAGE

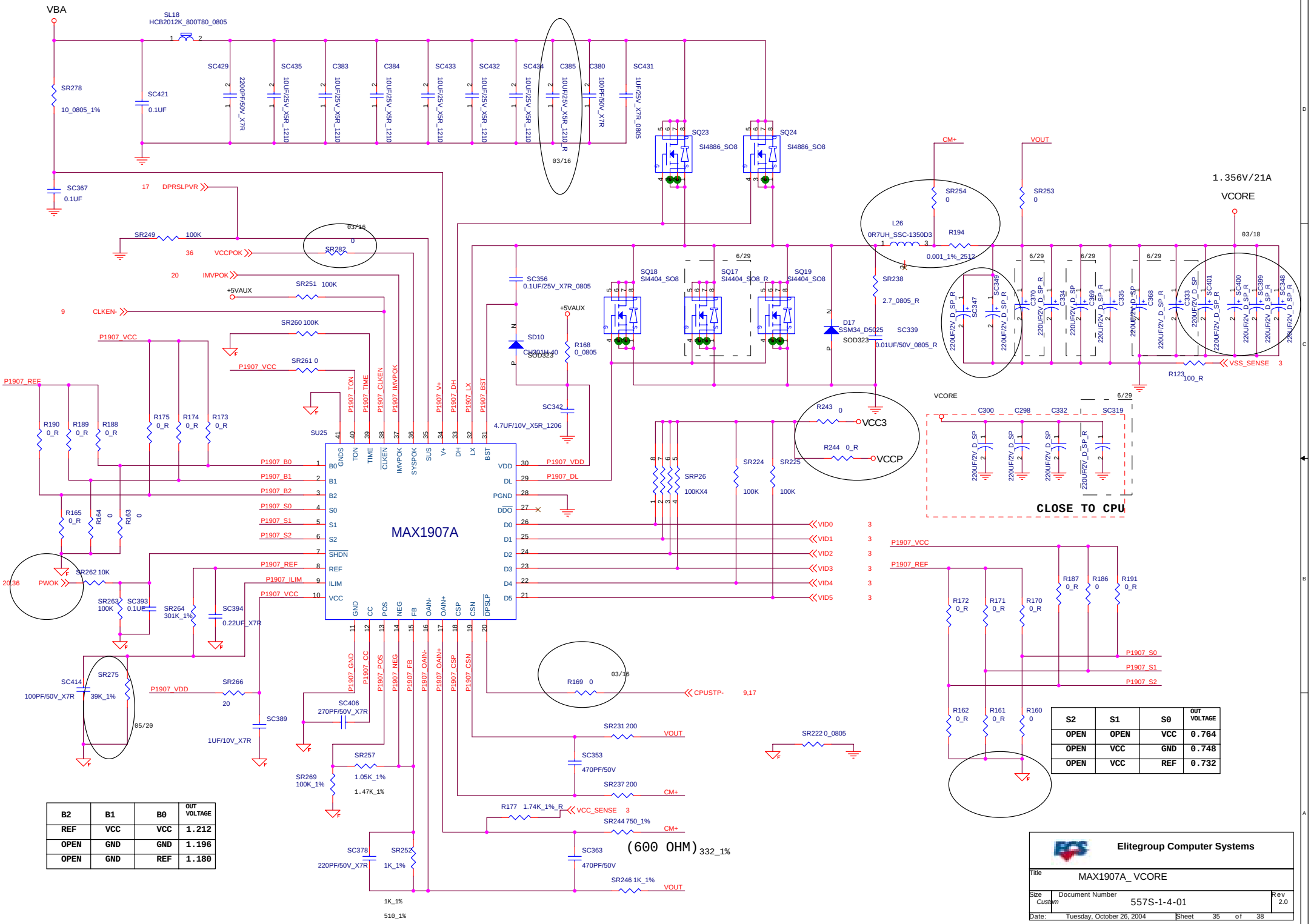
ALL THESE CLOSE TO CONNECTOR

Elitegroup Computer Systems	
Title	USB PORT
Size: B	Document Number: 557S-1-4-01
Rev: 2.0	
Date: Tuesday, October 26, 2004	Sheet 31 of 38


9/8 EMI 2.2*3.0 彈片
10/20 EMI
ADAPTER/BATTERY SELECT
CHARGE
16.8V/2A
Total Power set 60W
PreCharging Current Set 0.2A
Charging Current Set 2A
SUYIN_25037A_06_BATTERY
MB3887 SSOP24
(TO KBC)
SPEC. 75V
SPEC. 75V
Elitegroup Computer Systems
CHARGER AND SELECTOR
557S-1-4-01
Tuesday, October 26, 2004


+12VAUX
C345
100PF/50V_X7R
R193
3.57K_1%
R192
10K_1%
Z3
JOPEN_80X60 MIL
C366
4.7UF/25_1206
C364
0.1UF
U11
MAX1902
CSH3
CSL3
FB3
12OUT
VDD
SYNC
ON5
GND
REF
SKIP
RESET
FB5
CSL5
CSH5
ON3
DH3
LX3
BST3
DL3
SHDN
V+
VL
PGND
DL5
BST5
LX5
DH5
SEQ
1902_CSH3
1902_CSL3
1902_FB3
1902_12OUT
1902_VDD
1902_SYNC
1902_REF
1902_SKIP-
1902_REST-
1902_FB5
1902_CSL5
1902_CSH5
1902_SHDN-
1902_DH3
1902_LX3
1902_BST3
1902_DL3
1902_DL5
1902_BST5
1902_LX5
1902_DH5
1902_SEQ
ON3_5
R213
10K
R211
1M
SU34
FDS9412_SO8
10UF/25V_X5R_1210
SC447
10UF/25V_X5R_1210
SC446
100PF/50V_X7R
1902AB
C410
C409
2200PF/50V_X7R
C352
47PF/50V_X7R
R195
1M
1902_FB3
C381
0.1UF/25V_X7R_0805
SU33
FDS6680S_SO8
L43
7.6UH_QPCRH127
SR287
12mOHM_1%_2512
+3VAUX
+3VAUX/ 6A(MAX)
SZ5
JOPEN_250X60
220UF/4V_D_EPCOS
220UF/4V_D_EPCOS_R
220UF/4V_D_EPCOS
SC363
SC386
SC408
SC359
SC355
0.1UF
2200PF/50V_X7R
SD25
SSM14_D5025_R
POSCAP TPB
SD26
SC404
SC398
0.1UF
SC372
2200PF/50V_X7R
+5VAUX
+5VAUX/ 6A(MAX)
SZ6
JOPEN_250X60
SC413
150UF/6.3V_D_EPCOS
SC392
150UF/6.3V_D_EPCOS_R
SU32
C372
47PF/50V_X7R
R203
2M
1902_FB5
C382
0.1UF/25V_X7R_0805
FDS6680S_SO8
L42
10UH/31UH BCRH127B-1018
SR288
12mOHM_1%_2512
R219 0
05/20
SU35
10UF/25V_X5R_1210
10UF/25V_X5R_1210
FDS9412_SO8
SC445
SC444
C411
C412
2200PF/50V_X7R
100PF/50V_X7R
L30
HCB2012K_800T80_0805
VBA
100UF/25V_CV-AX_R
03/18
C416
SC428
2200PF/50V_X7R
C376
2200PF/50V_X7R
R178 0_0805
SC373
0.1UF
SC365
4.7UF/25_1206
SC364
4.7UF/25_1206
SD15
USM13_D5025
SOD323
C339
4.7UF/10V_X5R_1206
R182
10K
R181
1M
R184
10K_1%
C340
100PF/50V_X7R
R185
10.2K_1%
R202
0
R210
0_R
SC423
10_1%_0805
2.2UF/25V_1206
R218
0
SC427
4.7UF/10V_X5R_1206
D24
BAT54A
SR280
1902AB
SC345
C385
10PF/25V_0805
SQ16
DTA114EKA
SOT-23
SD12
1SS355
SD11
1SS355
SOD323
PSON-
17,20,34
S3AUXSW-
17,34
DKVA
SR235
36K
SD13
RLZ12B
LL-34
SR236
100K
SQ15
DTC114EKA
SOT-23
SR248
0
SR242
0_R
SR241
0_R
05/20
SR243
100K_R
GND-->SKIP MODE
VL OR VCC5-->PWM MODE
R180
0_R
R179
0
GND-->333khz
VL----->500khz
R183
100K
SD16
5.6V_LL34
SR281
10K
220K
SR255
100K
Elitegroup Computer Systems
34_MAXIM 1902 +3V/+5V/12VAUX
557S-1-4-01
Rev 2.0
Tuesday, October 26, 2004
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2.65V/4.8A
VCC_DIMM
VOUT1
VCC1.8V/3A
1.8VP
VBA
SU16 MAX1858_QSOP24
SUPPLY DDR_VTT
1.325V/1A
Elitegroup Computer Systems
VGA & DDR_DIMM ,DDR_VTT POWER
557S-1-4-01
Tuesday, October 26, 2004



B2	B1	B0	OUT VOLTAGE
REF	VCC	VCC	1.212
OPEN	GND	GND	1.196
OPEN	GND	REF	1.180

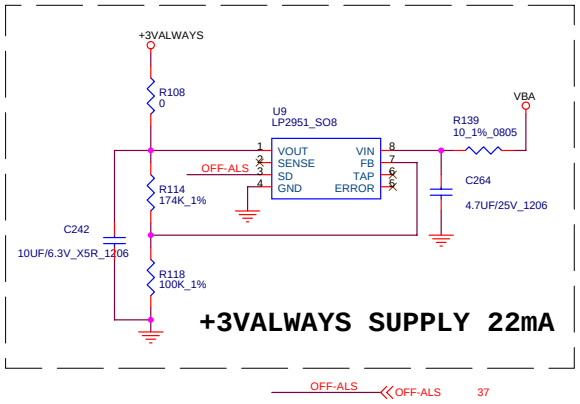
S2	S1	S0	OUT VOLTAGE
OPEN	OPEN	VCC	0.764
OPEN	VCC	GND	0.748
OPEN	VCC	REF	0.732

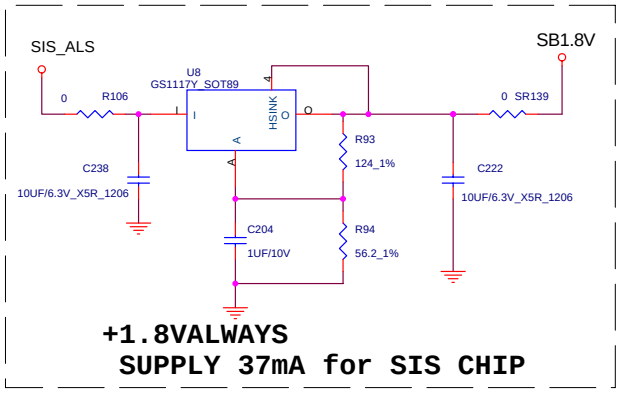
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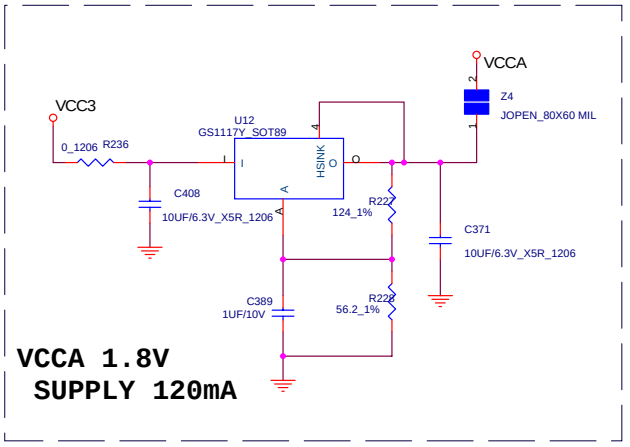
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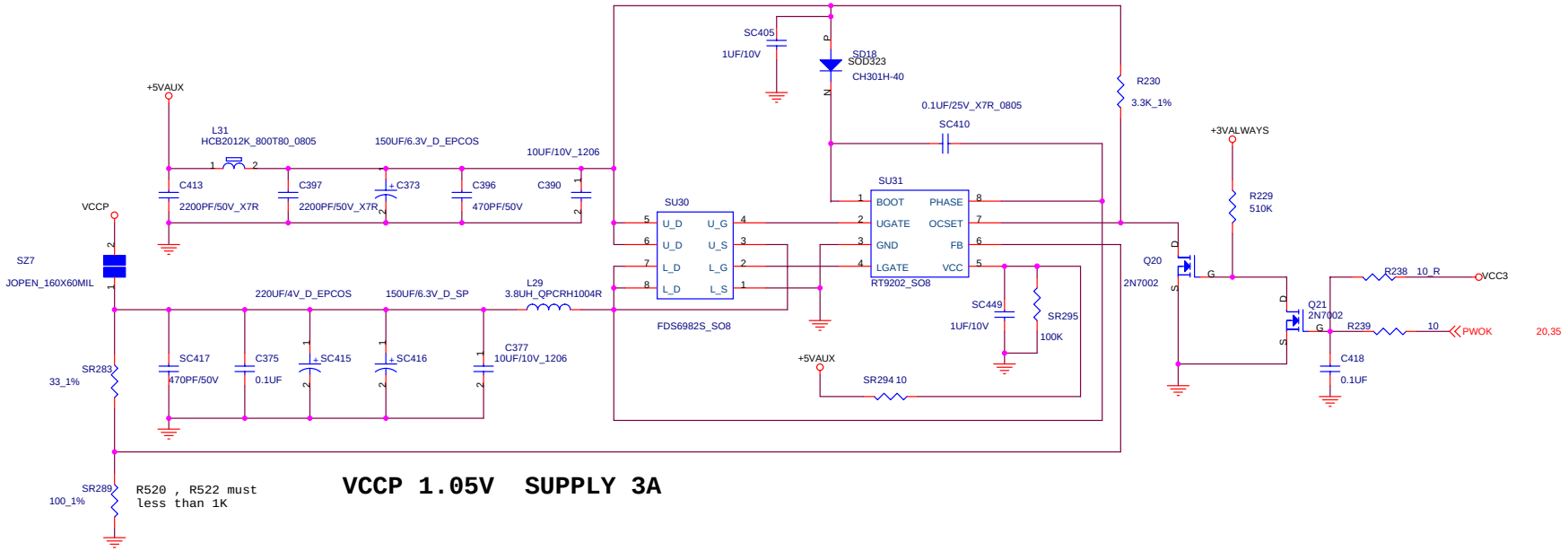
Size: Custom | Document Number: 557S-1-4-01 | Rev: 2.0

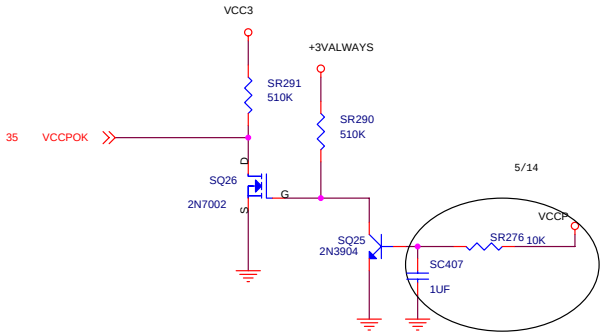
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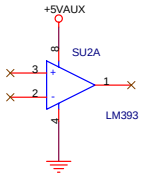






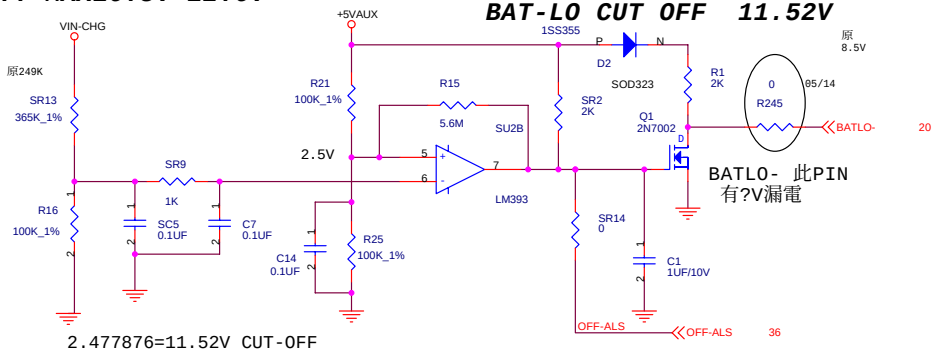


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Title: SB3.3V & SB1.8V & VCCP & VCCA		
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BATT MAX16.8V~12.6V

BAT-LO CUT PROTECT
BAT-LO CUT OFF 11.52V



2.477876=11.52V CUT-OFF

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Title: BAT-LO CUT PROTECT			
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B.0: 2004/3/31 Initial Release 15-F52-010010

C.0: 2004/6/8 Release 15-F52-010030

D.0:2004/7/28 Release 15-F52-010040

Page 13 Change R103 from 1.1K_1% to 1.91K_1%
Page 15 EMI add L44, L45, L46
Page 20 Delete D10,C78; change R50 from 4.7K to 0; add C77
Page 22 Reserve SC227, SL7, SC225, SC222, SCN2, SC231, SQ7, SR116, SR117
Page 23 Delete R214; reserve R217; add C419, C420, C421(C419, C420 are reserved)
Page 25 Modify LPC bus and SMBUS pin assignment; delete the signals fo SU36's pin1 and pin15; reserve SRP1
Page 27 Add R246, R247, R248, C422(R246 is 0; R247, R248, C422 are reserved) to support RTL8110SB
Page 29 Add SC462, SC463, SC465, C423, SR321(all are reserved) to support ALC655
Page 30 Add SR322, SR323, SQ31to avoid pop noise
Page 32 Change R17 from 8.2K_1% to 10.2K_1%; change SR234 from 976K_1% to 931K_1%
Page 34 Change C83 from 1UF/10V to 0.1UF/25V
Page 35 Reserve SQ17, C370, C369, C368, SC319 and add C333

1.0:2004/9/15 Release 15-F52-011000

Page 27 U5, RTL8100C replace RTL8110S(B)
Add SR268,SR326,SR327
DEL R246,C94
Page 28 EMI Add R260,R261,R262,R263,R264,R265,R267
U1, TN8515B replace GSN5009Z
Reserve SR53,SR54,SR55,SR56,SC73,SC76,SC78,SC79,SC80
Change SC74,SC75,SC77 to 0.1UF

2.0:2004/10/26 Release 15-F52-012000

Elitegroup Computer Systems		
Title: HISTORY		
Size: A	Document Number: 557S-1-4-01	Rev: 2.0
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