A901 SCHEMATICS

Diễn Đàn
KyThuatViTinh.Com

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REV. : 2.0

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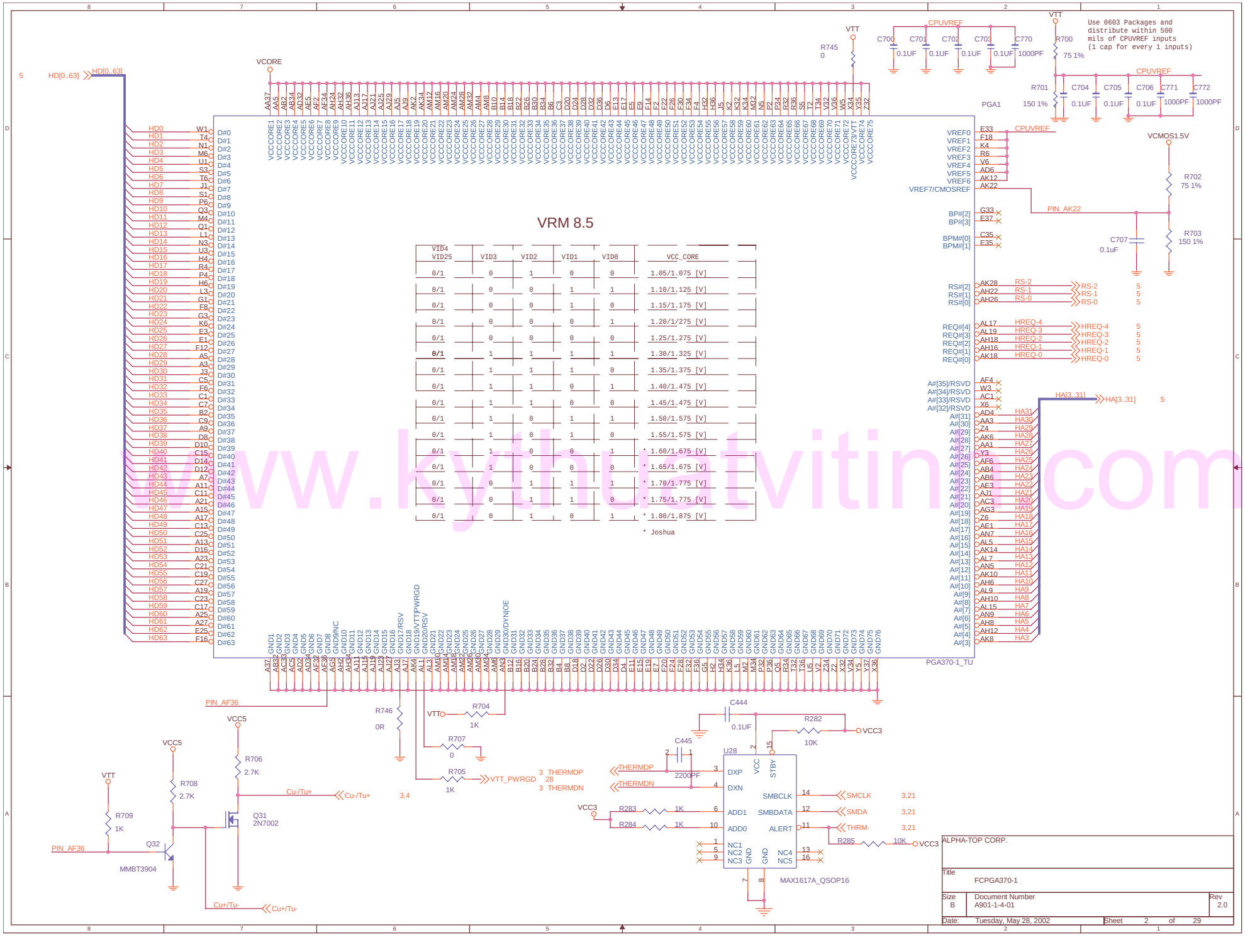
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ALPHA-TOP CORP.

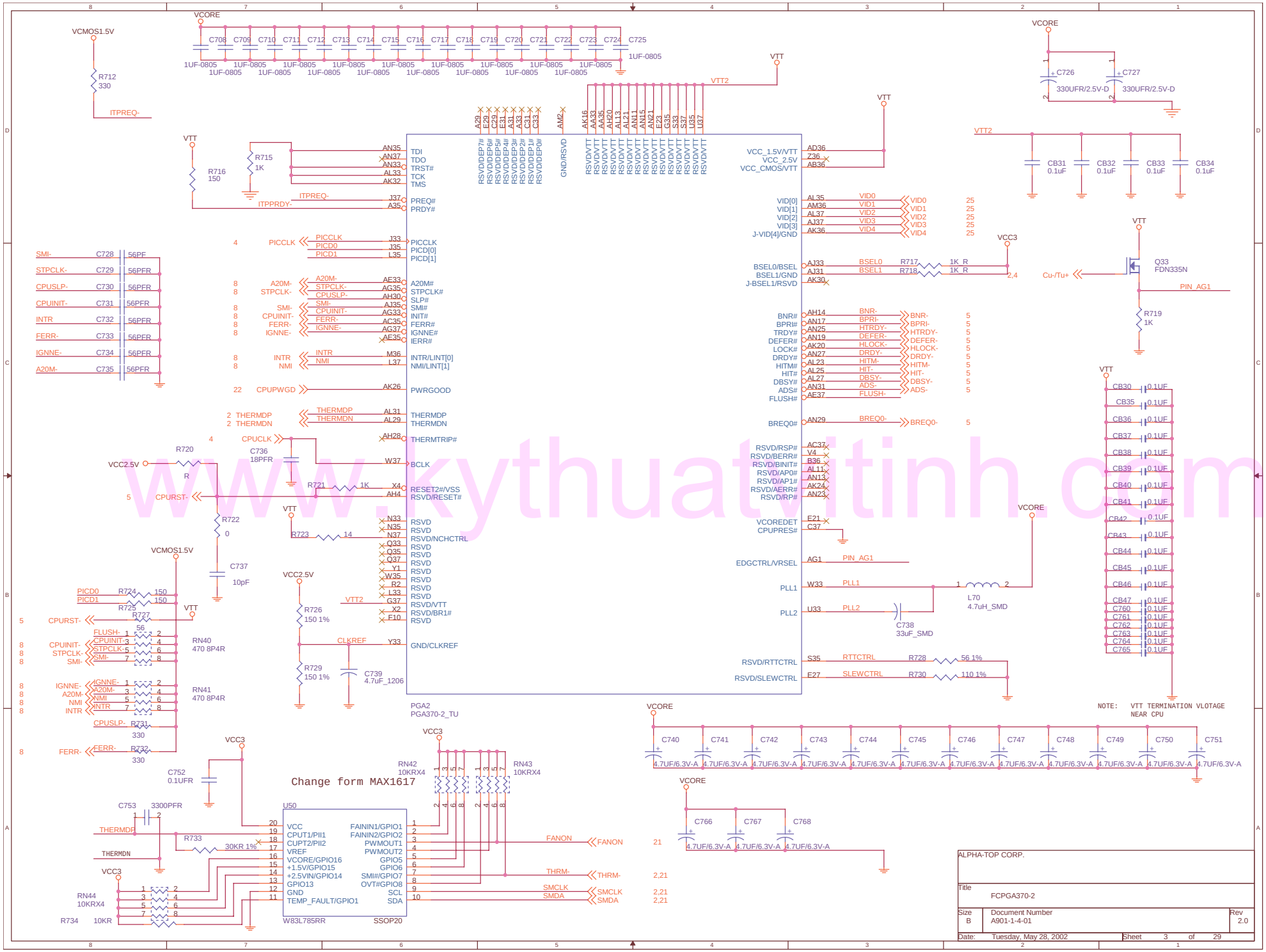
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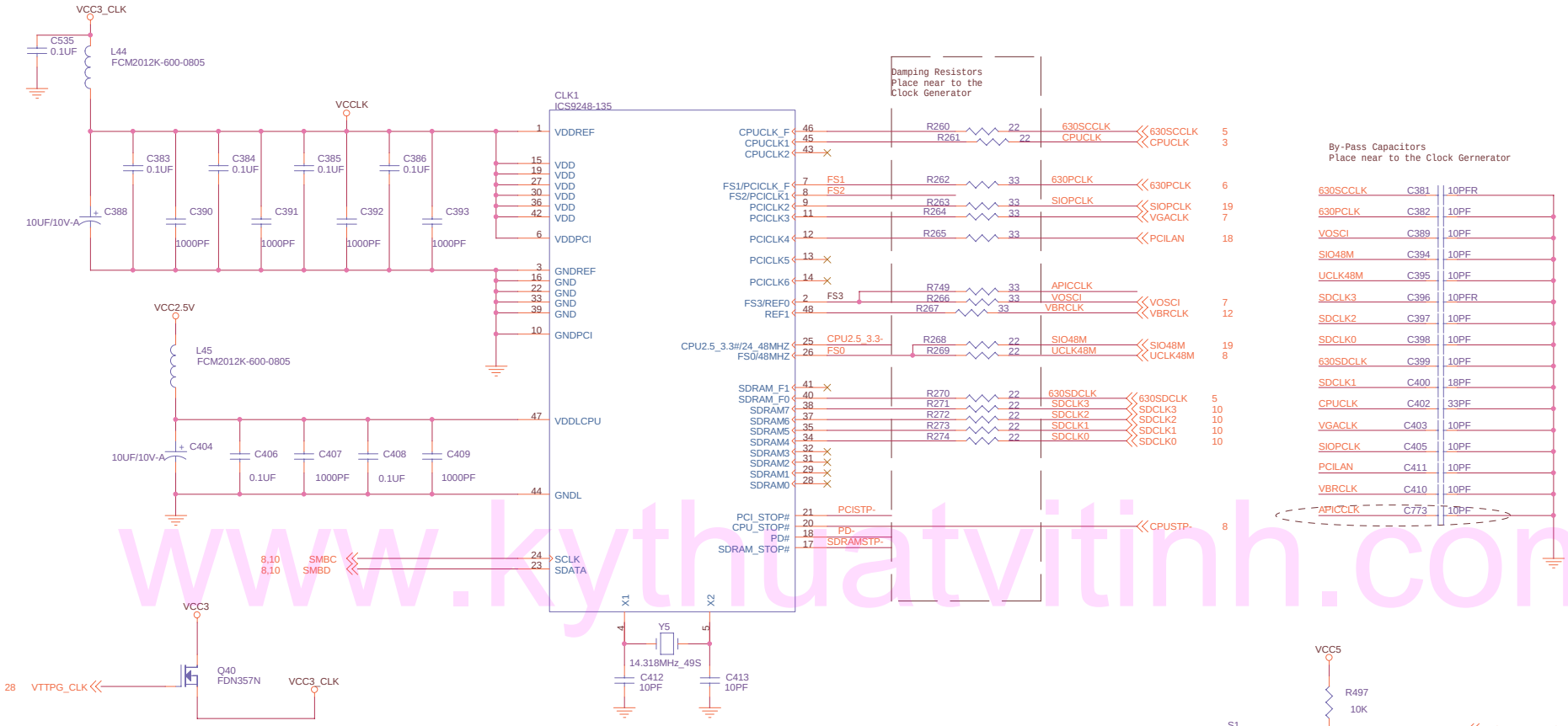


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Clock Generator

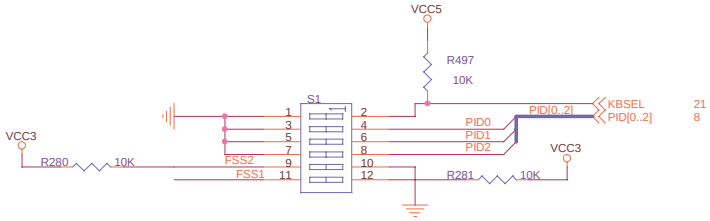


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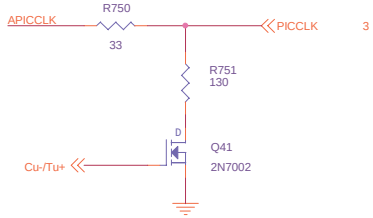
FS0~FS3 AND CPU2.5_3.3- HAVE INTERNAL PULL-DOWN 120K TO GND

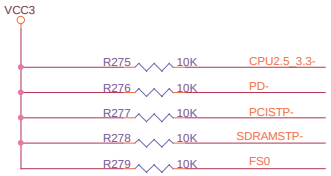
PD- HAS INTERNAL PULL-UP 120K TO VCC3

REF0 AND 48MHz HAS DOUBLE STRENGTH



(1)OFF <----------> ON(0)







CLOSE TO CLK GEN.

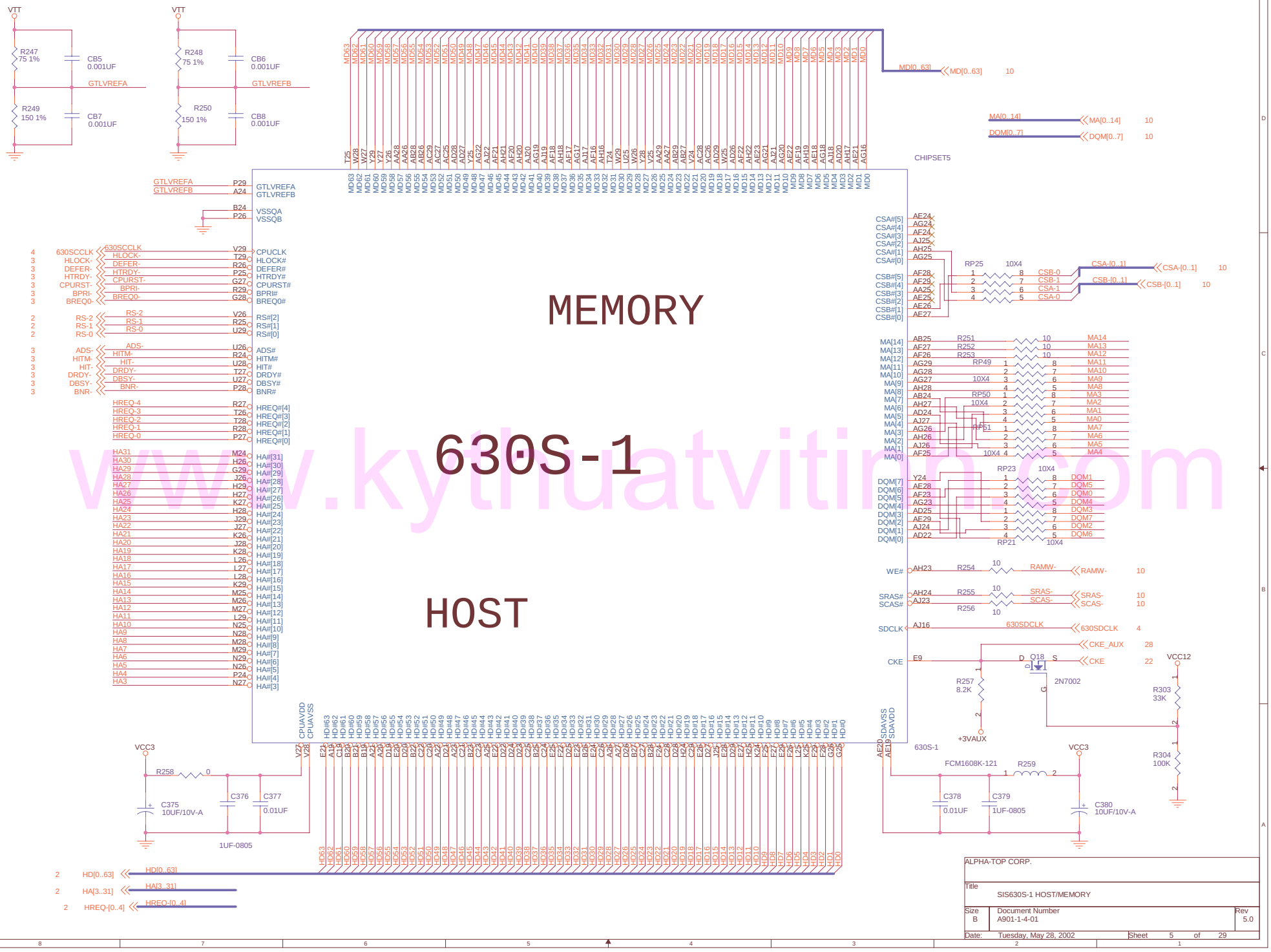
FS1/FS2	FREQUENCY
0	100
1	133

NOTE: FS1 CONTROLS CPU FREQUENCY
FS2 CONTROLS DRAM FREQUENCY

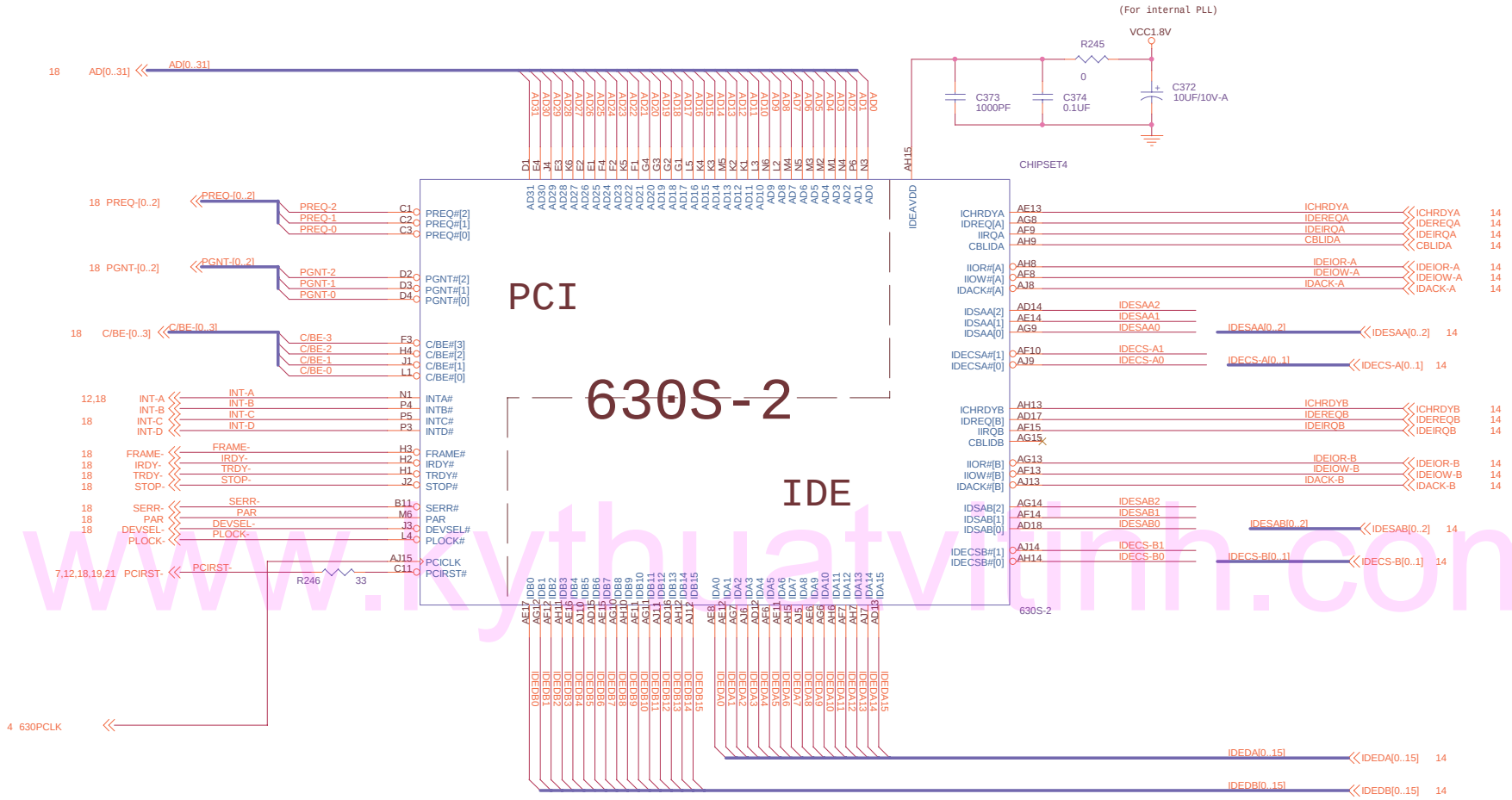
SIS630S CLOCK

(FS3)	(FS2)	(FS1)	(FS0)	CPU (MHz)	SDRAM (MHz)	PCI (MHz)
0	0	0	1	100	100	33.3
0	0	1	1	133.3	100	33.3
0	1	0	1	100	133.3	33.3
0	1	1	1	133.3	133.3	33.3

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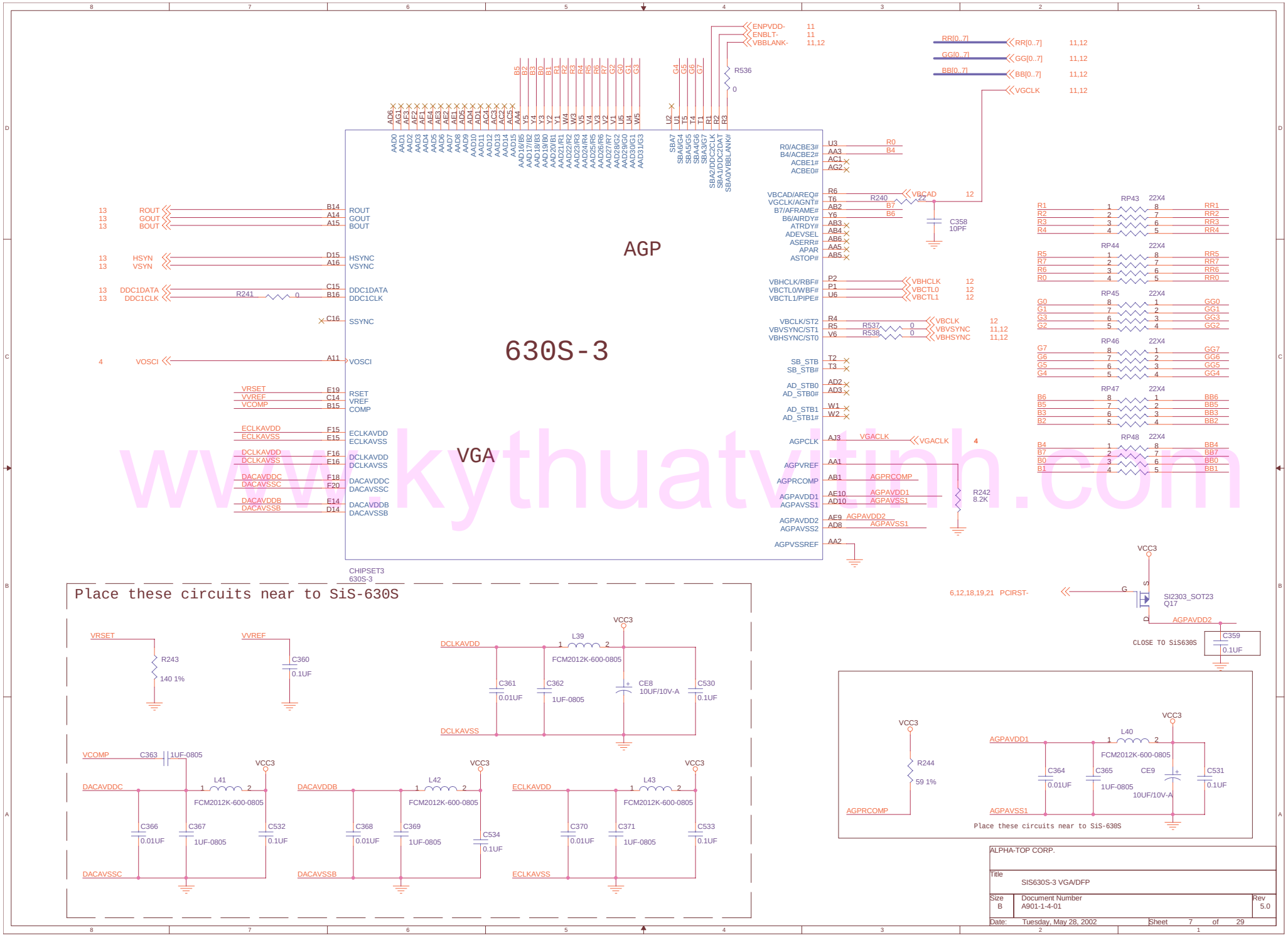


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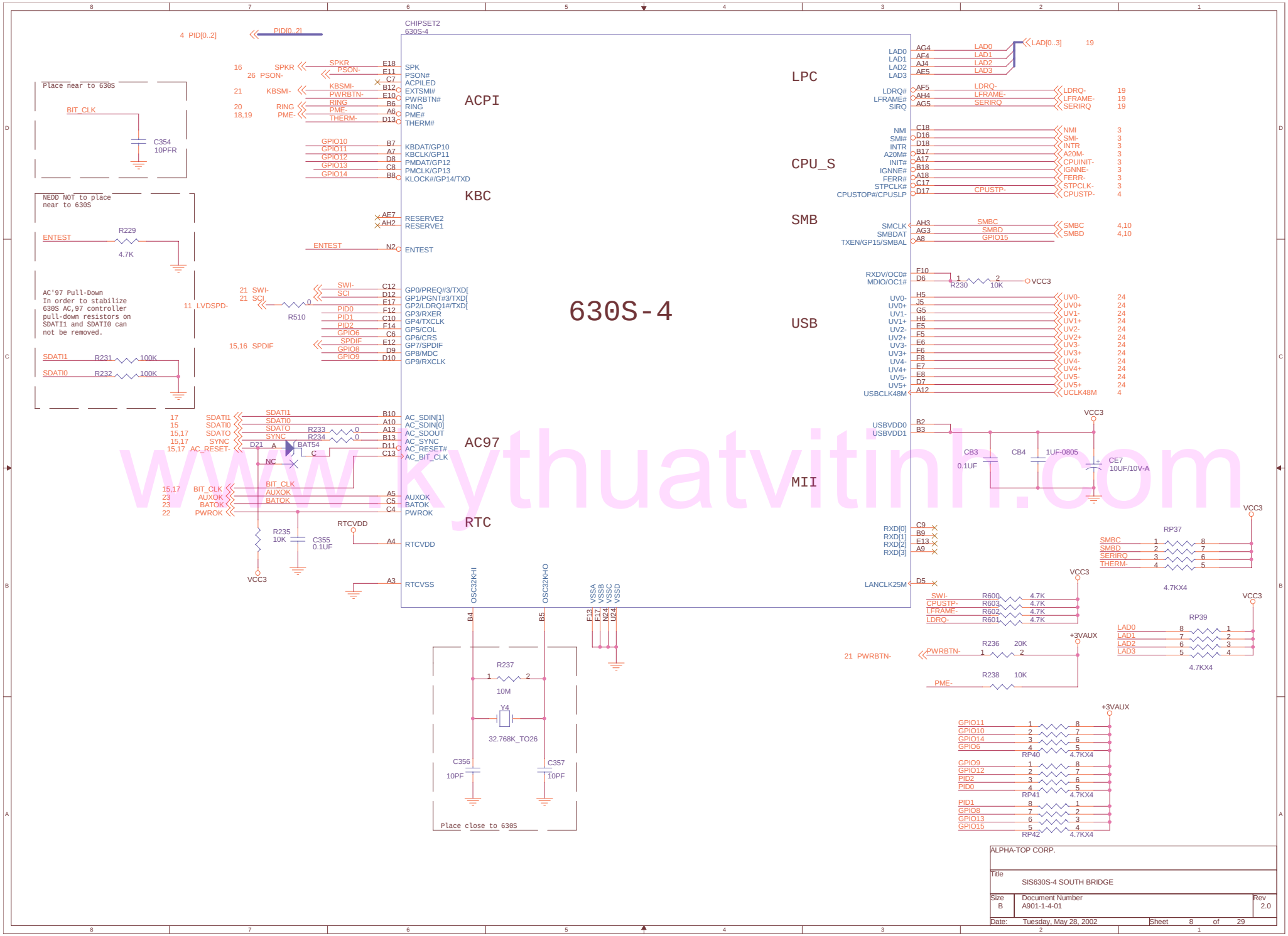


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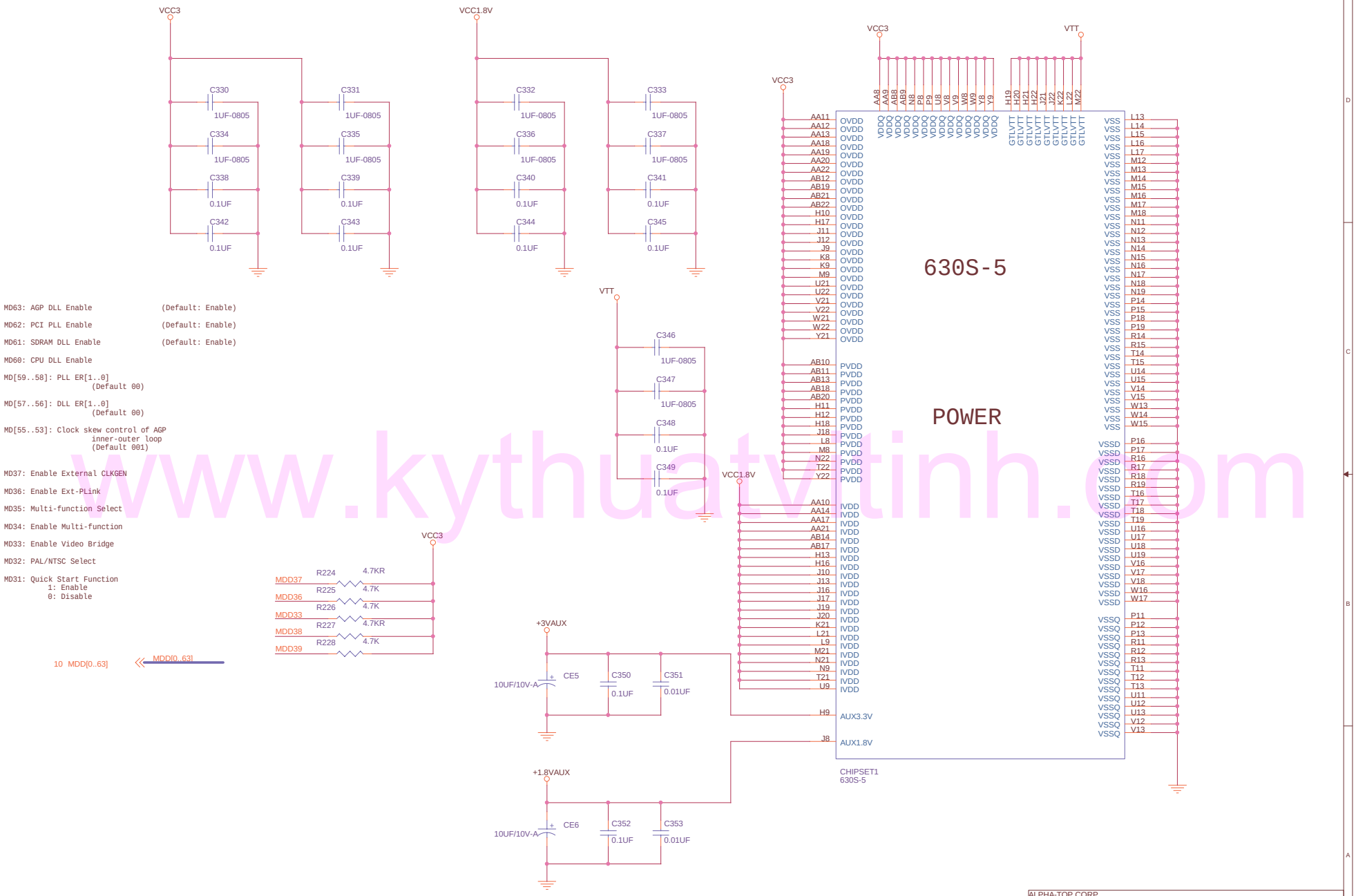
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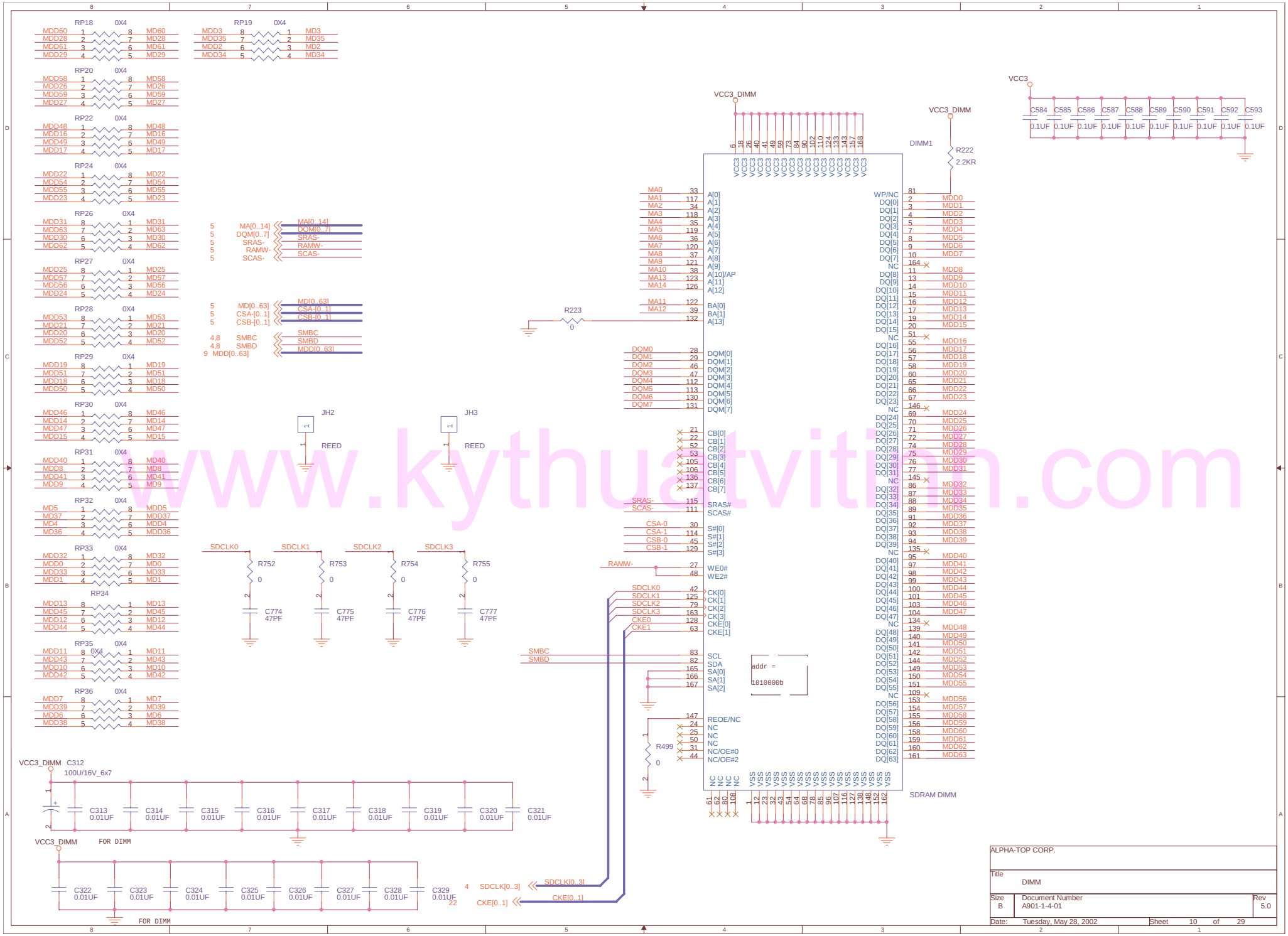


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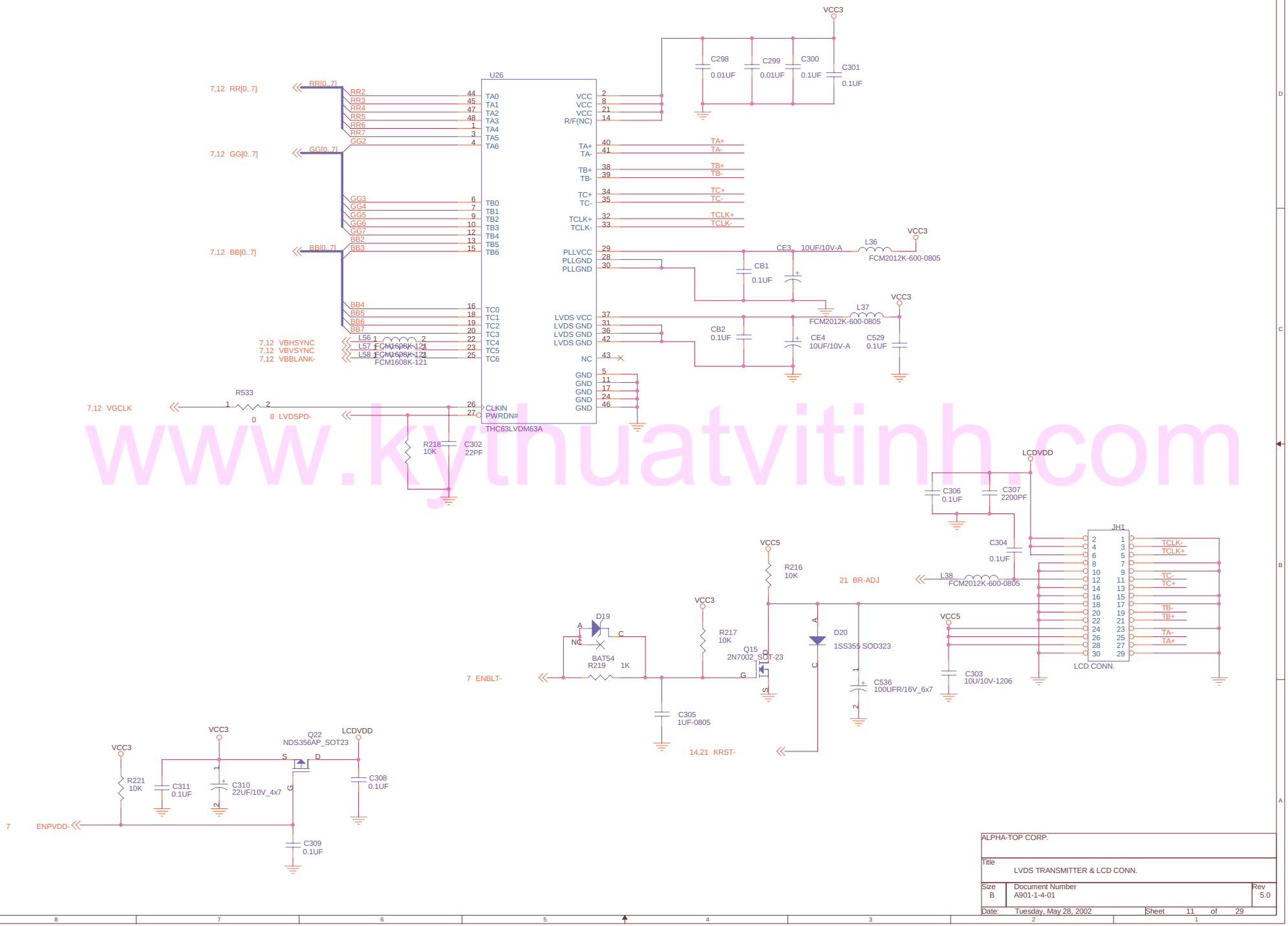


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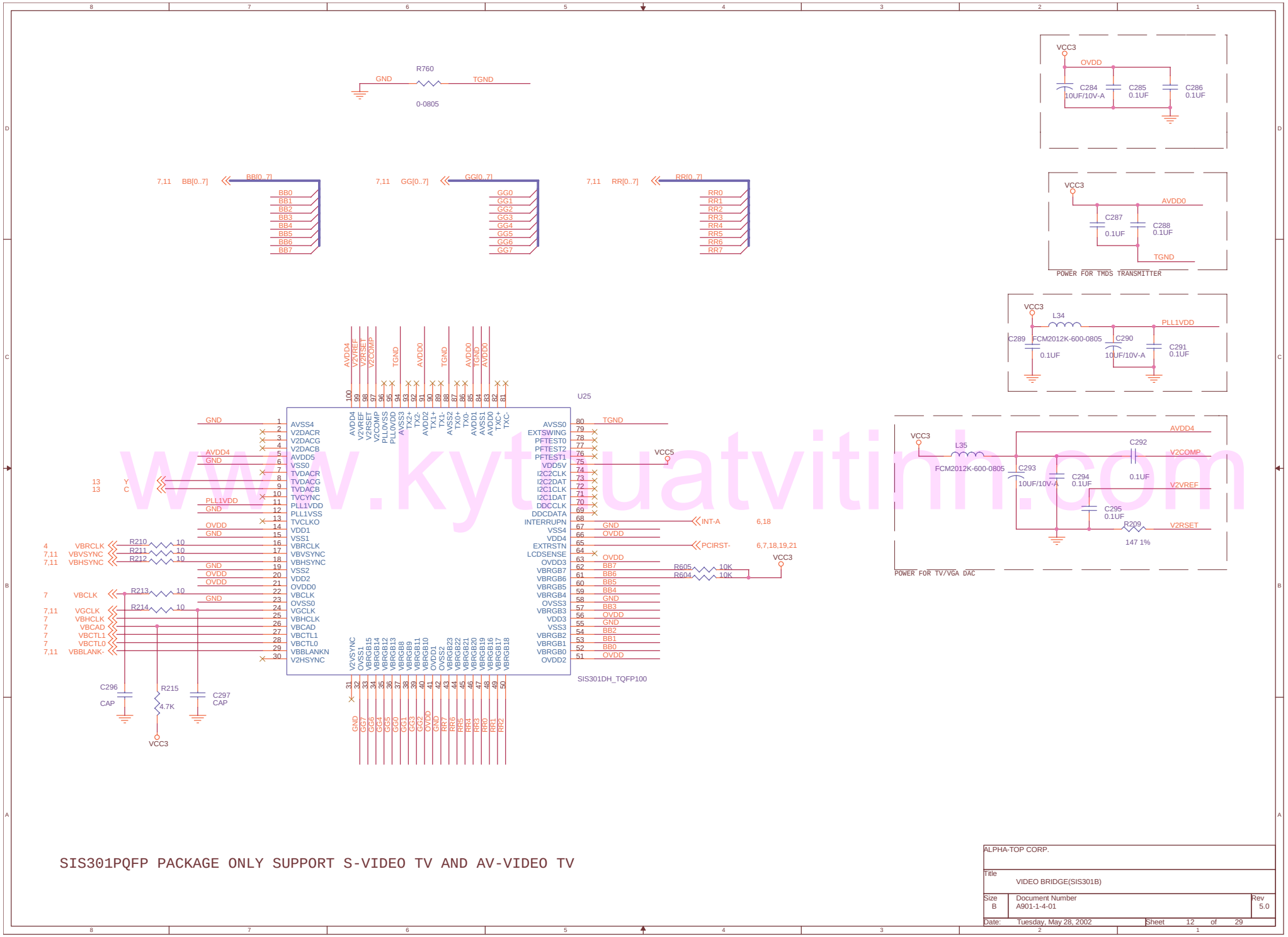
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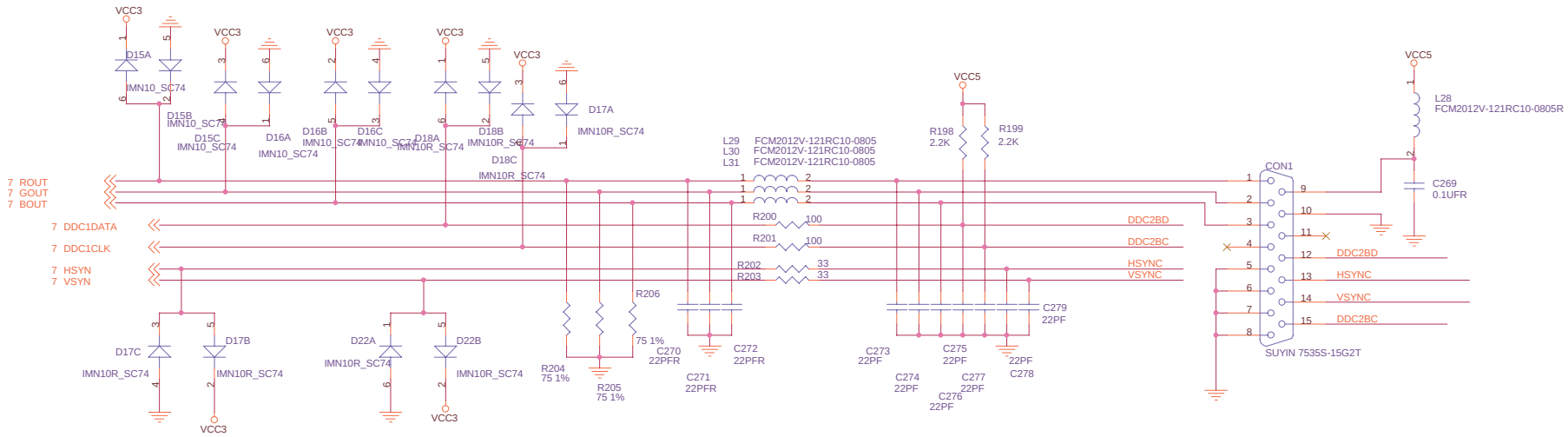
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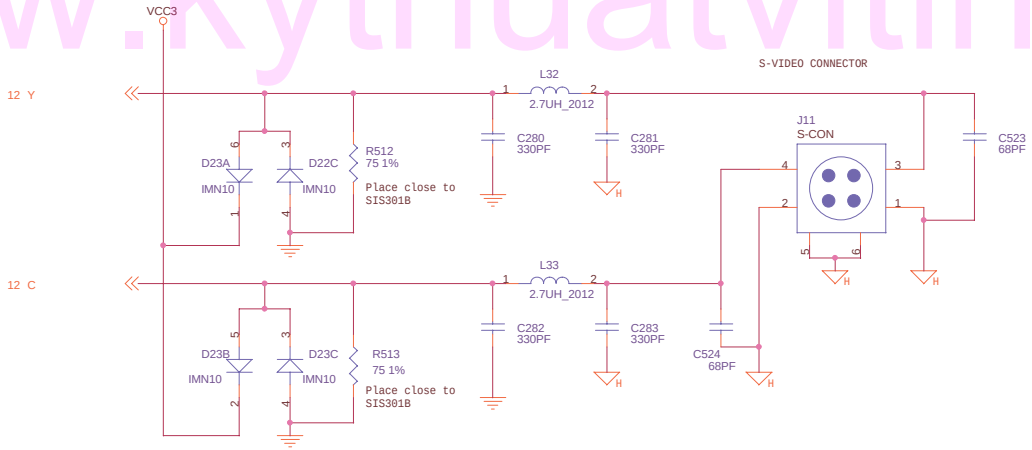
SIS301PQFP PACKAGE ONLY SUPPORT S-VIDEO TV AND AV-VIDEO TV

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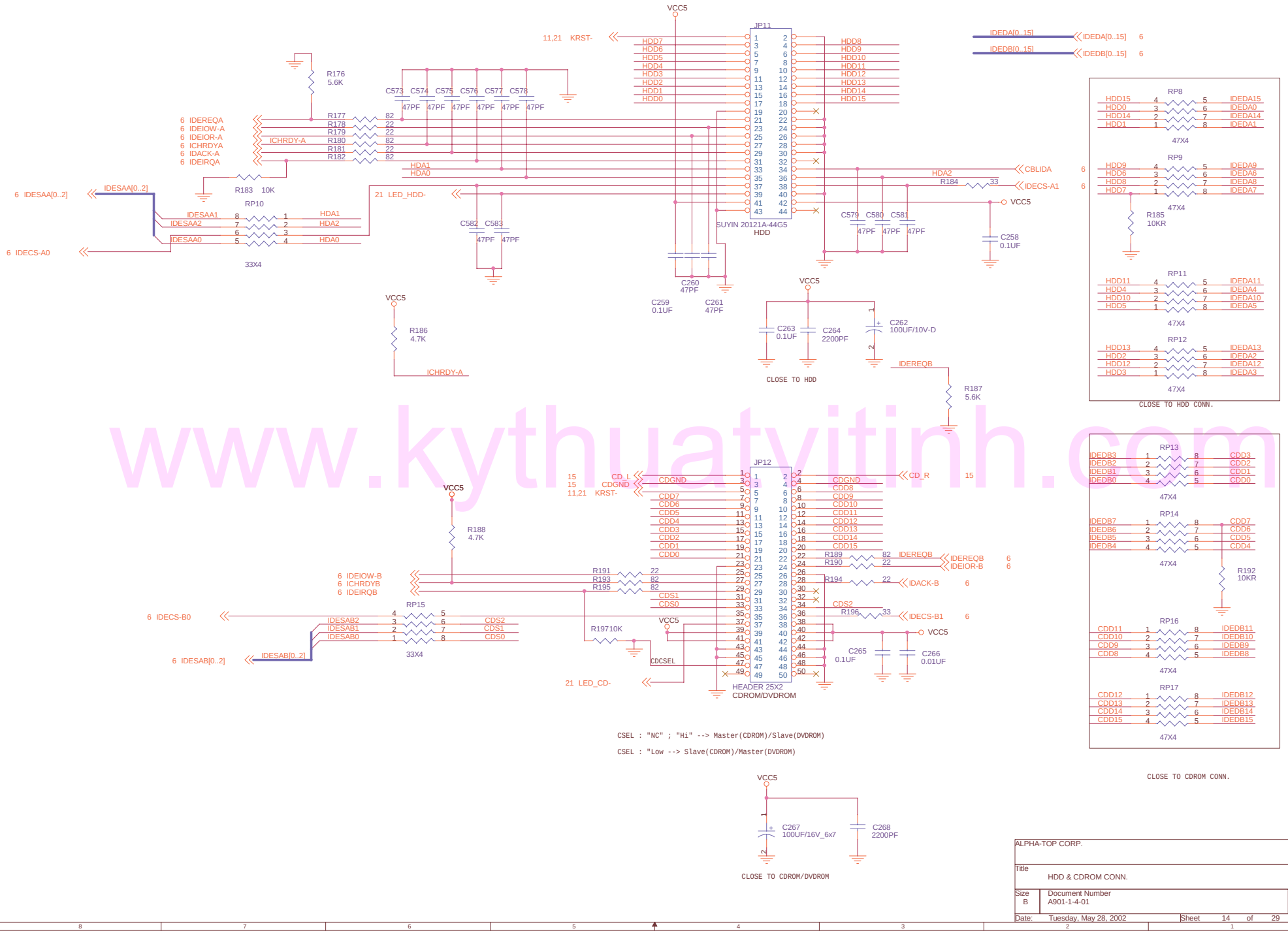
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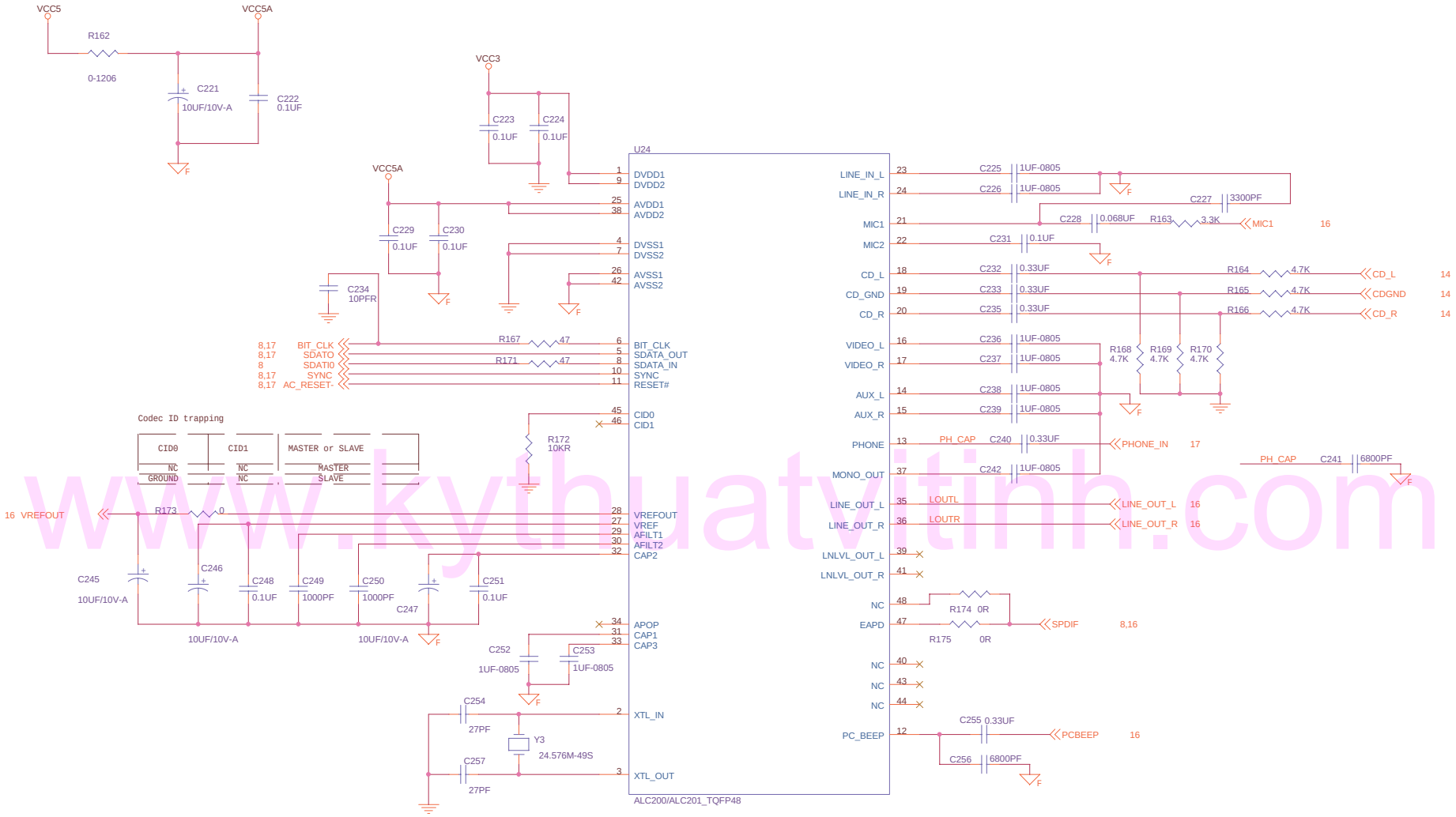


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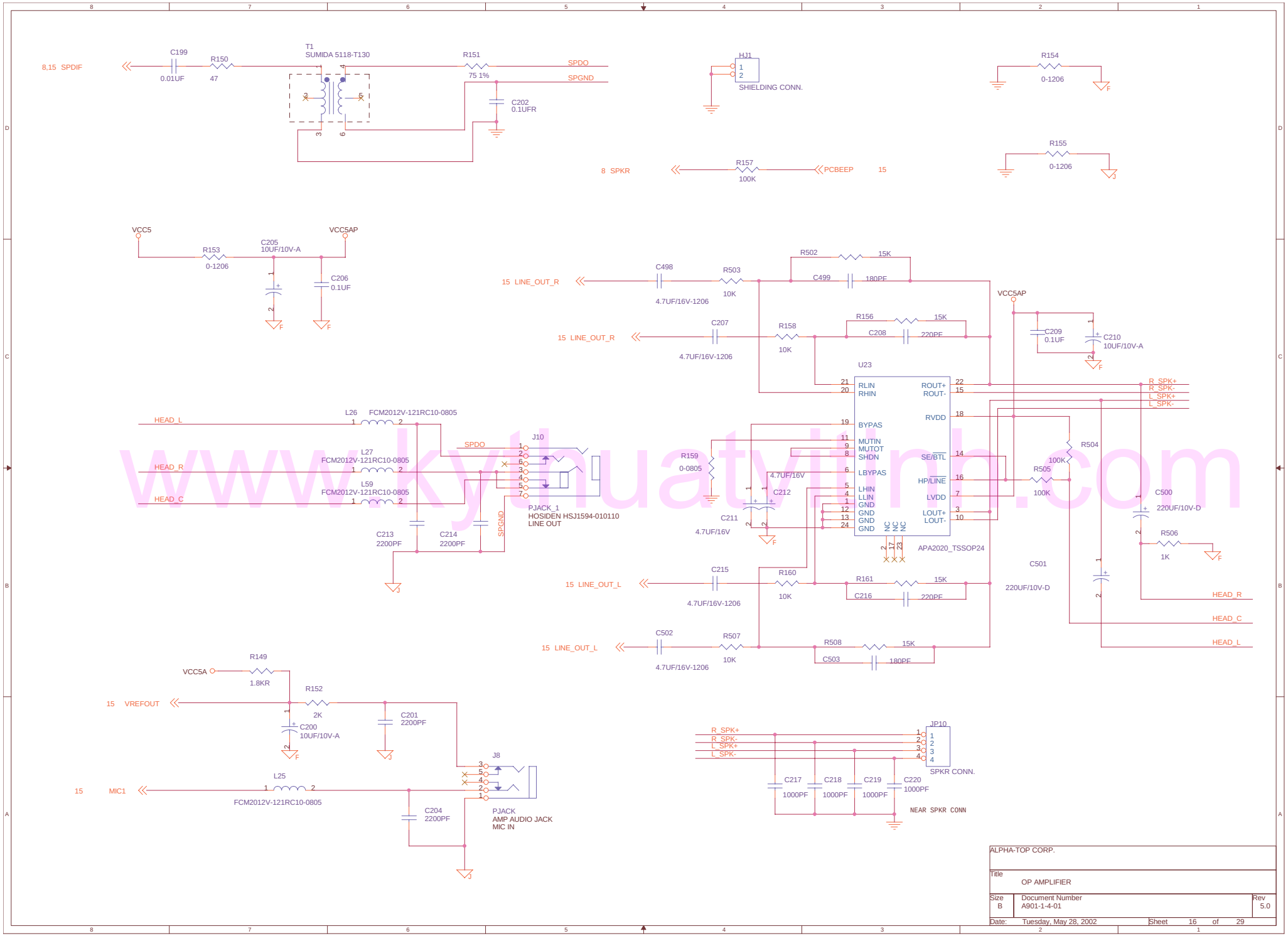
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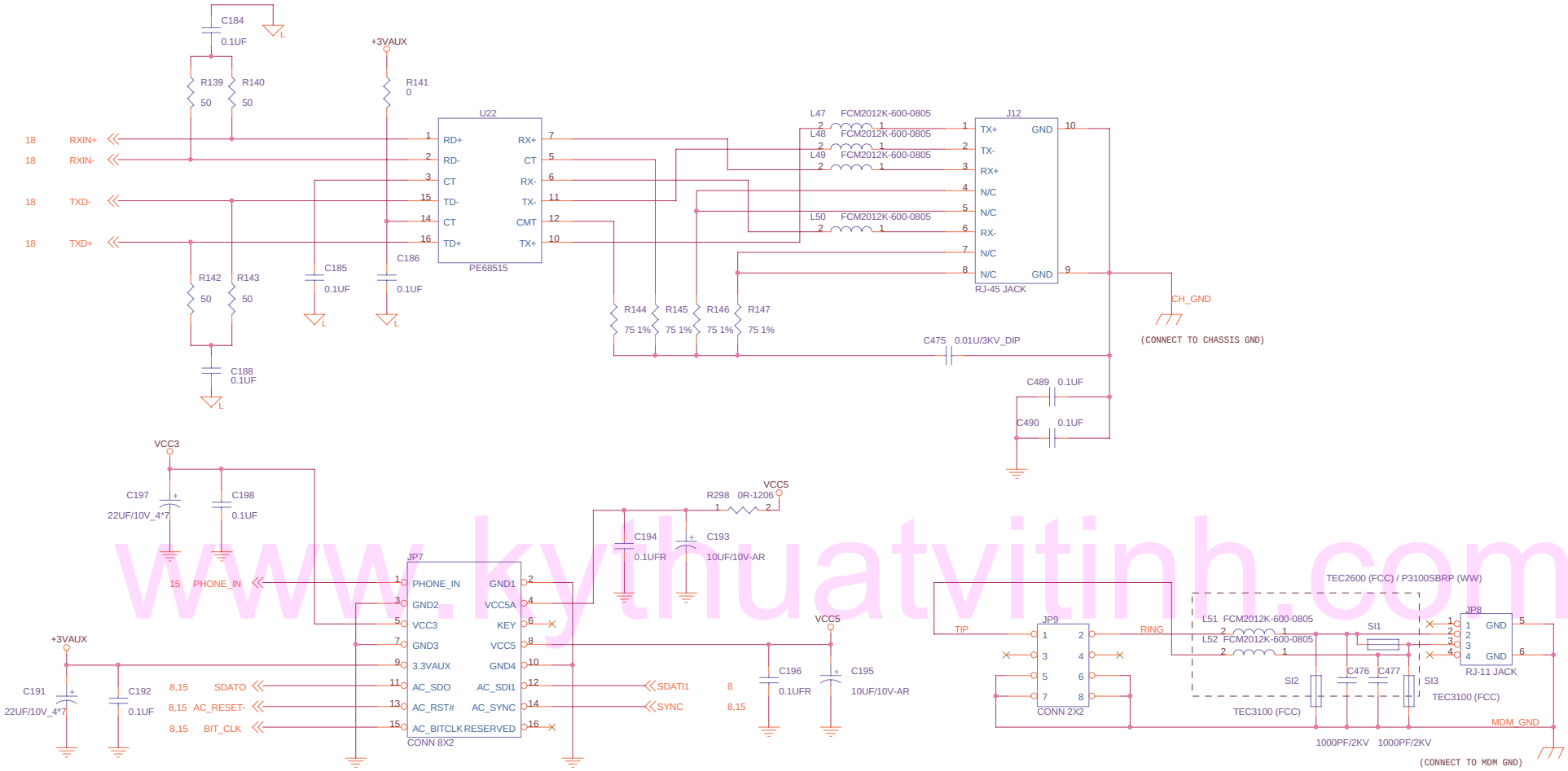


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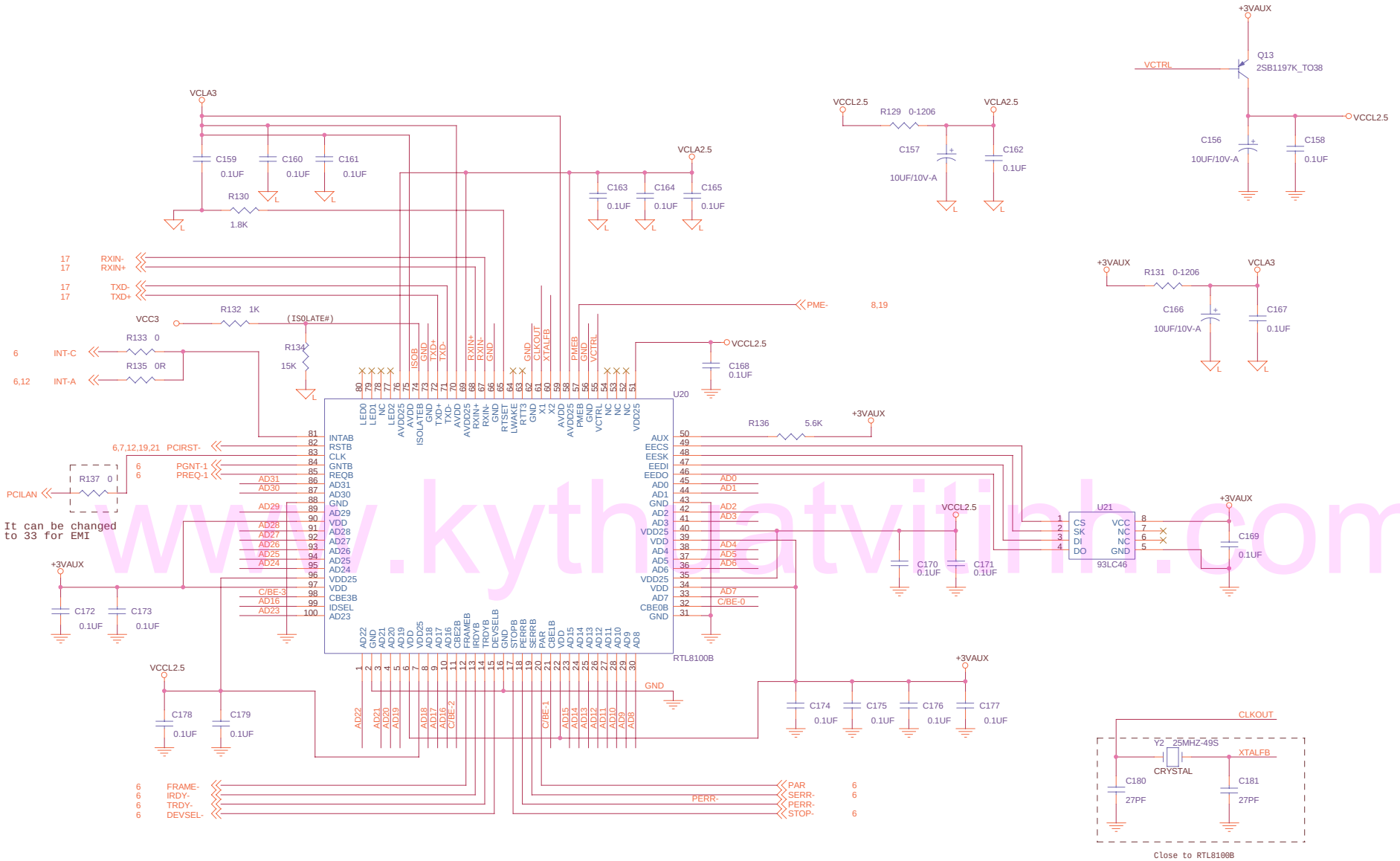
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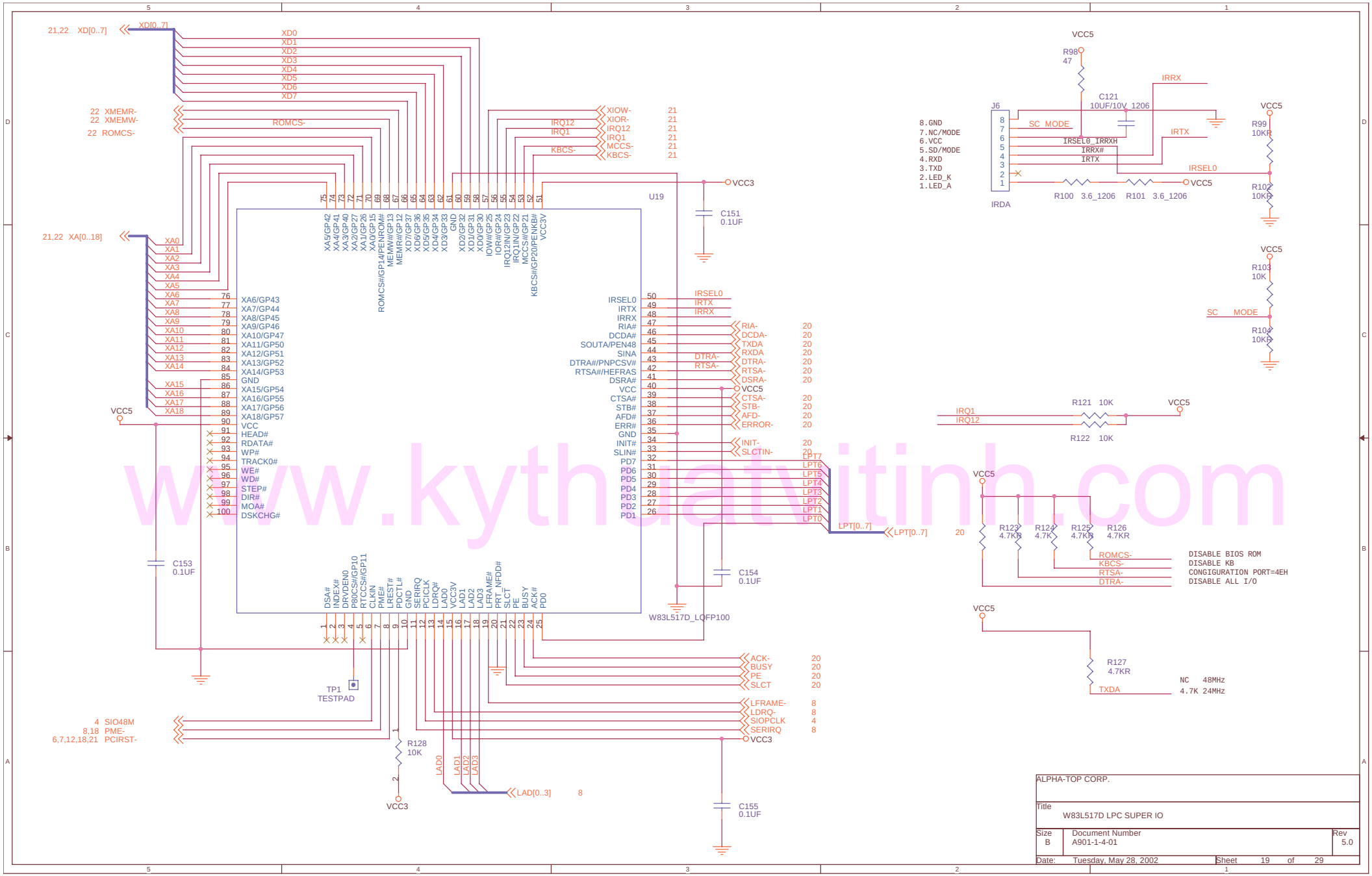
Europe/America use Sidactor(SI1)
Mainland China use Gas Tube(SI1,SI2,SI3)

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Title: RJ-11 / RJ-45 JACKS & MODEM CARD		
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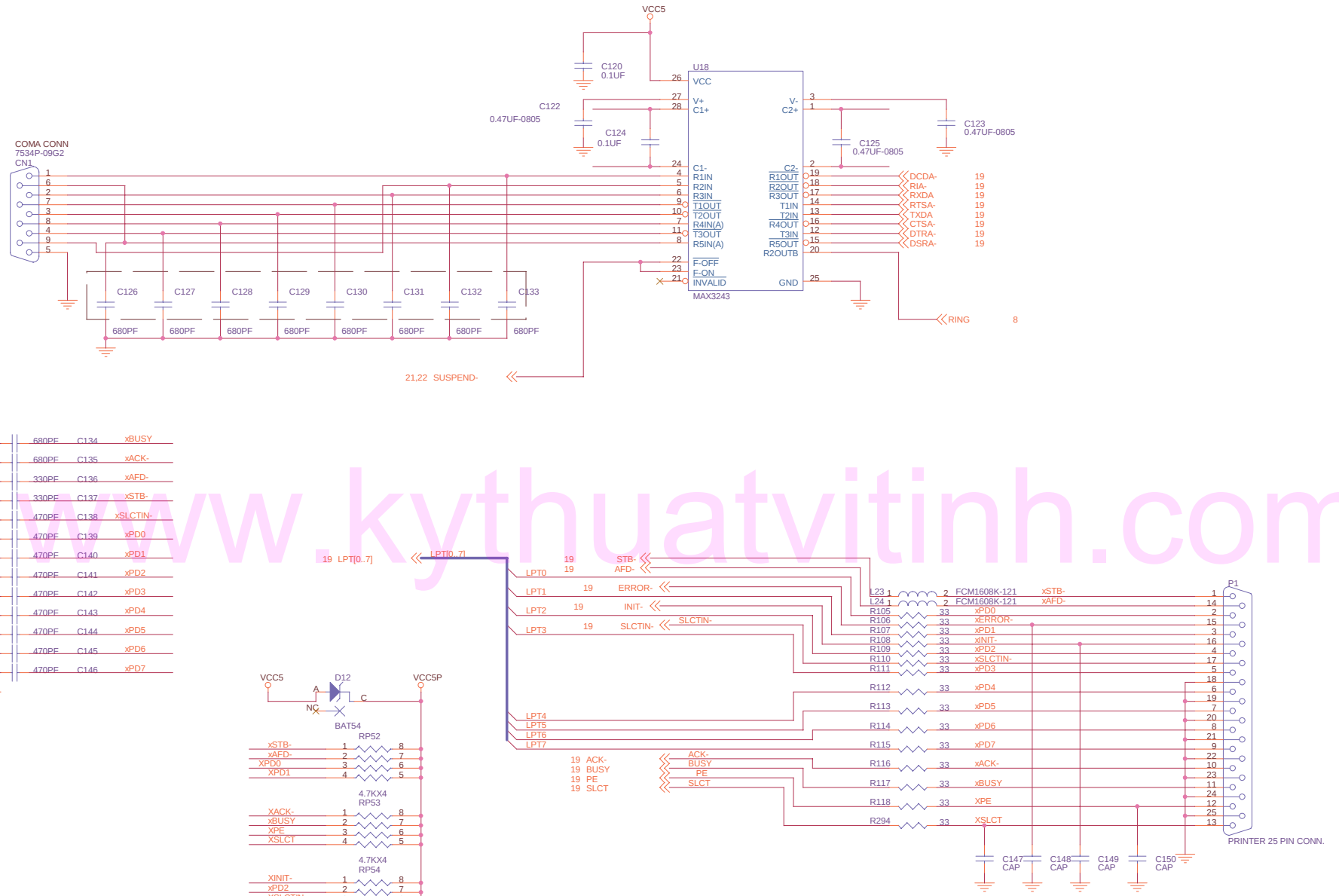
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ALPHA-TOP CORP.		
Title: REALTEK RTL8100B ETHERNET LAN		
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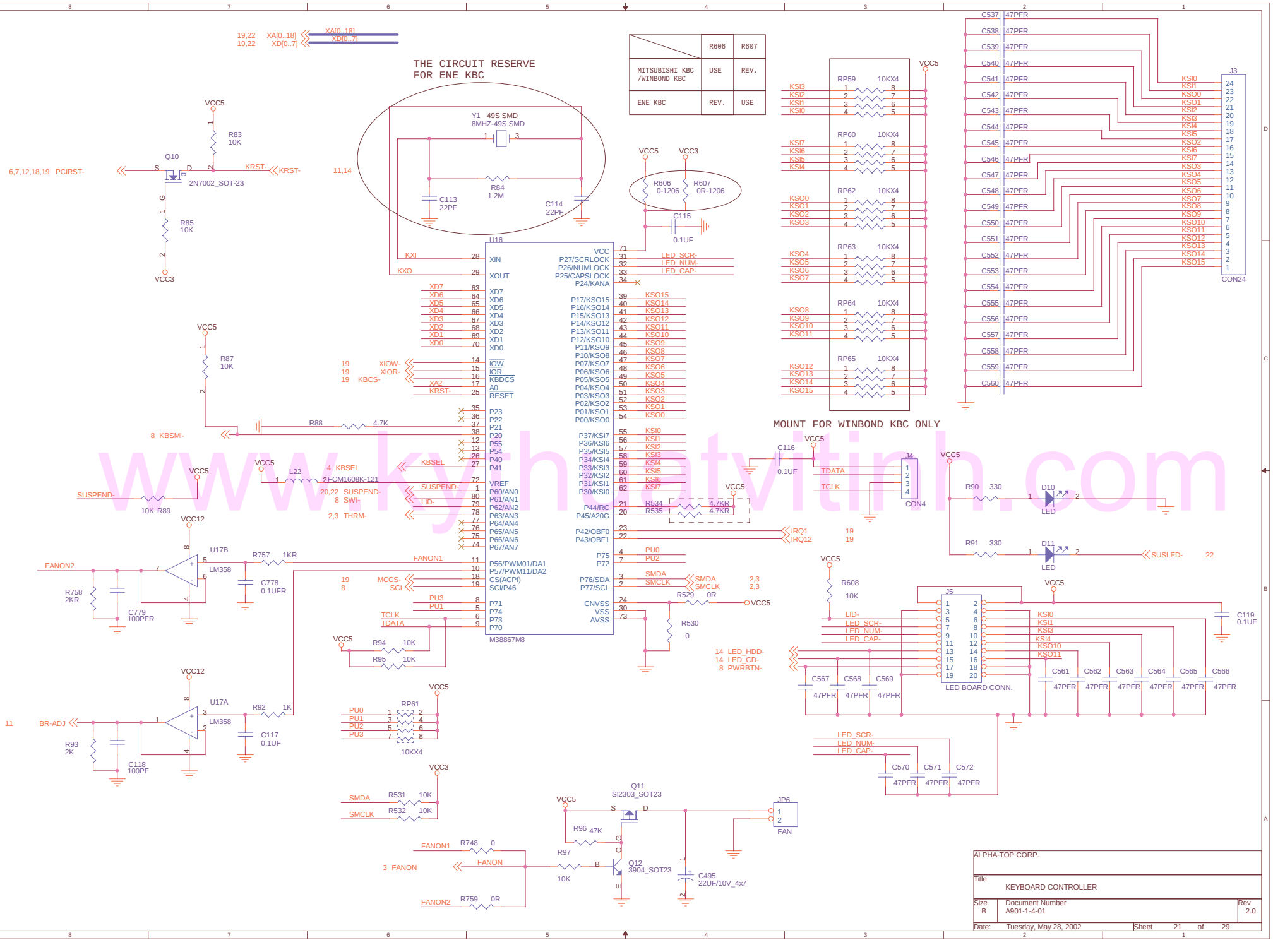
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ALPHA-TOP CORP.		
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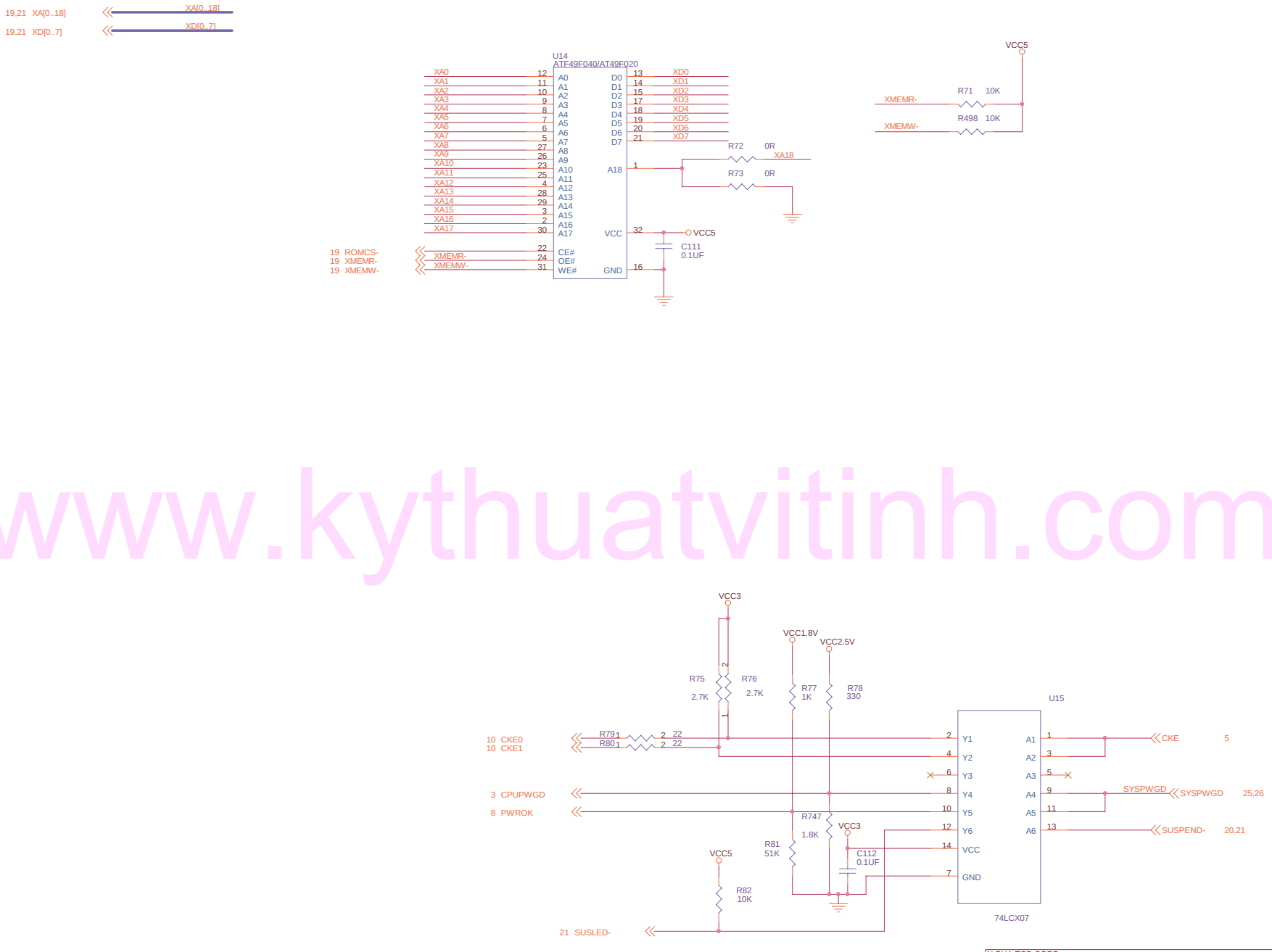


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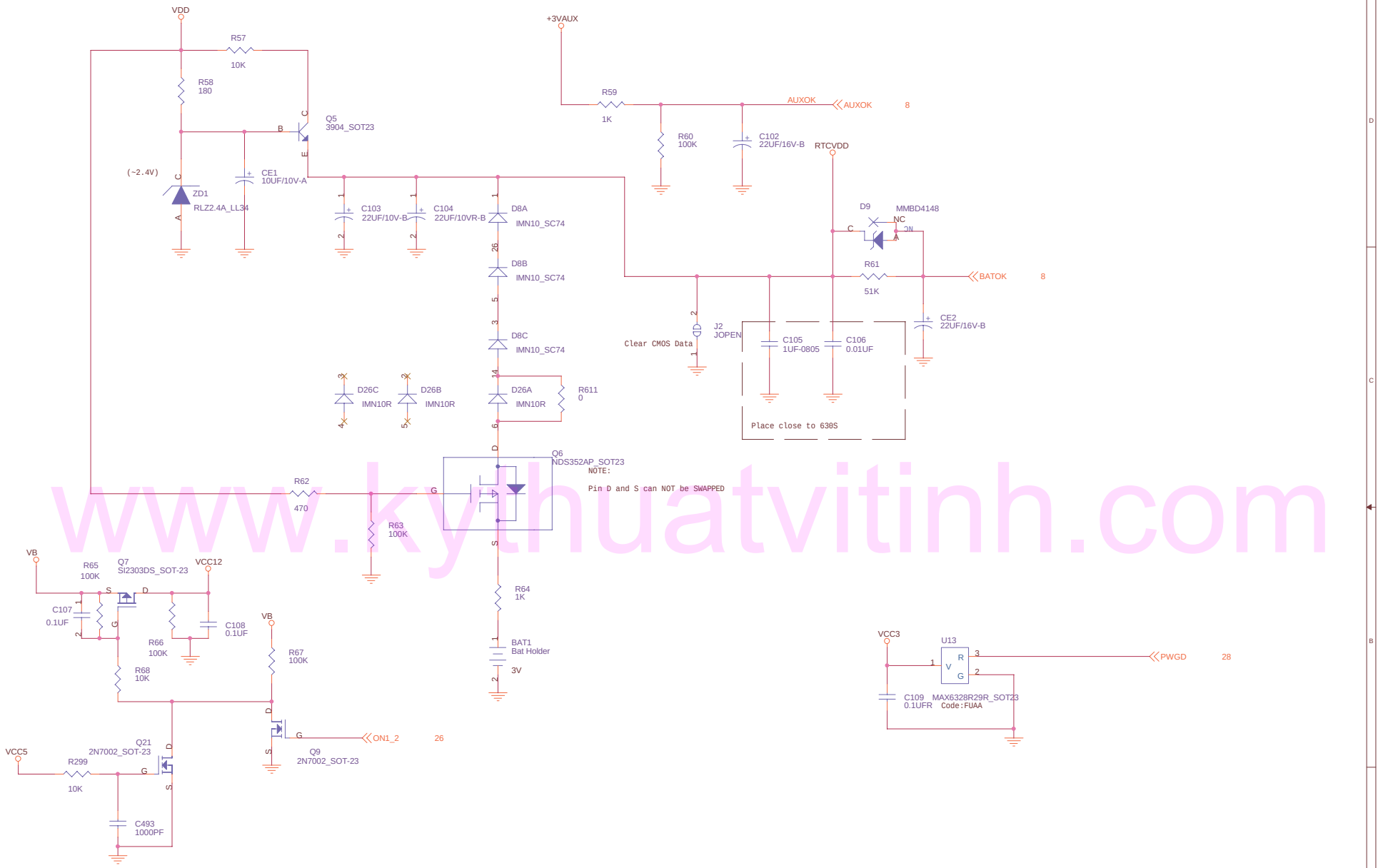


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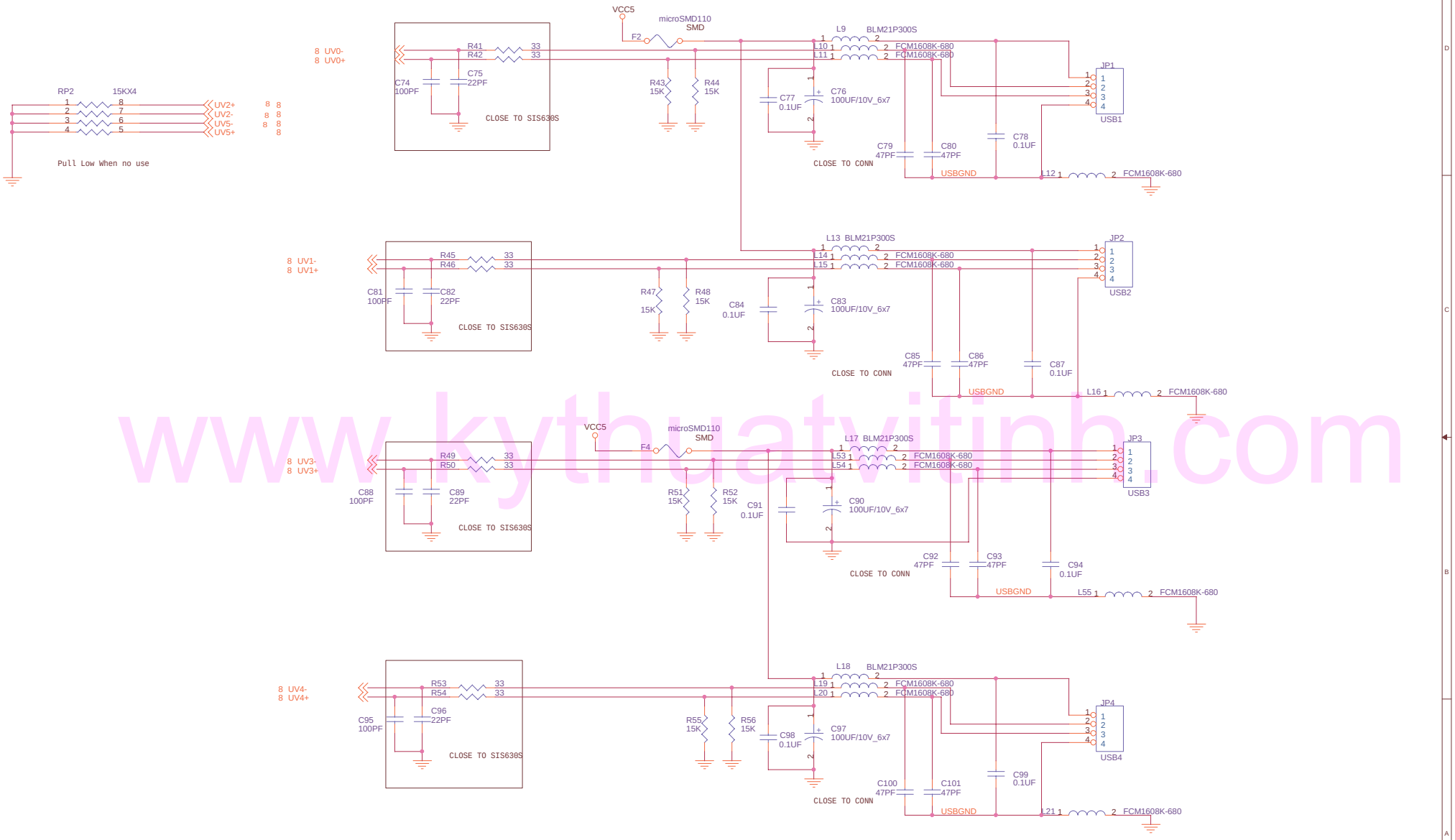
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ALPHA-TOP CORP.		
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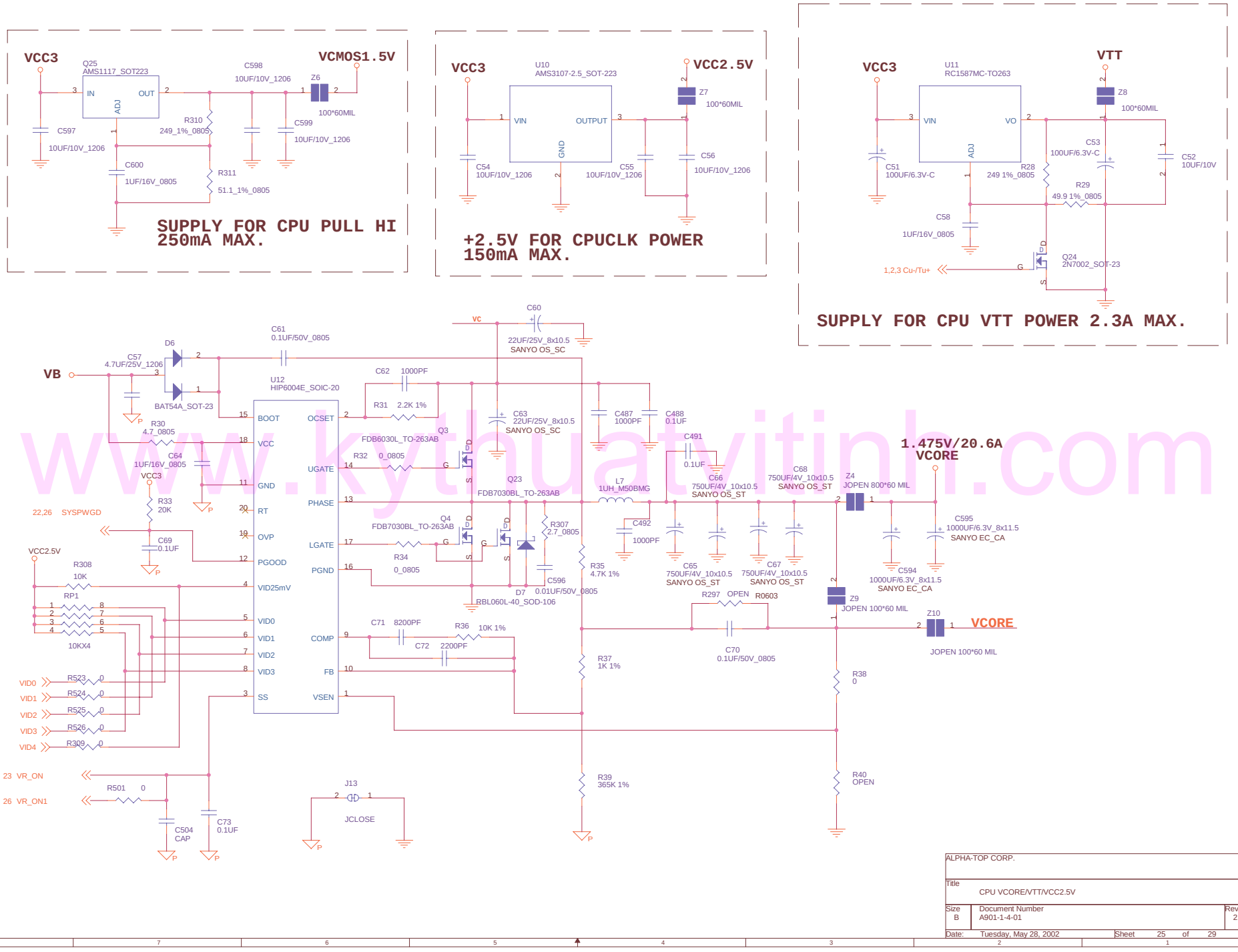
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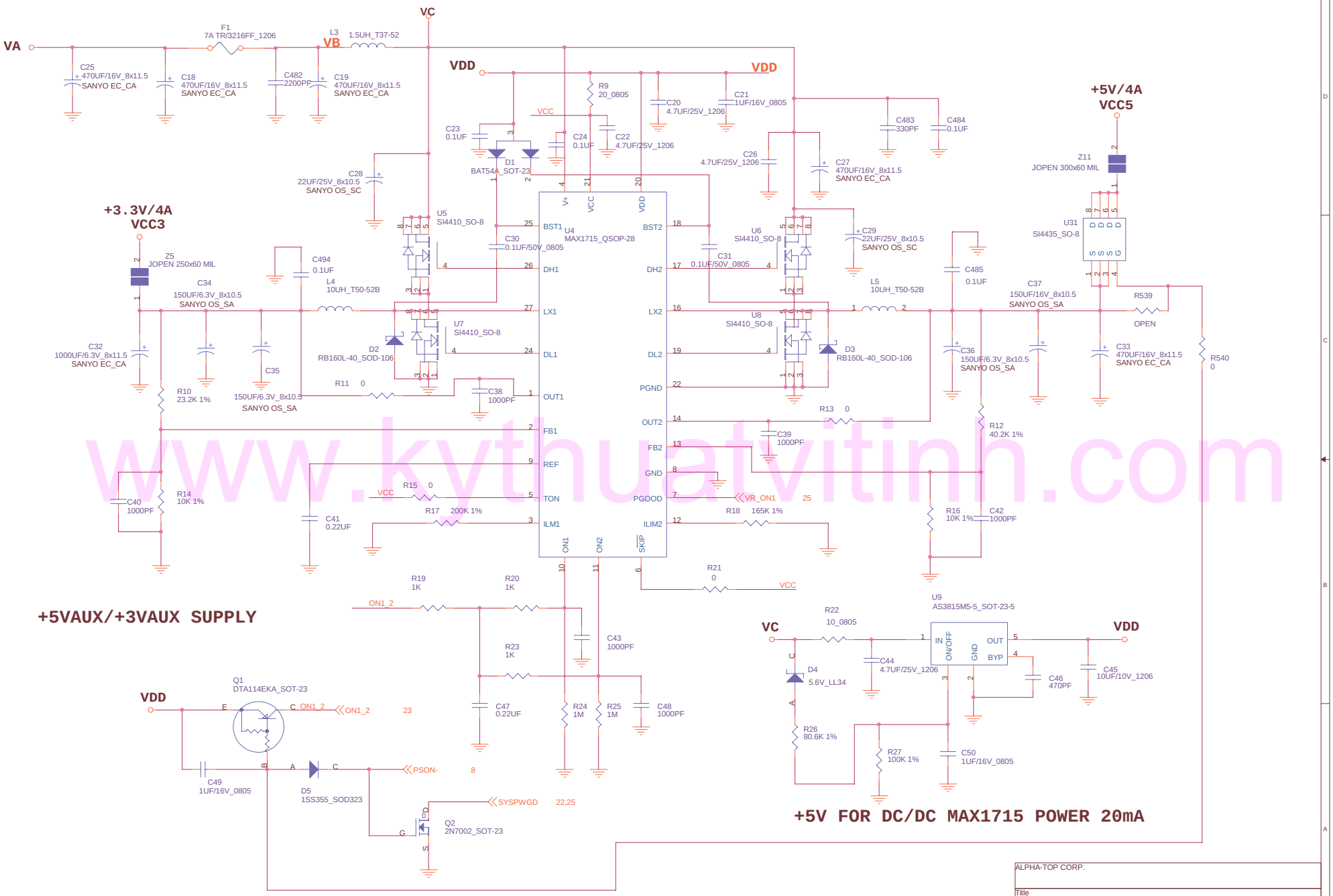
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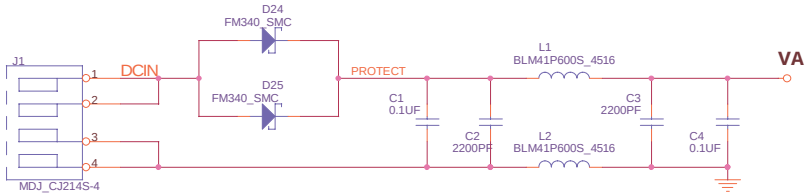


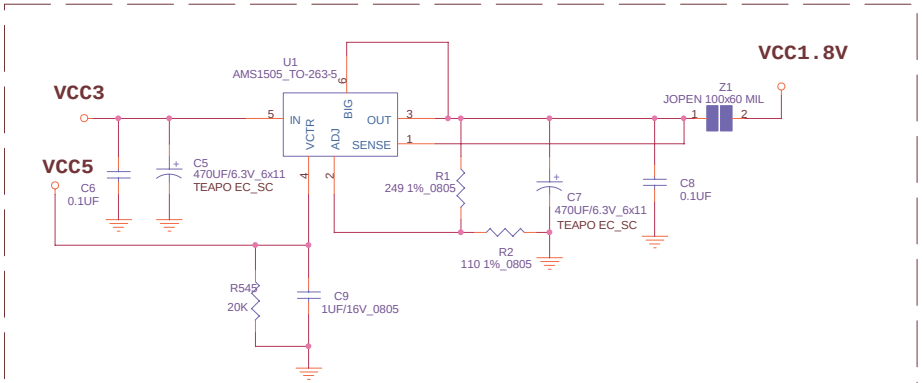
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ALPHA-TOP CORP.		
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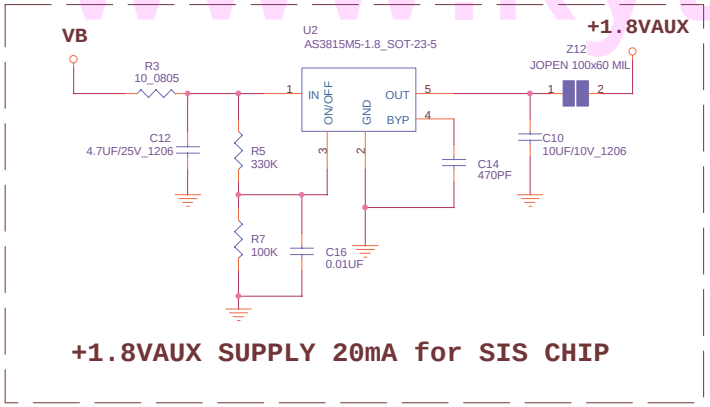


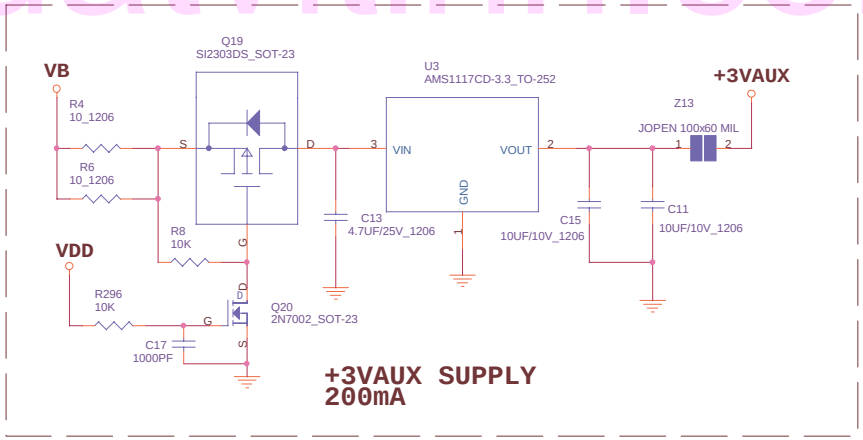
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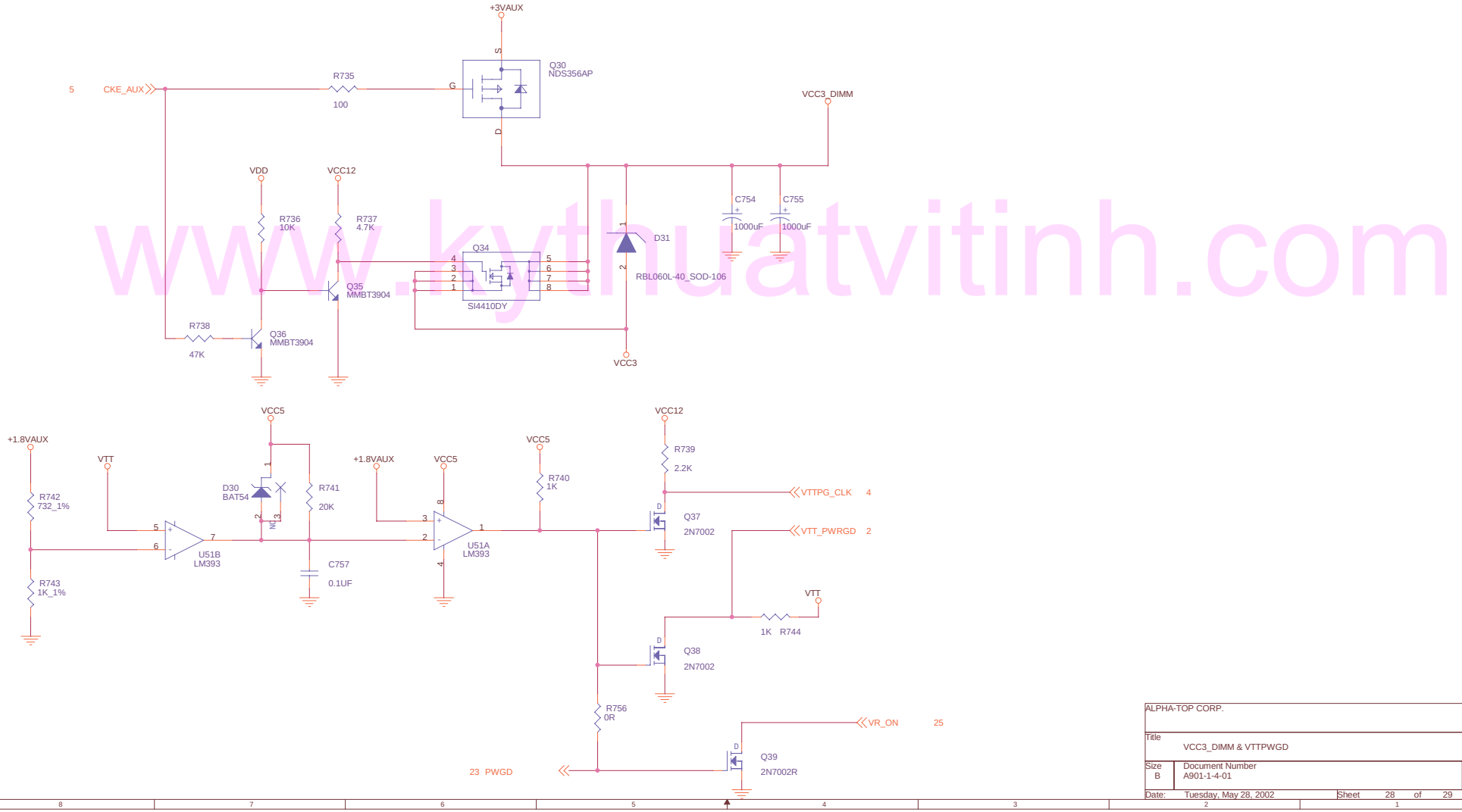


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REVISION HISTORY

V1.0	INITIAL RELEASE
V1.A	RISE THE OUTPUT CURRENT LIMIT
V1.B	CHANGE LAN CHIP AND IrDA MODULE FOR COST DOWN AND PART SHORTAGE
V2.0	UPDATE PCB REVISION FROM V1.0 TO V2.0

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ALPHA-TOP CORP.

Title: REVISION HISTORY

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