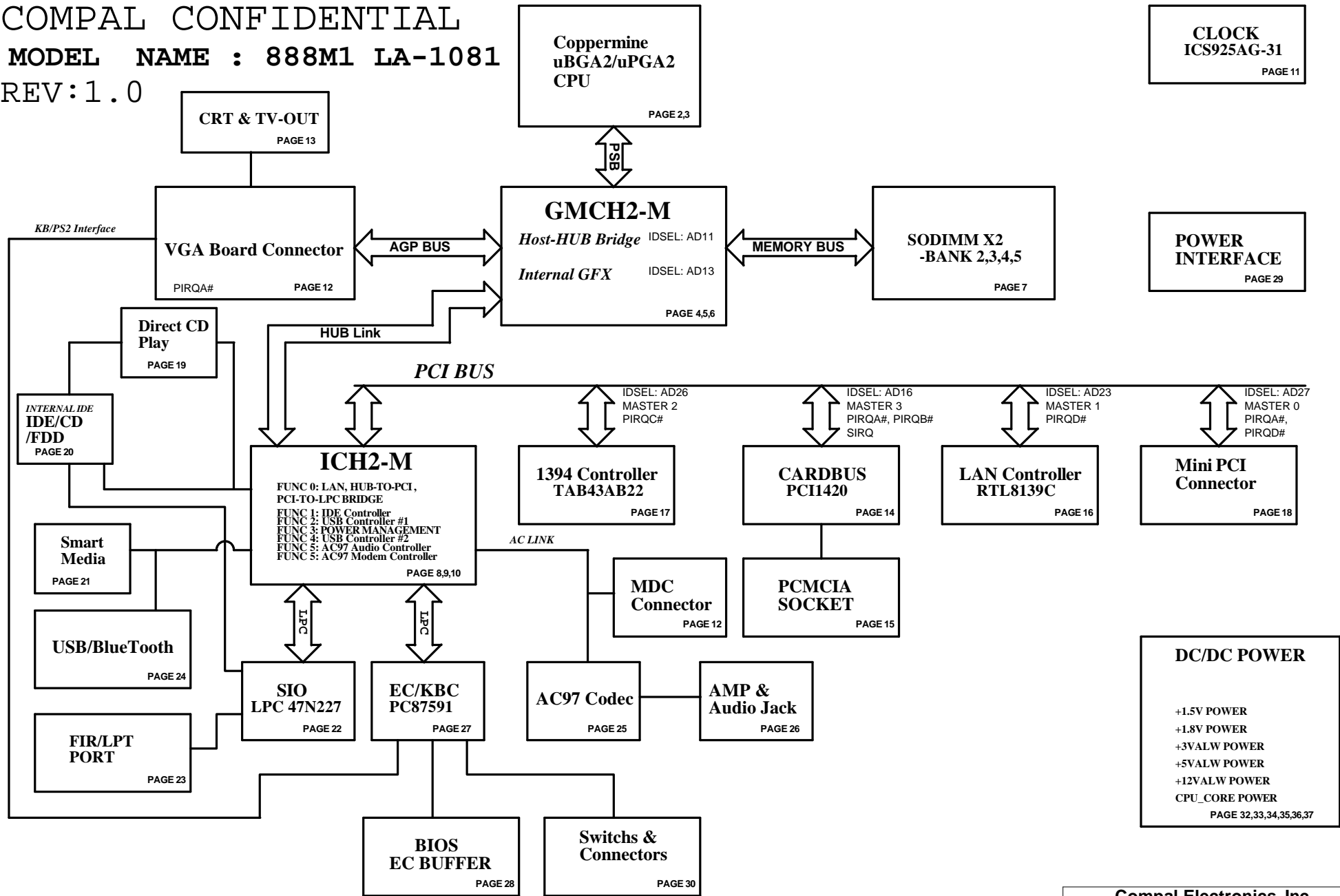
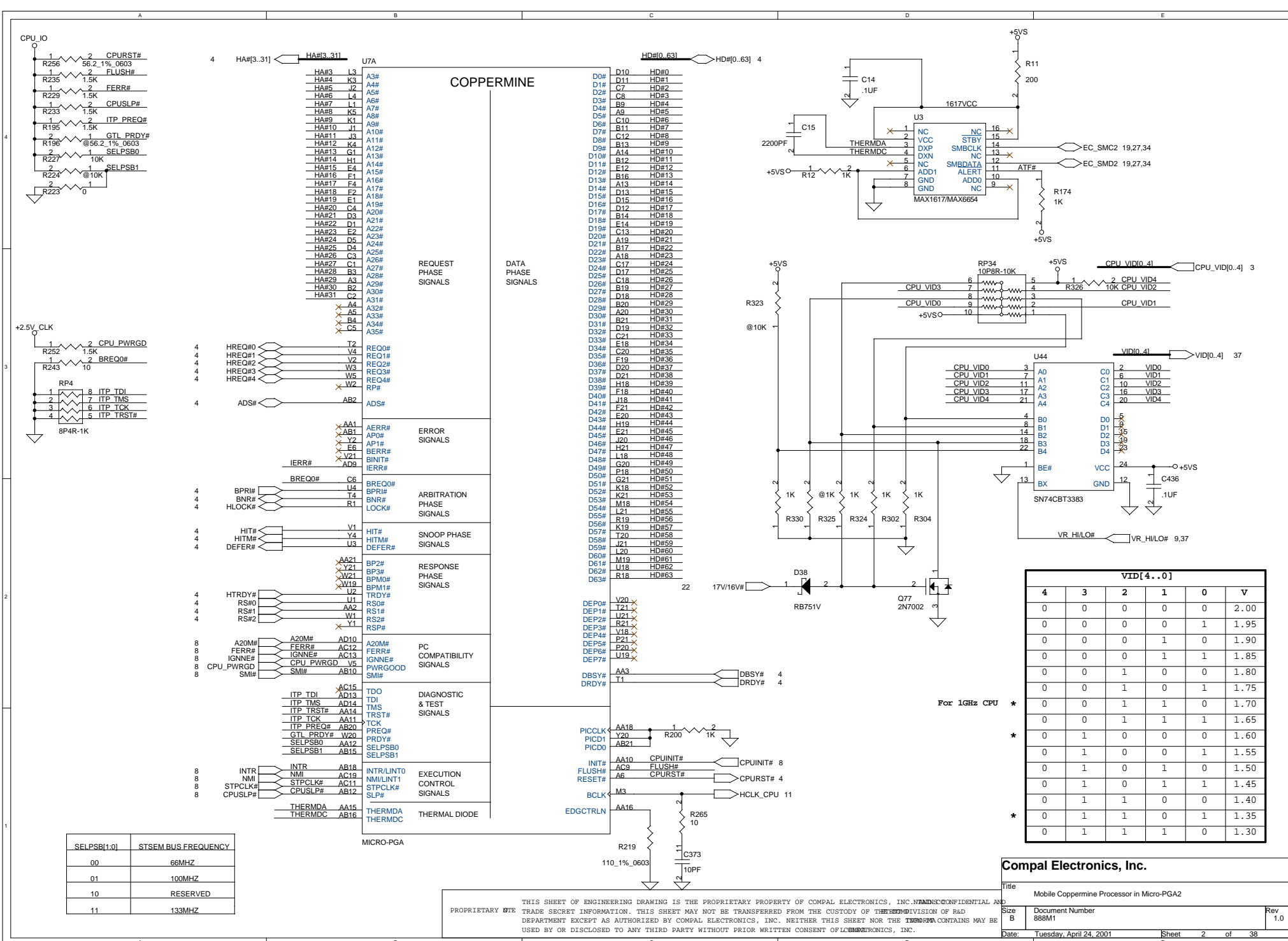


COMPAL CONFIDENTIAL
 MODEL NAME : 888M1 LA-1081
 REV:1.0



Compal Electronics, Inc.		
Title	888M1 COVER SHEET	
Size	Document Number	Rev
B	888M1	1.0
Date:	Tuesday, April 24, 2001	Sheet 1 of 38

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SELPSB1:0	STSEM.BUS.FREQUENCY
00	66MHZ
01	100MHZ
10	RESERVED
11	133MHZ

VID[4..0]					
4	3	2	1	0	V
0	0	0	0	0	2.00
0	0	0	0	1	1.95
0	0	0	1	0	1.90
0	0	0	1	1	1.85
0	0	1	0	0	1.80
0	0	1	0	1	1.75
0	0	1	1	0	1.70
0	0	1	1	1	1.65
0	1	0	0	0	1.60
0	1	0	0	1	1.55
0	1	0	1	0	1.50
0	1	0	1	1	1.45
0	1	1	0	0	1.40
0	1	1	1	0	1.35
0	1	1	1	1	1.30

Compal Electronics, Inc.

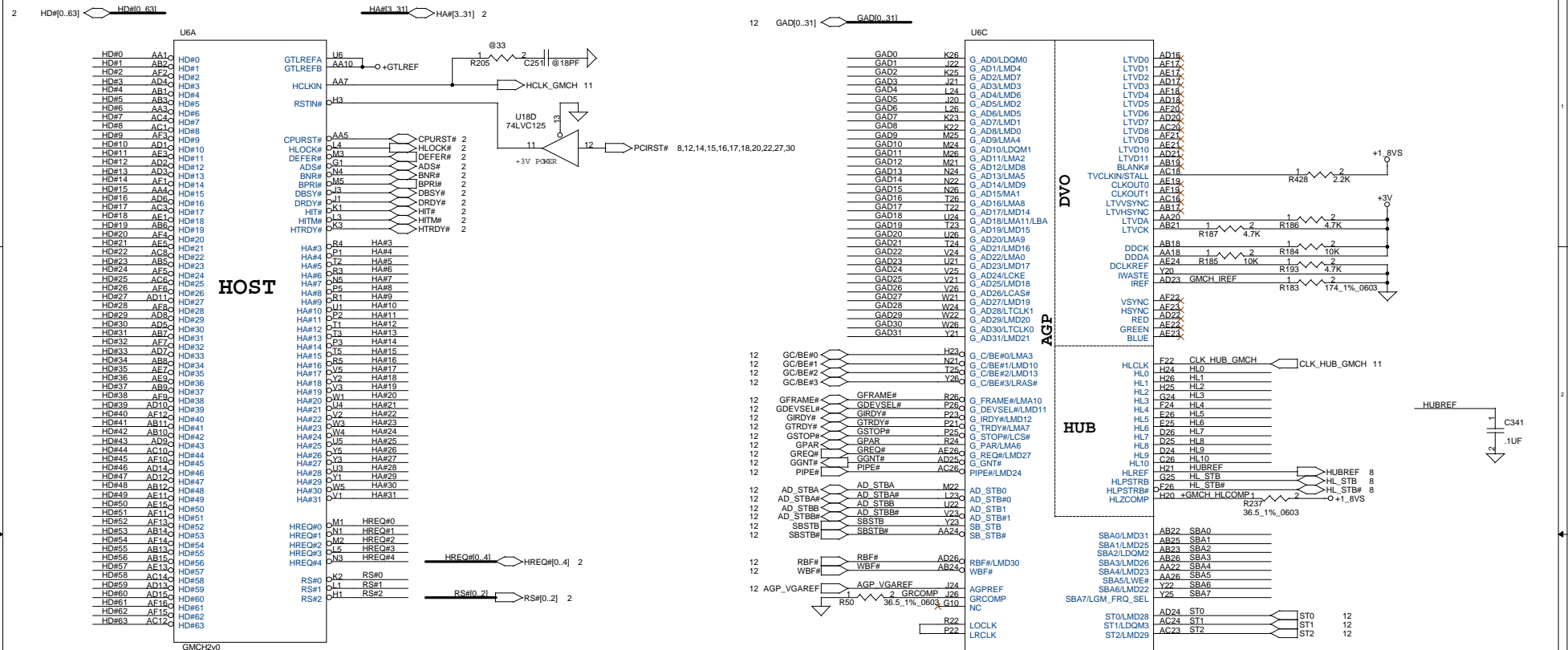
Title: Mobile Coppermine Processor in Micro-PGA2

Size: B Document Number: 888M1 Rev: 1.0

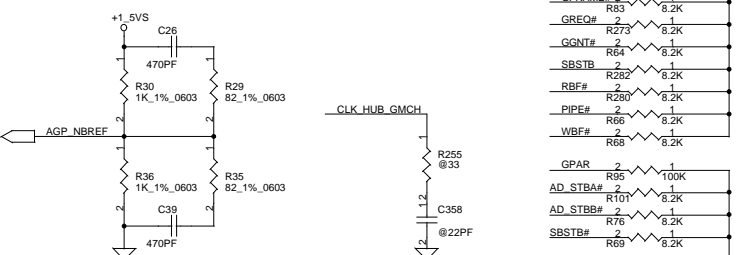
Date: Tuesday, April 24, 2001 Sheet: 2 of 38

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GMCH2-M-1/3(GTL+,AGP,HUB)



TYPEDET#	+VDDQ	AGP-REF
0	1.5V	0.5VDDQ
1	3.3V	0.4VDDQ



Place reference circuitry near GMC H2-M

Compal Electronics, Inc.

GMCH2-M-1/3(GTL+,AGP,HUB)

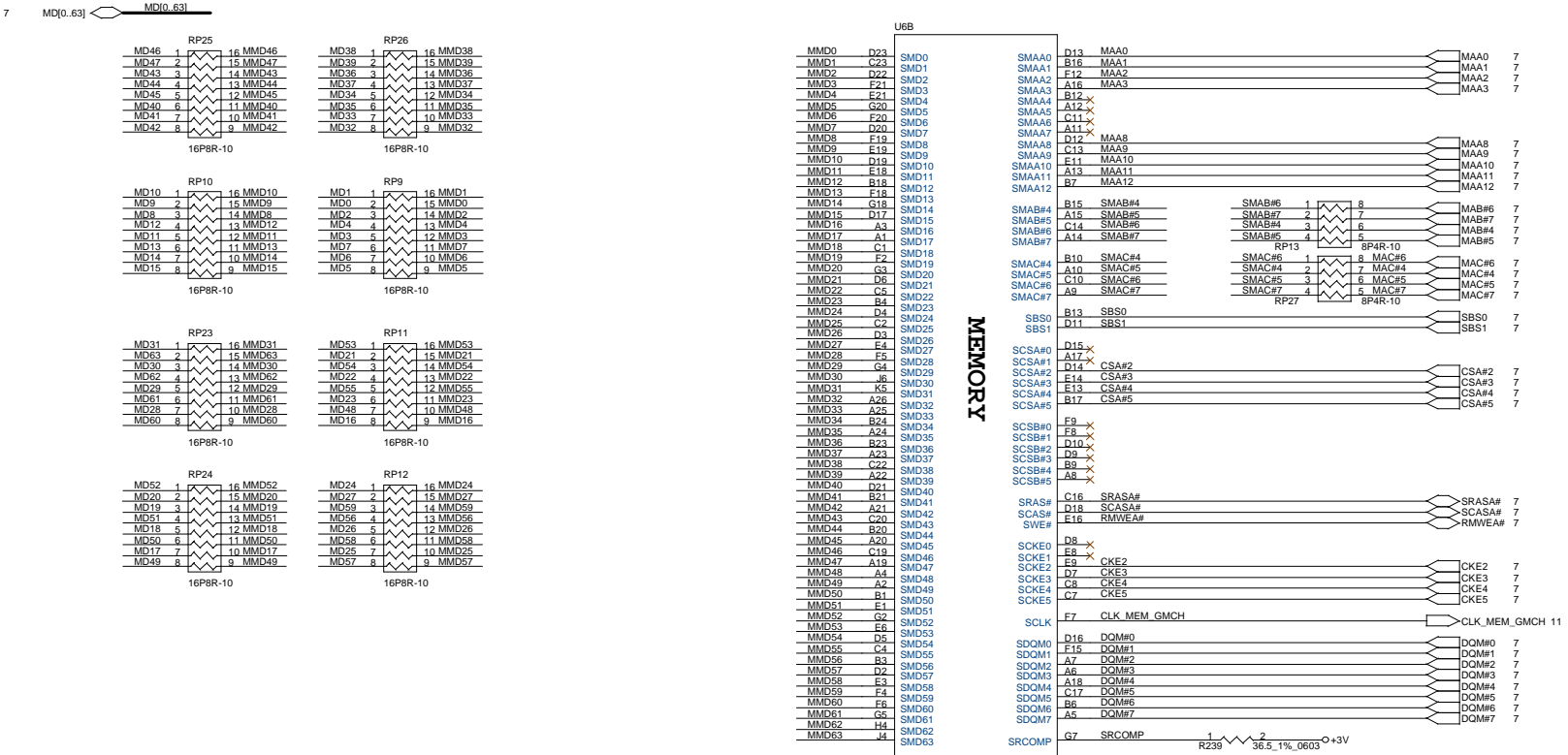
Title: _____

Size: _____ Document Number: **888M1** Rev: 1.0

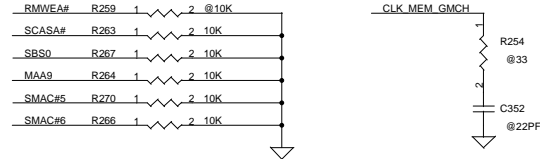
Date: Tuesday, April 24, 2001 Sheet: 4 of 38

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GMCH2-M-2/3(SDRAM)



Power-Up Strap Options				
Pin Name	Strap Description	Configuration	Interface	Internal
SCAS#	Host Freq.	"H" : 133MHz (Default) "L" : 100MHz	System Memory	PULL_UP
SWE#	Host Freq.	"H" : 100MHz (Default) "L" : 66MHz	System Memory	PULL_UP
SMAA11	IOQ Depth	"H" : 4 (Default) "L" : 1	System Memory	PULL_UP
SMAA9	ALL Z	"H" : Normal "L" : All Z	System Memory	PULL_UP
SMAA9	FSB P-MOS Kicker Enable	"H" : Enabled (Default) "L" : Disabled (Cumine)	System Memory	PULL_UP
SMAC6#	Enable VCH Serial Programming Mode	"H" : Enabled (Default) "L" : Disabled	System Memory	PULL_UP
SMAC5#	Enable Quick Start Support	"H" : Disabled (Stop Grant Mode) "L" : Enabled (Default) (Quick Start Mode)	System Memory	PULL_UP
VGA_LFSEL#	Local Memory Freq. Select	"H" : 133MHz (Default) "L" : 100MHz	AGP/LM	i815/i815-m

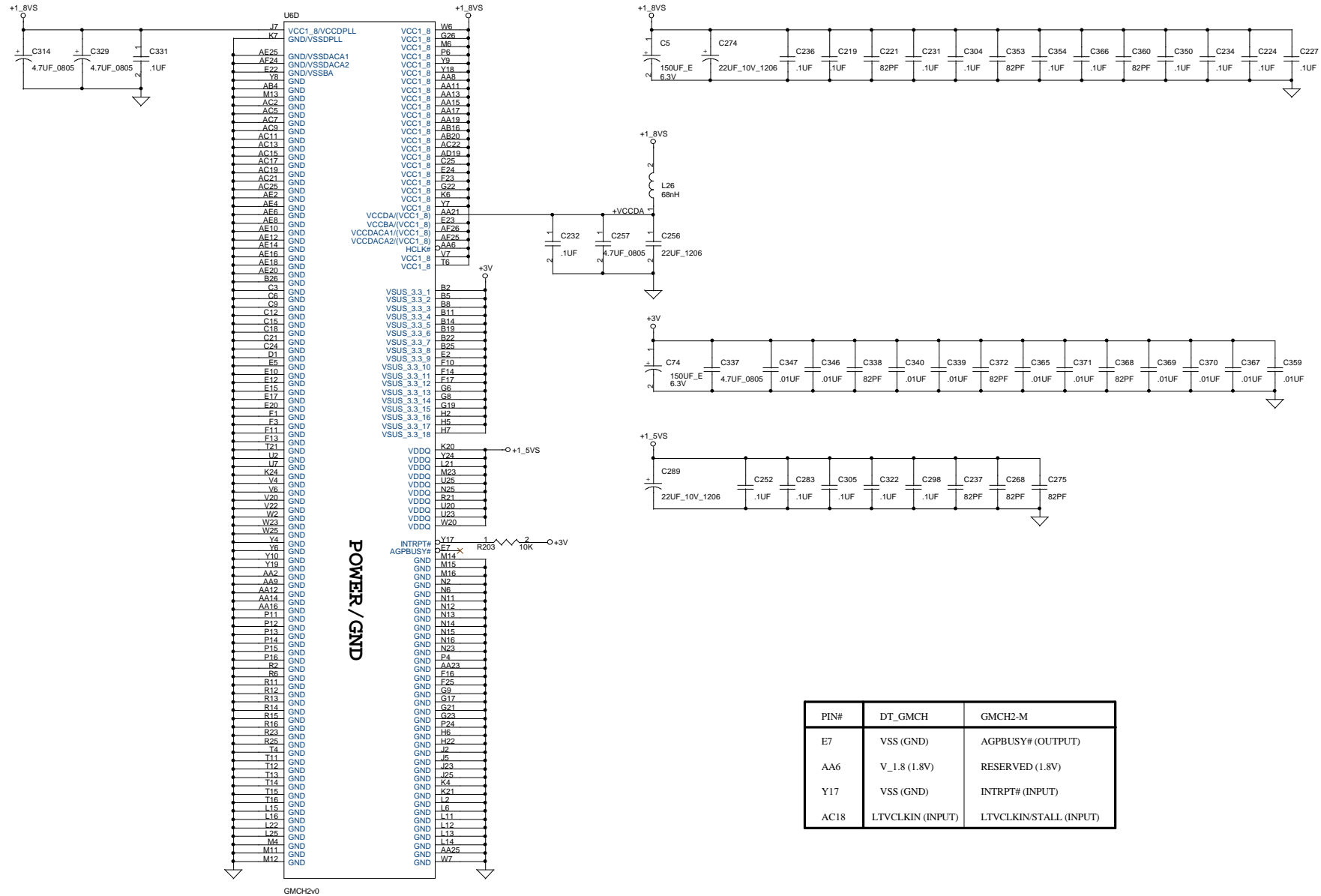


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Title		
GMCH2-M-2/3(SDRAM)		
Size	Document Number	Rev
B	888M1	1.0
Date:	Tuesday, April 24, 2001	Sheet 5 of 38

GMCH2-M-3/3(Power)

Please make sure the ESR is as small as possible.



PIN#	DT_GMCH	GMCH2-M
E7	VSS (GND)	AGPBUSY# (OUTPUT)
AA6	V_1.8 (1.8V)	RESERVED (1.8V)
Y17	VSS (GND)	INTRPT# (INPUT)
AC18	LTVCLKIN (INPUT)	LTVCLKIN/STALL (INPUT)

Compal Electronics, Inc.		
Title GMCH2-M-3/3(Power)		
Size B	Document Number 888M1	Rev 1.0
Date: Tuesday, April 24, 2001	Sheet 6	of 38

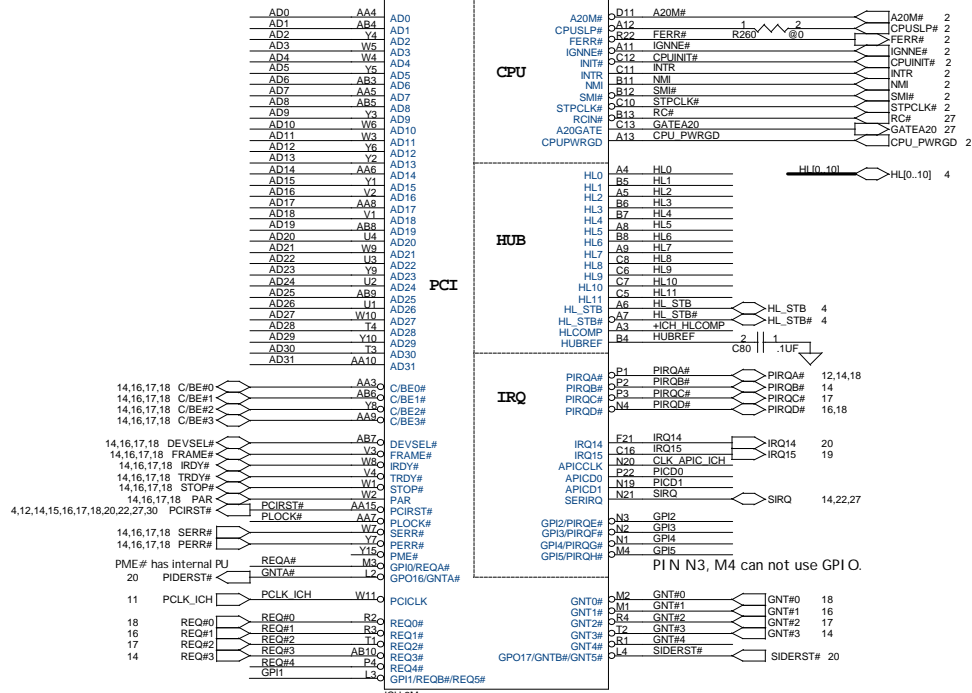
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ICH-2M

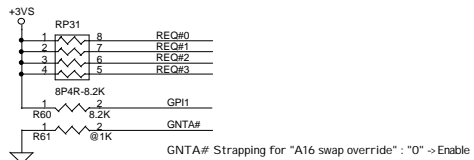
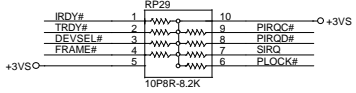
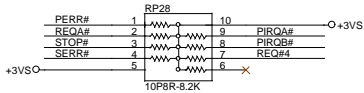
14,16,17,18 AD[0..31] \leftarrow AD[0..31]

(FW82801BAM)

U34A

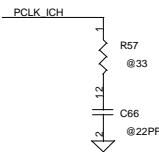


PCI Pullups

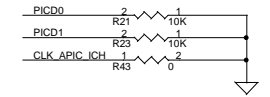
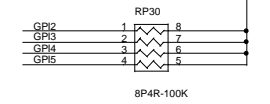
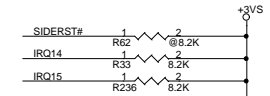
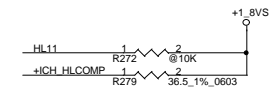


GNTA# Strapping for "A16 swap override": "0" -> Enable

PCI REQ ASSIGNMENT	
REQ#0	WLAN
REQ#1	LAN
REQ#2	1394
REQ#3	PCMCIA CONTROLLER
REQ#4	NC



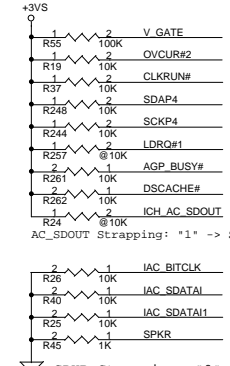
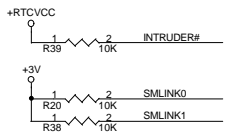
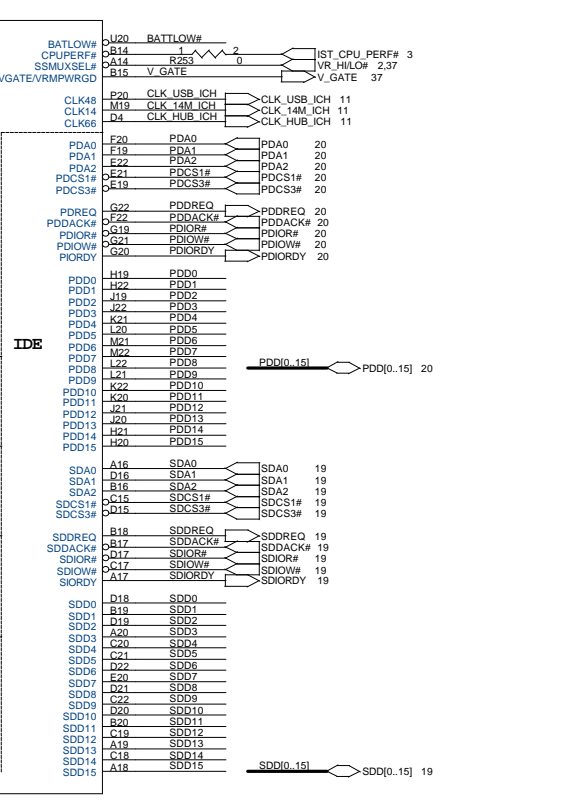
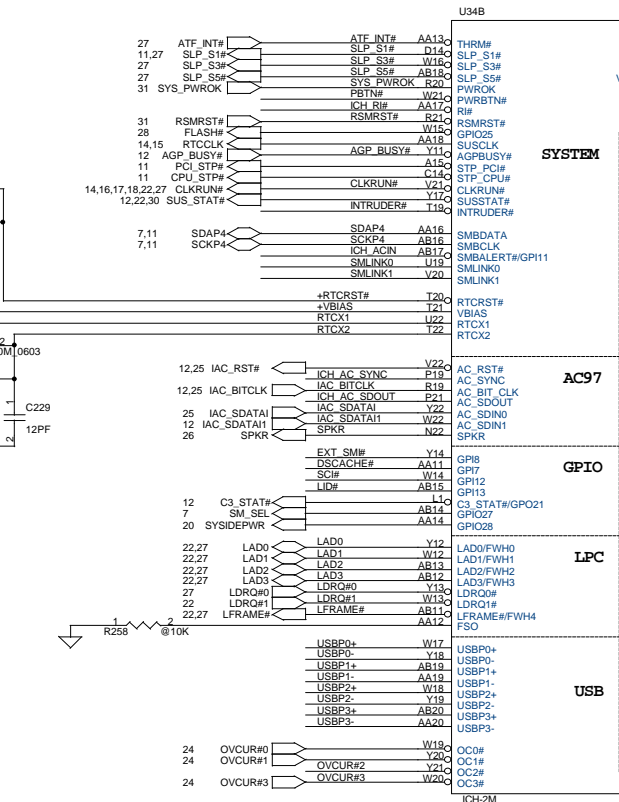
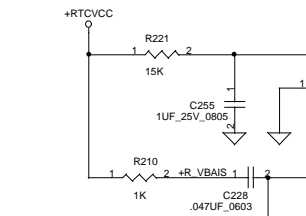
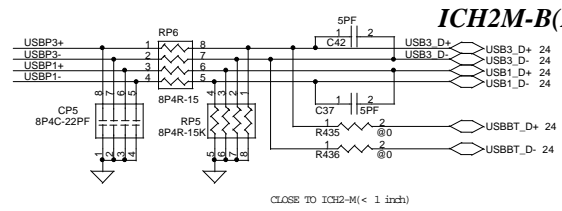
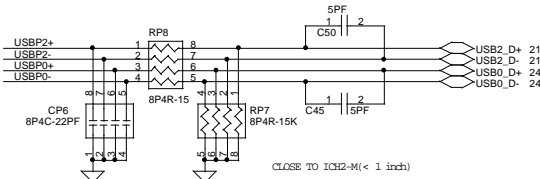
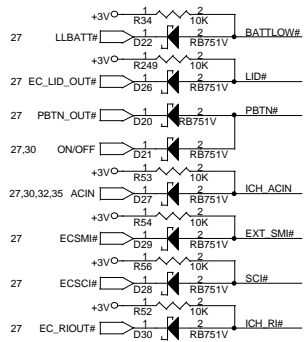
Place divider pair in middle of bus



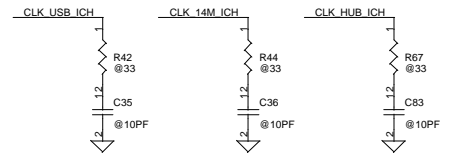
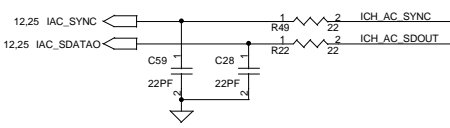
Compal Electronics, Inc. ICH2M-A(PCLHUB,CPU) & FW8		
Title		
Size	Document Number	Rev
B	888M1	1.0
Date:	Tuesday, April 24, 2001	Sheet 8 of 38

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ICH2M-B(IDE,LPC,GPIO)



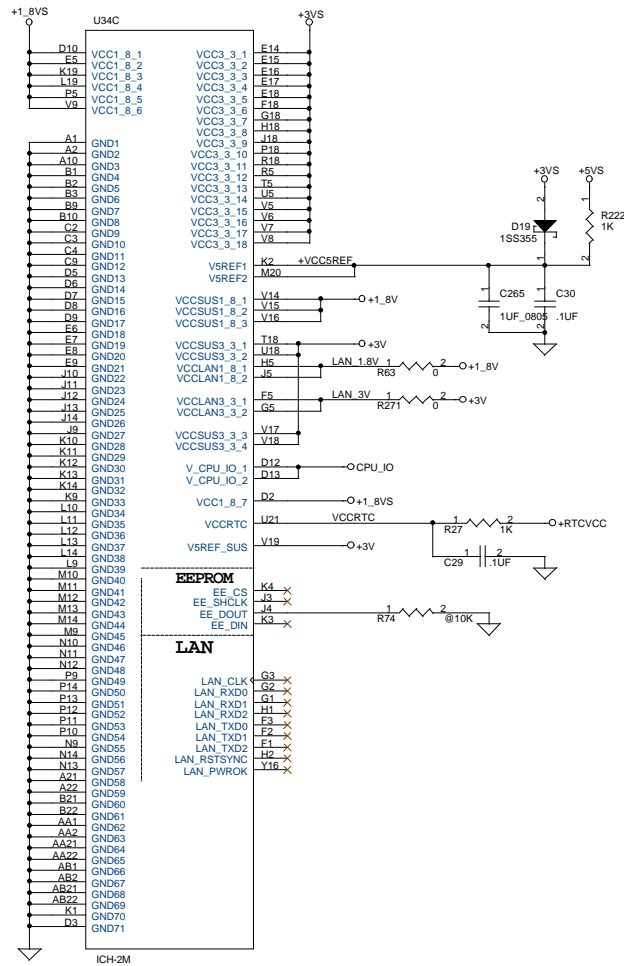
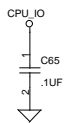
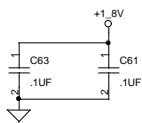
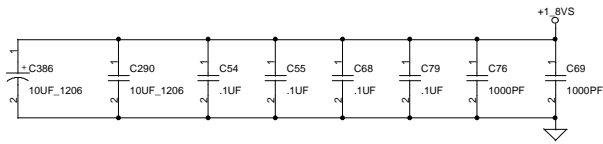
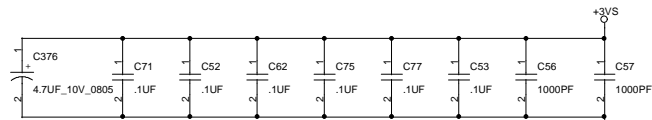
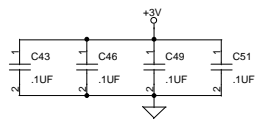
AC_SDOUT Strapping: "1" -> Safe Mode B oot
 SPKR Strapping: "0" -> No Reboot



Compal Electronics, Inc.		
Title: ICH2M-B(IDE,LPC,GPIO)		
Size B	Document Number: 888M1	Rev: 1.0
Date: Tuesday, April 24, 2001	Sheet: 9	of 38

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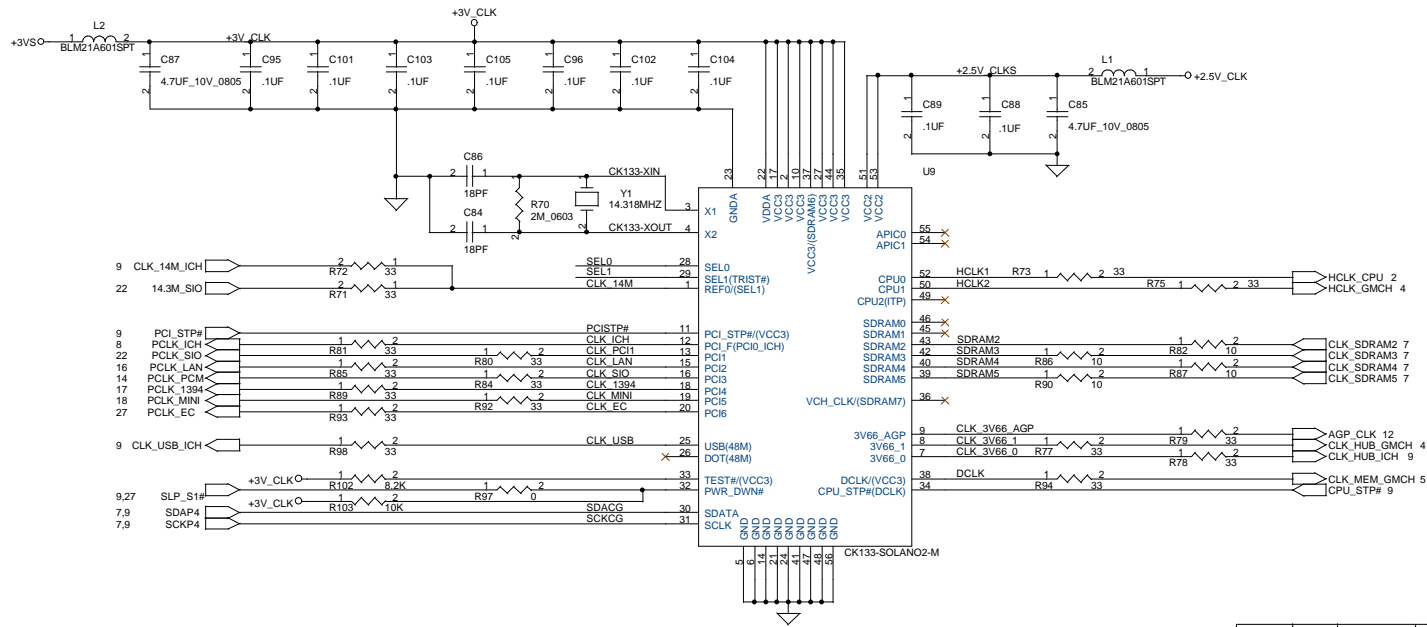
ICH2M-C(LAN,Power) & Pull-Up



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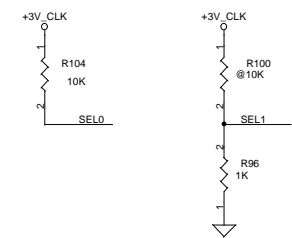
Compal Electronics, Inc.		
Title ICH2M-C(LAN,Power) & Pull-Up		
Size B	Document Number 888M1	Rev 1.0
Date: Tuesday, April 24, 2001	Sheet 10	of 38

Clock Generator



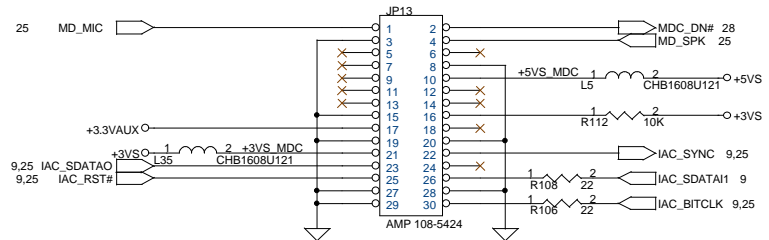
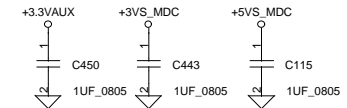
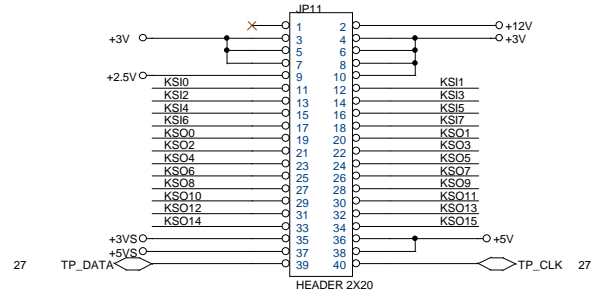
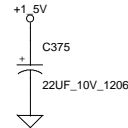
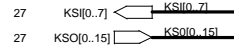
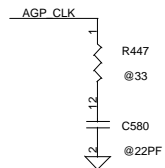
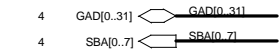
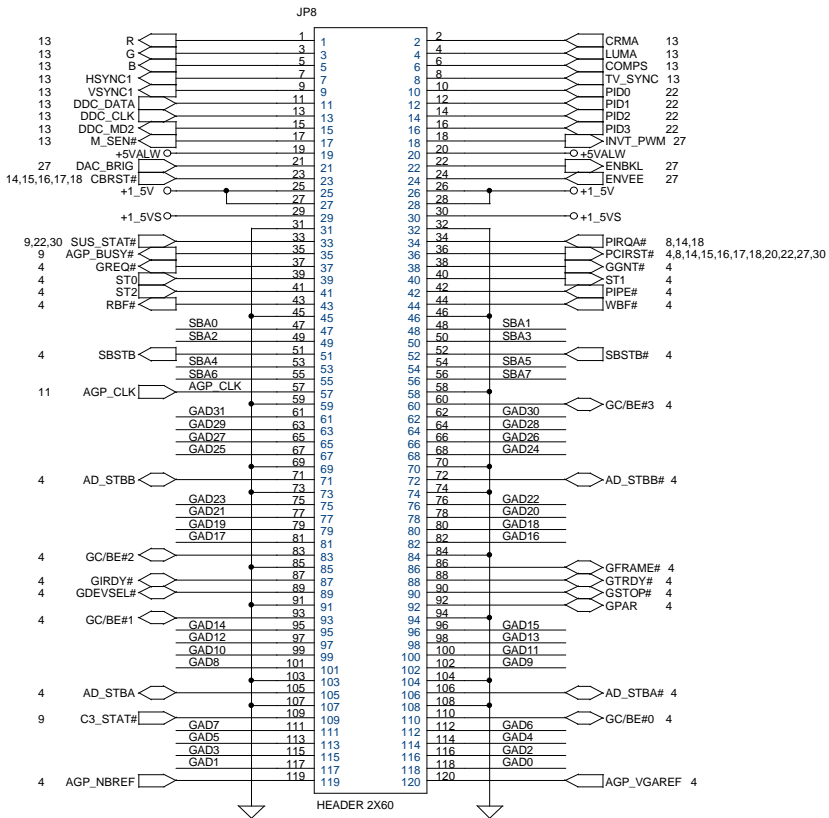
TSSOP-56 SA09250000 ICS 9 250AG-31

SEL1	SEL0	PSB	SDRAM
0	0	66	100
0	1	100	100
1	0	133	133
1	1	133	100



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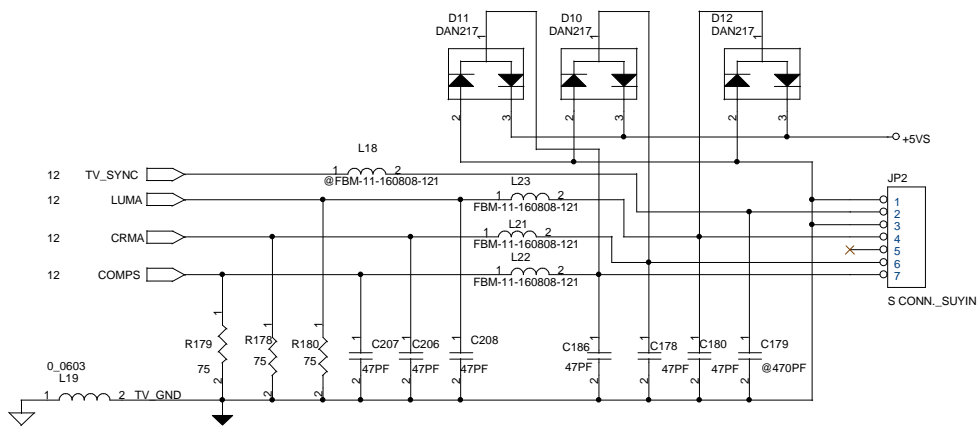
Title		
Compal Electronics, Inc.		
Clock Generator		
Size B	Document Number 888M1	Rev 1.0
Date: Tuesday, April 24, 2001	Sheet 11	of 38



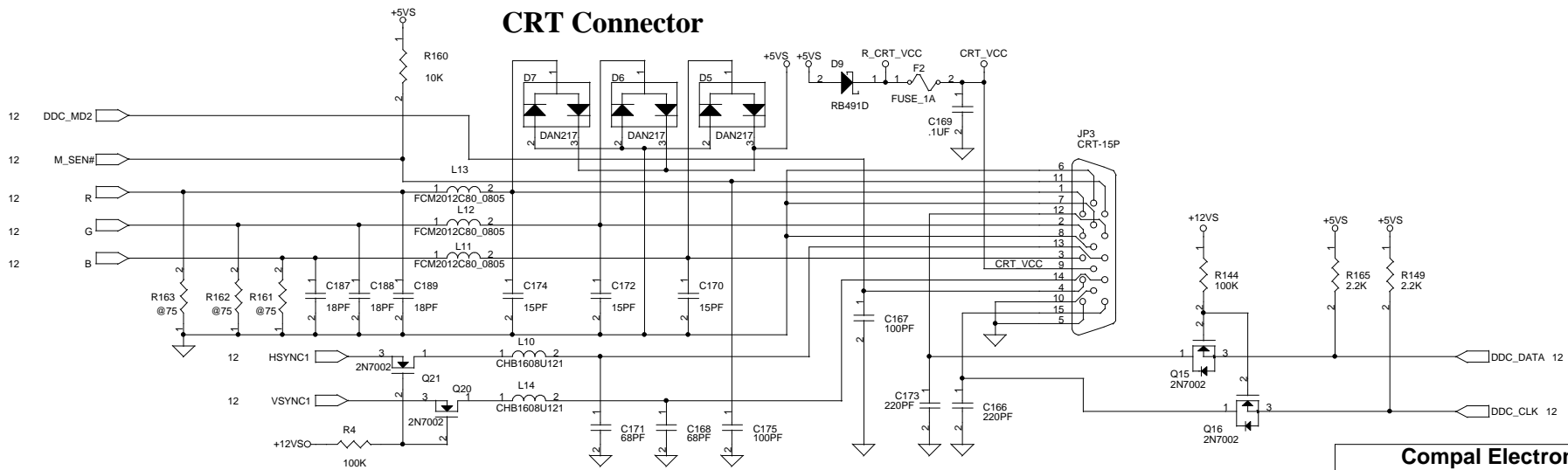
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Compal Electronics, Inc.

Title		VGA Connector & MDC Connector	
Size	Document Number	Rev	
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CRT Connector

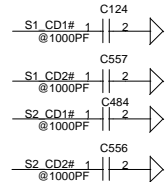
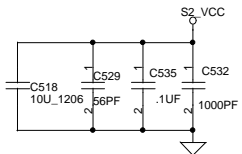
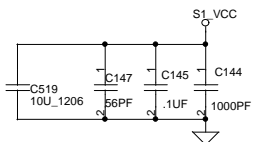
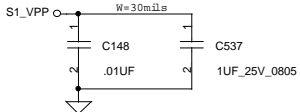
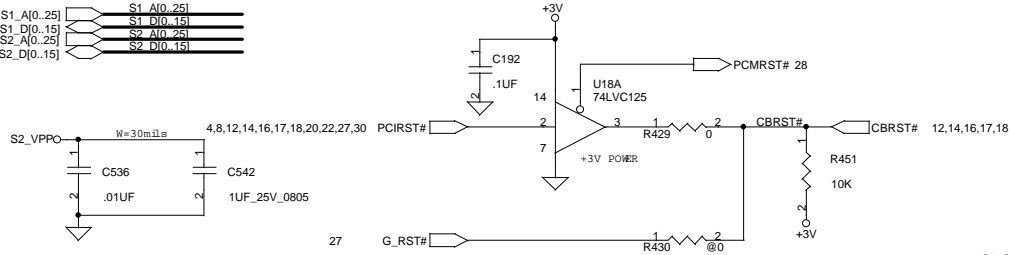
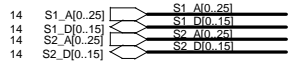
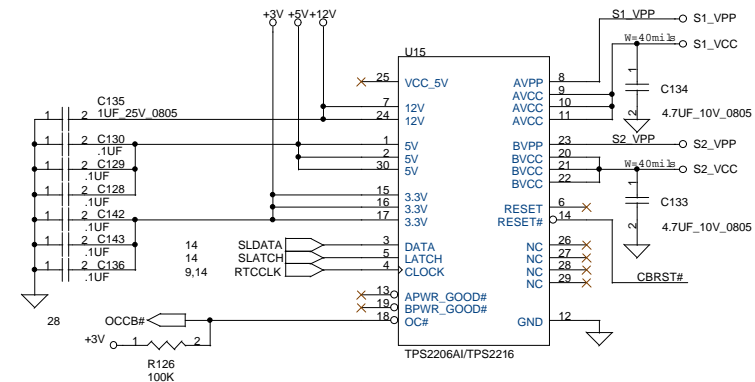


Compal Electronics, Inc.

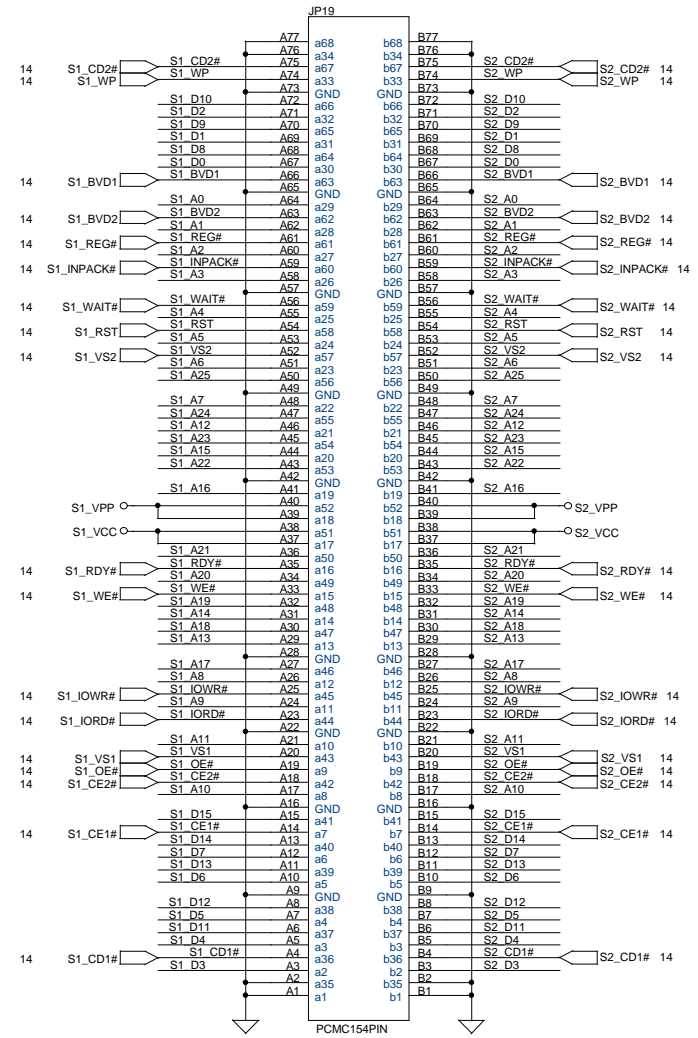
Title	CRT&TV-OUT Connector	
Size	Document Number	Rev
B	888M1	1.0
Date:	Tuesday, April 24, 2001	Sheet 13 of 38

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PCMCIA POWER CTRL.



CARDBUS SOCKET

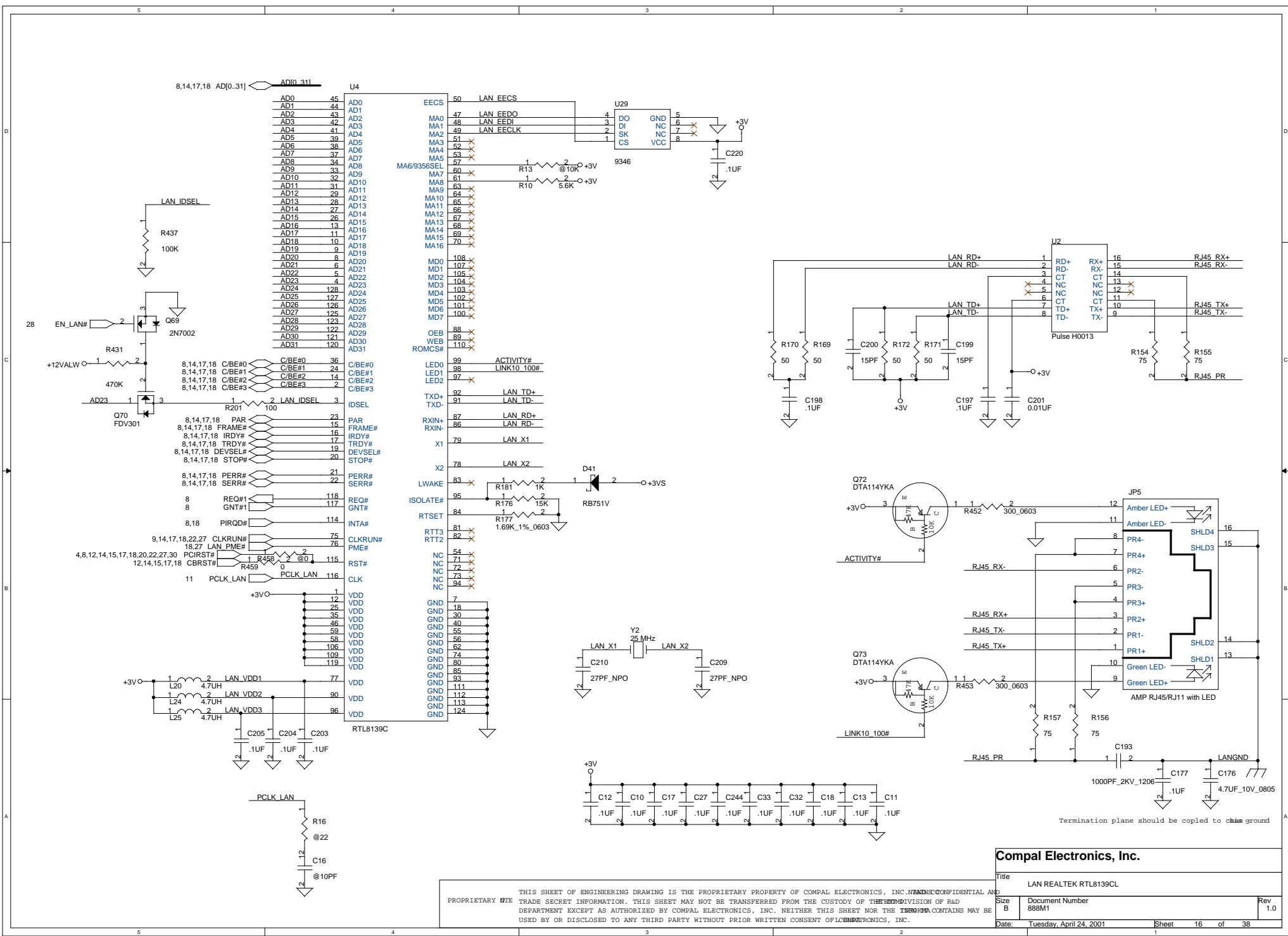


Compal Electronics, Inc

Title FCI PCMCIA SOCKET

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Compal Electronics, Inc.			
Title: LAN REALTEK RTL8139CL			
Size: B	Document Number: 888M1	Rev: 1.0	
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8,14,16,18 AD[0..31] AD[0..31]

AD26 1 2 1394 IDSEL
R398 100

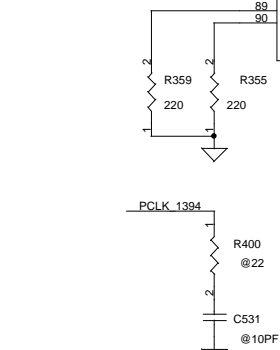
12,14,15,16,18 CBRST#

G_RST

GPIO3

GPIO2

PCLK 1394
R400 @22
C531 @10PF



U49
TSB43AB22

AD31	22	PCI_AD31
AD30	24	PCI_AD30
AD29	25	PCI_AD29
AD28	26	PCI_AD28
AD27	28	PCI_AD27
AD26	29	PCI_AD26
AD25	31	PCI_AD25
AD24	32	PCI_AD24
AD23	37	PCI_AD23
AD22	38	PCI_AD22
AD21	40	PCI_AD21
AD20	41	PCI_AD20
AD19	42	PCI_AD19
AD18	43	PCI_AD18
AD17	45	PCI_AD17
AD16	46	PCI_AD16
AD15	61	PCI_AD15
AD14	63	PCI_AD14
AD13	66	PCI_AD13
AD12	67	PCI_AD12
AD11	69	PCI_AD11
AD10	70	PCI_AD10
AD9	71	PCI_AD9
AD8	74	PCI_AD8
AD7	76	PCI_AD7
AD6	77	PCI_AD6
AD5	79	PCI_AD5
AD4	80	PCI_AD4
AD3	81	PCI_AD3
AD2	82	PCI_AD2
AD1	84	PCI_AD1
AD0	84	PCI_AD0
C/BE#3	34	PCI_C/BE3
C/BE#2	47	PCI_C/BE2
C/BE#1	60	PCI_C/BE1
C/BE0	73	PCI_C/BE0
PCLK 1394	16	PCI_CLK
GNT#2	18	PCI_GNT
REQ#2	19	PCI_REQ
1394 IDSEL	36	PCI_IDSEL
FRAME#	49	PCI_FRAME
IRDY#	50	PCI_IRDY
TRDY#	52	PCI_TRDY
DEVSEL#	53	PCI_DEVSEL
STOP#	54	PCI_STOP
PERR#	56	PCI_PERR
PIRQC#	13	PCI_PERR
1394 PME#	21	PCI_PME/CSTSCHG
SERR#	57	PCI_SERR
PAR	58	PCI_PAR
CLKRUN#	12	PCI_CLKRUN
PCIRST#	85	PCI_RST

TSB43AB22

PCI BUS INTERFACE

PHY PORT 2

BIAS CURRENT

OSCILLATOR

FILTER

EEPROM 2 WIRE BUS

POWER CLASS

PHY PORT 1

TEST9

TEST8

TEST3

TEST2

TEST1

TEST0

TEST9

TEST8

TEST3

TEST2

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CPS

TPBIAS1
TPA1+
TPA1-
TPB1+
TPB1-

R0

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X0

X1

FILTER0
FILTER1

SDA
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PC0
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TPBIAS0
TPA0+
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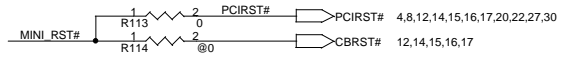
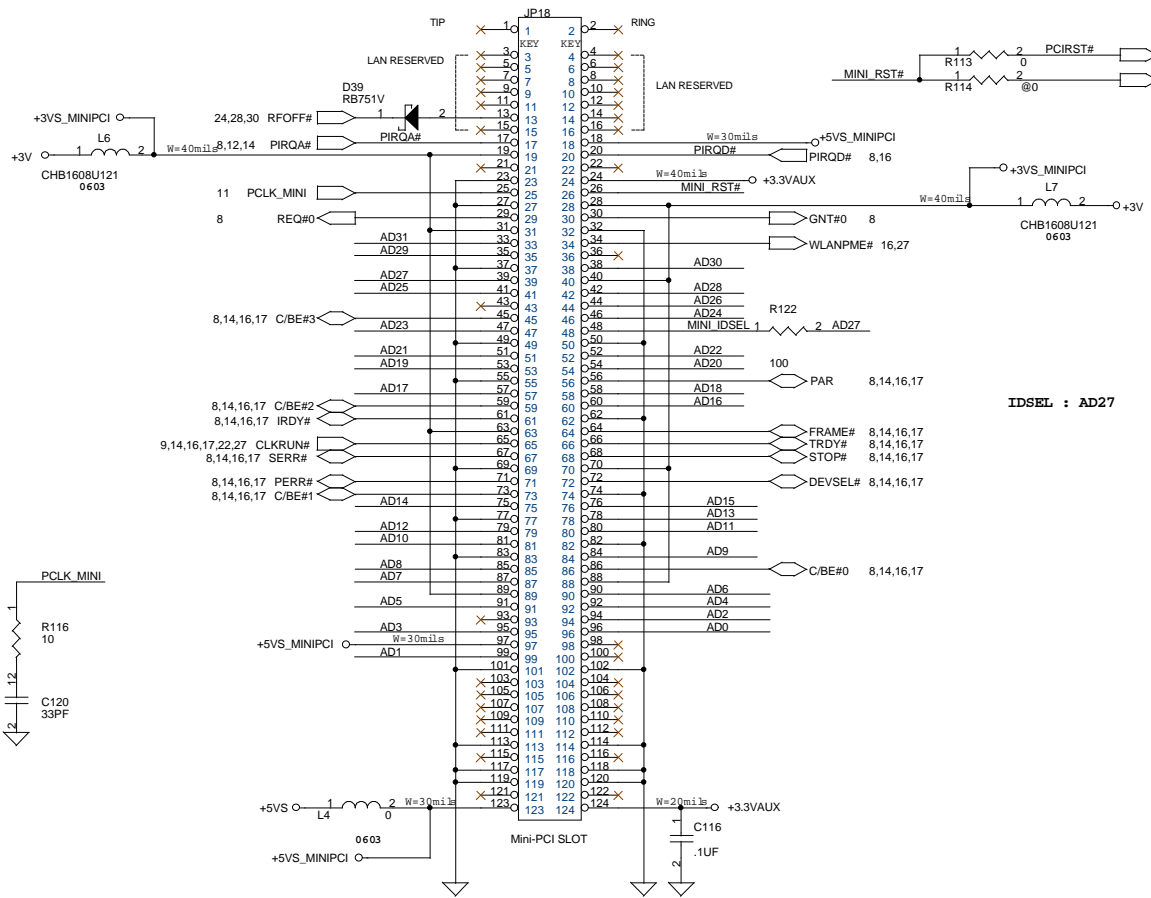
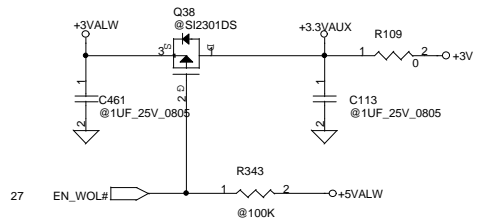
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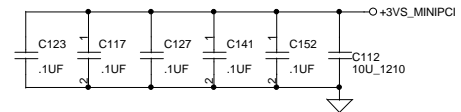
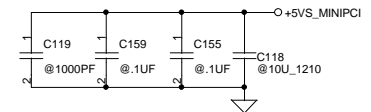
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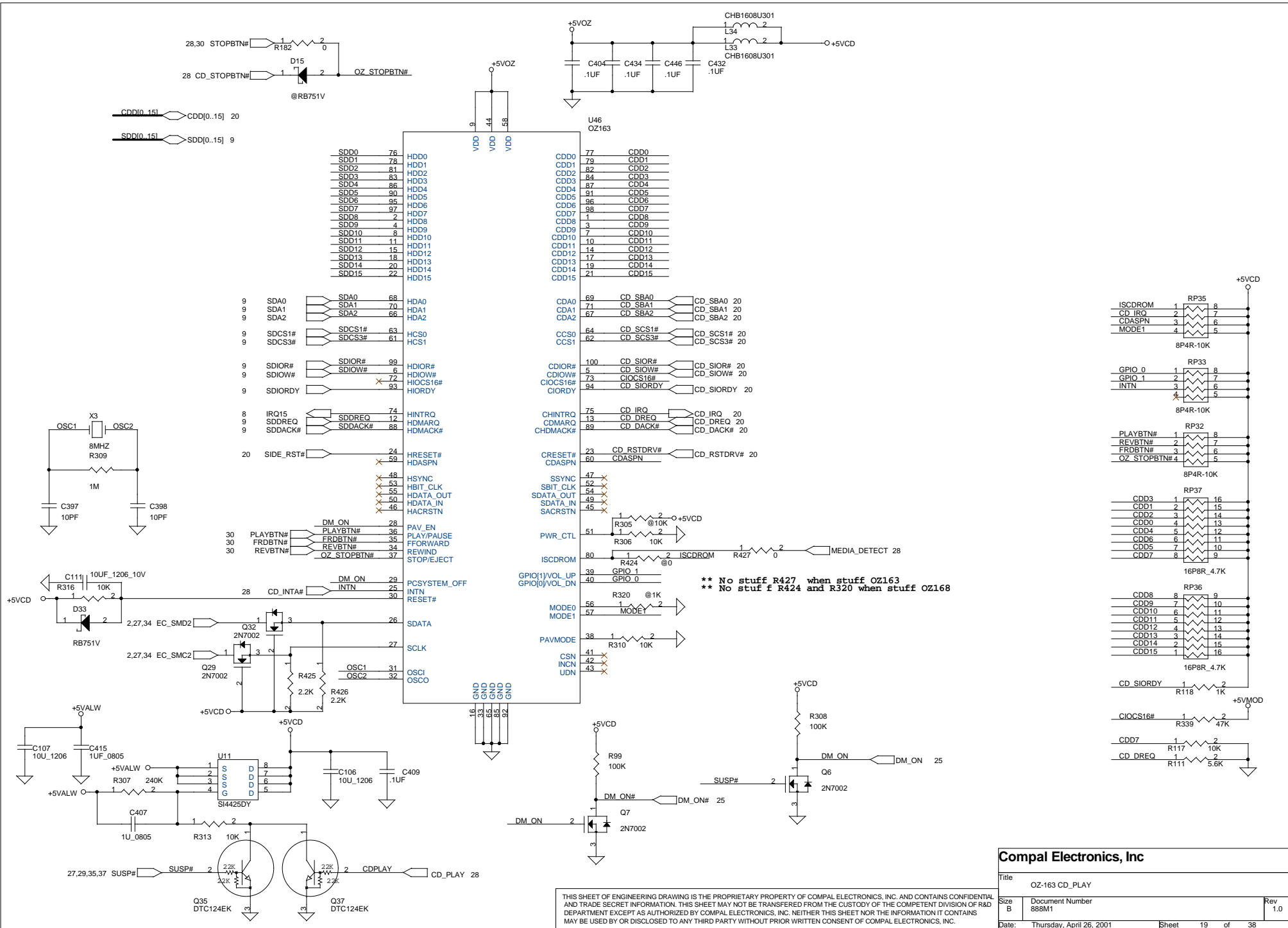
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AD[0..31] AD[0..31] 8,14,16,17

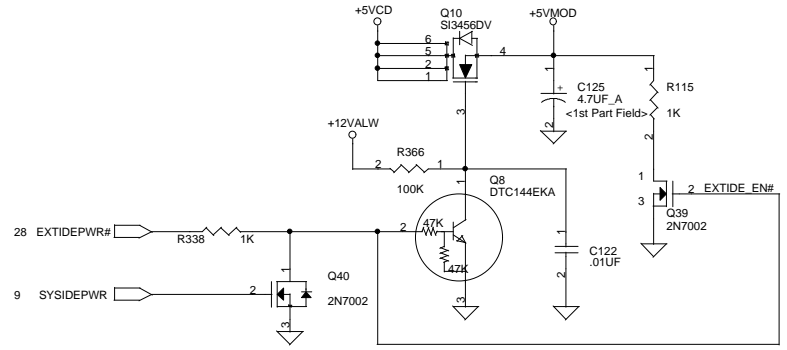
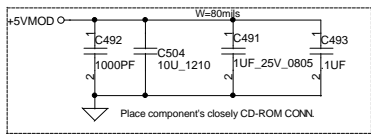
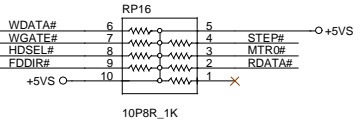
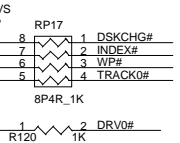
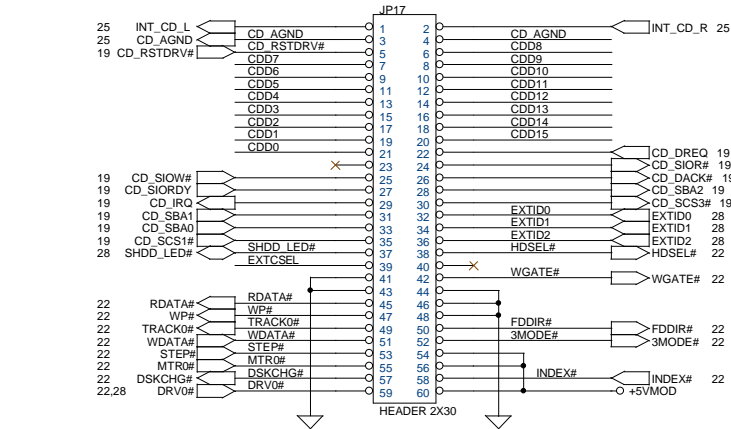
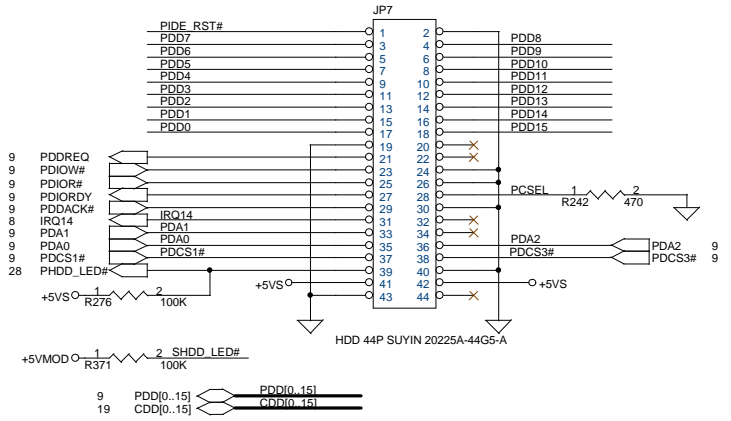
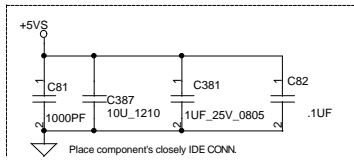
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Compal Electronics, Inc		
Title MINI_PCI		
Size B	Document Number 888M1	Rev 1.0
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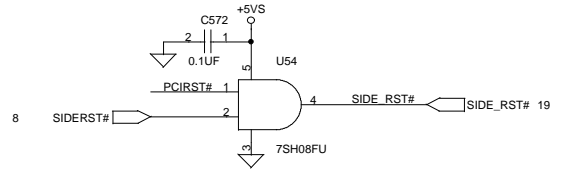
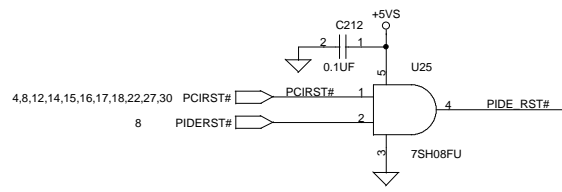


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IDE,CD-ROM Module CONN.

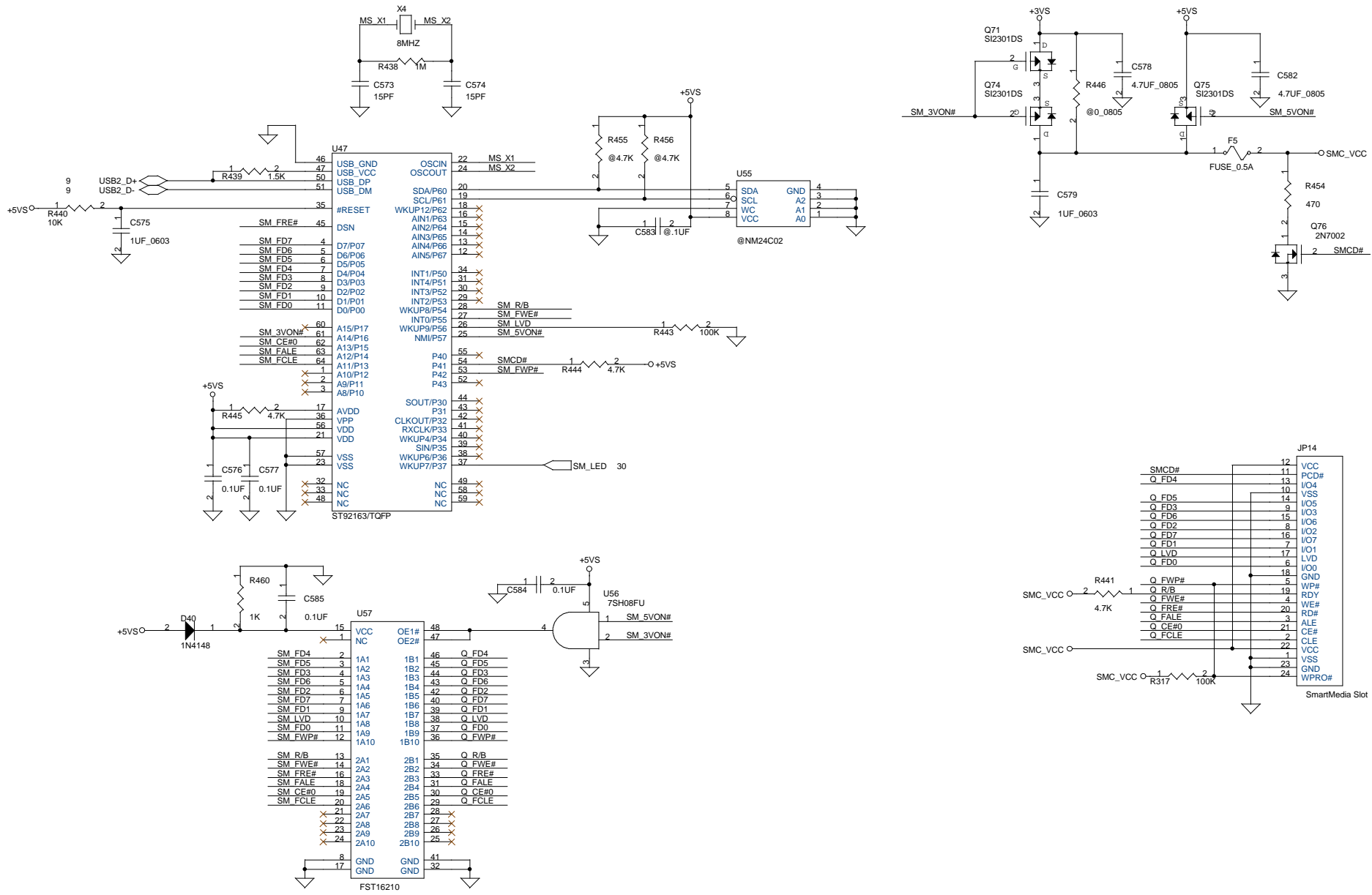


SI3456DV: N CHANNEL
VGS: 4.5V, RDS: 65 mOHM
Id(MAX): 5.1A
VGS,+20V



Compal Electronics, Inc		
Title	IDE/ FDD MODULE CONN.	
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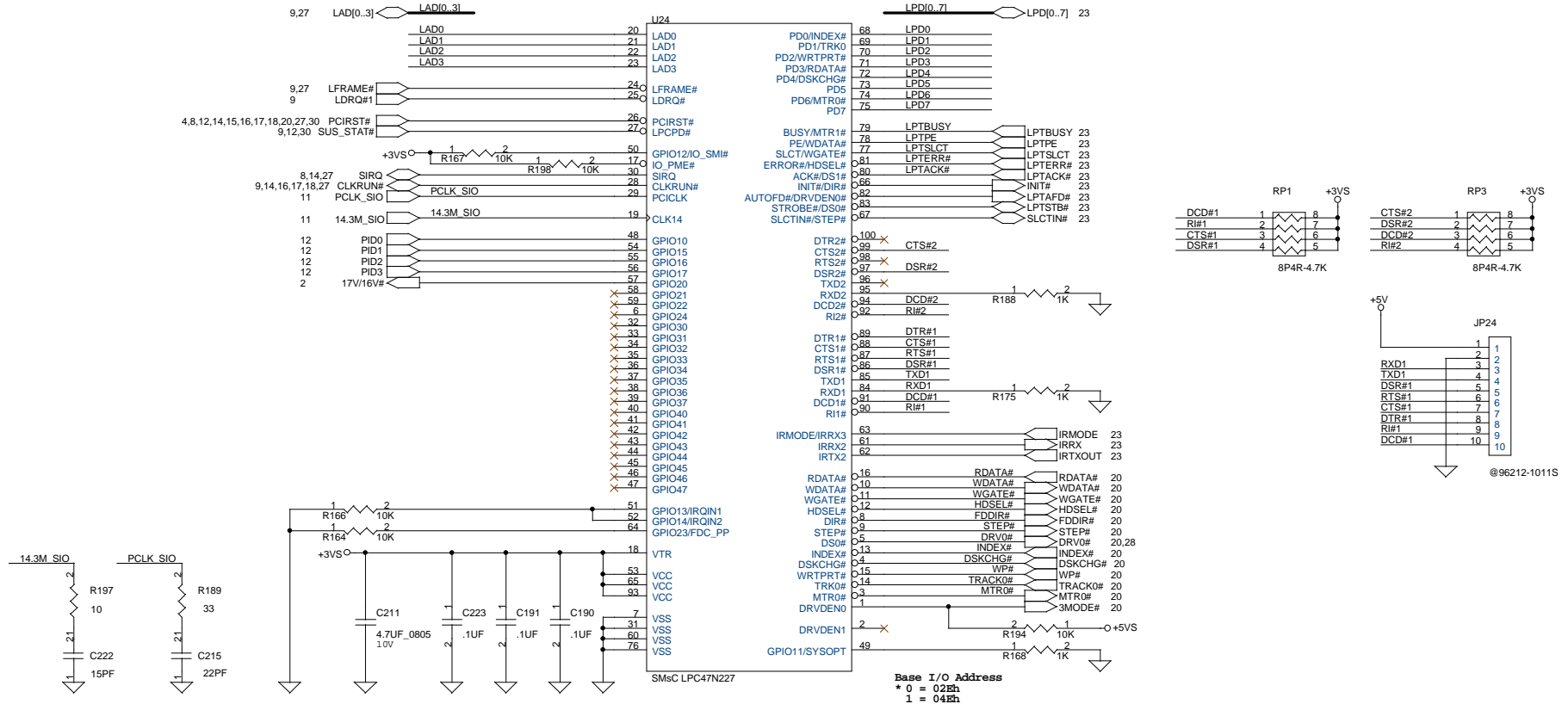


Compal Electronics, Inc.

Title		SmartMedia Interface	
Size	B	Document Number	888M1
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SUPER I/O SMsC FDC47N227

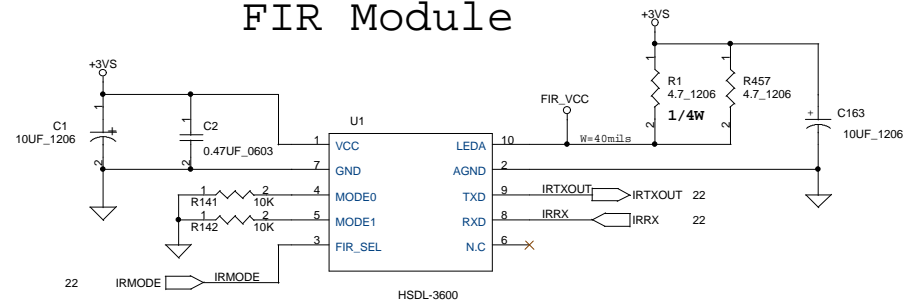


Compal Electronics, Inc.

Title		SUPER I/O	
Size	Document Number	888M1	
Date:	Tuesday, April 24, 2001	Sheet	22 of 38

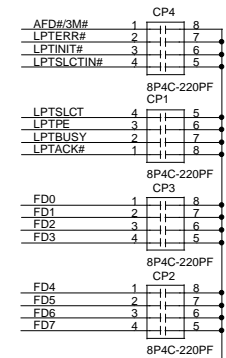
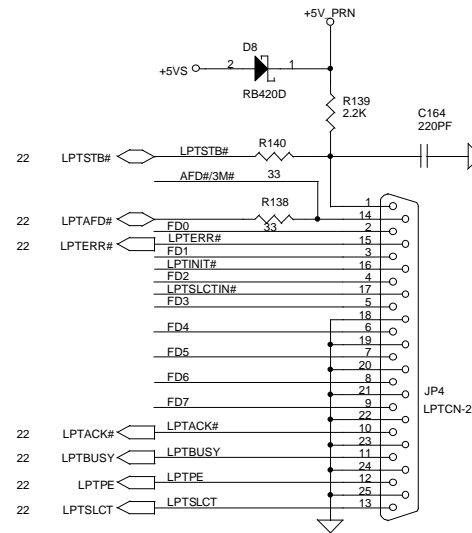
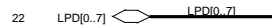
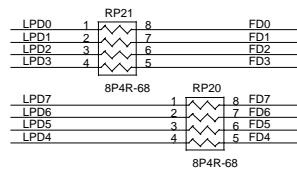
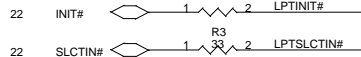
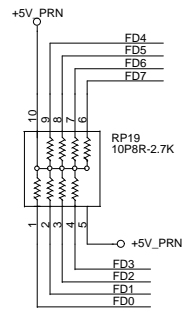
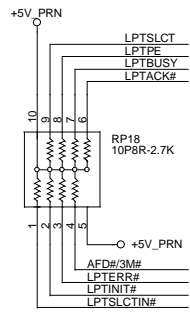
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FIR Module



The component's most place
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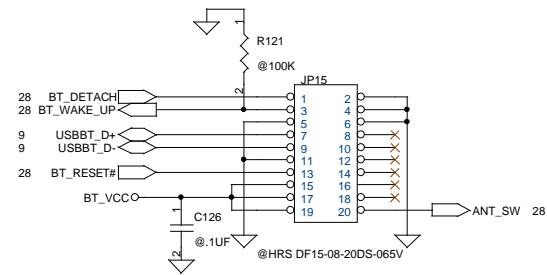
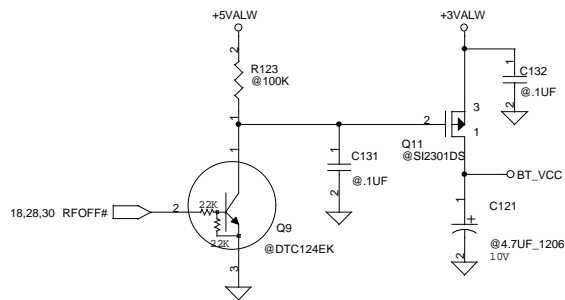
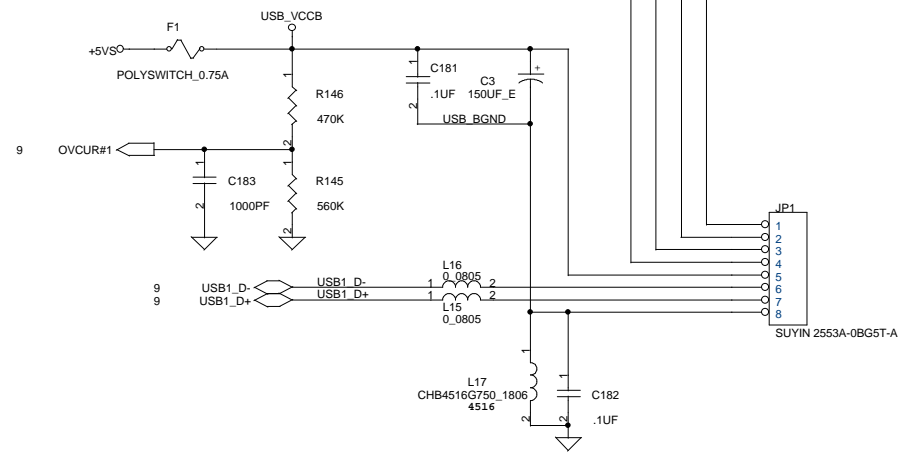
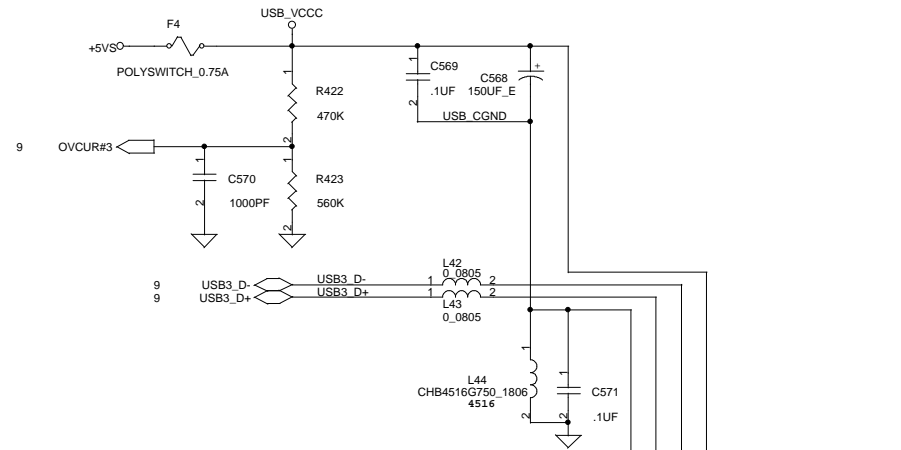
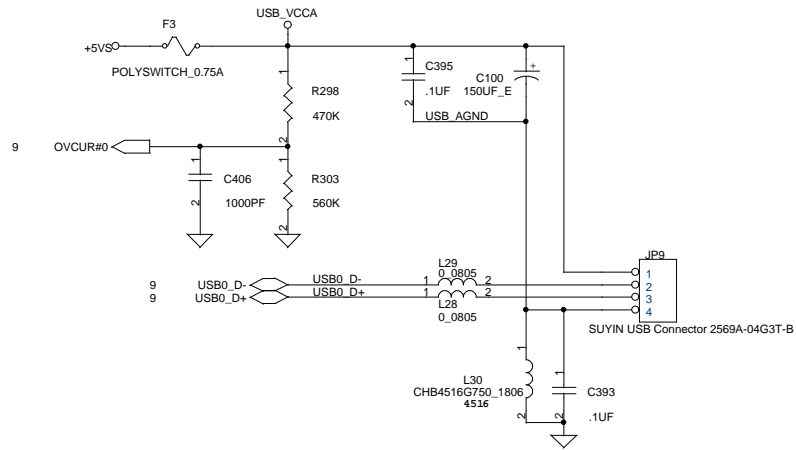
PARALLEL PORT



Compal Electronics, Inc.

Title		
PARALLEL PORT		
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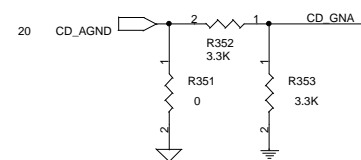
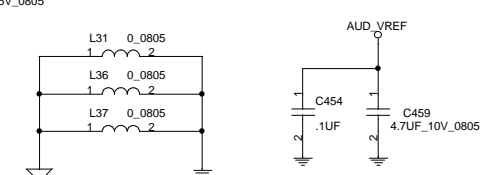
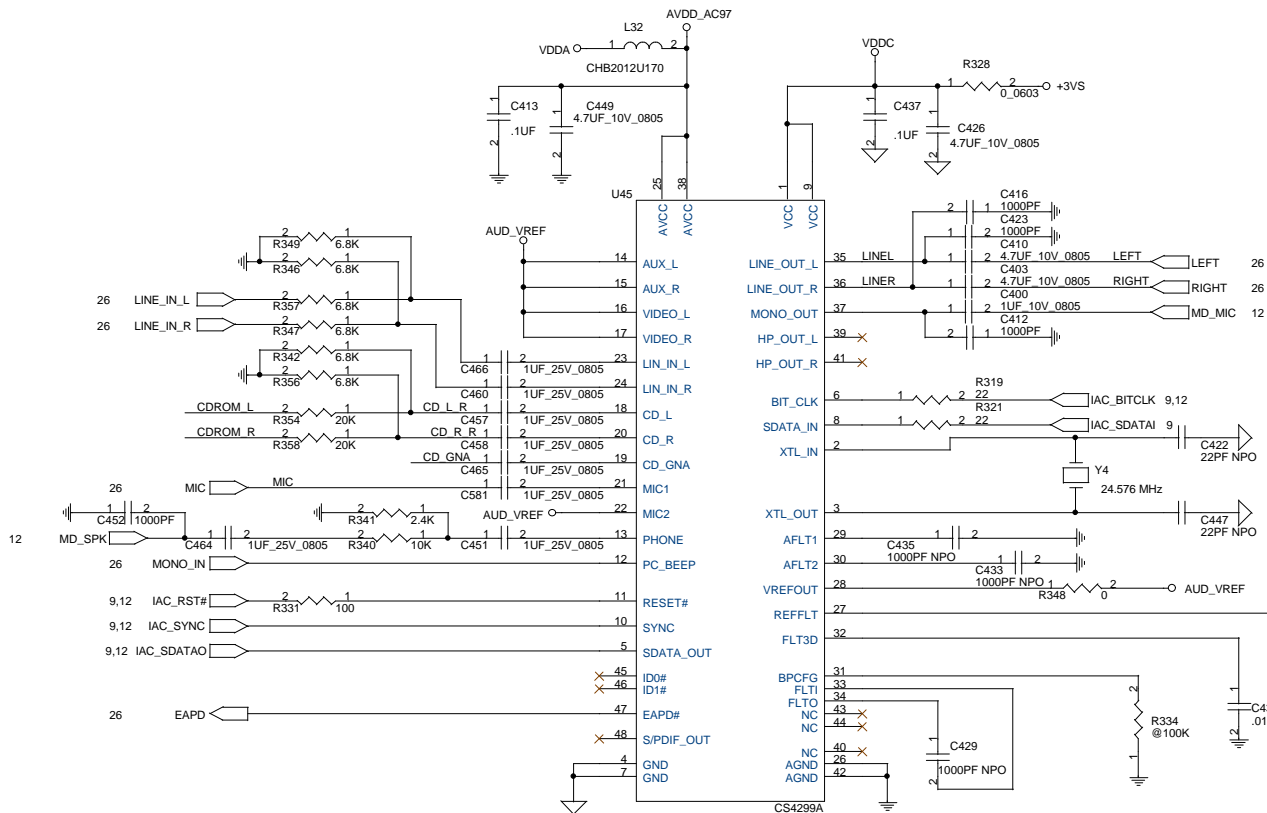
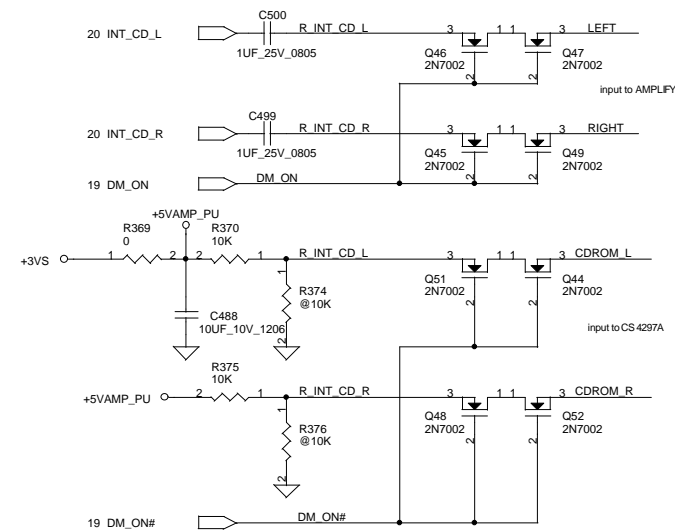
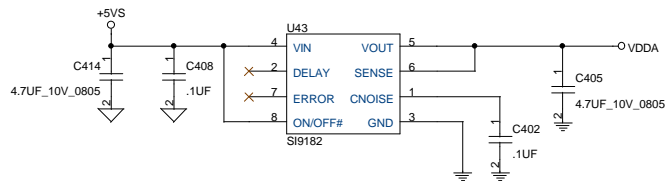


Compal Electronics, Inc

Title		USB & FIR
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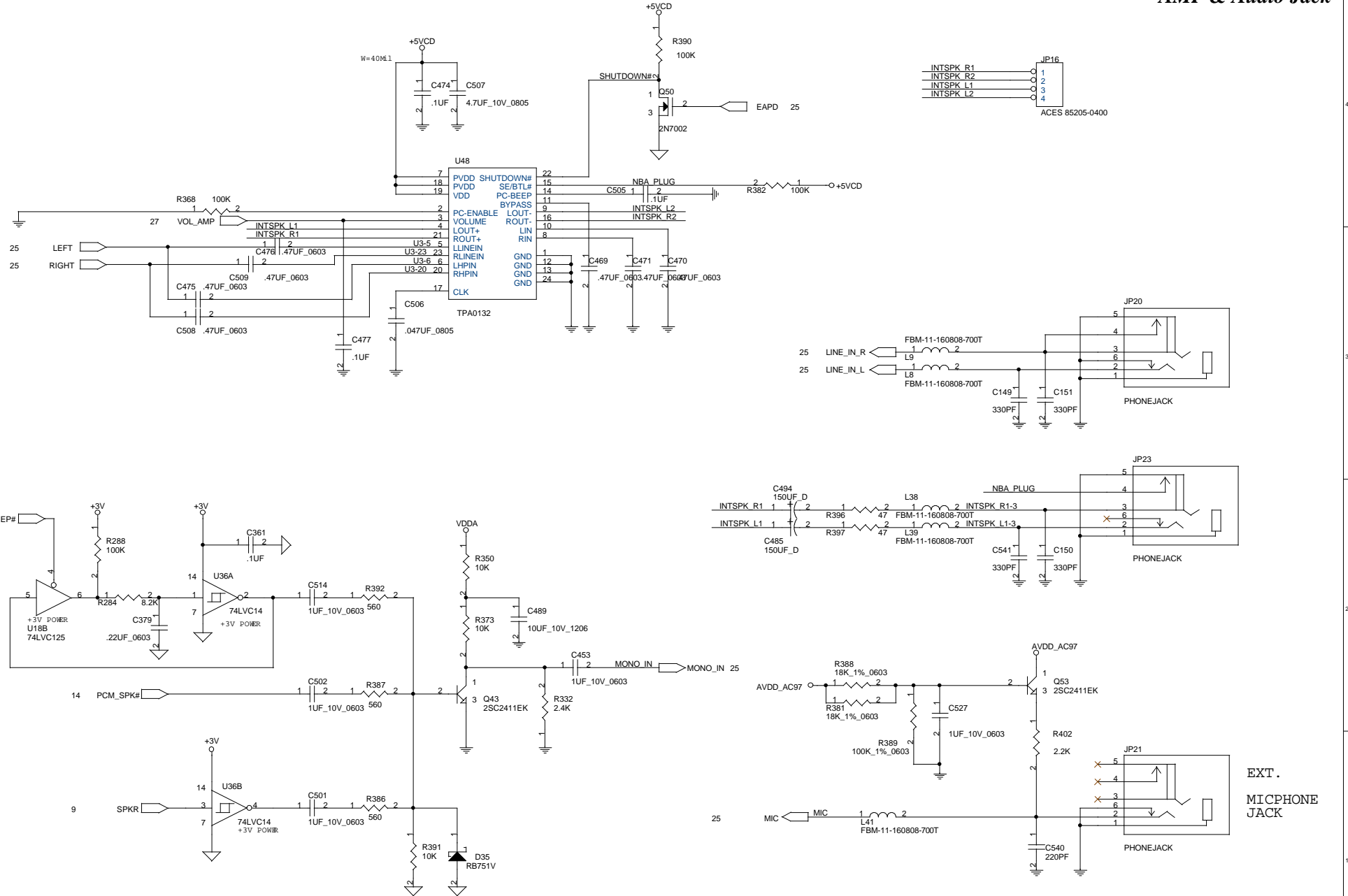
AC97 Codec



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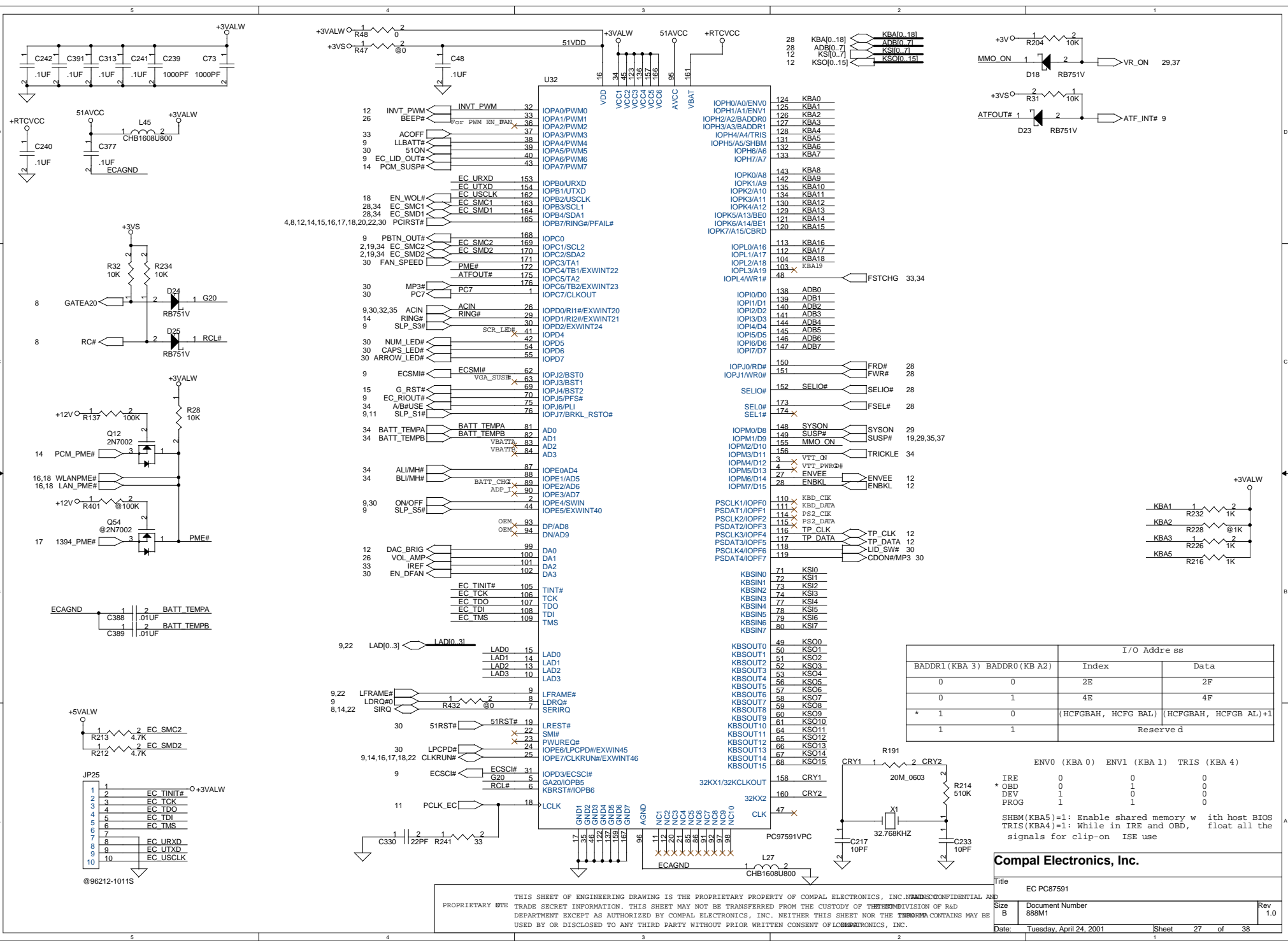
Compal Electronics, Inc.			
Title AC97 CODEC			
Size B	Document Number 888M1	Rev 1.0	
Date Tuesday, April 24, 2001	Sheet 25	of 38	

AMP & Audio Jack



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Compal Electronics, Inc.		
AMP & Audio Jack		
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U32

12	INVT_PWM	32	IOPA0/PWM0	124	KBA0
26	BEEP#	33	IOPA1/PWM1	125	KBA1
		36	IOPA2/PWM2	126	KBA2
33	ACOFF	37	IOPA3/PWM3	127	KBA3
9	LLBATT#	38	IOPA4/PWM4	128	KBA4
30	51ON	39	IOPA5/PWM5	131	KBA5
9	EC_LID_OUT#	40	IOPA6/PWM6	132	KBA6
14	PCM_SUSP#	43	IOPA7/PWM7	133	KBA7
			IOPH7/A7		
	EC_URXD	153	IOPK0/A8	143	KBA8
	EC_UTXD	154	IOPK1/A9	142	KBA9
18	EN_WOL#	162	IOPB1/UTXD	135	KBA10
28,34	EC_SMC1	163	IOPB2/USCLK	134	KBA11
28,34	EC_SMD1	164	IOPB3/SCL1	130	KBA12
	PCIRST#	165	IOPB4/SDA1	129	KBA13
			IOPB7/RING#/PFAIL#	121	KBA14
			IOPC0	120	KBA15
9	PBTN_OUT#	168	IOPC0		
2,19,34	EC_SMC2	169	IOPC1/SCL2	113	KBA16
2,19,34	EC_SMD2	170	IOPC2/SDA2	112	KBA17
30	FAN_SPEED	171	IOPC3/TA1	104	KBA18
			IOPC4/TB1/EXWINT22	103	KBA19
30	MP3#	172	IOPC5/TA2	48	FSTCHG
30	PC7	176	IOPC6/TB2/EXWINT23		
			IOPC7/CLKOUT		
9,30,32,35	ACIN	26	IOPD0/RI1#/EXWINT20	138	ADB0
14	RING#	29	IOPD1/RI2#/EXWINT21	139	ADB1
9	SLP_S3#	30	IOPD2/EXWINT24	140	ADB2
	SCR_LED#	41	IOPD3/D3	141	ADB3
30	NUM_LED#	42	IOPD4	144	ADB4
30	CAPS_LED#	54	IOPD5	145	ADB5
30	ARROW_LED#	55	IOPD6	146	ADB6
			IOPD7	147	ADB7
9	ECSM#	62	IOPJ0/RD#	150	FRD#
	VGA_SUSP#	63	IOPJ1/WRO#	151	FWR#
15	G_RST#	69			
9	EC_RIOUT#	70	SELIO#	152	SELIO#
34	A/B#USE	75	SEL0#	173	FSEL#
9,11	SLP_S1#	76	SEL1#	174	
34	BATT_TEMP#	81	IOPM0/D8	148	SYSON
34	BATT_TEMP#	82	IOPM1/D9	149	SUSP#
			IOPM2/D10	155	MMO_ON
			IOPM3/D11	156	TRICKLE
			IOPM4/D12	3	VTT_ON
			IOPM5/D13	4	VTT_PWRGD#
			IOPM6/D14	27	ENVEE
			IOPM7/D15	28	ENBKL
			PSCLK1/OPF0	110	KBD_CLK
			PSDAT1/OPF1	111	KBD_DATA
			PSCLK2/OPF2	114	PS2_CLK
			PSDAT2/OPF3	115	PS2_DATA
			PSCLK3/OPF4	116	TP_CLK
			PSDAT3/OPF5	117	TP_DATA
			PSCLK4/OPF6	118	LID_SW#
			PSDAT4/OPF7	119	CDON#/MP3
			KBSIN0	71	KSIO
			KBSIN1	72	KSIO
			KBSIN2	73	KSIO
			KBSIN3	74	KSIO
			KBSIN4	77	KSIO
			KBSIN5	78	KSIO
			KBSIN6	79	KSIO
			KBSIN7	80	KSIO
			KBSOUT0	49	KS00
			KBSOUT1	50	KS01
			KBSOUT2	51	KS02
			KBSOUT3	52	KS03
			KBSOUT4	53	KS04
			KBSOUT5	56	KS05
			KBSOUT6	57	KS06
			KBSOUT7	58	KS07
			KBSOUT8	59	KS08
			KBSOUT9	60	KS09
			KBSOUT10	61	KS010
			KBSOUT11	64	KS011
			KBSOUT12	65	KS012
			KBSOUT13	66	KS013
			KBSOUT14	67	KS014
			KBSOUT15	68	KS015

I/O Address			
BADDR1 (KBA 3)	BADDR0 (KB A2)	Index	Data
0	0	2E	2F
0	1	4E	4F
* 1	0	(HCFGBAH, HCFG BAL)	(HCFGBAH, HCFG AL)+1
1	1		Reserved

	ENV0 (KBA 0)	ENV1 (KBA 1)	TRIS (KBA 4)
IRE	0	0	0
* OBD	0	1	0
DEV	1	0	0
PROG	1	0	0

SHBM(KBA5)=1: Enable shared memory with host BIOS
 TRIS(KBA4)=1: While in IRE and OBD, float all the signals for clip-on ISE use

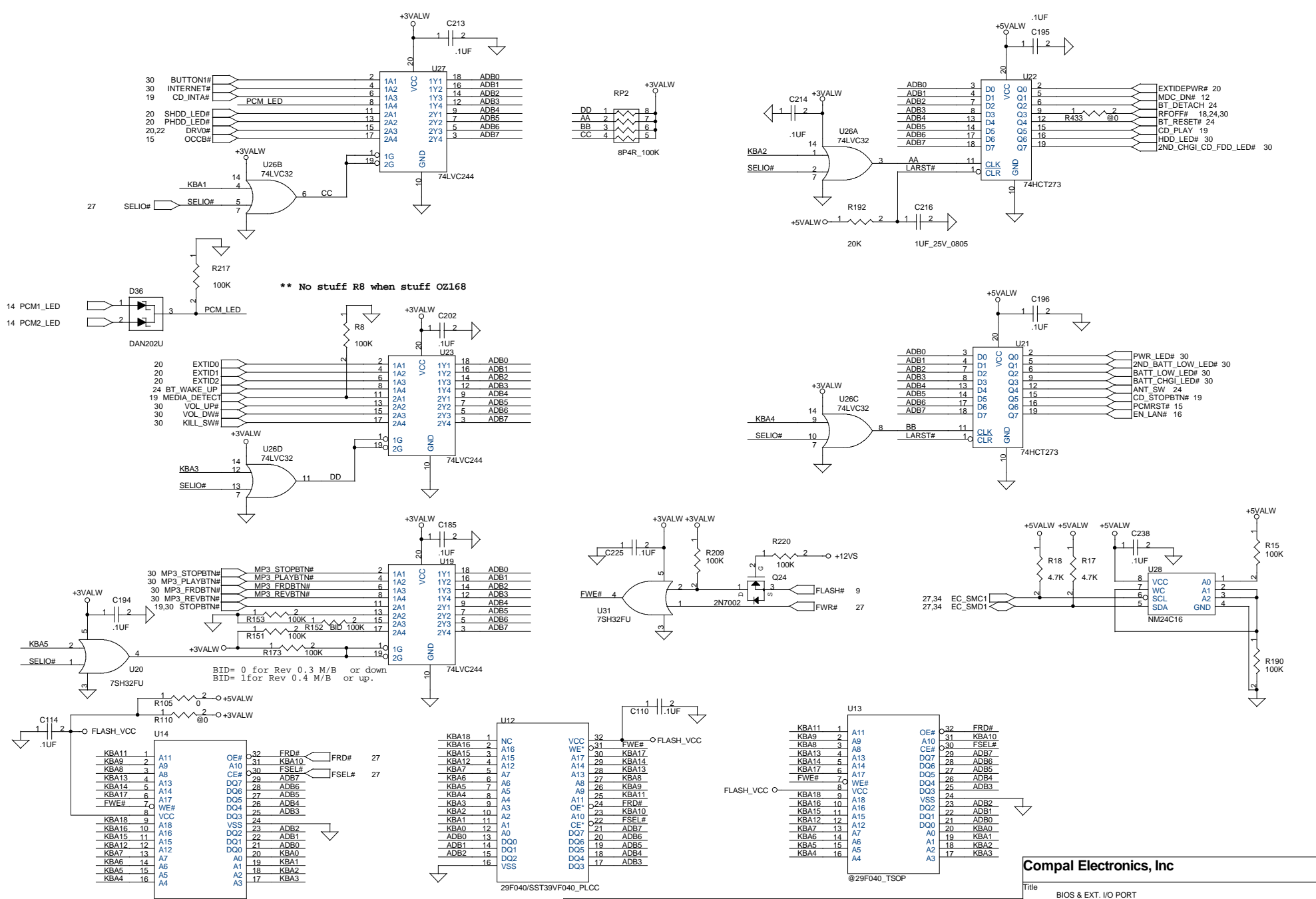
Compal Electronics, Inc.

Title: EC PC87591

Size: B Document Number: 888M1 Rev: 1.0

Date: Tuesday, April 24, 2001 Sheet: 27 of 38

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**** No stuff R8 when stuff OZ168**

BID= 0 for Rev 0.3 M/B or down
 BID= 1 for Rev 0.4 M/B or up.

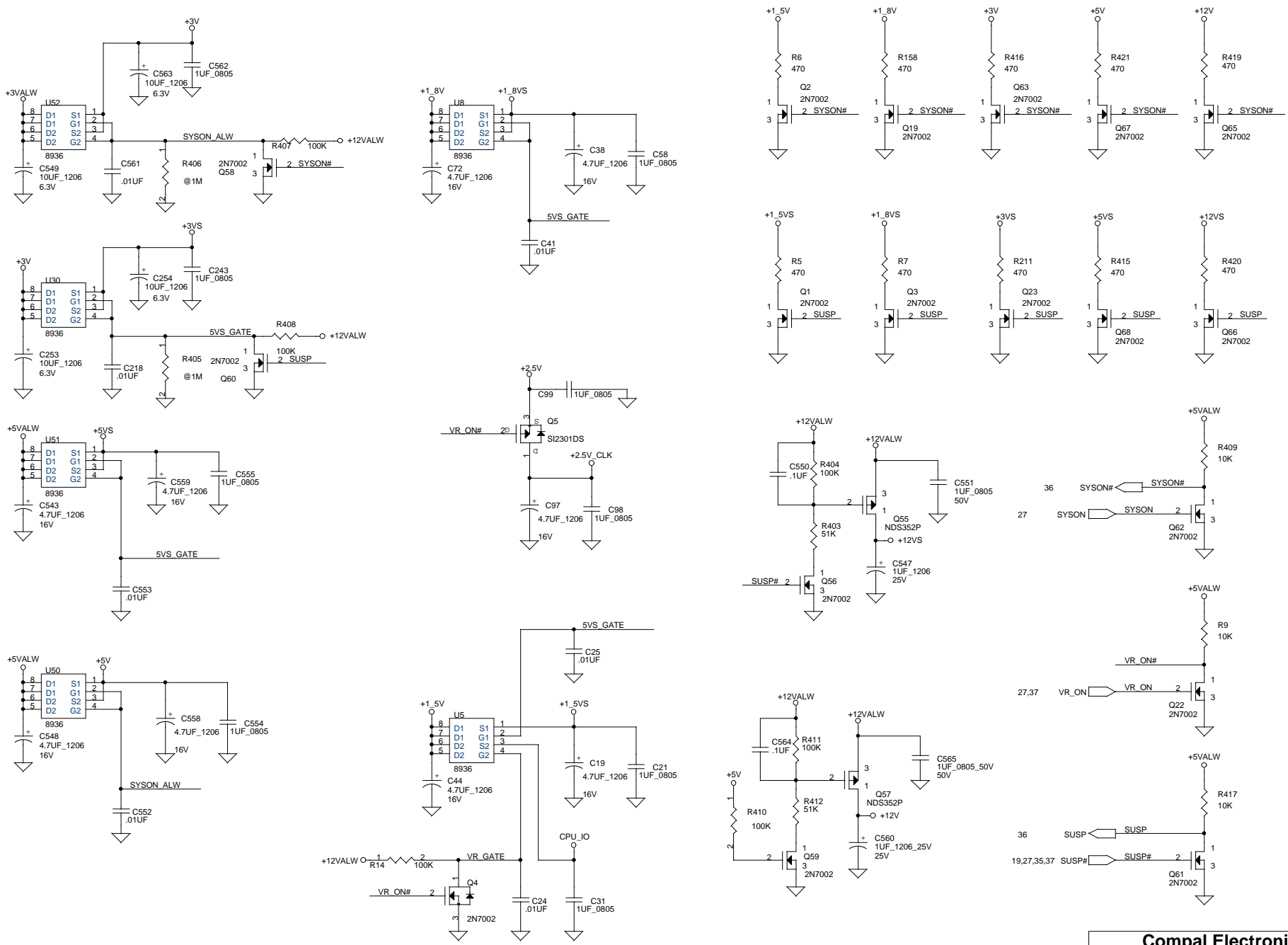
@SST39VF040_TSOP

@29F040_TSOP

27 KBA[0..18] KBA[0..18]
 27 ADB[0..7] ADB[0..7]

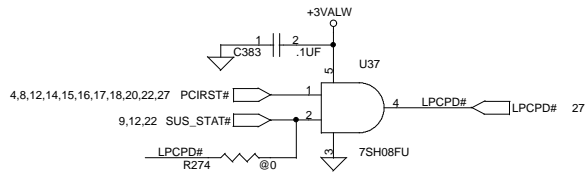
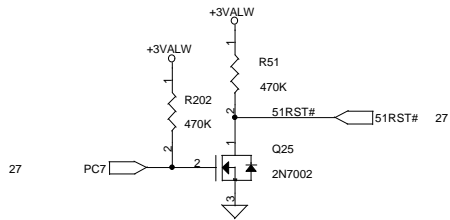
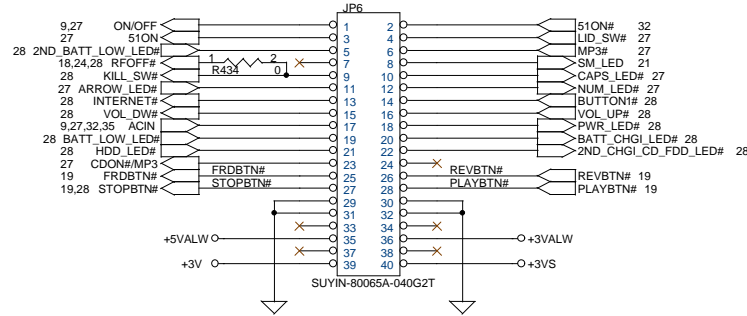
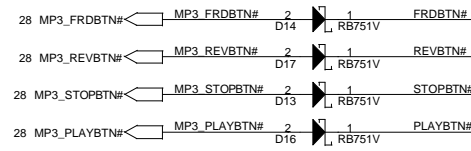
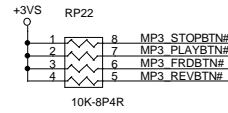
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Compal Electronics, Inc		
Title	BIOS & EXT. I/O PORT	
Size B	Document Number	Rev
	888M1	1.0
Date:	Wednesday, April 25, 2001	Sheet 28 of 38

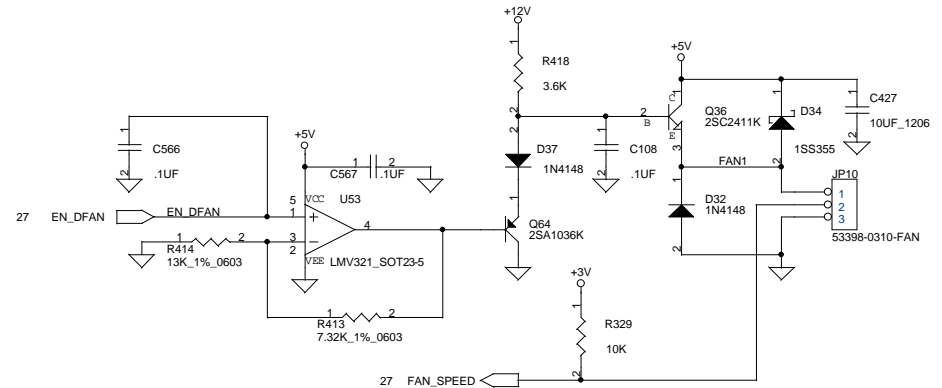


Compal Electronics, Inc.		
Title	POWER CONTROL CKT	
Size	Document Number	Rev
B	888M1	1.0
Date:	Tuesday, April 24, 2001	Sheet 29 of 38

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For PC87591 REV 0.A Only

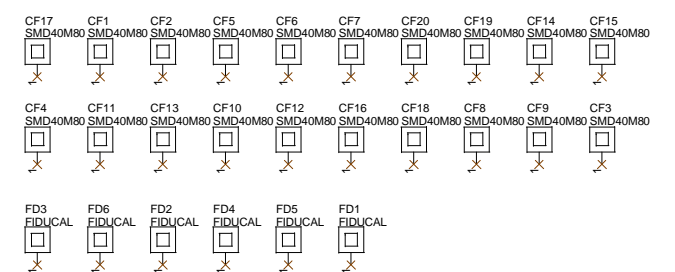
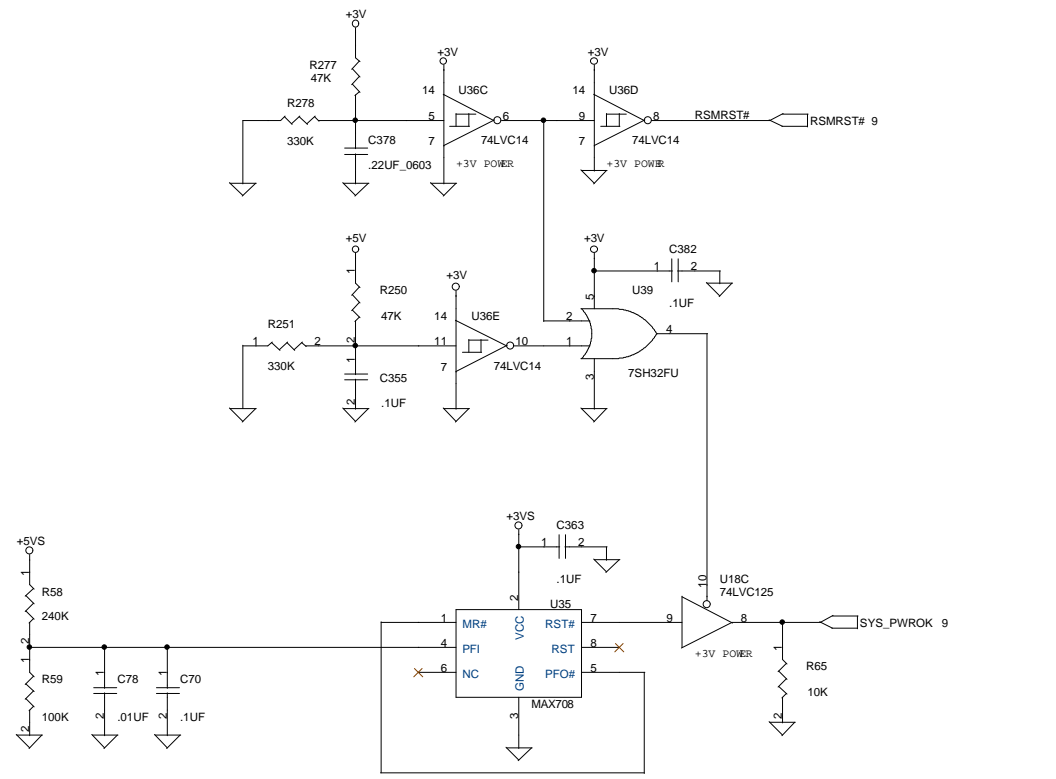
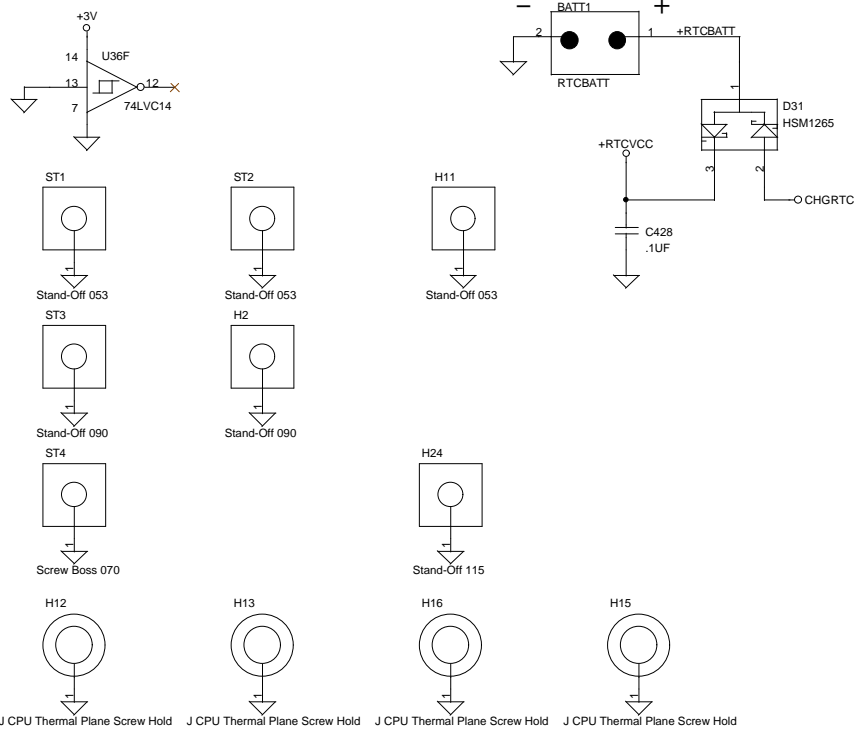


Compal Electronics, Inc.

Title			Switches & Connectors		
Size	Document Number	Rev			
B	888M1	1.0			
Date:	Tuesday, April 24, 2001	Sheet	30	of	38

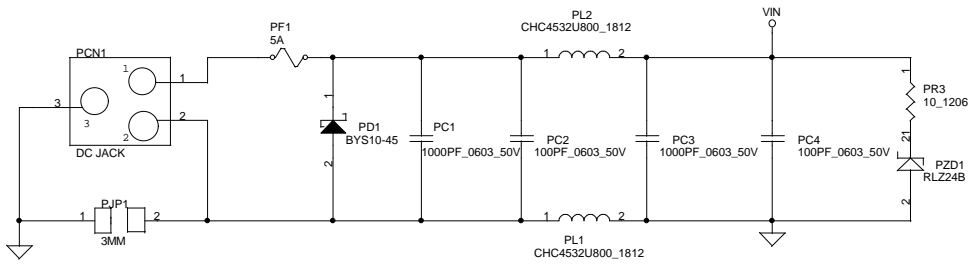
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RTC BATT

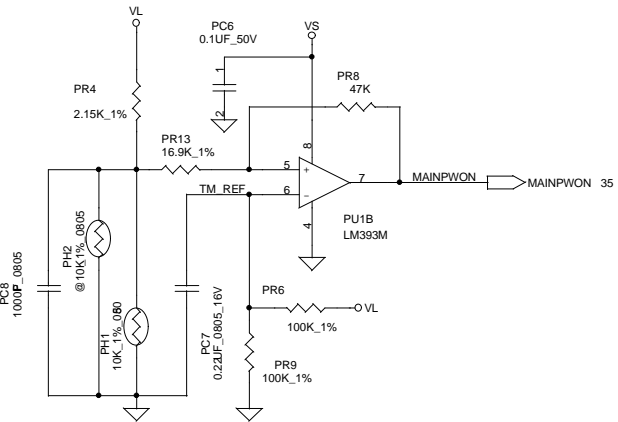


Compal Electronics, Inc.			
Title		RESET	
Size	Document Number	Rev	
B	888M1	1.0	
Date:	Tuesday, April 24, 2001	Sheet	31 of 38

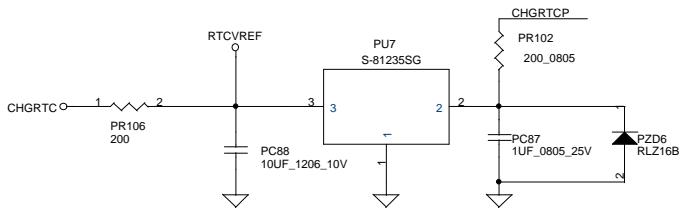
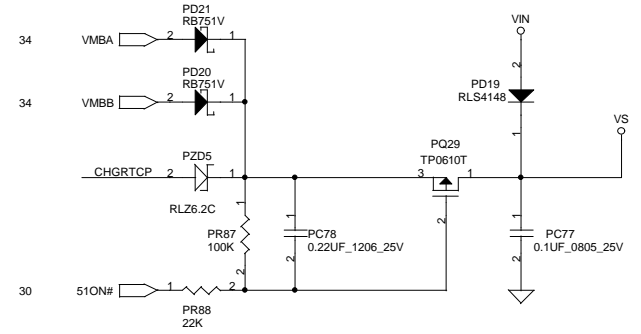
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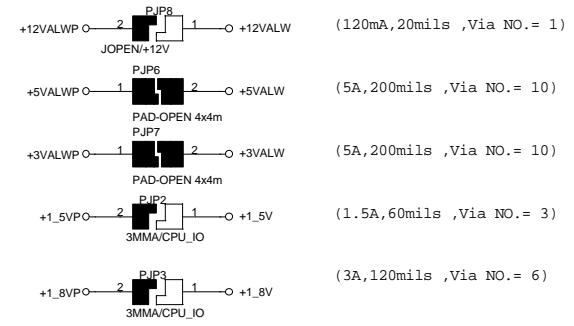
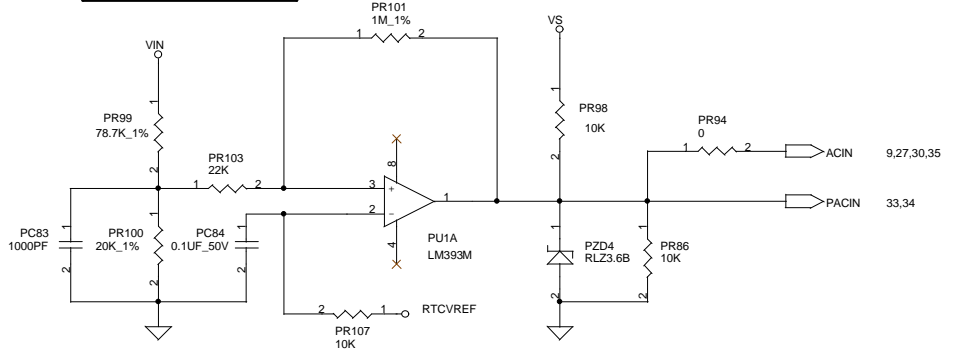
CPU thermal protection at 99 degree C
Recovery at 51 degree C



PH1 under CPU side
PH2 close to RAM door

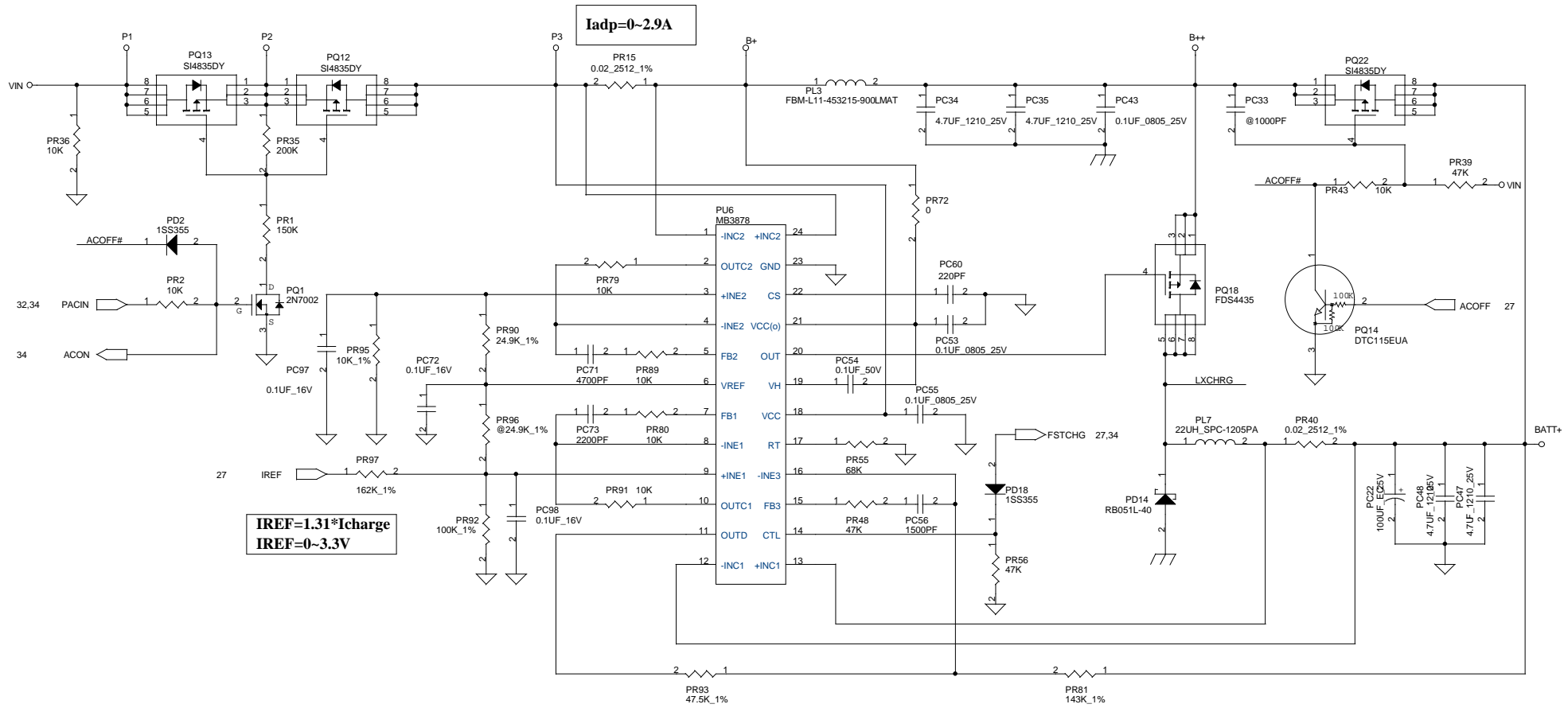


Vin Detector
High 18.7 17.9 17.1
Low 18.0 17.3 16.5



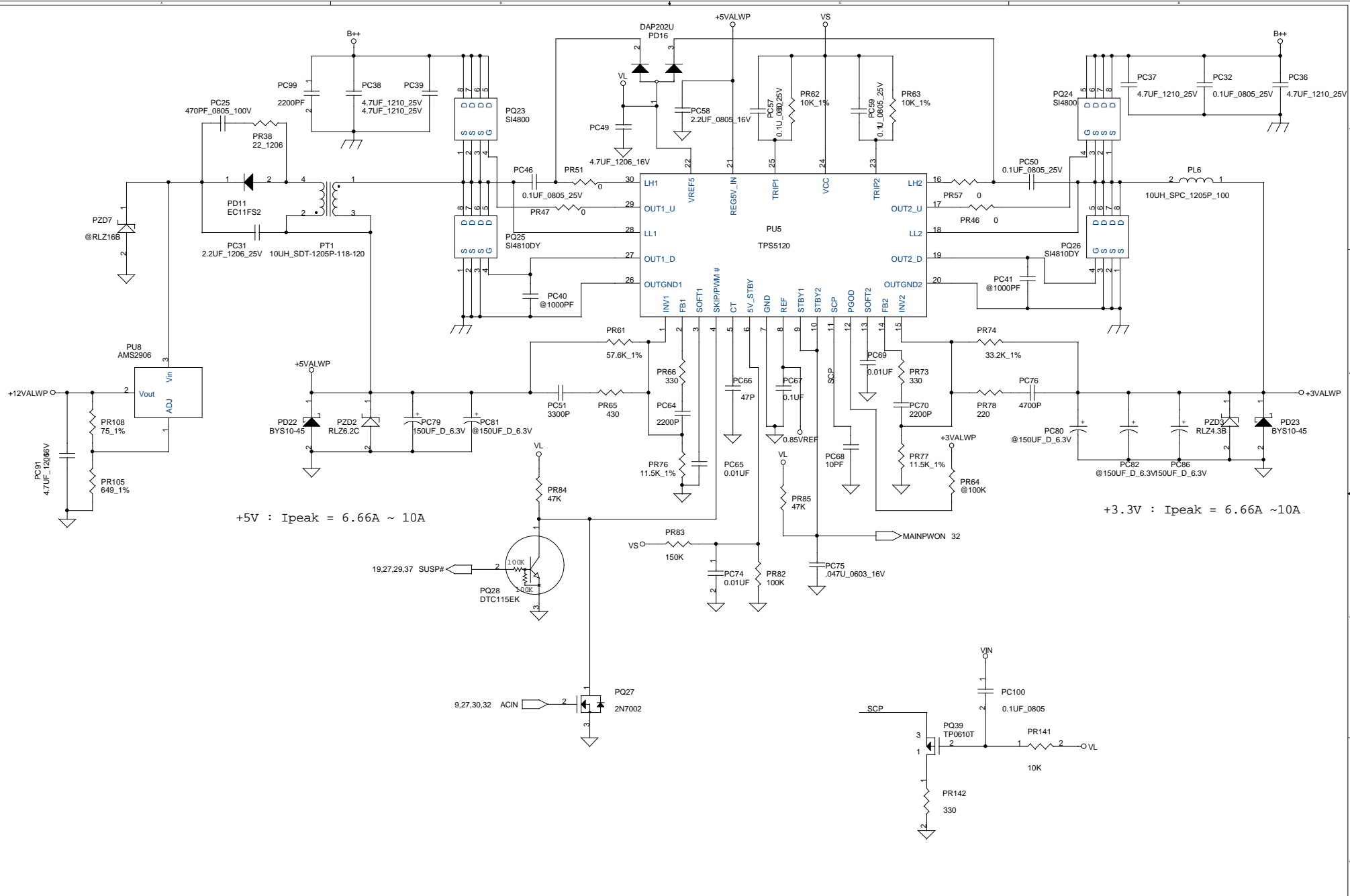
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COMPAL ELECTRONICS, INC		
Title Connector / DC-DC Interface		
Size B	Document Number 888M1	Rev 1.0
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COMPAL ELECTRONICS, INC		
Title	CHARGER	
Size	Document Number	Rev
B	888M1	1.0
Date:	Tuesday, April 24, 2001	Sheet 33 of 38

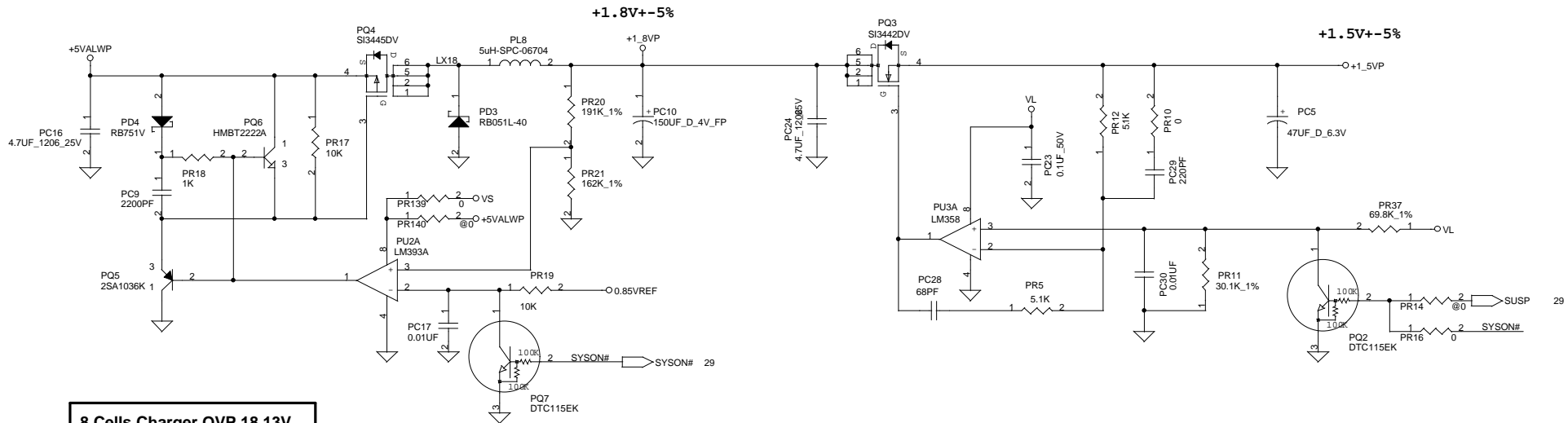


+5V : Ipeak = 6.66A ~ 10A

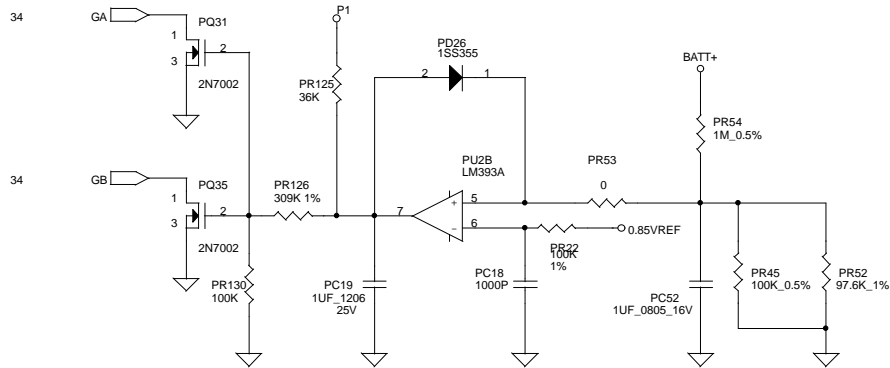
+3.3V : Ipeak = 6.66A ~10A

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COMPAL ELECTRONICS, INC			
Title		5V/3.3V/12V	
Size	Document Number	Rev	
B	888M1	1.0	
Date:	Tuesday, April 24, 2001	Sheet	35 of 38



8 Cells Charger OVP 18.13V



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COMPAL ELECTRONICS, INC		
Title		
1.8V/ CPU_IOP		
Size	Document Number	Rev
B	888M1	1.0
Date:	Tuesday, April 24, 2001	Sheet 36 of 38

888M1 PIR LIST

02/22/01 Written byferry

P04: Change VCC of Pull High to +3V at R186, R181&R4 and R185
P05: Remove @ atR266
P06: Change value of C256 to 100nF
Change VCC of Pull High to +3V R203
P07: Change VCC of U10 and C90 to +3V
P09: Remove @ atR45
Add @ 0 Ohm R435 and R436 between USB3_D+/- and USB2_D+/-
P10: Change VCC of R53 to 1.8V
Change VCC of R271 to +3V
P12: Change net to CBRST# at pin2 of JP8
P13: Delete net CRISIS# at pin5 of JP2
Delete Q18, Q17, R150, R159, R144 and R147
Change value of R160 to 10K
Change value of R144 to 10K
Change value of R149, R165 to 2.2K
Change VCC of Pull High to +5V at R160, R161 and R149
Change VCC of Pull High to +1.2V at R144
P14: Change net CBRST# at pin 1 of U16
P15: Change net to CBRST# at pin 1 of U15
Add 0 Ohm R429 between Pin3 of U15 and CBRST#
Add @ 0 Ohm R430 between G_RST# and CBRST#
Change net PCIRST# at pin4 of U18
Change net PCMRST# at pin4 of U18
P16: Add Q69, R431, R437 and Q70 to enable LAN
Change VCC of Pull High to +3V at R181
Delete C84
Short pin2 of R154 and R155 to pin1 of R154 and R157
P17: Change net CBRST# to pin4 of U49
Delete net between Pin 96 of U49 and Pin of RP38
P18: Add net RPOFF# at pin1 of JP18
Change net CBRST# at pin2 of R114
P19: Add @ at R82
Remove @ at D15
P20: Delete net SPDIAG# at pin2 of JP17
Delete net SIDE_PRES# at pin4 of JP17
Delete R289, R318, Q28, and Q33
Add U54 and C572 for SDRAM
P21: Change circuit of SmartMedia to 92163
P22: Delete C165, R143 and U17
P24: Change net RPOFF# at pin of Q9
Change net USBBT_D+/- at Pin7 and Pin of JP15
P25: Change value of R329 to 100K
P26: Change value of C494 and C485 to 150uF
Delete JP22 and change type of JP16 to 4 pin connector
Delete C385 and U40 and replace U18
P27: Add @ 0 Ohm R432 between LDRQ#0 and pin of U32
Add @ at R28
P28: Change net KILL_SW# at pin of U23
Add @ 0 Ohm R433 between pin9 of U22 and RPOFF#
Delete net BT_LED# at pin of U21
Add net PCMRST# at pin of U21
Add net EN_LAN# at pin of U21
P30: Delete C392, C384, U41 and U38
Change type of JP6 to B to B connector
Add 0 Ohm R434 between Pin 9 of JP6 and RPOFF#
Change net KILL_SW# at Pin of U6
Change net LBCPD# at pin of 37
P31: Delete U33 and R351
Delete Q26 and Replace L8C
Change value of C378 to 0.022 0603
Change type of H11 to SMD-OPF
Pin5 tied to Pin1 of J35
P32: Change PCN1 DC JACK foot print to 5 pin for DFX.
P33: Change PQ14 footprint to 0203-Q.
P34: Battery A/B SMD ESD diode pull high from +5V to +3V.
P34: P3 fuse change from SA slow to 7A fast 1206 for ME.
P35: PC57/PC59/PR62/PR63 connect from Bto VS.
P36: Change PC5 from 68UF_EC_25V to Panasonic 47UF_25V for ME.
Add 0 Ohm PR139 between VS and pin of PU2
Add @ 0 Ohm PR140 between +5VALW and pin of PU2

02/26/01 Written byferry

P12: Change VCC to +12V at pin of JP11
P21: Add C578, R446, and C579
Add @2SC2411K at Q71

02/27/01 Written byferry

P32: Add R2

03/02/01 Written byferry

P04: Add buffer between PCIRST# and CH2-M
P12: Add R447 and C508 at GR_CLK
P16: Add R448, R449, R450, D38, D33 and D40
P35: Add PR14100K

03/05/01 Written byferry

P25: Add C581 for MIC circuit

03/06/01 Written byferry

P21: Change net SM_FRE# to pin of J47
P31: Change H29, H5 footprint
Delete H32 and H6

***** Rev0.3 PIR List *****

03/19/01 Written byferry

P32: Change value of PC8 to 100K_0805
Add @ on R2

P33: Add PC97 in pin of PU6
Add PC98 in pin of PU6

P34: Change value of PR110 and PR 116 549K 1%
Change value of PR109 and PR 117 243K 1%
Change VCC of ESD Diode to +5VALW in PD10, PD276, and PD7
P35: Add PC?? in B+
Add @ in P27
Change net VS to B+ in Pin of PU5
Change value of PR108 to 5K 1%
Change value of PR105 to 49 1%
Change value of PR61 to 50K 1%
Change value of PR76 and PR77 to 5K 1%
Change value of PR74 to 22K 1%
Change value of PR83 to 50K
Add @ in PR41
Delete PR104, PC85, PR58, and PR75
Change value of PU8 to M2906
Change pull high VCC to +3VALW at PR64

03/21/01 Written byferry

P09: Remove @ in D20
P12: Connection R447 and C580
P16: Delete R448, R449, R450, D38, D33 and D40
Change type of RJ45 to with LED JP5
Add R452, R453, Q72, and Q73
P17: Add @ in C524 and C567
Connection Pin87 of U49 and Pin of RP38
P19: Remove @ in R182 and Add @ in D15
P21: Add Q74, Q75, Q76, R454, and C582
Change value of Q71 to 2010DS
Add @ in R46
Add U55, R455, R456, and C583
Change value of R443 to 10K
Add net SM_5VON, SM_3VON, SM_LED, and SM_LVD
Change value of C575 to FQ0603
P24: Add @ in R123, Q9, C131, Q11, C132, C121, R12126C and JP15
P28: Add @ in R8
P30: Add net SM_LED in pin of JP6
P31: Update Standoff

03/23/01 Written byferry

P20: Change VCC of Q10 to 5VDC
Change type of JP7 to SUVIN 2020-465-A
P32: Change PR94 10K to 0 Ohm
P34: Change PR110, PR116 to 549K and PR109, PR117 to 243K. (U2P, ME L11.09V)
P35: Add PC100, PR141, PR142, PQ39

03/26/01 Written byferry

P13: Change value of L19 to 0_0805
P21: Add F5 for SMVCC

03/28/01 Written byferry

P13: Change FootPrint foJP2
P19: Change VCC of R339 to 5VDC
P31: Modify Hold side in H and H29

***** Rev1.0 PIR List *****

04/13/01 Written byferry

P02: Add D38 and Q77 for 1.6V/3V delete
P16: Change value of R452 and R453 800_0603
Update LED circuit
P18: Add D39 for wireless RPOFF#
P22: Add net 17V/16V# in pin of U24
P23: Change value of R1 to 1_1206
Add R457 4.7_206
P27: Change net BUTTON_LOCK# to MP3# in pin of U32
P28: Add net BID in pin of U19
R151 pin 1 tied to 3VALW
P30: Delete net BUTTON_LOCK# in pin of JP6
Add net MP3# in pin of JP6
P31: Delete Screw Hole H17
P33: Change value of PR to 10K
Delete P3
P34: Change value PR120 to 270K
P35: Change net B+ to VS in pin of PU5

04/16/01 Written byferry

P16: Add R458, R459 for LAN reset
P28: Add net 2ND_BATT_LOW_LED# in pin of U21
Change net name from CD_FDD_LED# to 2ND_CHCH_FDD_LED#
P30: Add net 2ND_BATT_LOW_LED# in pin of JP6
Change net name from CD_FDD_LED# to 2ND_CHCH_FDD_LED#

04/18/01 Written byferry

P21: Delete RP39, RP40, and R442
Add D40, R460, C585, C584, U56 and U57
Change R444 pull high VCC to +5V

04/19/01 Written byferry

P13: Update JP3 footprint
P16: Add D41
P19: Change value of D33 to RB751
P21: Move net SM_FRE# to pin of U47
Move net SM_5VON# to pin of U47
Remove @ in R455, R456, C583 and U55
P23: Update JP4 footprint
P23: Update ST1, ST2, ST3, ST4, H11, H2, H24, H12, H13, H14, H15 footprint

04/20/01 Written byferry

P35: Change value of PT1 to 100H-SDT-500118-120

04/24/01 Written byferry

P09: Change value of CP6 and CP5 to R44R-22PF
P16: Swap signals on pin10 and pin of JP5
P21: Change footprint foJP4

04/25/01 Written byferry

P21: Add @ in R455, R456, C583 and U55
P28: Remove @ in R8

04/26/01 Written byferry

P19: Change value of R30 to 240K
Change value of R133 to 10K

Compal Electronics, Inc.			
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