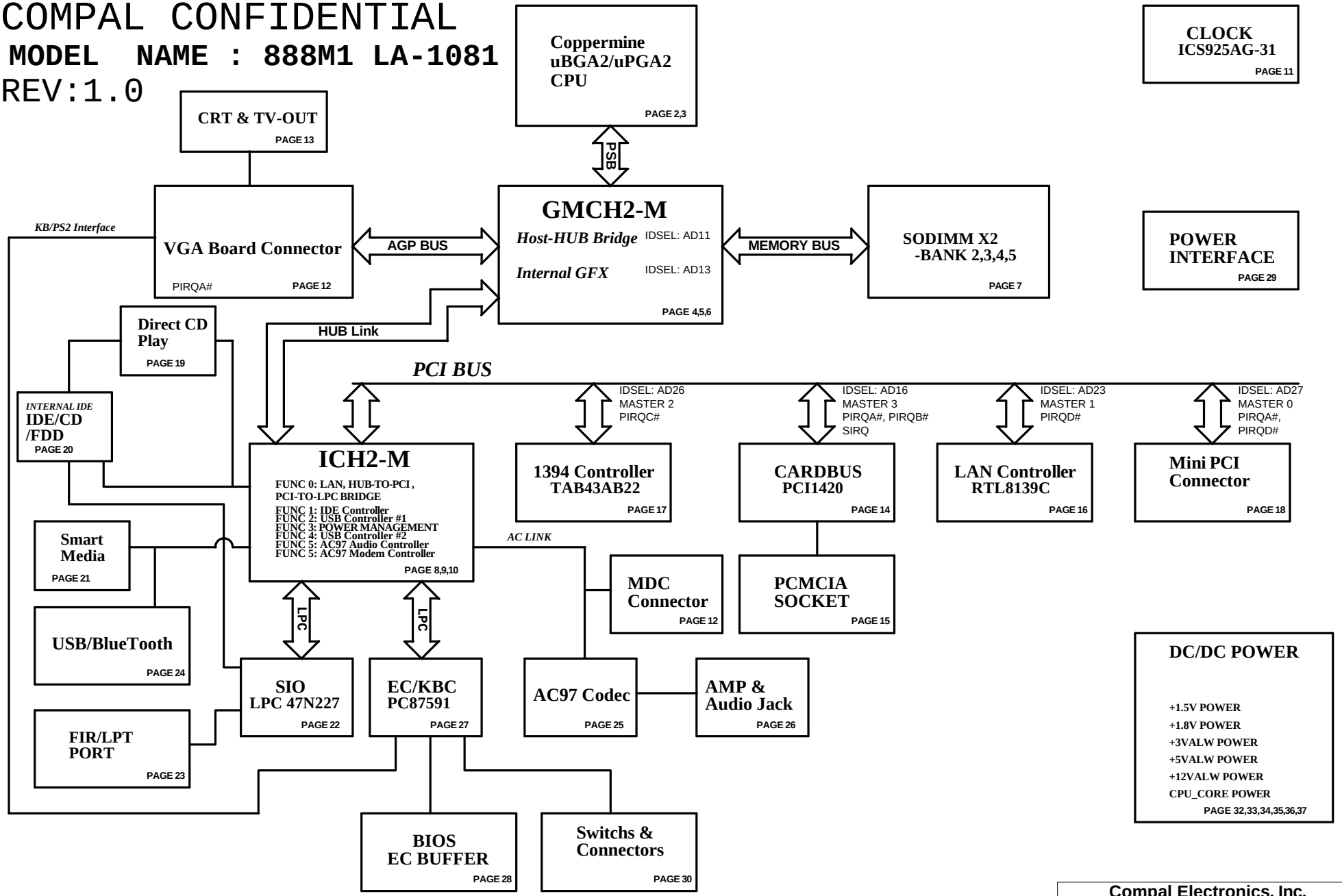
COMPAL CONFIDENTIAL

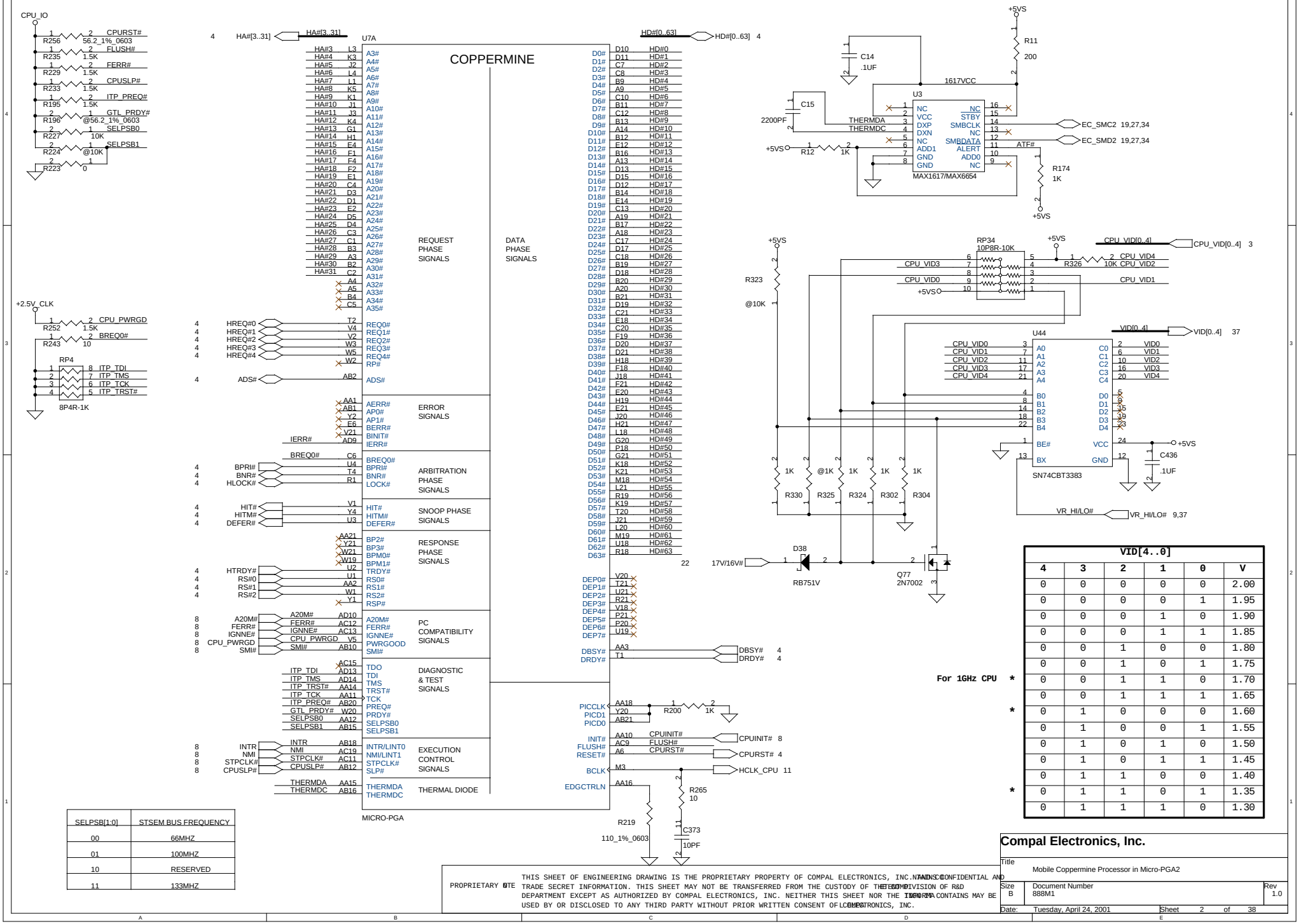
MODEL NAME : 888M1 LA-1081

REV:1.0



Compal Electronics, Inc.

Title	888M1 COVER SHEET	
Size B	Document Number 888M1	Rev 1.0
Date: Tuesday, April 24, 2001	Sheet 1 of 38	



SELPSB[1:0]	STSEM BUS FREQUENCY
00	66MHZ
01	100MHZ
10	RESERVED
11	133MHZ

VID[4..0]

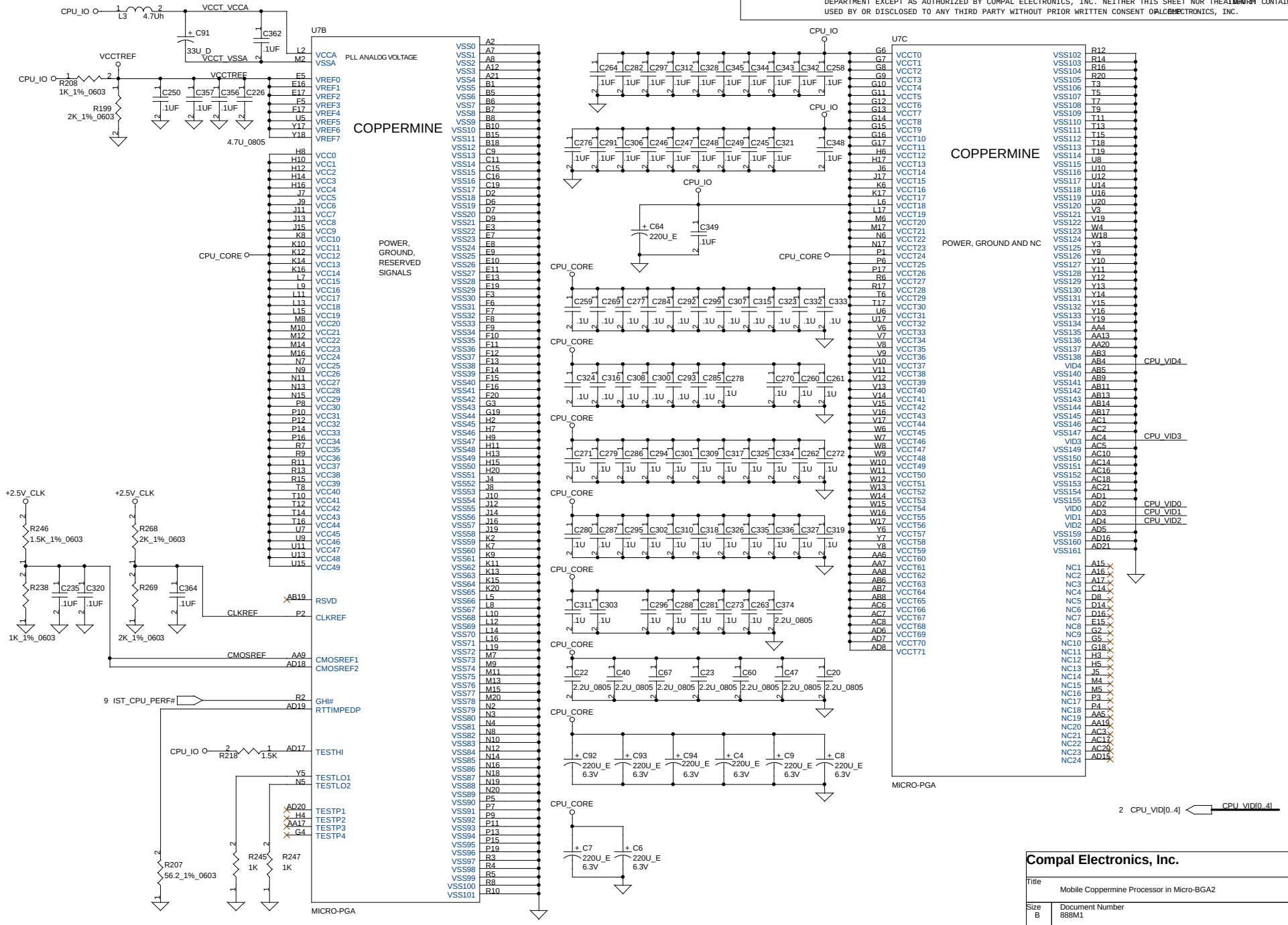
4	3	2	1	0	V
0	0	0	0	0	2.00
0	0	0	0	1	1.95
0	0	0	1	0	1.90
0	0	0	1	1	1.85
0	0	1	0	0	1.80
0	0	1	0	1	1.75
For 1GHz CPU * 0	0	1	1	0	1.70
0	0	1	1	1	1.65
* 0	1	0	0	0	1.60
0	1	0	0	1	1.55
0	1	0	1	0	1.50
0	1	0	1	1	1.45
0	1	1	0	0	1.40
* 0	1	1	0	1	1.35
0	1	1	1	0	1.30

Compal Electronics, Inc.

Title: Mobile Coppermine Processor in Micro-PGA2

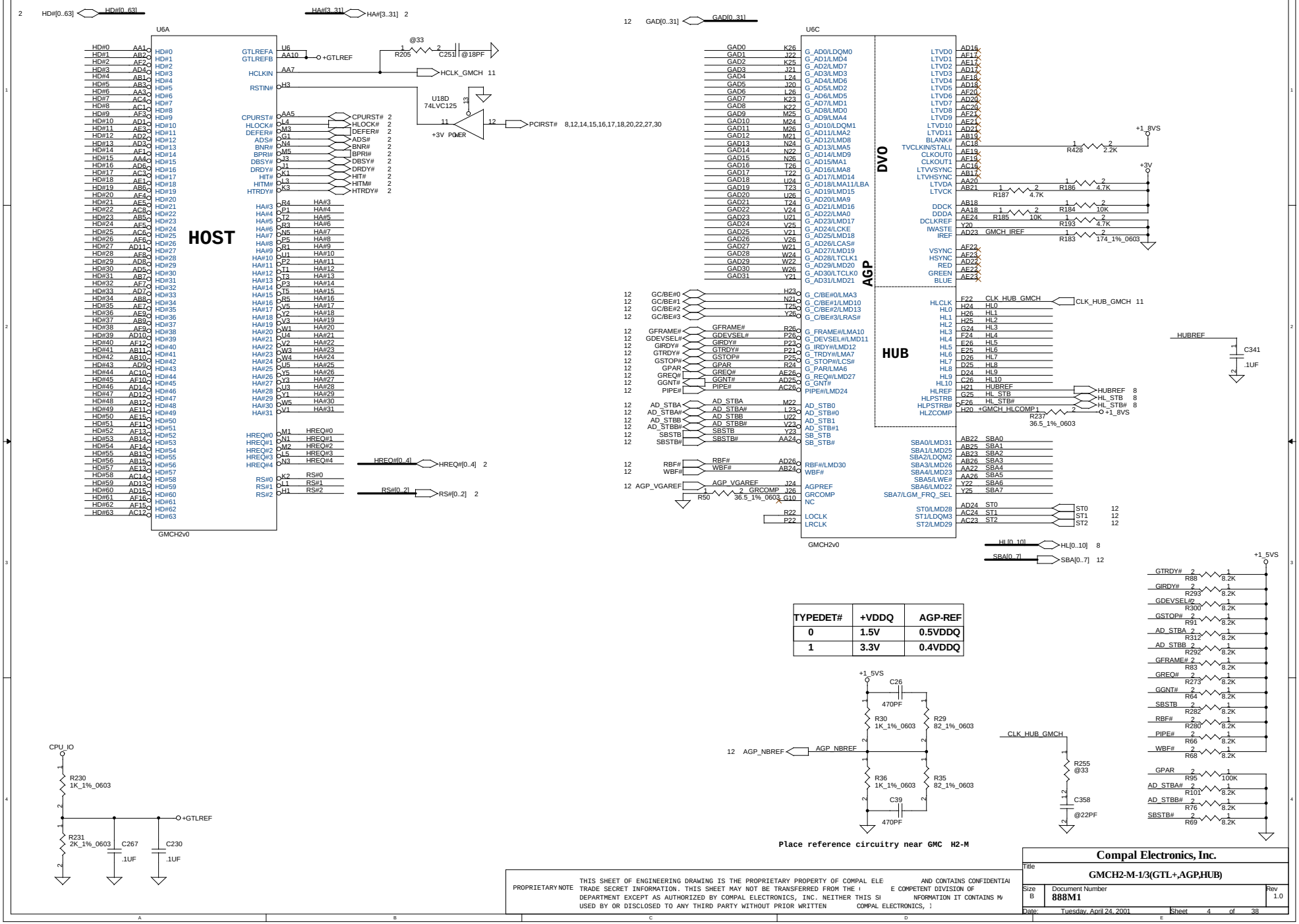
Size: B | Document Number: 888M1 | Rev: 1.0

Date: Tuesday, April 24, 2001 | Sheet 2 of 38



Compal Electronics, Inc.			
Title: Mobile Coppermine Processor in Micro-BGA2			
Size: B	Document Number: 888M1		Rev: 1.0
Date: Tuesday, April 24, 2001	Sheet 3 of 38		

GMCH2-M-1/3(GTL+,AGP,HUB)

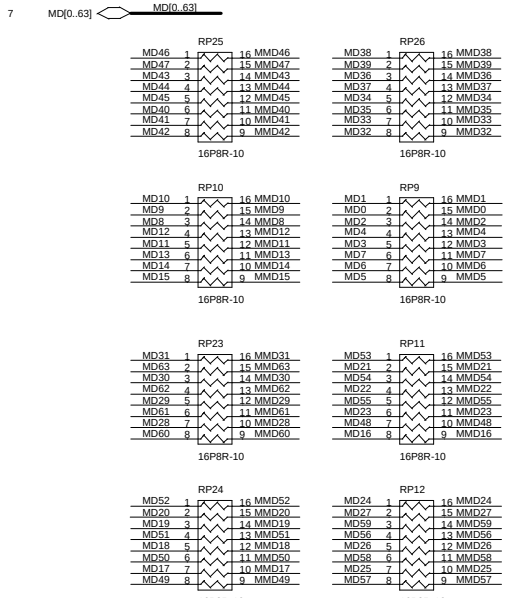


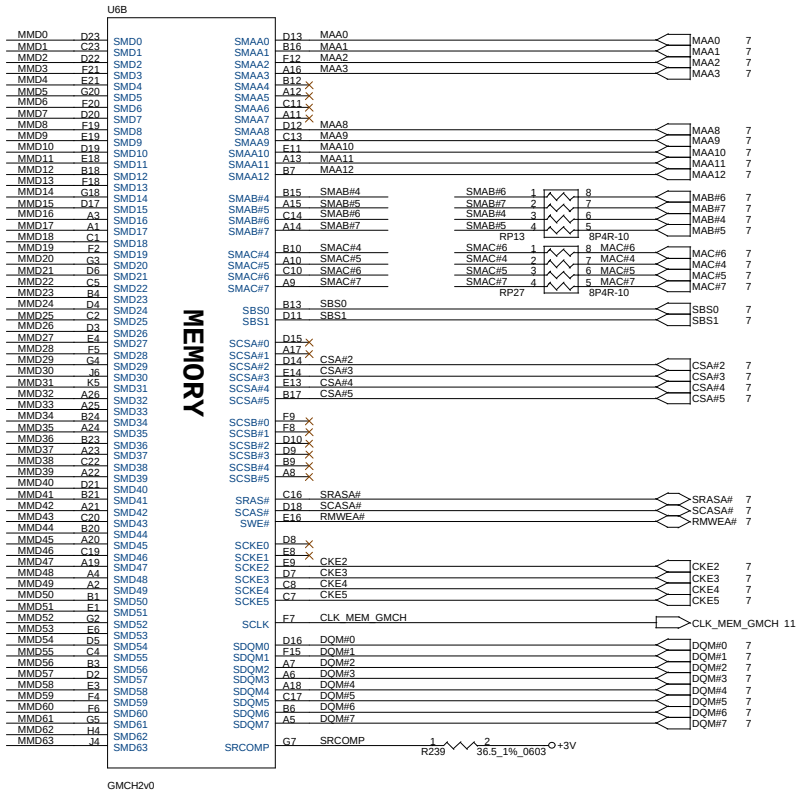
TYPEDET#	+VDDQ	AGP-REF
0	1.5V	0.5VDDQ
1	3.3V	0.4VDDQ

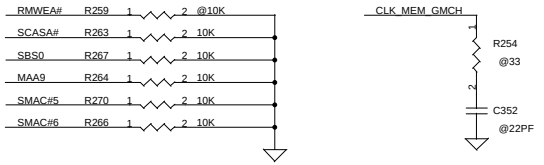
Compal Electronics, Inc.

Title	GMCH2-M-1/3(GTL+,AGP,HUB)	
Size B	Document Number 888M1	Rev 1.0
Date: Tuesday, April 24, 2001	Sheet 4 of 38	

GMCH2-M-2/3(SDRAM)







Power-Up Strap Options

Pin Name	Strap Description	Configuration	Interface	Internal
SCAS#	Host Freq.	"H" : 133MHz (Default) "L" : 100MHz	System Memory	PULL_UP
SWE#	Host Freq.	"H" : 100MHz (Default) "L" : 66MHz	System Memory	PULL_UP
SMAA11	IOQ Depth	"H" : 4 (Default) "L" : 1	System Memory	PULL_UP
SMAA10	ALL Z	"H" : Normal "L" : All Z	System Memory	PULL_UP
SMAA9	FSB P-MOS Kicker Enable	"H" : Enabled (Default) "L" : Disabled (Cumine)	System Memory	PULL_UP
SMAC6#	Enable VCH Serial Programming Mode	"H" : Enabled (Default) "L" : Disabled	System Memory	PULL_UP
SMAC5#	Enable Quick Start Support	"H" : Disabled (Stop Grant Mode) "L" : Enabled (Default) (Quick Start Mode)	System Memory	PULL_UP
VGA_LFSEL#	Local Memory Freq. Select	"H" : 133MHz (Default) "L" : 100MHz	AGP/LM	i815/i815-m

Compal Electronics, Inc.

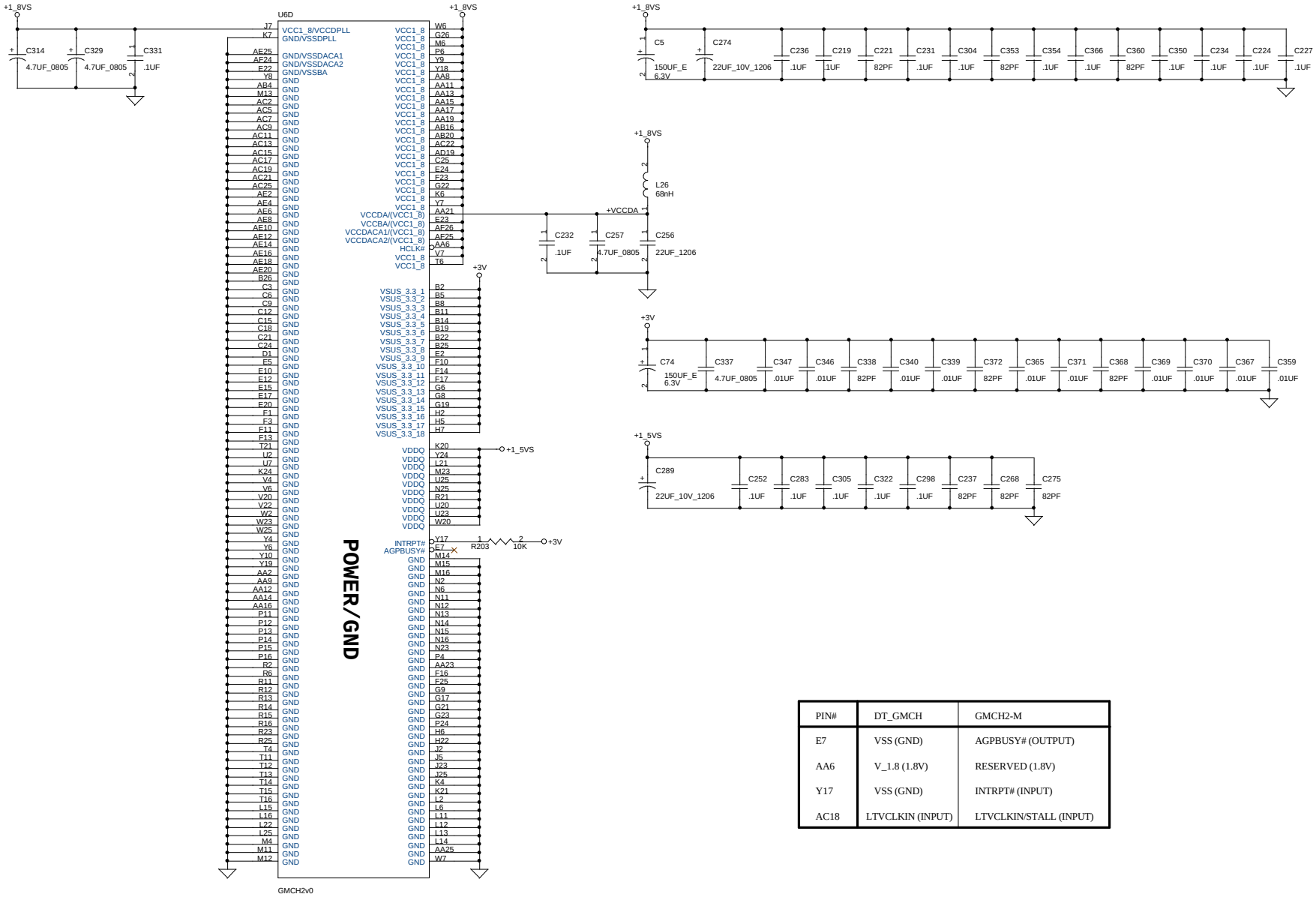
Title: GMCH2-M-2/3(SDRAM)

Size: B | Document Number: 888M1 | Rev: 1.0

Date: Tuesday, April 24, 2001 | Sheet 5 of 38

GMCH2-M-3/3(Power)

Please make sre the ESR a art nmbe



PIN#	DT_GMCH	GMCH2-M
E7	VSS (GND)	AGPBUSY# (OUTPUT)
AA6	V_1.8 (1.8V)	RESERVED (1.8V)
Y17	VSS (GND)	INTRPT# (INPUT)
AC18	LTVCLKIN (INPUT)	LTVCLKIN/STALL (INPUT)

Compal Electronics, Inc.

Title: **GMCH2-M-3/3(Power)**

Size: B | Document Number: **888M1** | Rev: 1.0

Date: Tuesday, April 24, 2001 | Sheet 6 of 38

SO-DIM 144 PINS RAM MODULE CONN.

BANK2/3

BANK4/5

DIMM0

DIMM1

SO-DIMM144 REVERSE

SO-DIMM144-STANDARD

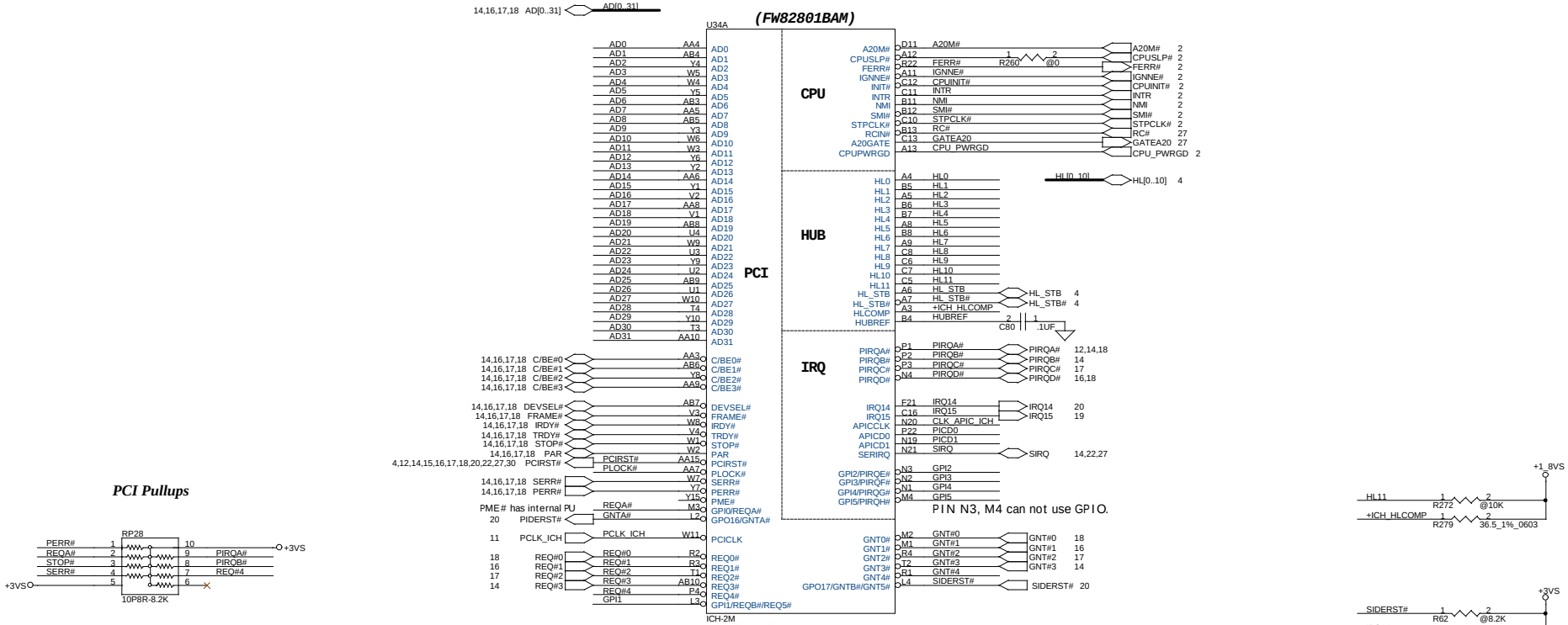
74HC4052

8P4R-10K

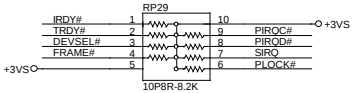
Compal Electronics, Inc.	
Title	S.O. DIMM CONNECTOR
Size: B	Document Number: 888M1
Rev: 1.0	
Date: Tuesday, April 24, 2001	Sheet 7 of 38

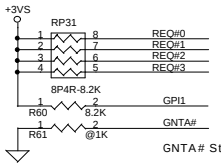
ICH-2M

(FW82801BAM)



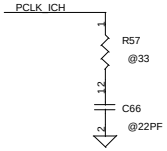
PCI Pullups

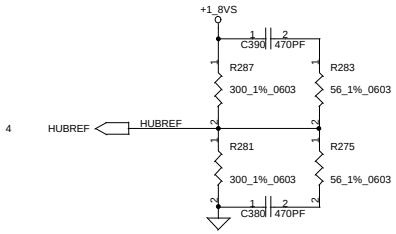




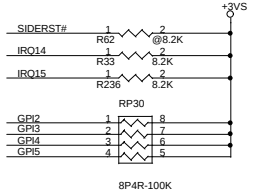
GNTA# Strapping for "A16 swap override" : "0" -> Enable

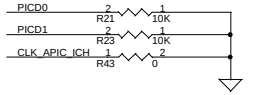
PCI REQ ASSIGMENT	
REQ#0	WLAN
REQ#1	LAN
REQ#2	1394
REQ#3	PCMCIA CONTROLLER
REQ#4	NC





Place divider pair in middle o f bus





Compal Electronics, Inc.

Title: **ICH2M-A(PCI,HUB,CPU) & FWH**

Size	Document Number	Rev
B	**888M1**	1.0

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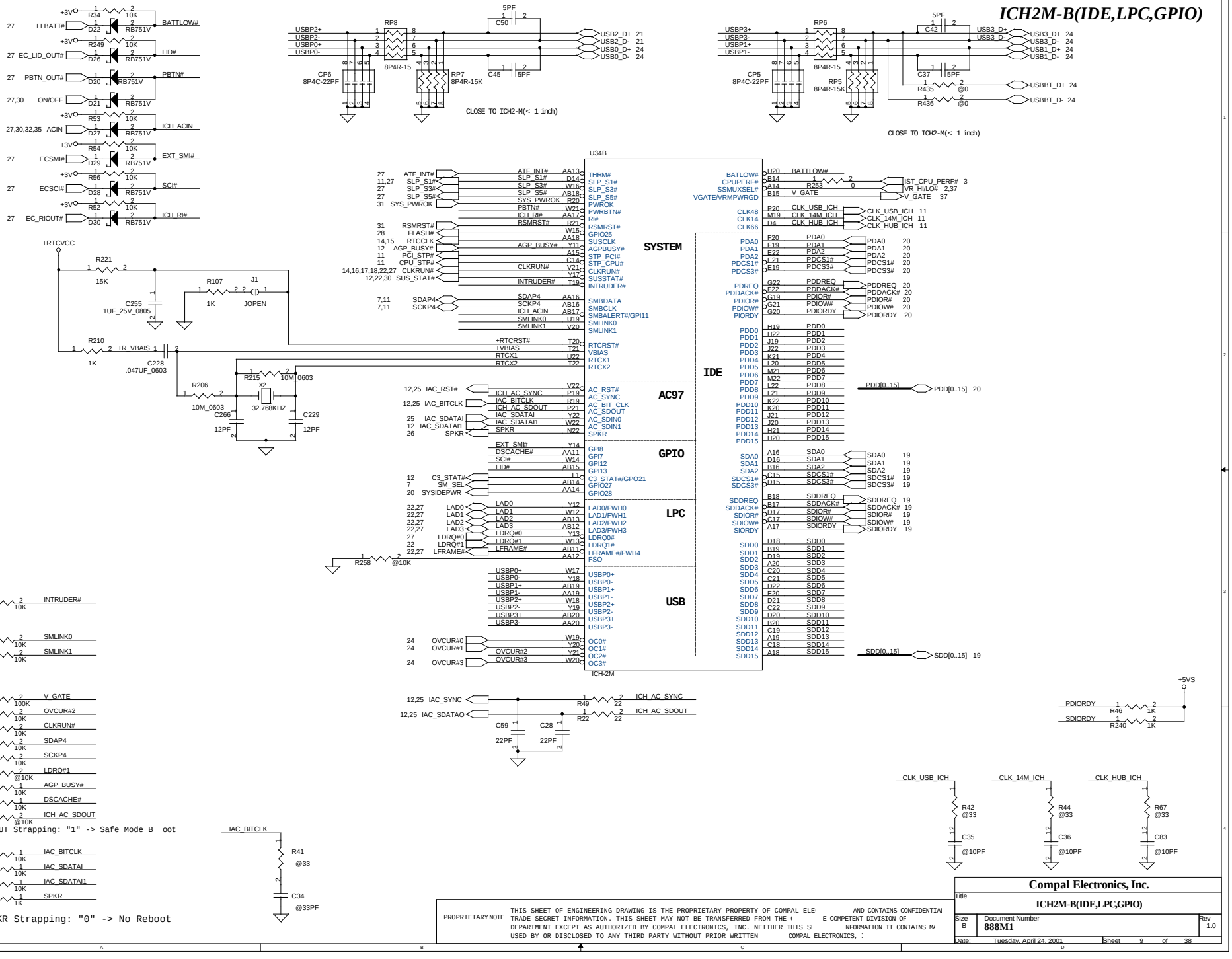
ICH2M-B(IDE,LPC,GPIO)



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Compal Electronics, Inc.

Title: **ICH2M-B(IDE,LPC,GPIO)**

Size	Document Number	Rev
B	**888M1**	1.0

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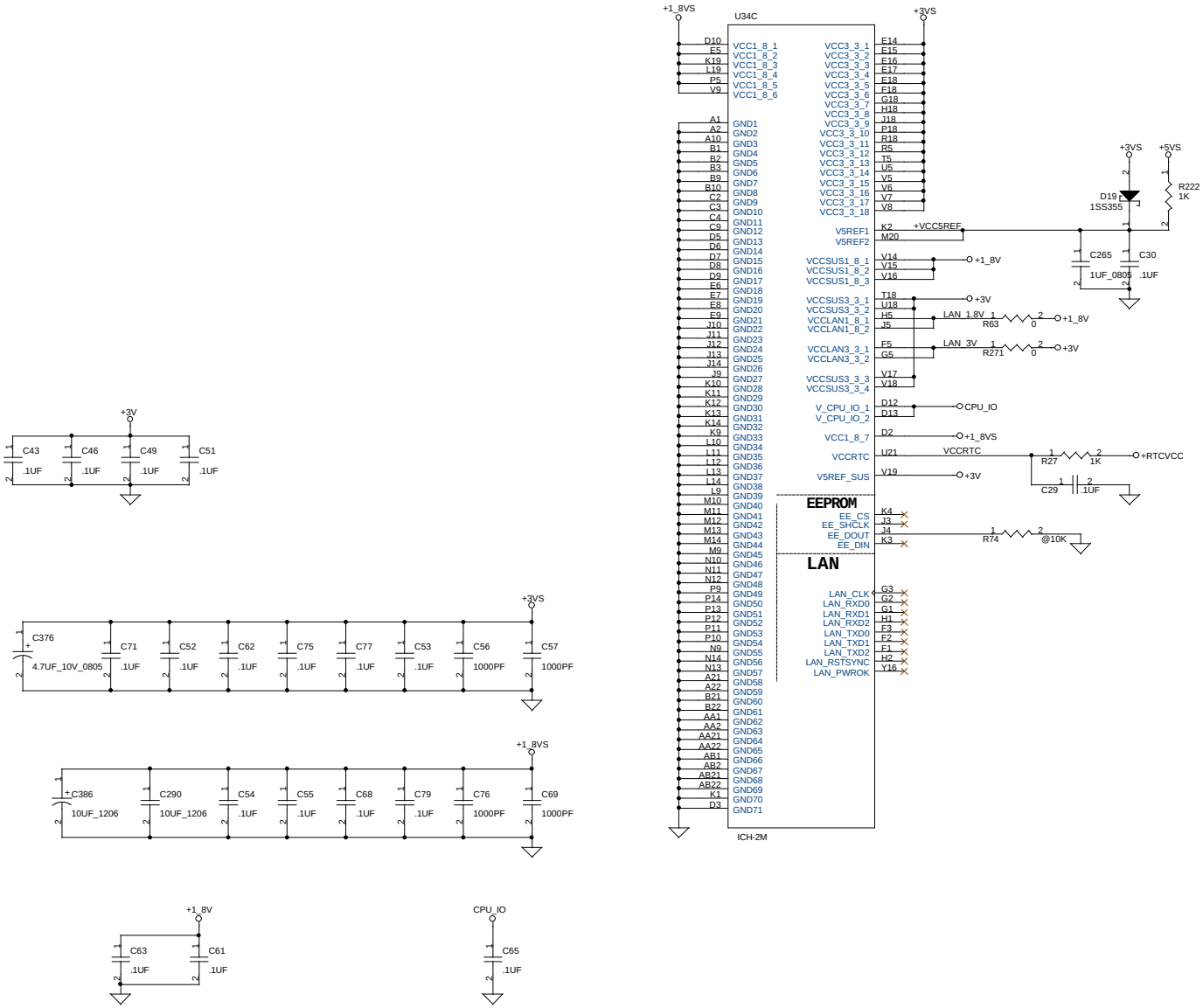
ICH2M-C(LAN,Power) & Pull-Up



Compal Electronics, Inc.

Title: **ICH2M-C(LAN,Power) & Pull-Up**

Size	Document Number	Rev
B	**888M1**	1.0

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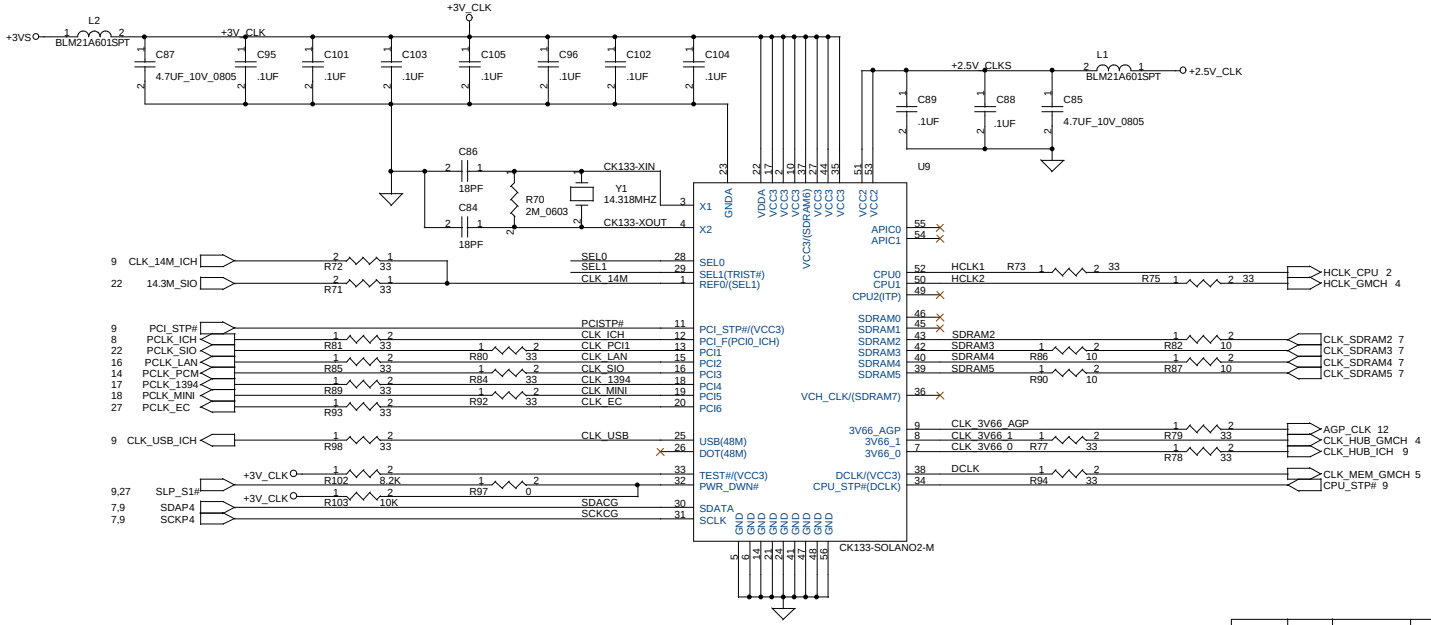
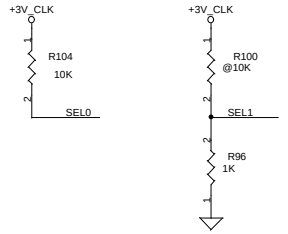
Clock Generator

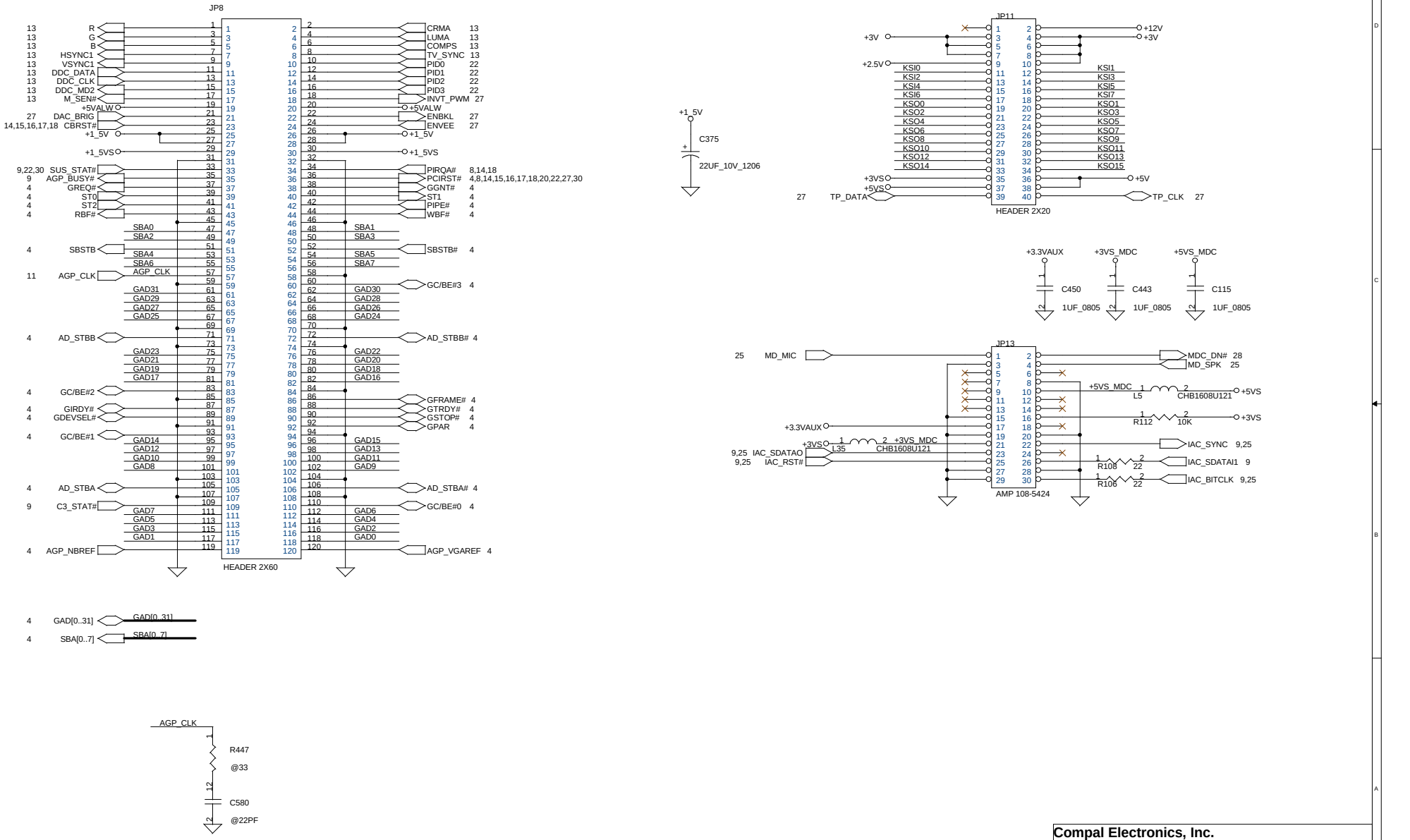


TSSOP-56 SA092500000 ICS 9 250AG-31

	SEL1	SEL0	PSB	SDRAM
	0	0	66	100
*	0	1	100	100
	1	0	133	133
	1	1	133	100

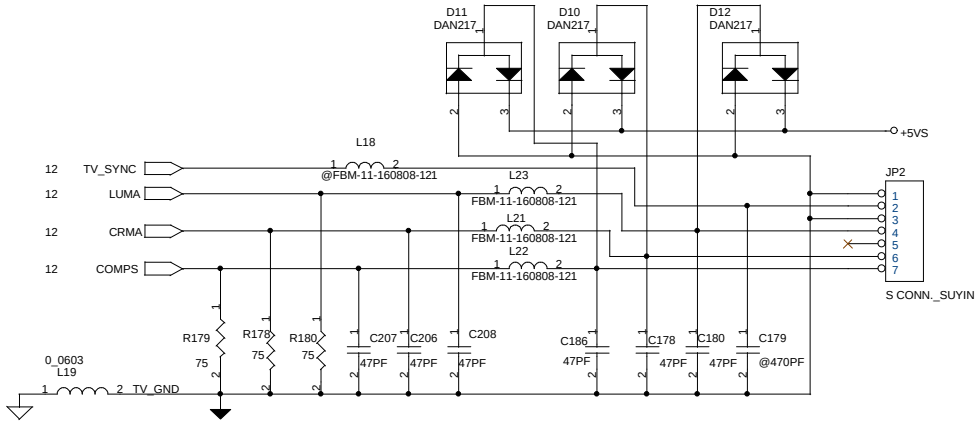


Compal Electronics, Inc.		
Title: Clock Generator		
Size: B	Document Number: 888M1	Rev: 1.0
Date: Tuesday, April 24, 2001	Sheet 11 of 38	

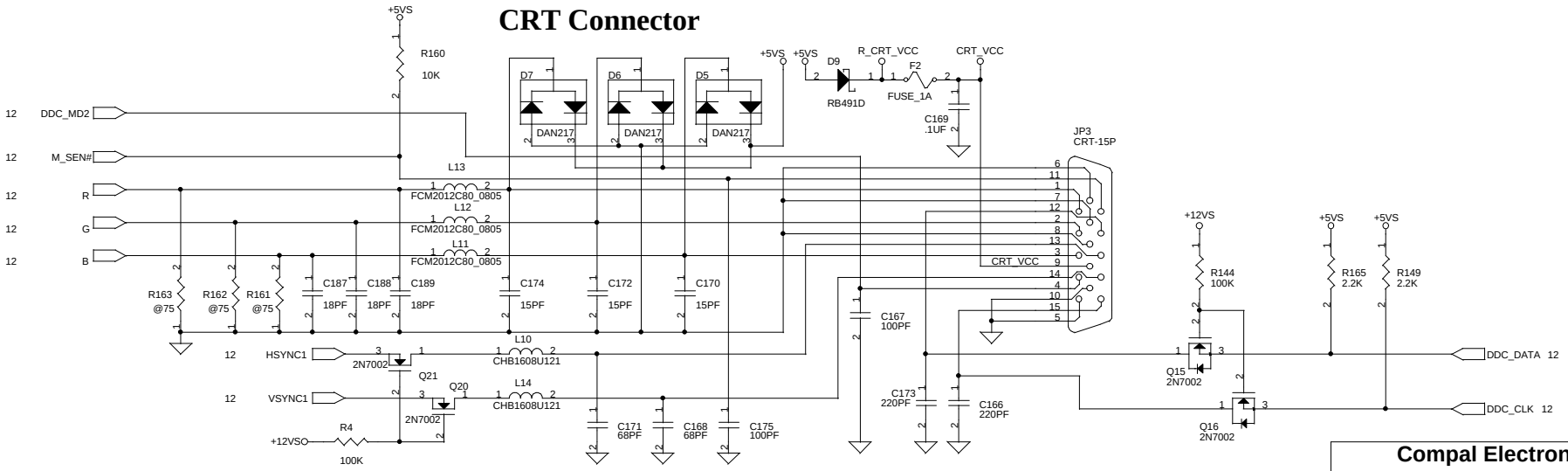


Compal Electronics, Inc.

Title	VGA Connector & MDC Connector		
Size B	Document Number 888M1		Rev 1.0
Date: Tuesday, April 24, 2001	Sheet 12 of 38		



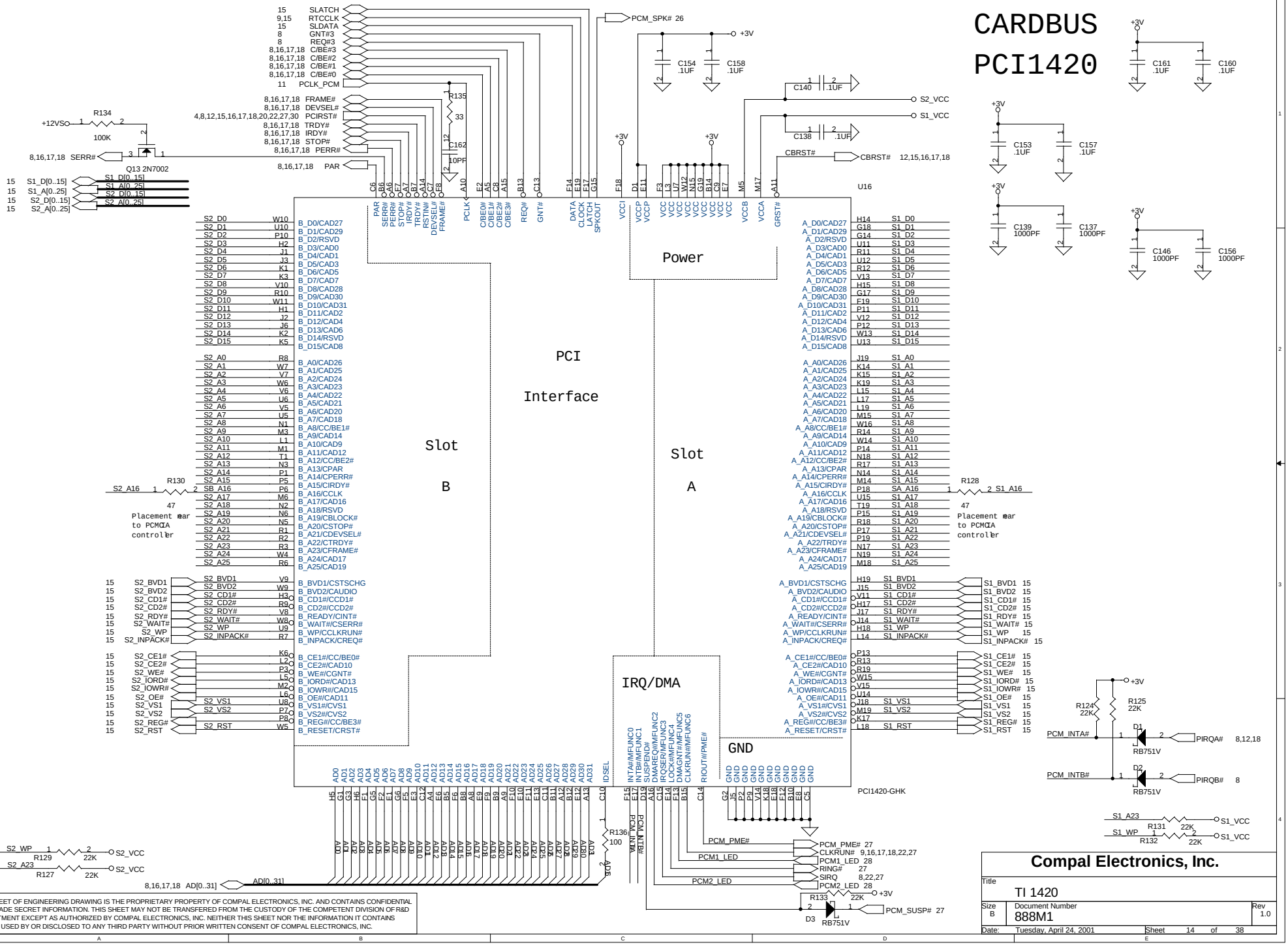
CRT Connector



Compal Electronics, Inc.

Title	CRT&TV-OUT Connector	
Size B	Document Number 888M1	Rev 1.0
Date: Tuesday, April 24, 2001	Sheet 13 of 38	

CARDBUS PCI1420



Compal Electronics, Inc.

Title	TI 1420		
Size B	Document Number 888M1		Rev 1.0
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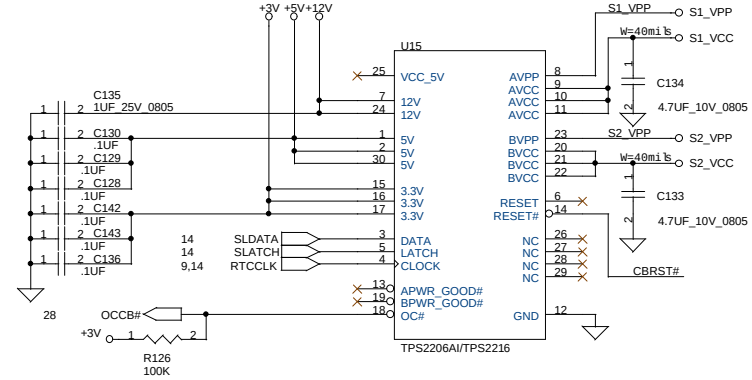
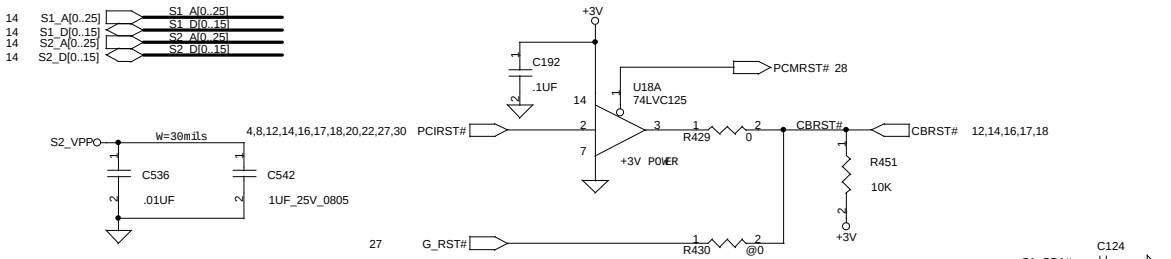
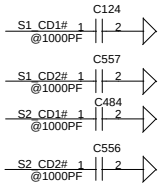
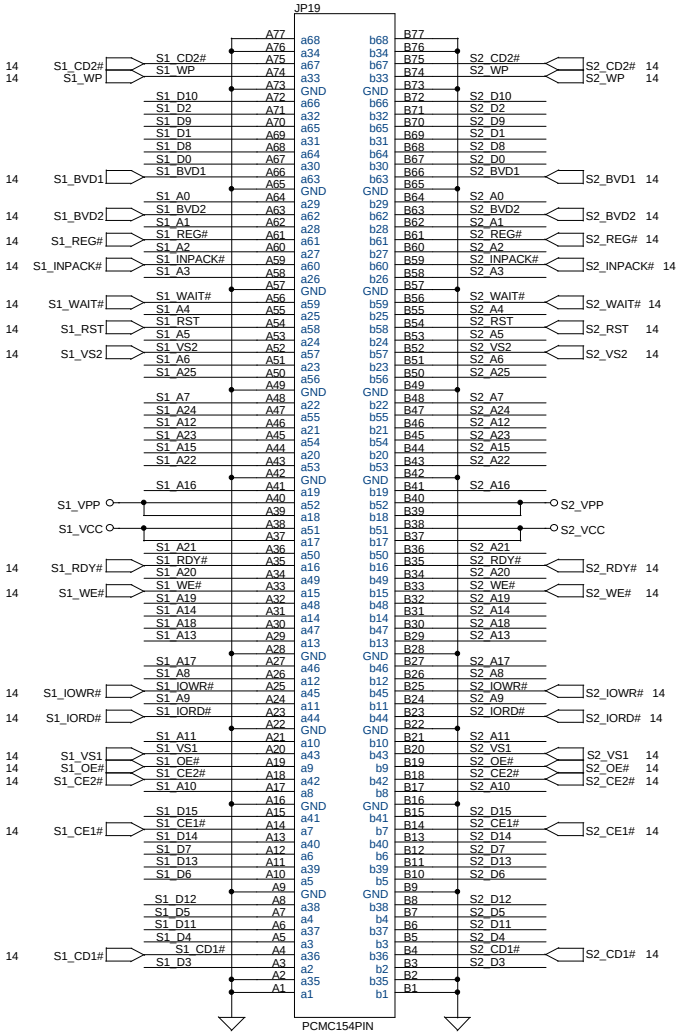
PCMCIA POWER CTRL.







CARDBUS SOCKET



Compal Electronics, Inc

Title	FCI PCMCIA SOCKET	
Size: B	Document Number: 888M1	Rev: 1.0
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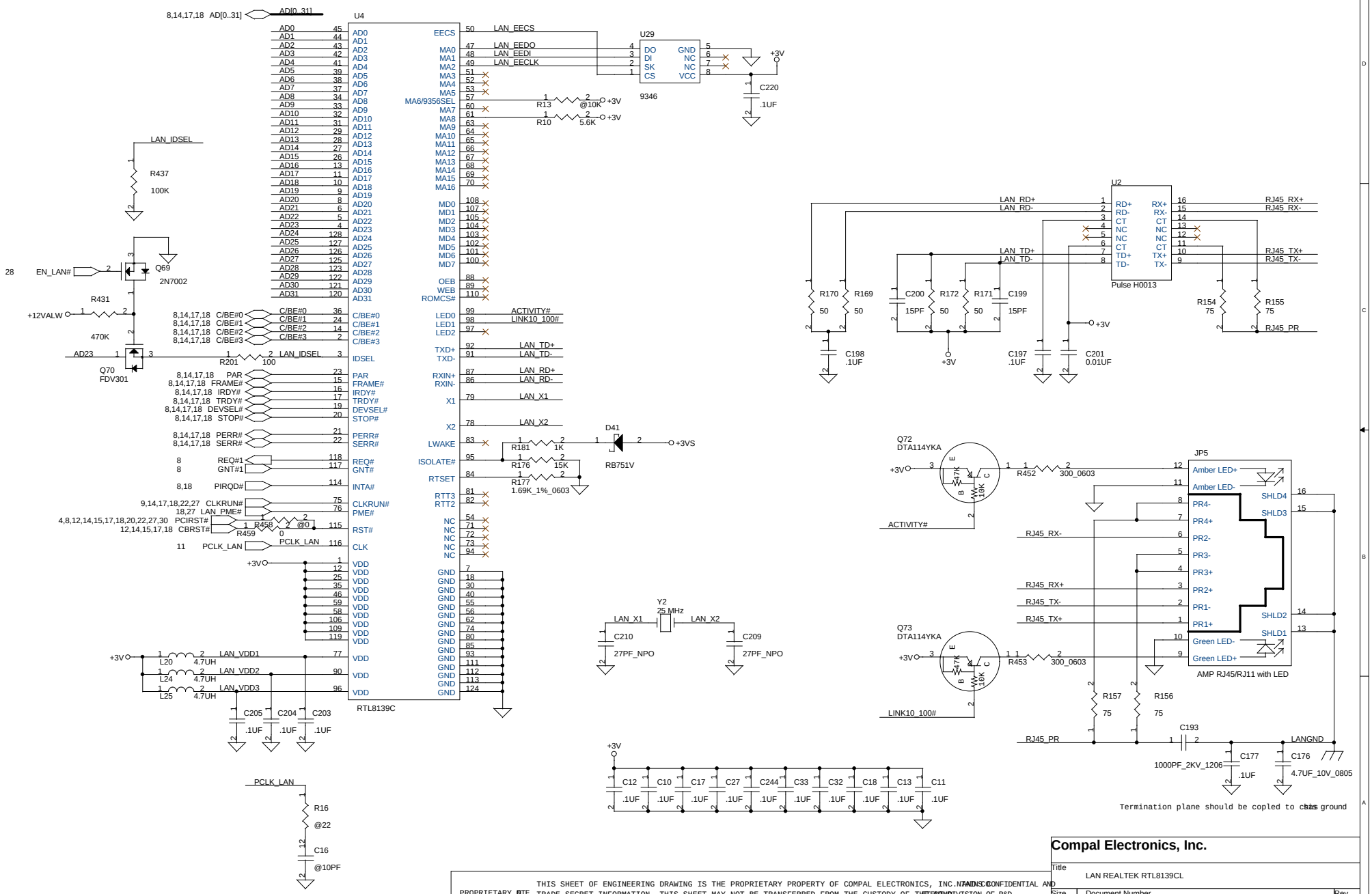
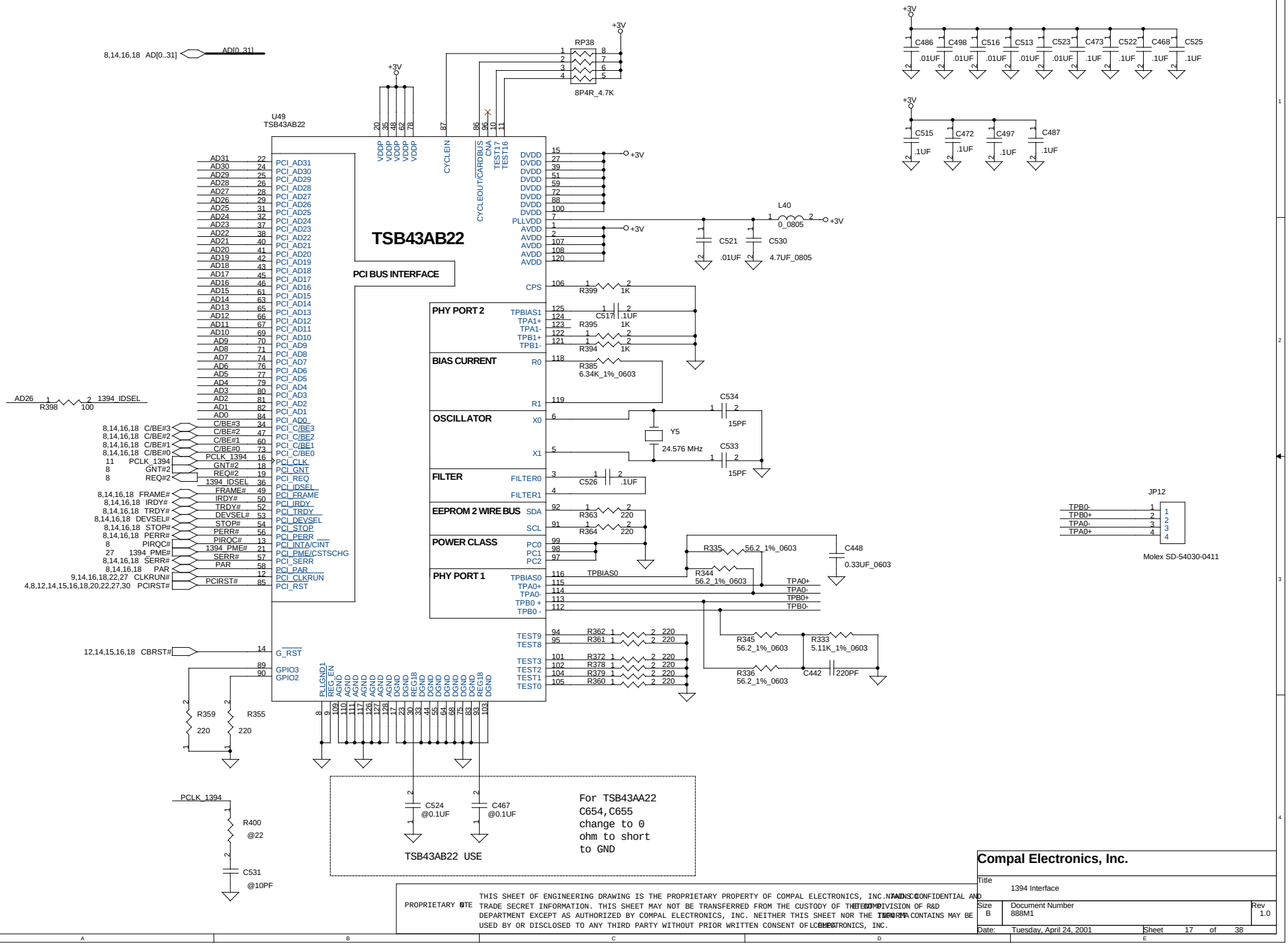


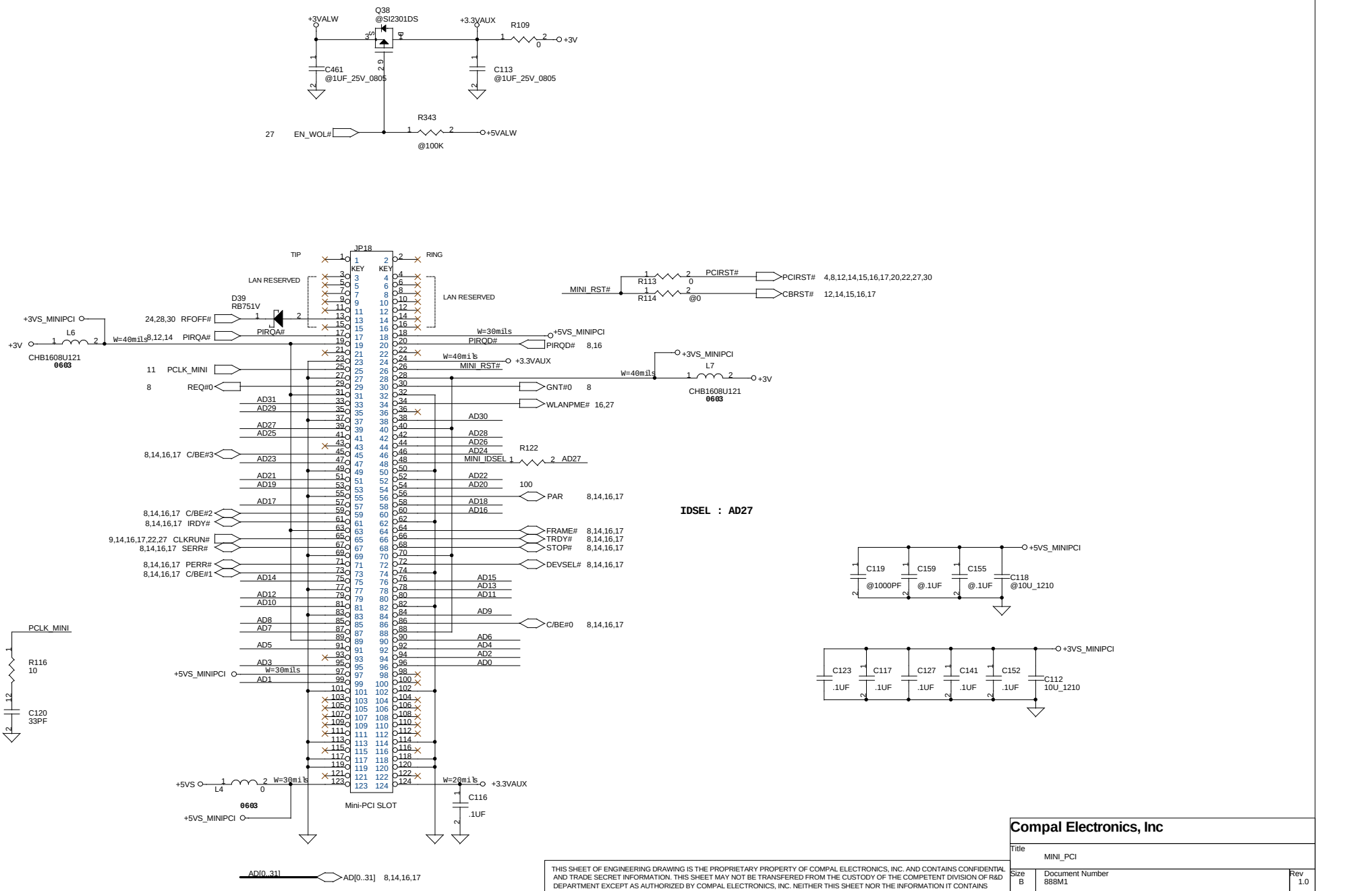
Compal Electronics, Inc.

Title	LAN REALTEK RTL8139CL		
Size B	Document Number 888M1		Rev 1.0
Date: Tuesday, April 24, 2001	Sheet 16	of 38	



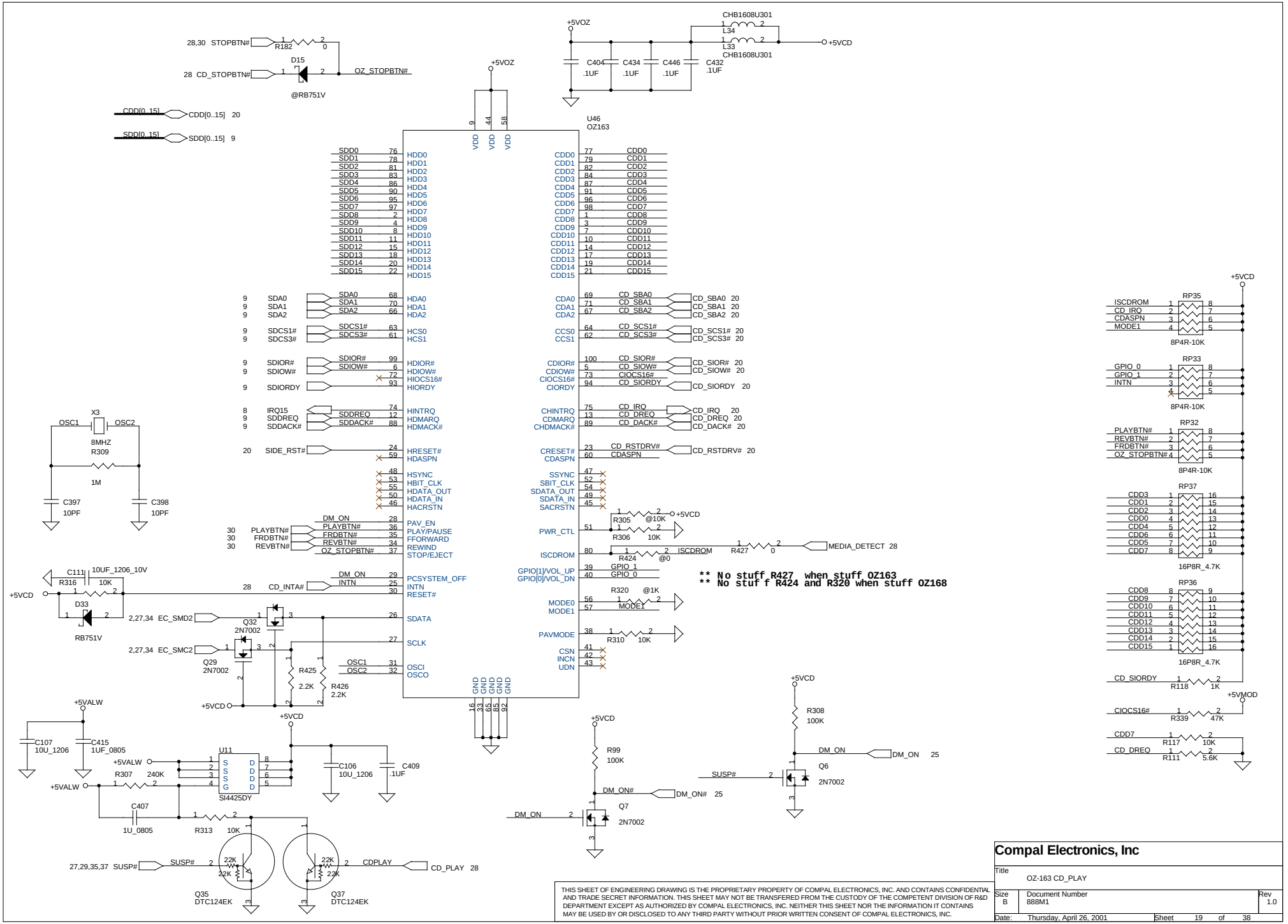
Compal Electronics, Inc.

Title	1394 Interface		
Size B	Document Number 888M1		Rev 1.0
Date: Tuesday, April 24, 2001	Sheet 17	of 38	



Compal Electronics, Inc

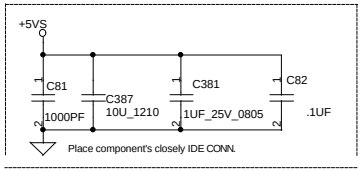
Title	MINI_PCI		
Size B	Document Number 888M1		Rev 1.0
Date: Tuesday, April 24, 2001	Sheet 18	of 38	

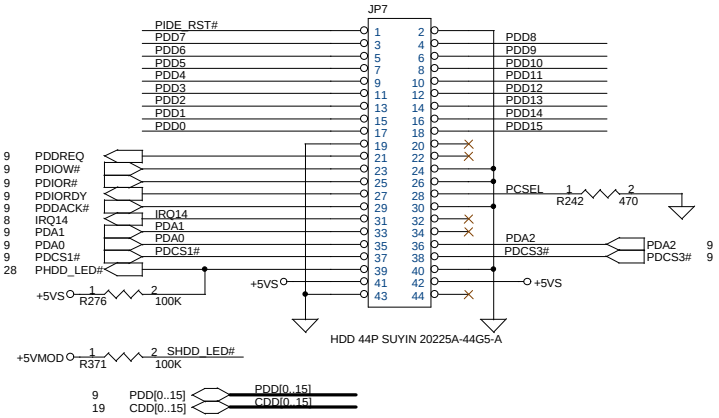


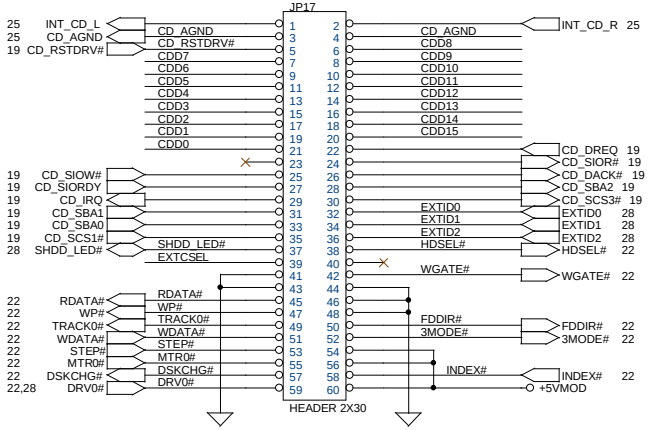
Compal Electronics, Inc

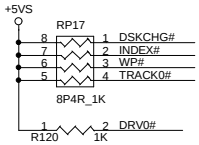
Title	OZ-163 CD_PLAY		
Size B	Document Number 888M1		Rev 1.0
Date: Thursday, April 26, 2001	Sheet 19	of 38	

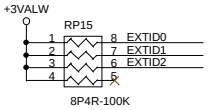
IDE,CD-ROM Module CONN.

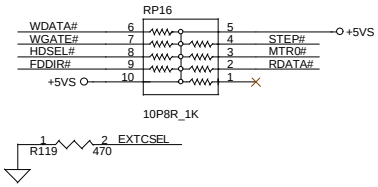


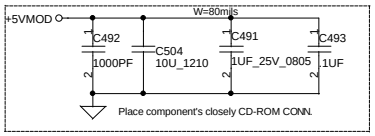


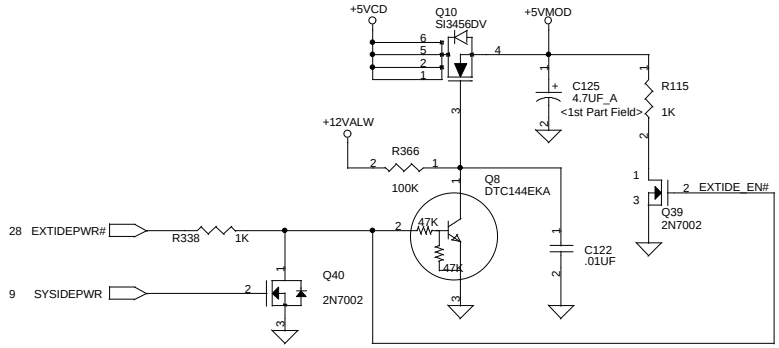




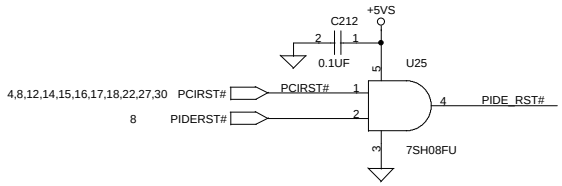


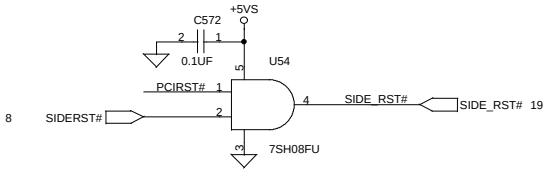






SI3456DV: N CHANNEL
VGS: 4.5V, RDS: 65 mOHM
Id(MAX): 5.1A
VGS,+-20V





Compal Electronics, Inc

Title	IDE/ FDD MODULE CONN.		
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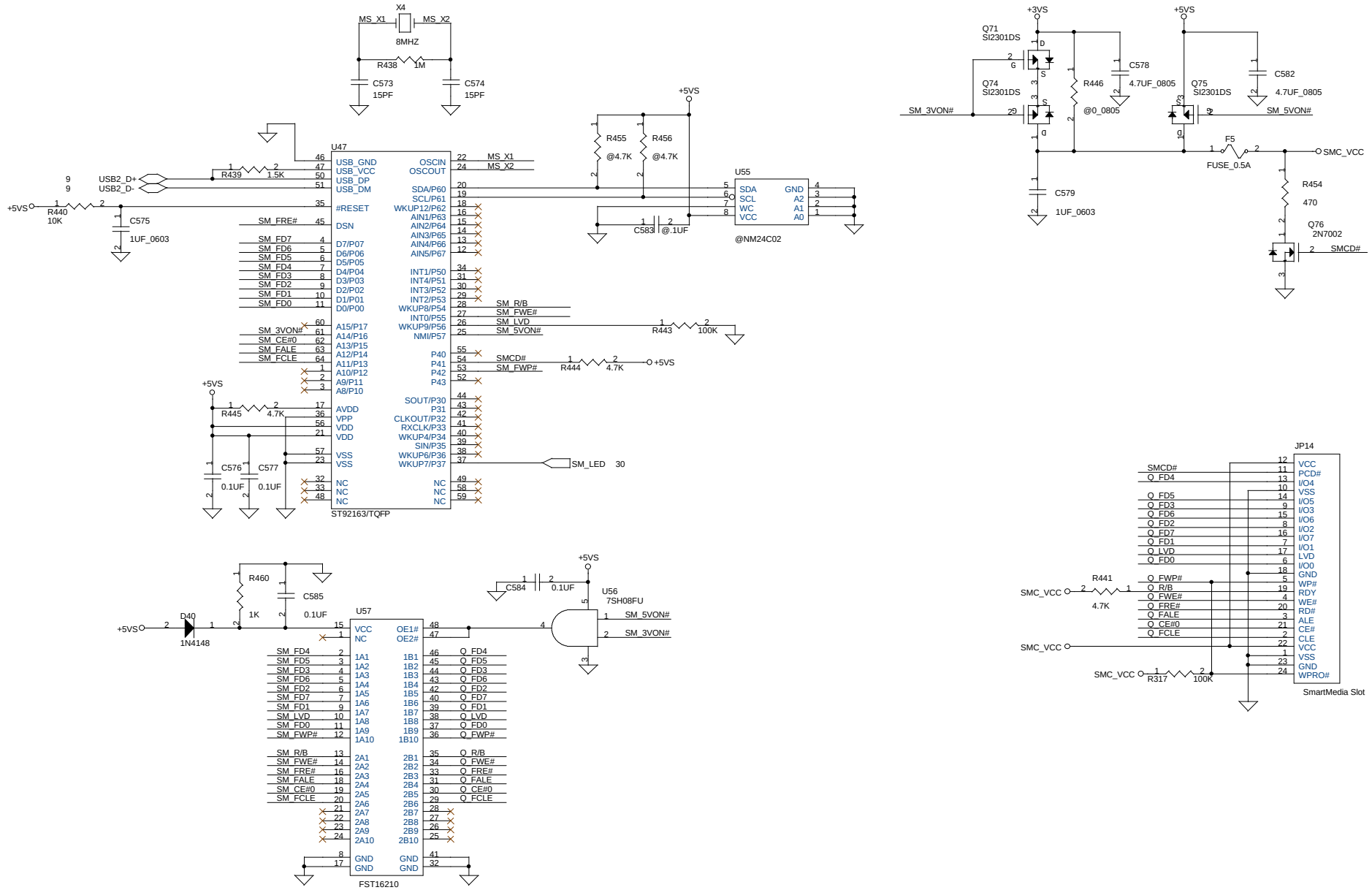
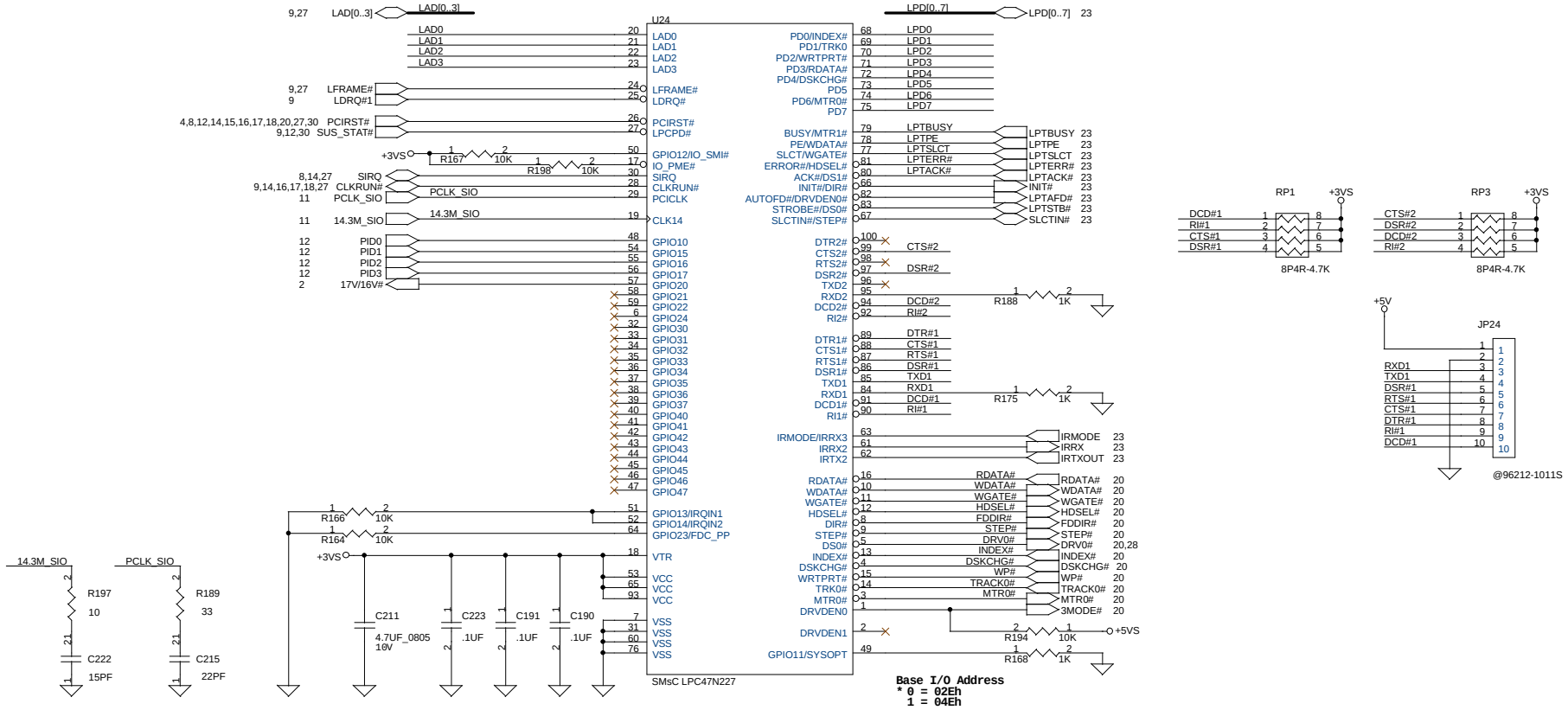
Date: Tuesday, April 24, 2001	Sheet 20 of 38		



Compal Electronics, Inc.

Title	SmartMedia Interface		
Size B	Document Number 888M1		Rev 1.0
Date: Wednesday, April 25, 2001	Sheet 21	of 38	

SUPER I/O SMsC FDC47N227



PROPRIETARY NOTE

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Compal Electronics, Inc.

Title	SUPER I/O		
Size B	Document Number 888M1		Rev 1.0
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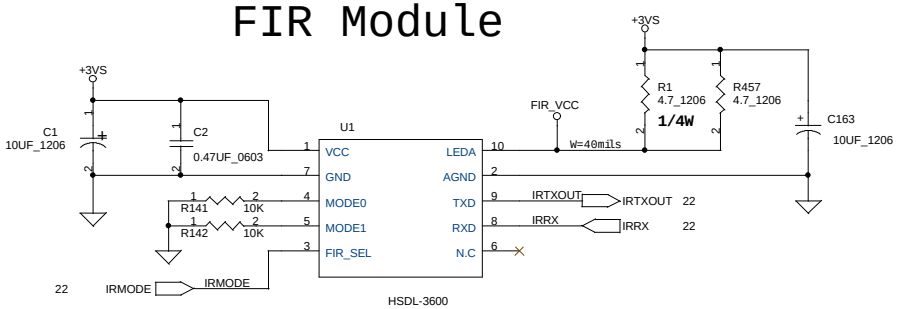
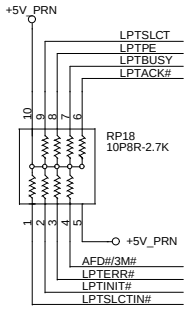
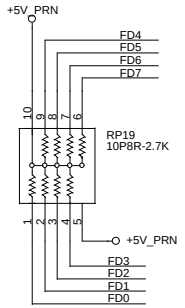
FIR Module

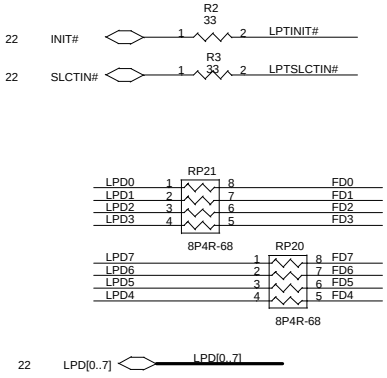


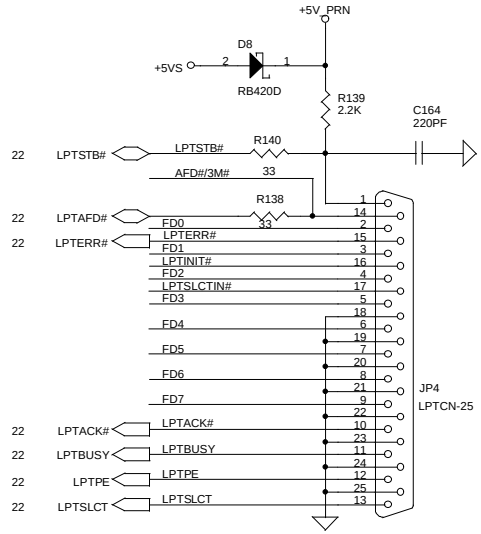
The component's most place cloely IRDA MODULE.

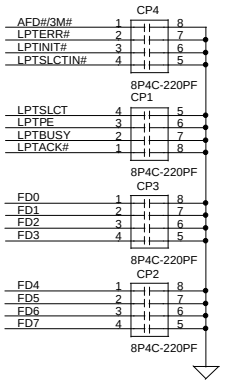
PARALLEL PORT











Compal Electronics, Inc.

Title	PARALLEL PORT		
Size	Document Number 888M1		Rev 1.0
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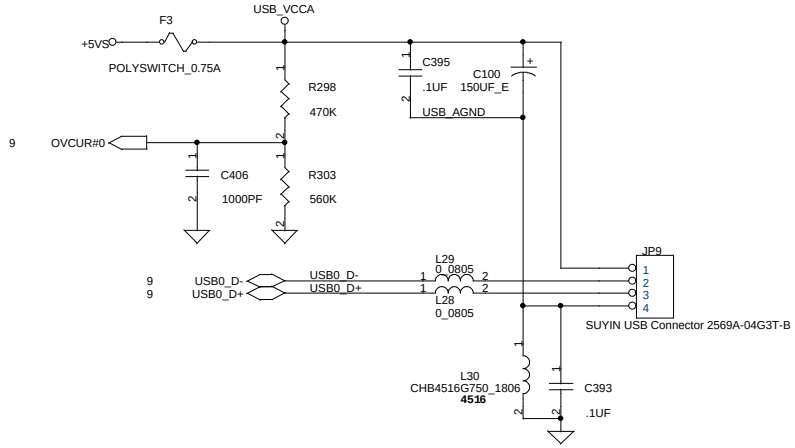

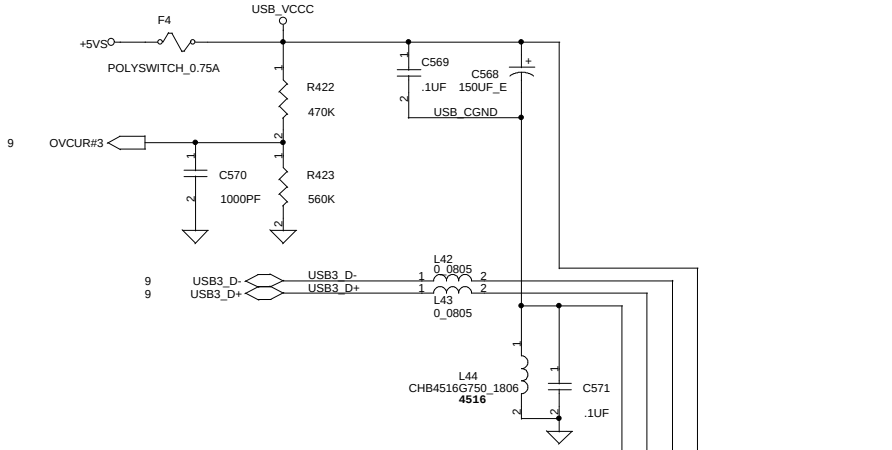

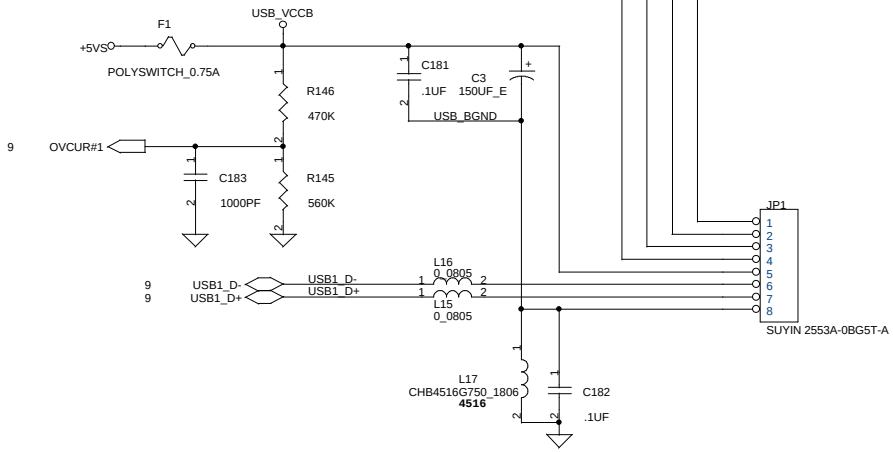

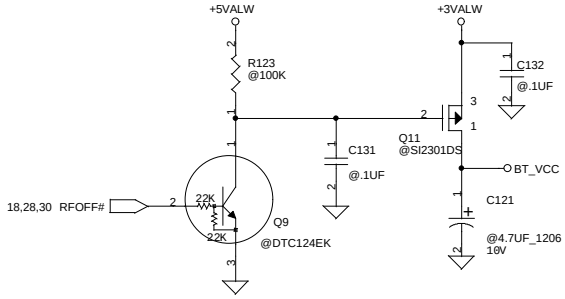

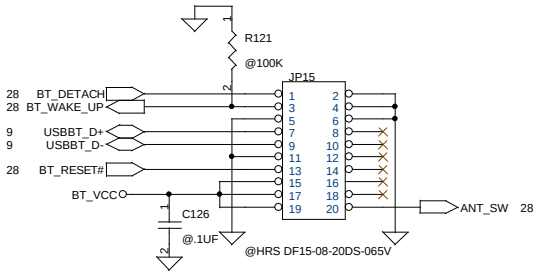

Compal Electronics, Inc

Title	USB & FIR	
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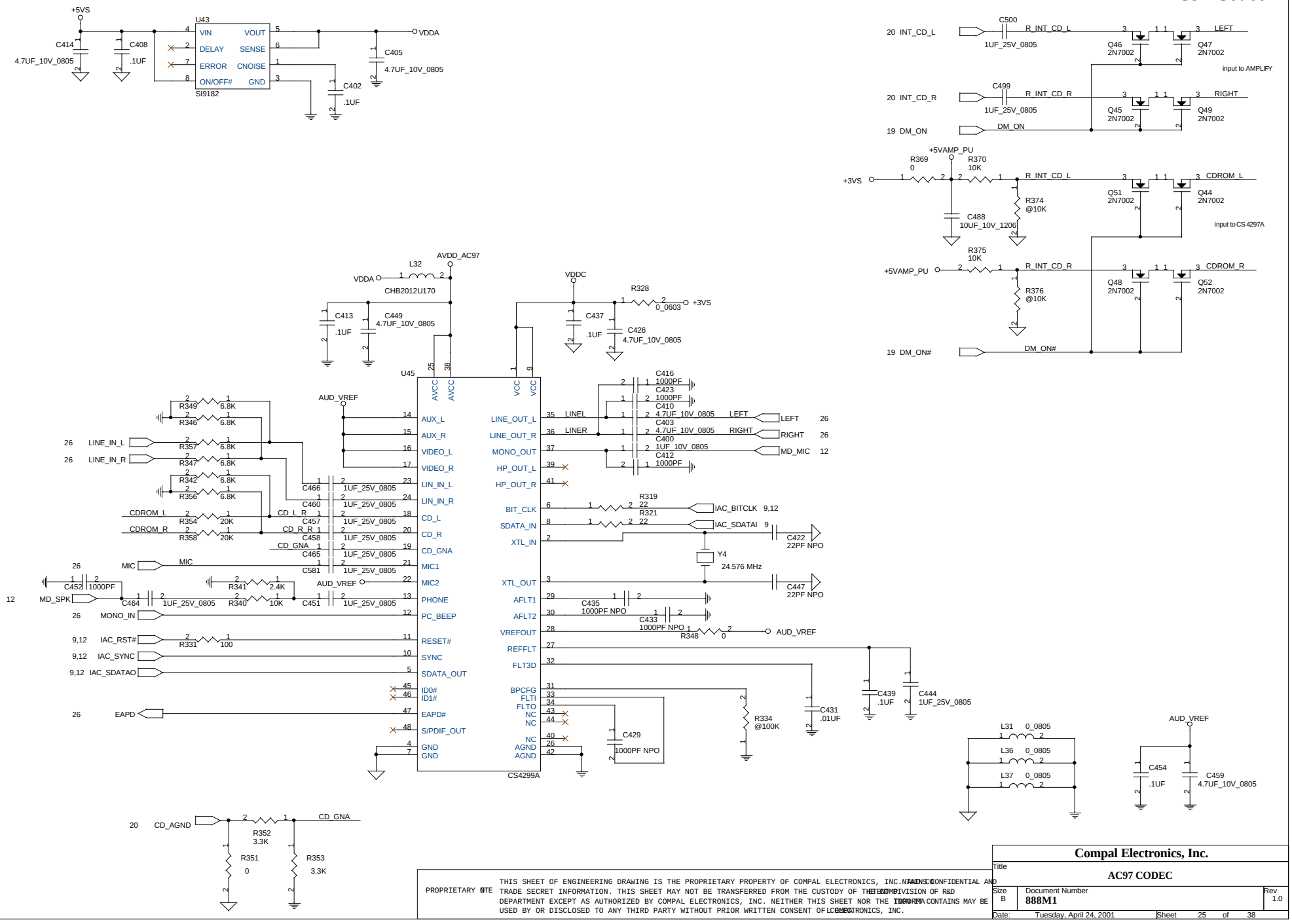
AC97 Codec



Compal Electronics, Inc.			
Title: AC97 CODEC			
Size: B	Document Number: 888M1		Rev: 1.0
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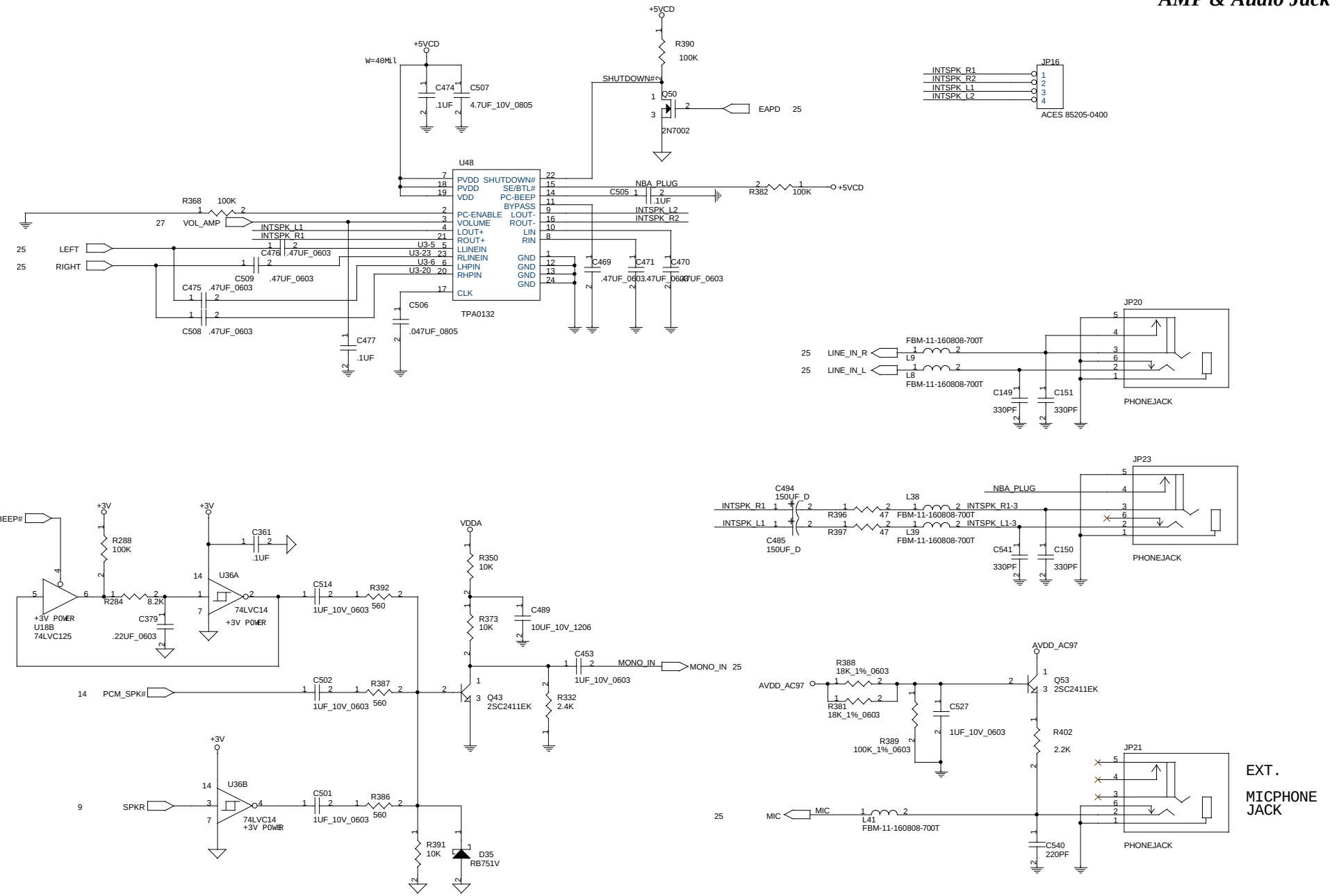
AMP & Audio Jack



Compal Electronics, Inc.			
Title: AMP & Audio Jack			
Size: B	Document Number: 888M1		Rev: 1.0
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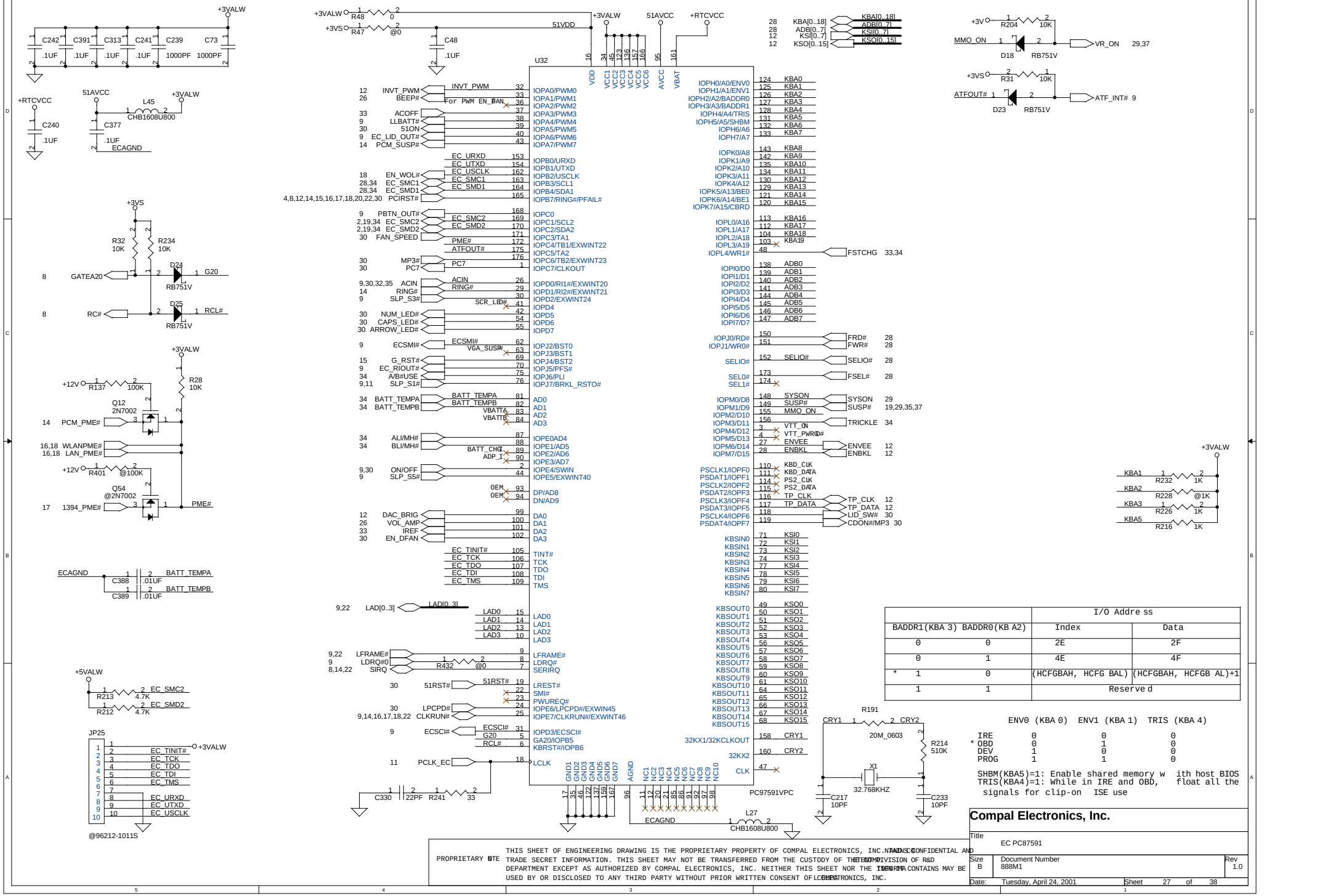

	I/O Addre ss	
BADDR1(KBA 3) BADDR0(KB A2)	Index	Data
0 0	2E	2F
0 1	4E	4F
* 1 0	(HCFGBAH, HCFG BAL)	(HCFGBAH, HCFGB AL)+1
1 1	Reserve d	

	ENV0 (KBA 0)	ENV1 (KBA 1)	TRIS (KBA 4)
IRE	0	0	0
* OBD	0	1	0
DEV	1	0	0
PROG	1	1	0

SHBM(KBA5)=1: Enable shared memory w ith host BIOS
TRIS(KBA4)=1: While in IRE and OBD, float all the signals for clip-on ISE use

Compal Electronics, Inc.

Title: EC PC87591

Size: B | Document Number: 888M1 | Rev: 1.0

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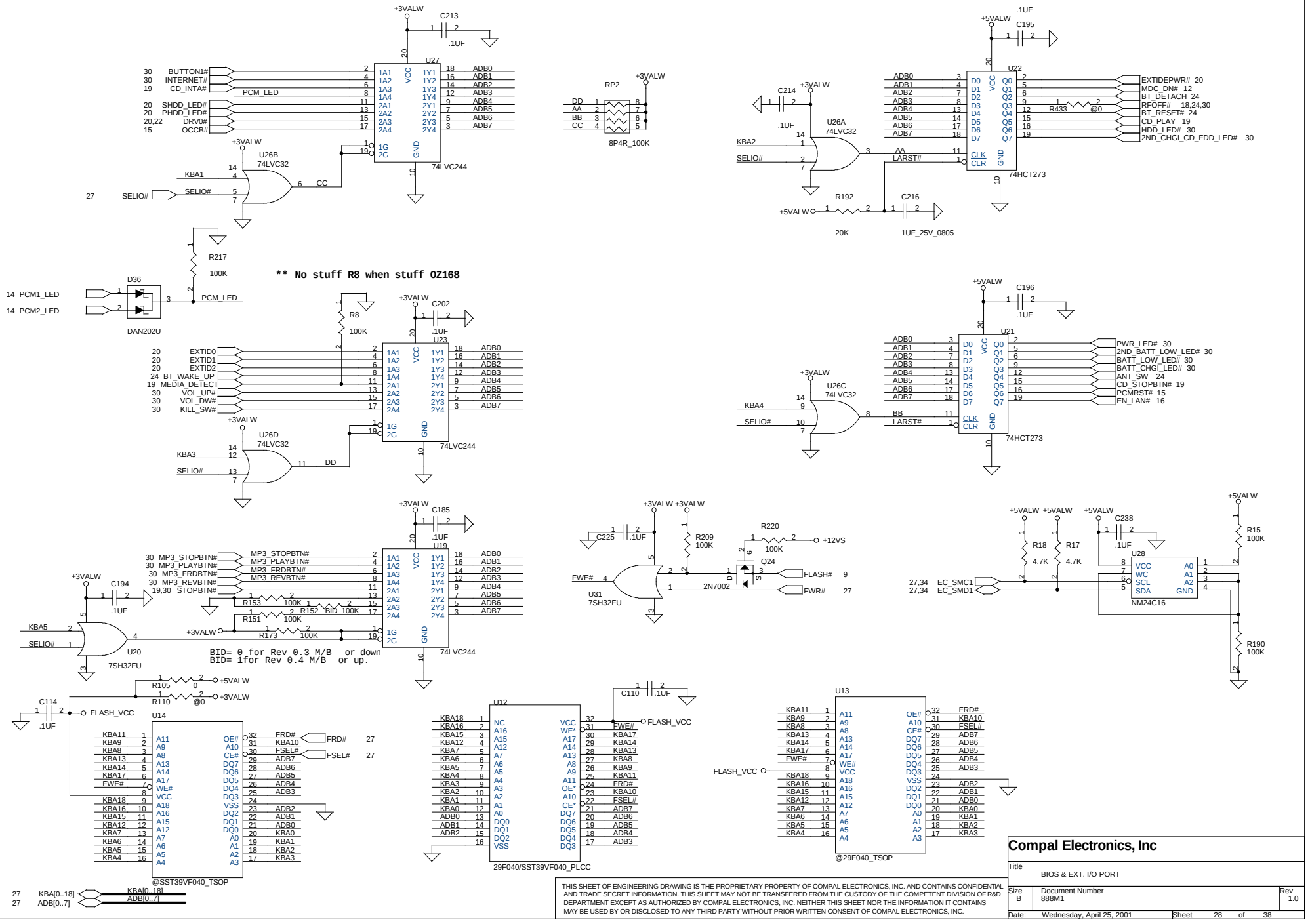

Compal Electronics, Inc

Title: BIOS & EXT. I/O PORT

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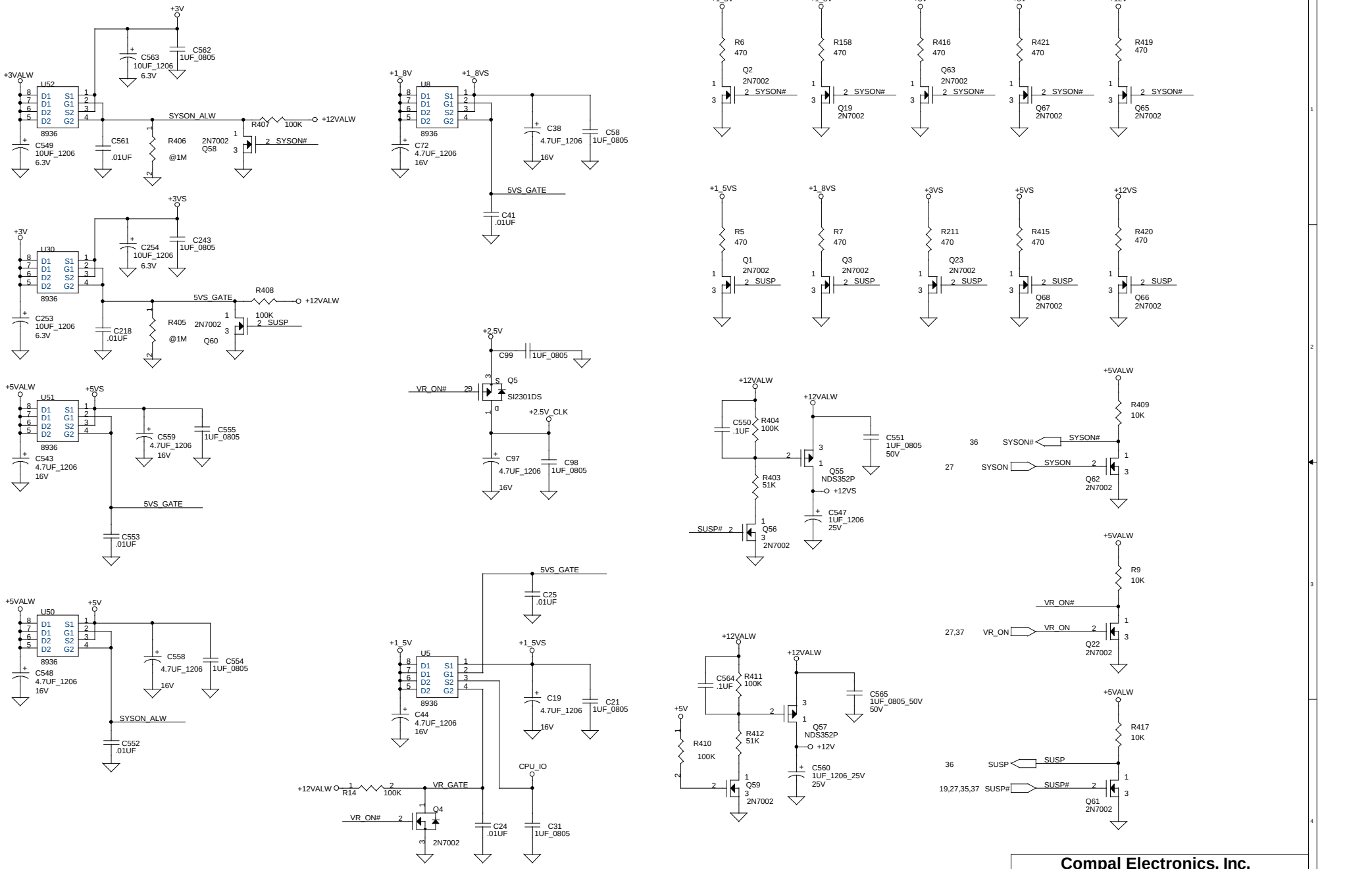
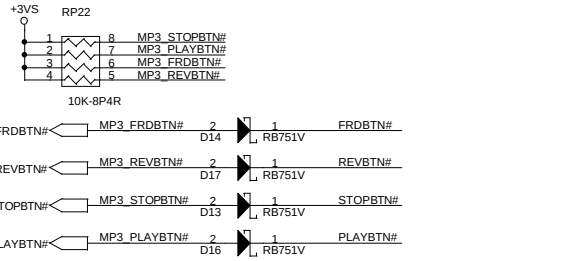
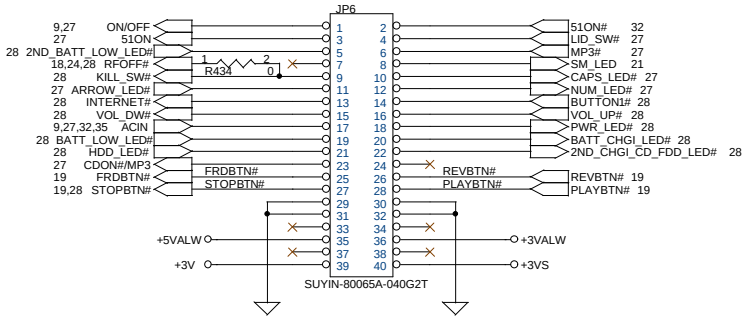
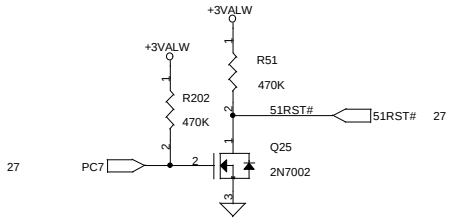


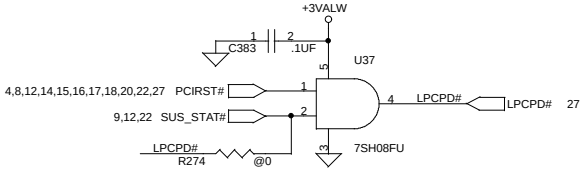
Compal Electronics, Inc.

Title	POWER CONTROL CKT	
Size: B	Document Number: 888M1	Rev: 1.0
Date: Tuesday, April 24, 2001	Sheet 29 of 38	



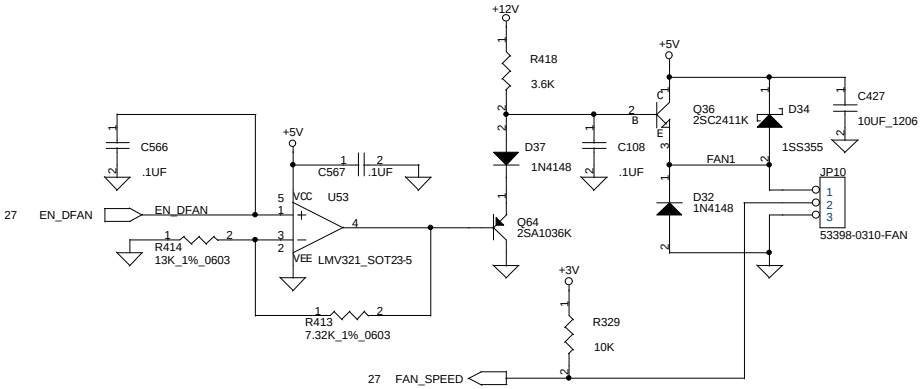






For PC87591 REV 0.A Only

FAN CONN.



Compal Electronics, Inc.

Title	Switchs & Connectors	
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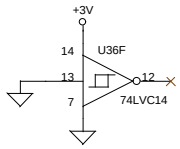
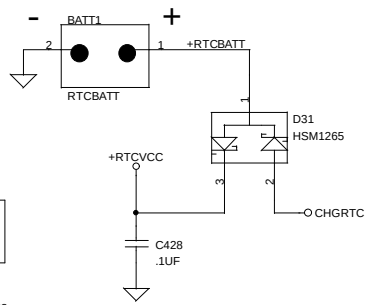
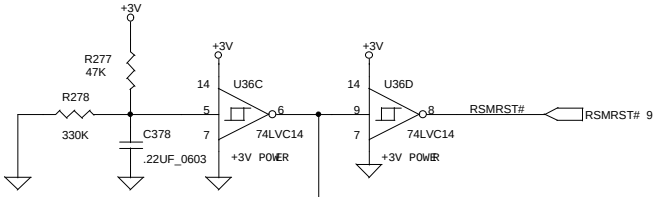
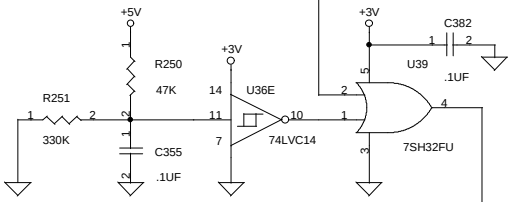
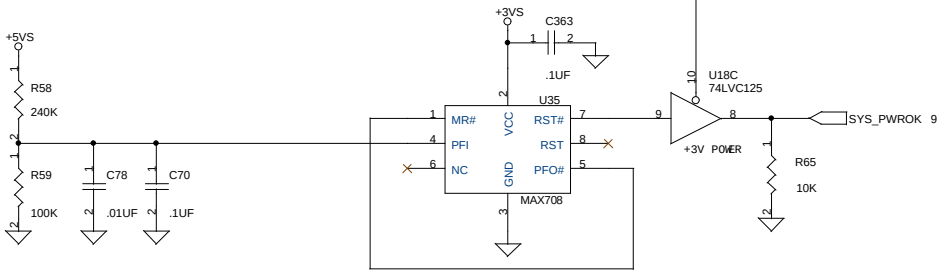
RTC BATT











ST1 Stand-Off 053
ST2 Stand-Off 053
H11 Stand-Off 053
ST3 Stand-Off 090
H2 Stand-Off 090
ST4 Screw Boss 070
H24 Stand-Off 115
H12 J CPU Thermal Plane Screw Hold
H13 J CPU Thermal Plane Screw Hold
H16 J CPU Thermal Plane Screw Hold
H15 J CPU Thermal Plane Screw Hold
H26 I Fixed Position Hold
H5 F Fixed Position Hold
H29 M Fixed Position Hold
H3 A Screw Hold
H23 C Screw Hold
H31 D Screw Hold
H1 F Screw Hold
H4 G Screw Hold
H27 I Screw Hold
H14 L Screw Hold
H30 M Screw Hold
H22 N Screw Hold
H9 O Screw Hold
H10 O Screw Hold
H18 O Screw Hold
H19 O Screw Hold
H28 O Screw Hold
H25 O Screw Hold
H7 E HDD Frame Hold
H8 E HDD Frame Hold
H20 E HDD Frame Hold
H21 E HDD Frame Hold

CF17 SMD40M80 CF1 SMD40M80 CF2 SMD40M80 CF5 SMD40M80 CF6 SMD40M80 CF7 SMD40M80 CF20 SMD40M80 CF19 SMD40M80 CF14 SMD40M80 CF15 SMD40M80
CF4 SMD40M80 CF11 SMD40M80 CF13 SMD40M80 CF10 SMD40M80 CF12 SMD40M80 CF16 SMD40M80 CF18 SMD40M80 CF8 SMD40M80 CF9 SMD40M80 CF3 SMD40M80
FD3 FIDUCAL FD6 FIDUCAL FD2 FIDUCAL FD4 FIDUCAL FD5 FIDUCAL FD1 FIDUCAL

Compal Electronics, Inc.

Title	RESET	
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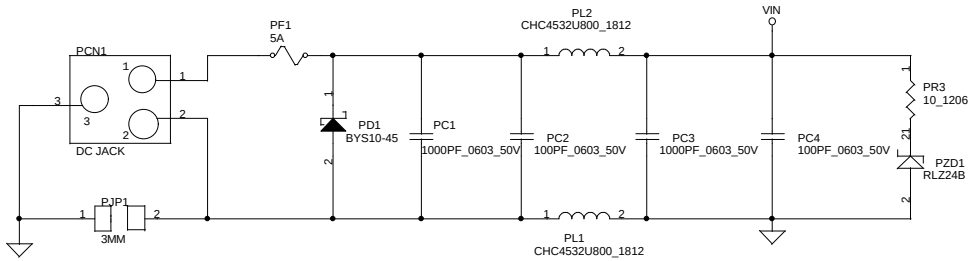
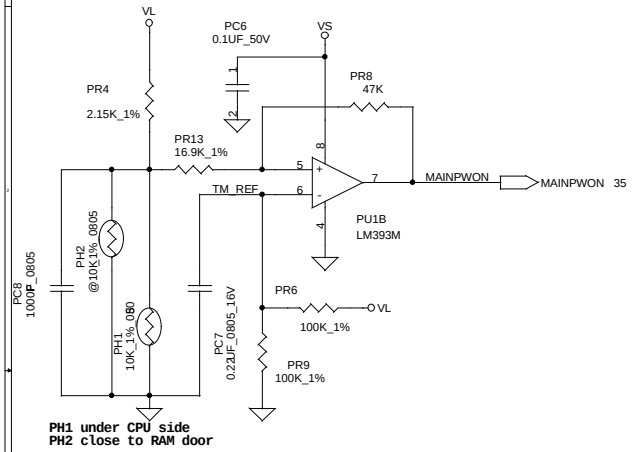
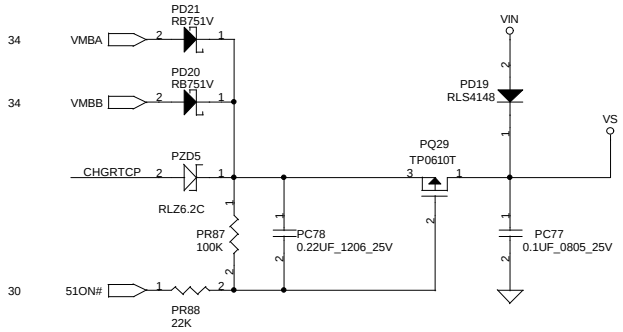
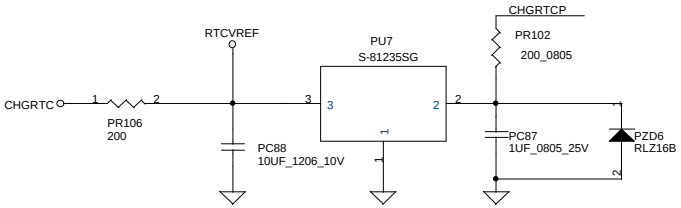


CPU thermal protection at 99 degree C
Recovery at 51 degree C



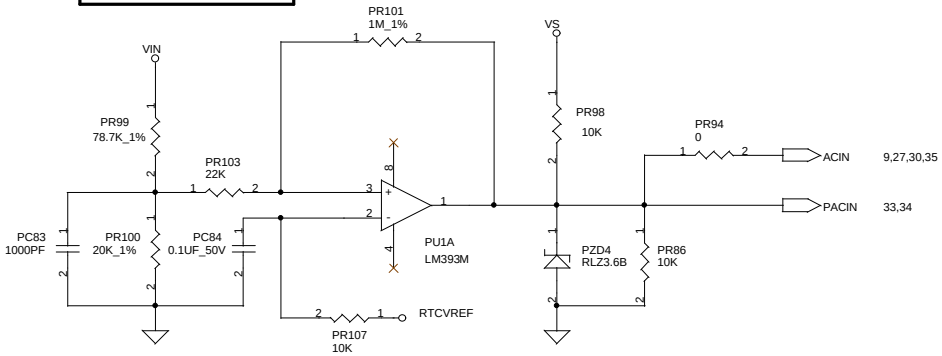
PH1 under CPU side
PH2 close to RAM door

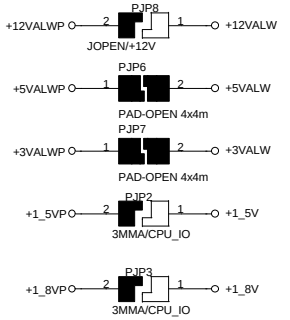




Vin Detector

High 18.7 17.9 17.1
Low 18.0 17.3 16.5





(120mA,20mils ,Via NO.= 1)

(5A,200mils ,Via NO.= 10)

(5A,200mils ,Via NO.= 10)

(1.5A,60mils ,Via NO.= 3)

(3A,120mils ,Via NO.= 6)

COMPAL ELECTRONICS, INC

Title: Connector / DC-DC Interface

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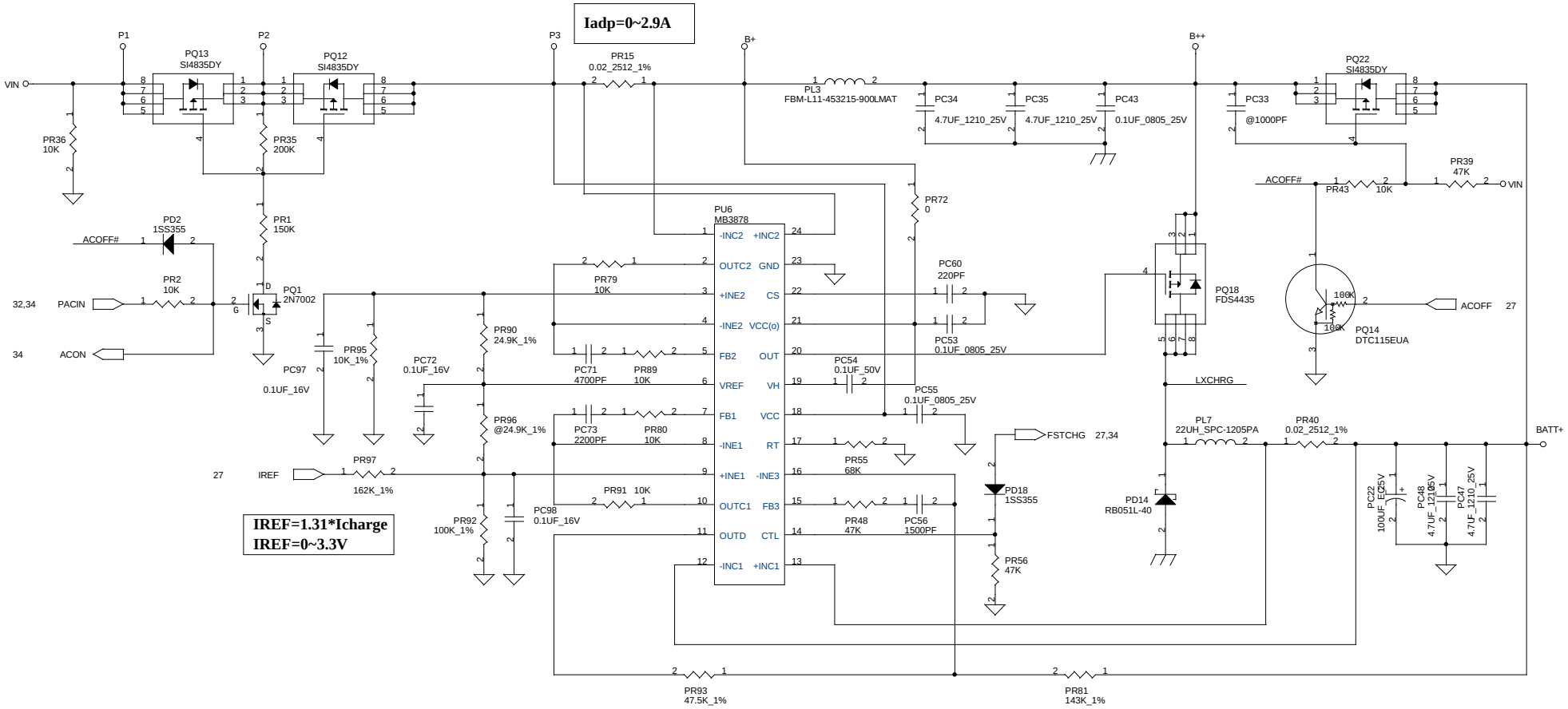

COMPAL ELECTRONICS, INC

Title	CHARGER	
Size: B	Document Number: 888M1	Rev: 1.0
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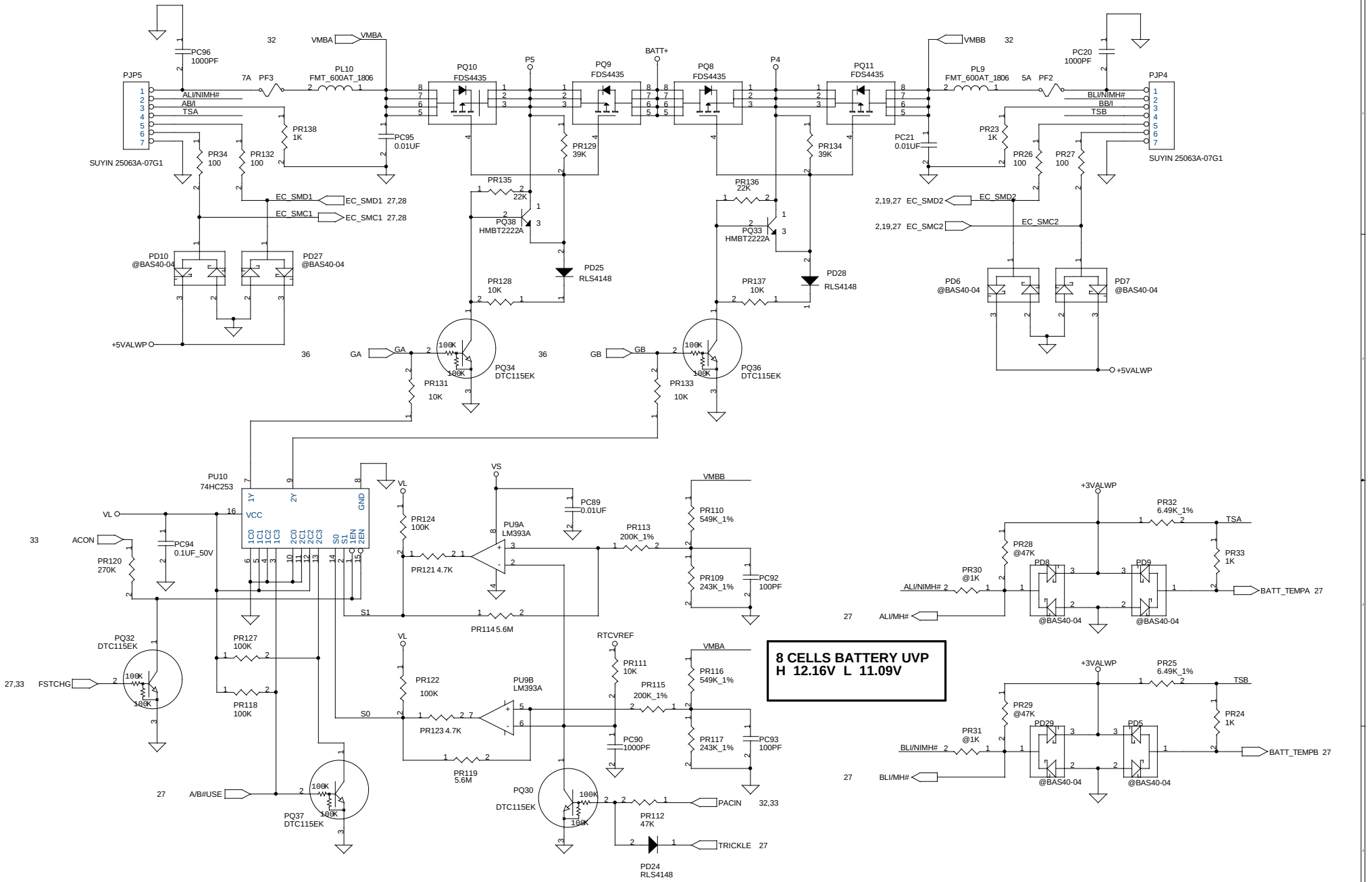

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COMPAL ELECTRONICS, INC

Title	Charger Slecter	
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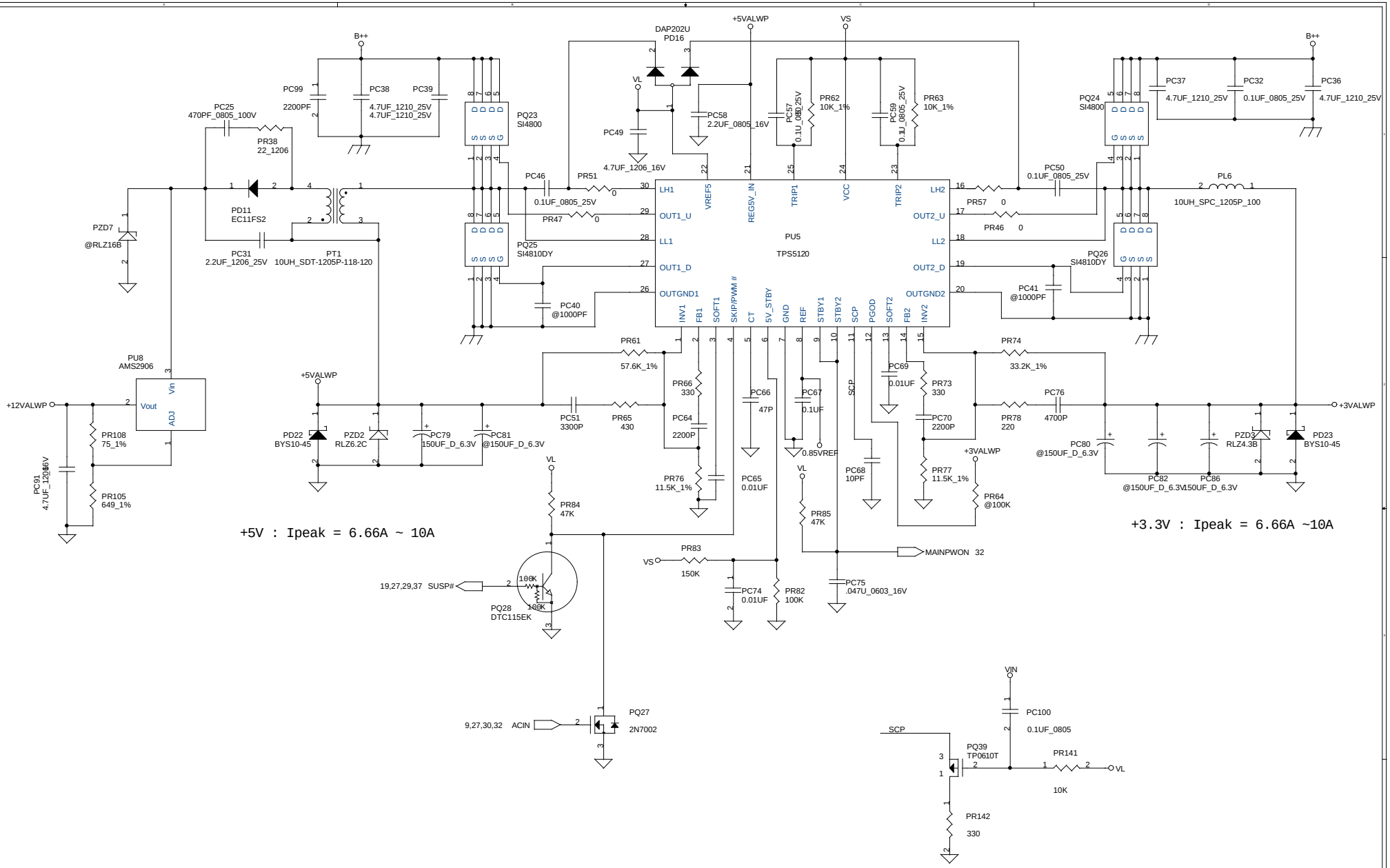

COMPAL ELECTRONICS, INC

Title: 5V/3.3V/12V

Size	Document Number	Rev
B	888M1	1.0

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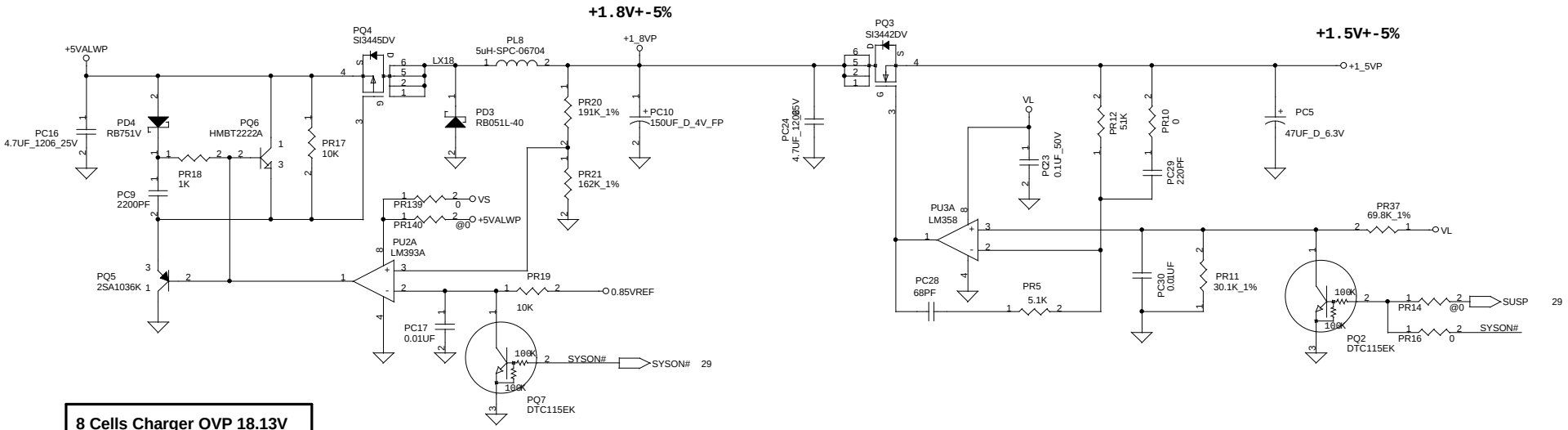
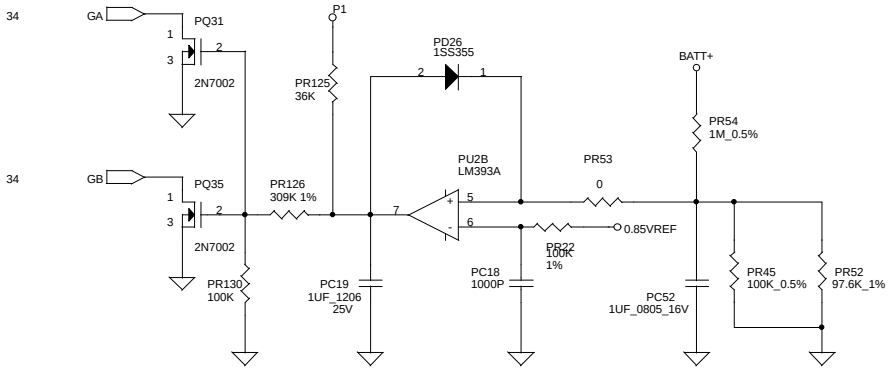


8 Cells Charger OVP 18.13V



COMPAL ELECTRONICS, INC

Title: 1.8V/ CPU_IOP

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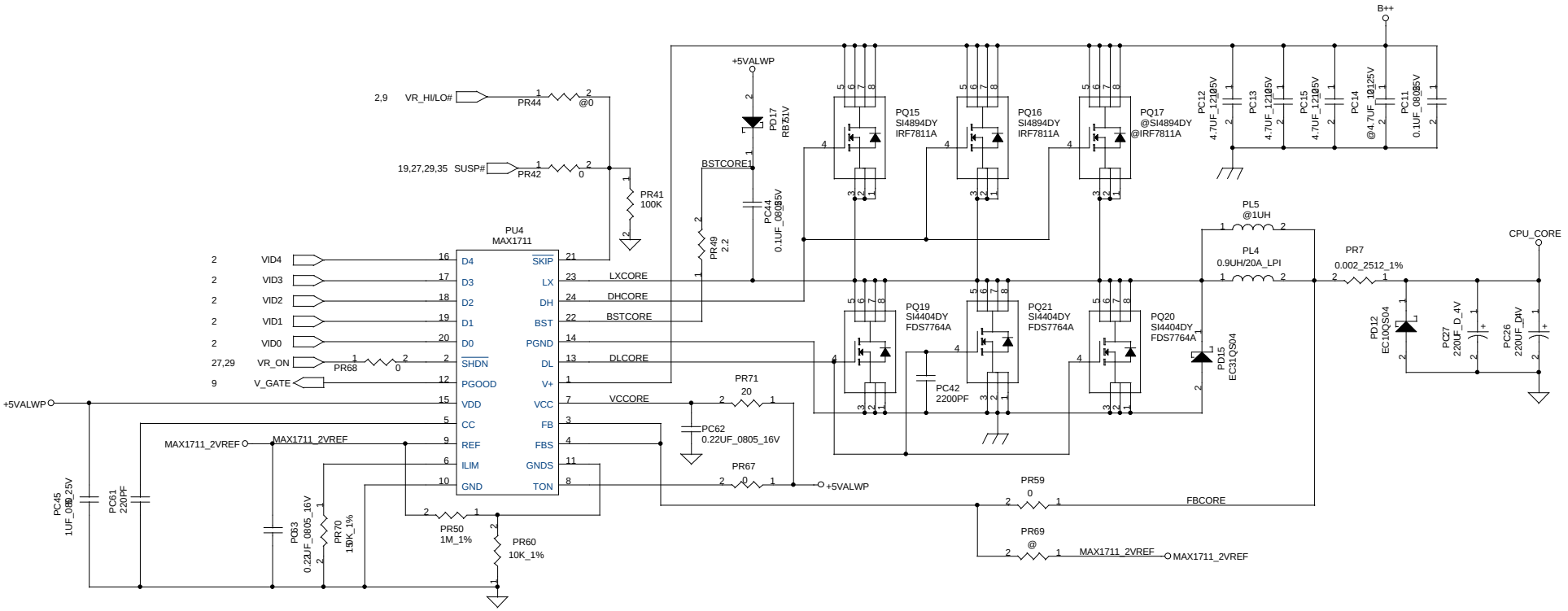

COMPAL ELECTRONICS, INC

Title: CPU_COREP

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888M1 PIR LIST

02/22/01 Written by Jerry

P04: Change VCC of Pull High to +3V at R186, R181, R184, R and R185
P05: Remove @ at R266
P06: Change value of C256 to U22_1206
Change VCC of Pull High to +3V at R203
P07: Change VCC of U10 and C96 to +3V
P09: Remove @ at R45
Add @0 Ohm R435 and R436 between USB3_D+/- and USBBT_D+/-
P10: Change VCC of R63 to 1_8V
Change VCC of R271 to +3V
P12: Change net to CBRST# at pin2 of JP8
P13: Delete net CRISIS# at pin5 of JP2
Delete Q18, Q17, R150, E159, R148, and R147
Change value of R160 to 10K
Change value of R144 to 100K
Change value of R149, R165 to 2.2K
Change VCC of Pull High to +5VS at R160, R5, 16 and R149
Change VCC of Pull High to +12VS at R144
P14: Change net CBRST# at pin 1A1 of U16
P15: Change net to CBRST# at pin# 1 of U15
Add 0 Ohm R429 between Pin3 of U18 and CBRST#
Add @0 Ohm R430 between G_RST# and CBRST#
Change net PCIRST# at pin2 of U18
Change net PCMRST# at pin1 of U18
P16: Add Q69, R431, R437 and Q70 for enable LAN
Change VCC of Pull High to +3VALW at R181
Delete C184
Short pin2 of R154 and R155 to pin1 of R156 and R157
P17: Change net CBRST# to Pin1 of U49
Delete net between Pin 96 of U49 and Pin2 of RP38
P18: Add net RFOFF# at pin10 of JP18
Change net CBRST# at pin2 for R114
P19: Add @ at D82
Remove @ at D15
P20: Delete net SPDIAG# at pin28 of JP17
Delete net SIDE_PRES# at pin40 of JP17
Delete R289, R318, Q28, and Q33
Add U54 and C572 for SIDE Reset
P21: Change circuit of SmartMedia rf ST92163
P22: Delete C165, R143 and U17
P24: Change net RFOFF# at pin1 of Q9
Change net USBBT_D+/- at Pin7 and Pin8 of JP15
P25: Change value of R328 to 0_0603
P26: Change value of C494 and C485 to 150UF
Delete JP22 and change type of JP16 to 4 pin connector
Delete C385 and U40 and replace U18B
P27: Add @0 Ohm R432 between LDRQ#0 and Pin9 of U32
Add @ at R28
P28: Change net KILL_SW# at Pin71 of U23
Add @0 Ohm R433 between pin9 of U22 and RFOFF#
Delete net BT_LED# at pin5 of U21
Add net PCMRST# at pin16 of U21
Add net EN_LAN# at pin10 of U21
P30: Delete C392, C384, U41, and U38
Change type of JP6 to B to B connector
Add 0 Ohm R434 between Pin 9 of JP6 and RFOFF#
Change net KILL_SW# at Pin 9 of JP6
Change net LPCPD# at Pin4 of 37
P31: Delete U33 and C351
Delete Q26 and Replace U18C
Change value of C378 to 0.022_0603
Change type of H11 to Shield-OFF
Pin5 tied to Pin1 of U35
P32: Change PCN1 DC JACK foot print to 5pin for DFX.
P33: Change PQ14 footprint to OT23-Q.
P34: Battery A/B SMBus ESD diode pull high from +5valw to +3valwp.
P34: PF3 fuse change from 5A slow to 7A fast 12mil for ME.
P35: PC57/PC59/PR62/PR63 connect from +B to VS.
P36: Change PC5 from 68UF_EC_25V to Panasonic 47U_6.3V for ME.
Add 0 Ohm PR139 between VS and pin3 of PU2
Add @0 Ohm PR140 between +5VALW and pin1 of PU2

02/26/01 Written by Jerry

P12: Change VCC to +12V at pin 2 of JP11
P21: Add C578, R446, and C579
Add @2SC2411K at Q71

02/27/01 Written by Jerry

P32: Add H2

03/02/01 Written by Jerry

P04: Add buffer between PCIRST# and G20CH2-M
P12: Add R447 and C508 at GA_CLK
P16: Add R448, R449, R450, D38, D39, and D40
P35: Add PR141 100K

03/05/01 Written by Jerry

P25: Add C581 for MIC ircuit

03/06/01 Written by Jerry

P21: Change net SM_FRE# to Pin52 of 47
P31: Change H29, H5 for footprint
Delete H32 and H6

********** Rev0.3 PIR List **********

03/19/01 Written by Jerry

P32: Change value of PC8 to 100PF_0805
Add @ on H2

P33: Add PC97 in pin2 of PU6
Add PC98 in pin9 of PU6
P34: Change value of PR110 and PR 111 to 549K_1%
Change value of PR109 and PR 117 to 243K_1%
Change VCC of ESD Diode to +5VALWP in PD10, PD27, PD6, and PD7
P35: Add PC?? in B++
Add @ in PD7
Change net VS to B++ in Pin2 of PU5
Change value of PR108 to 75_1%
Change value of PR105 to 649_1%
Change value of PR61 to 56K_1%
Change value of PR76 and PR77 to 11.5K_1%
Change value of PR74 to 32K_1%
Change value of PR83 to 150K
Add @ in PR141
Delete PR104, PC85, PR58, and PR75
Change value of PU8 to MS2906
Change pull high VCC to +3VALW in PR64

03/21/01 Written by Jerry

P09: Remove @ in D20
P12: Connection R447 and C580
P16: Delete R448, R449, R450, D38, D39, and D40
Change type of RJ45 to with LED in JP5
Add R452, R453, , Q72, and Q73
P17: Add @ in C524 and C467
Connection Pin87 of U49 and Pin2 of RP38
P19: Remove @ in R182 and Add in D15
P21: Add Q74, Q75, Q76, R454, and C582
Change value of Q71 to 2N7001DS
Add @ in R46
Add U55, R455, R456, and C583
Change value of R443 to 100K
Add net SM_5VON, SM_3VON, SM_LED, and SM_LVD
Change value of C575 to 1U_0603
P24: Add @ in R123, Q9, C131, Q11, C132, C121, R121, 126, C and JP15
P28: Add @ in R8
P30: Add net SM_LED in pin6 of JP6
P31: Update Stand Off

03/23/01 Written by Jerry

P20: Change VCC of Q10 to +5VCD
Change type of JP7 to SUYIN 20252SG4G5-A
P32: Change PR94 10K to 0 ohm
P34: Change PR110, PR116 to 549k and PR109, PR117 to 243k. (UVL:12.16 L:11.09V)
P35: Add PC100, PR141 , PR142, PQ39

03/26/01 Written by Jerry

P13: Change value of L19 to 0_0805
P21:Add F5 for SMVCC

03/28/01 Written by Jerry

P13: Change FootPrint for JP2
P19: Change VCC of R339 to 5VMOD
P31: Modify Hold side in H5 and H29

********** Rev1.0 PIR List **********

04/13/01 Written by Jerry

P02: Add D38 and Q77 for 1.6V/1.7 delete
P16: Change value of R452 and R453 to 000_0603
Update LED circuit
P18: Add D39 for wireless RFOFF#
P22: Add net 17V/16V# in pin50 of U24
P23: Change value of R1 to 4.7_1206
Add R457 4.7_1206
P27: Change net BUTTON_LOCK# to MP3# in pin176 of U32
P28: Add net BID in pin10 of U19
R151 pin 1 tied to 3VALW
P30: Delete net BUTTON_LOCK# in pin2 in of JP6
Add net MP3# in pin6 for JP6
P31: Delete Screw Hole H17
P33: Change value of PR12 to 10K
Delete PU13
P34: Change value PR120 to 270K
P35: Change net B++ to VS in pin1 and 2 of PU5

04/16/01 Written by Jerry

P16: Add R458, R459 for LAN Reset
P28: Add net 2ND_BATT_LOW_LED# in pin5 of U21
Change net name from CD_FDD_LED# to 2ND_CHGD_FDD_LED#
P30: Add net 2ND_BATT_LOW_LED# in pin5 of JP6
Change net name from CD_FDD_LED# to 2ND_CHGD_FDD_LED#

04/18/01 Written by Jerry

P21: Delete RP39, RP40, and R442
Add D40, R460, C585, C584, U56, and U57
Change R444 pull high VCC to +5VS

04/19/01 Written by Jerry

P13: Update JP3 foot print
P16: Add D41
P19: Change value of D33 to RB751
P21: Move net SM_FRE# to pin54 of U47
Move net SM_5VON# to pin25 of U47
Remove @ in R455, R456, C583, and U55
P23: Update JP4 foot print
P23: Update ST1, ST2, ST3, ST4, H11, H2, H24, H12, H13, H10, H15 footprint

04/20/01 Written by Jerry

P35: Change value of PT1 to 10UH-SDT-5020118-120

04/24/01 Written by Jerry

P09: Change value of CP6 and CP5 to EP4R-22PF
P16: Swap signals on pin10 and pin1 of JP5
P21: Change footprint for JP14

04/25/01 Written by Jerry

P21: Add @ in R455, R456, C583, and U55
P28: Remove @ on R8

04/26/01 Written by Jerry

P19: Change value of R307 to 240K
Change value of R313 to 10K

Compal Electronics, Inc.

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