

# QOQAE

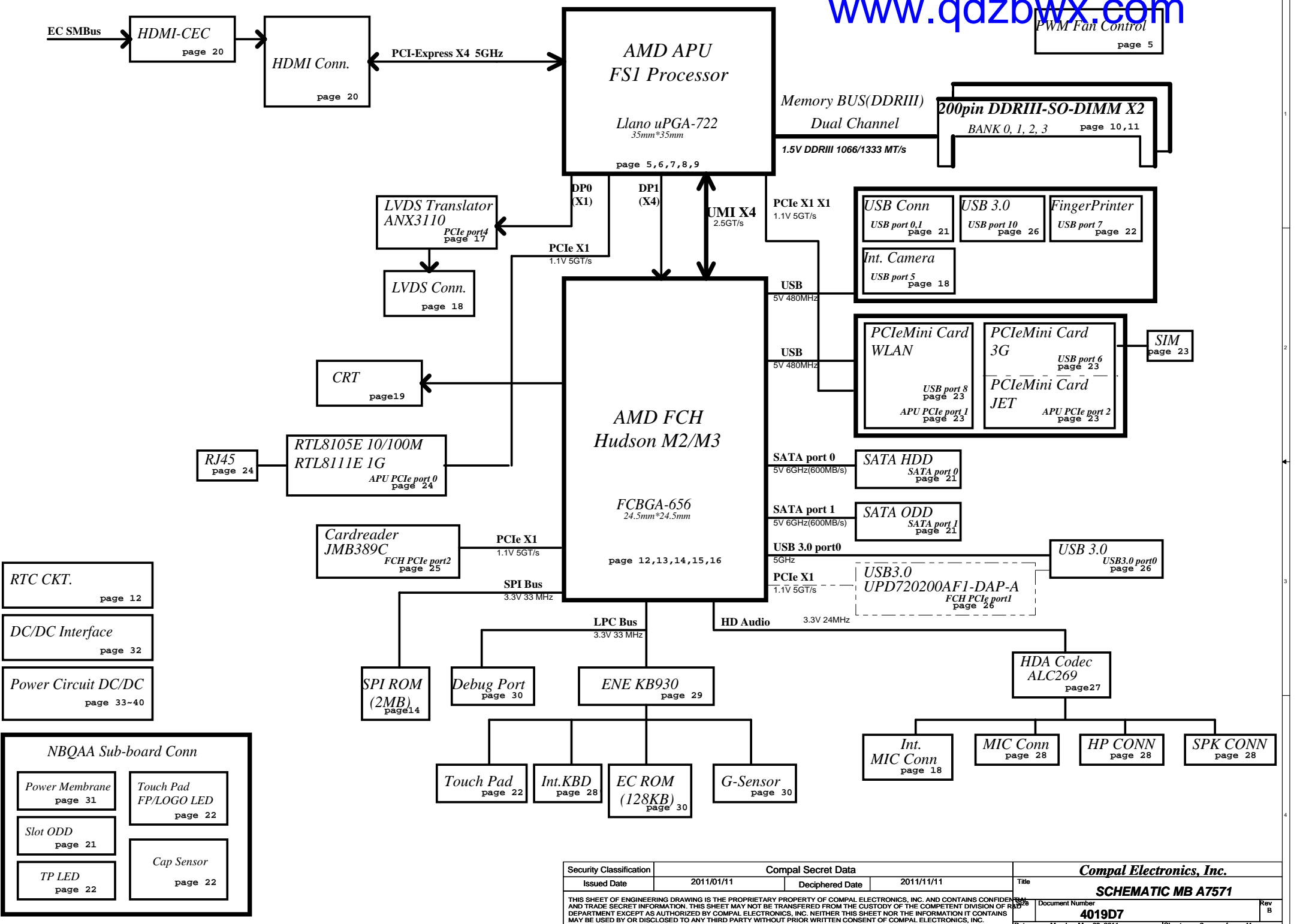
## Ontario 10AS

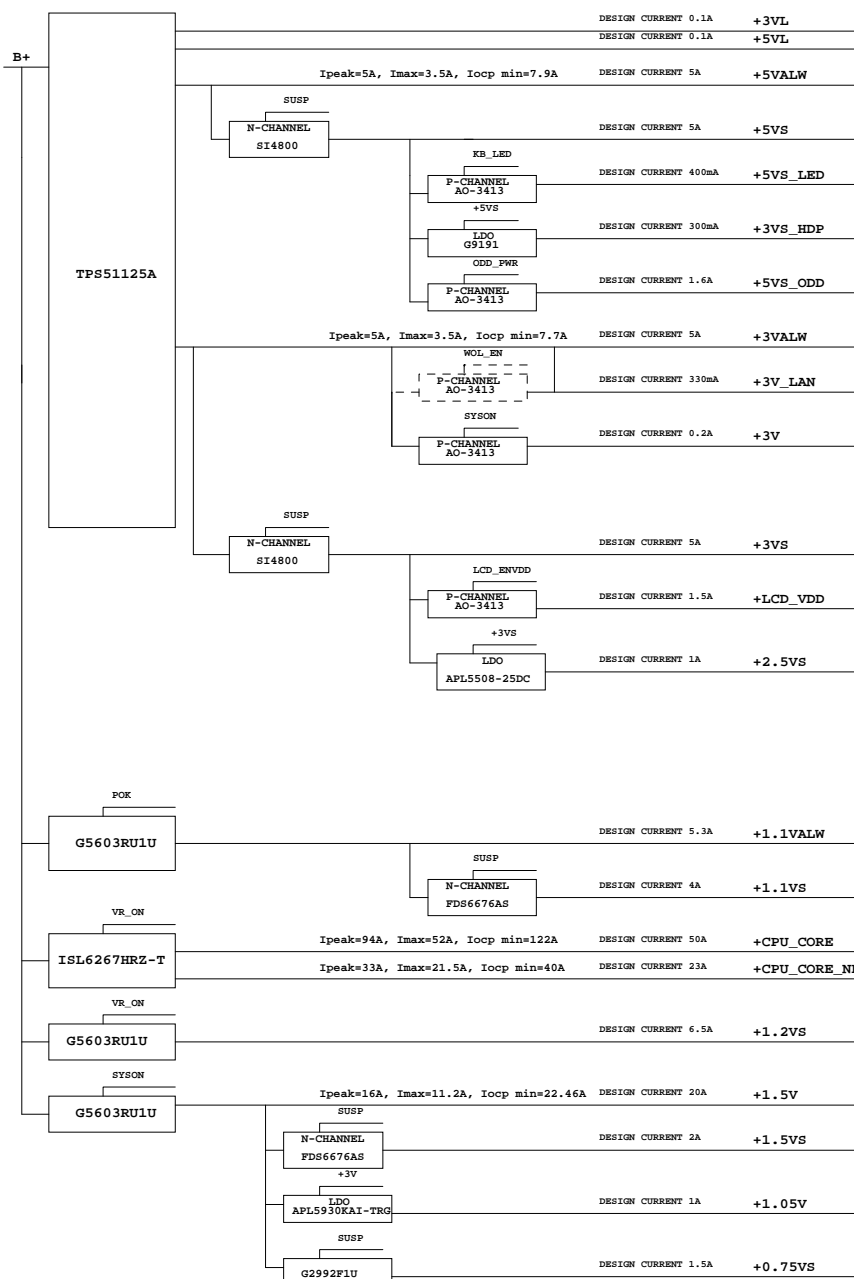
# LA-7571P REV 1.0 Schematic

AMD Llano FS1 Processor / Hudson M2/M3

2011-05-04 Rev 1.0

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2011/01/11	Deciphered Date	2011/11/11	Title	<b>SCHMATIC MB A7571</b>
<small>THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&amp;D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.</small>				Document Number	Rev
				4019D7	B
Date	Monday, May 09, 2011	Sheet	1	of	41





**Voltage Rails**

( O MEANS ON X MEANS OFF )

power plane State	+RTCVC	B+	+5VL +3VL	+5VALW +3VALW +1.1VALW +VSB	+1.5V +3V +1.05V	+5VS +3VS +2.5VS +1.5VS +1.2VS +1.1VS +0.75VS +CPU_CORE +CPU_CORE_NB
	S0	O	O	O	O	O
S1	O	O	O	O	O	O
S3	O	O	O	O	O	X
S5 S4/AC	O	O	O	O	X	X
S5 S4/ Battery only	O	O	O	X	X	X
S5 S4/AC & Battery don't exist	O	X	X	X	X	X

**BTO Option Table**

Function	HDMI-CEC		LAN		CAM+MIC	KB LED	Renesas USB3.0
description		Y	U	V	X	K	Reserved
explain	HDMI	HDMI+CEC	10/100	10/100/1000	CAM	KB LED	Renesas USB3.0
BTO		CEC@	8105E@	8111E@	CAM@	KBL@	RENE@

Function	ODD		Mini Card		Chipset		EC	
description		T	G	J	FCH		N/A	Reserved
explain	Normal	Slot	3G	JET	Hudson-M3		KB-930	KB-9012
BTO	ODD0@	ODD1@	3G@	JET@	3GJET@	HUDM3R1@ HUDM3R3@	KB930@	KB9012@

Function	FCH		UMA
description	HM2	HM3	UM
explain	Hudson-M2	Hudson-M3	
BTO	M2@	M3@	

STATE \ SIGNAL	SLP_S3#	SLP_S5#
Full ON	HIGH	HIGH
S1 (Power On Suspend)	HIGH	HIGH
S3 (Suspend to RAM)	LOW	HIGH
S4 (Suspend to Disk)	LOW	HIGH
S5 (Soft OFF)	LOW	LOW
G3	LOW	LOW

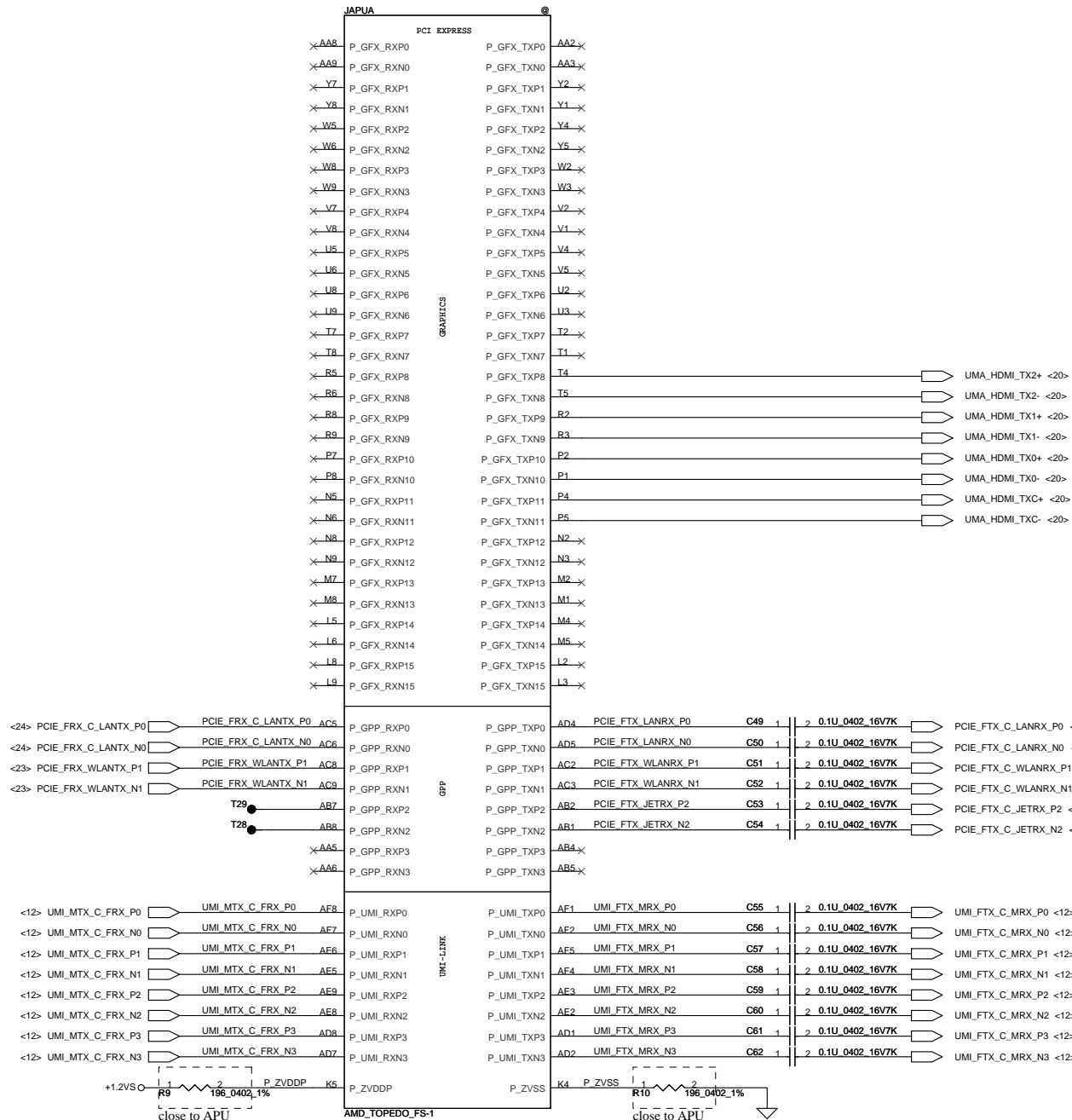
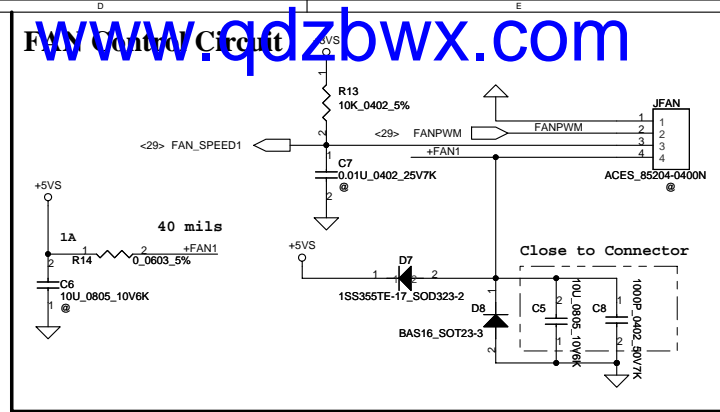
**FCH SM Bus Address (SCL0/SDA0)**

Power	Device	HEX	Address
+3VS	DDR SO-DIMM 0	A0 H	1010 0000 b
+3VS	DDR SO-DIMM 1	A4 H	1010 0100 b
+3VS	WLAN		
+3VS	3G		

**EC SM Bus1 Address**

**EC SM Bus2 Address**

Power	Device	HEX	Address	Power	Device	HEX	Address
+3VL	Smart Battery	16 H	0001 0110 b	+3VS	G-Sensor	40 H	0100 0000 b
+3VL	HDMI-CEC	34 H	0011 0100 b				
Power	Device	HEX	Address				
+3VL	Cap. Sensor		Virtual I2C				



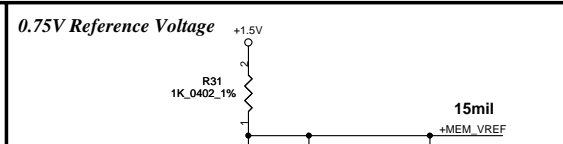
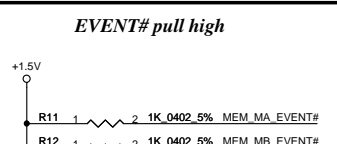
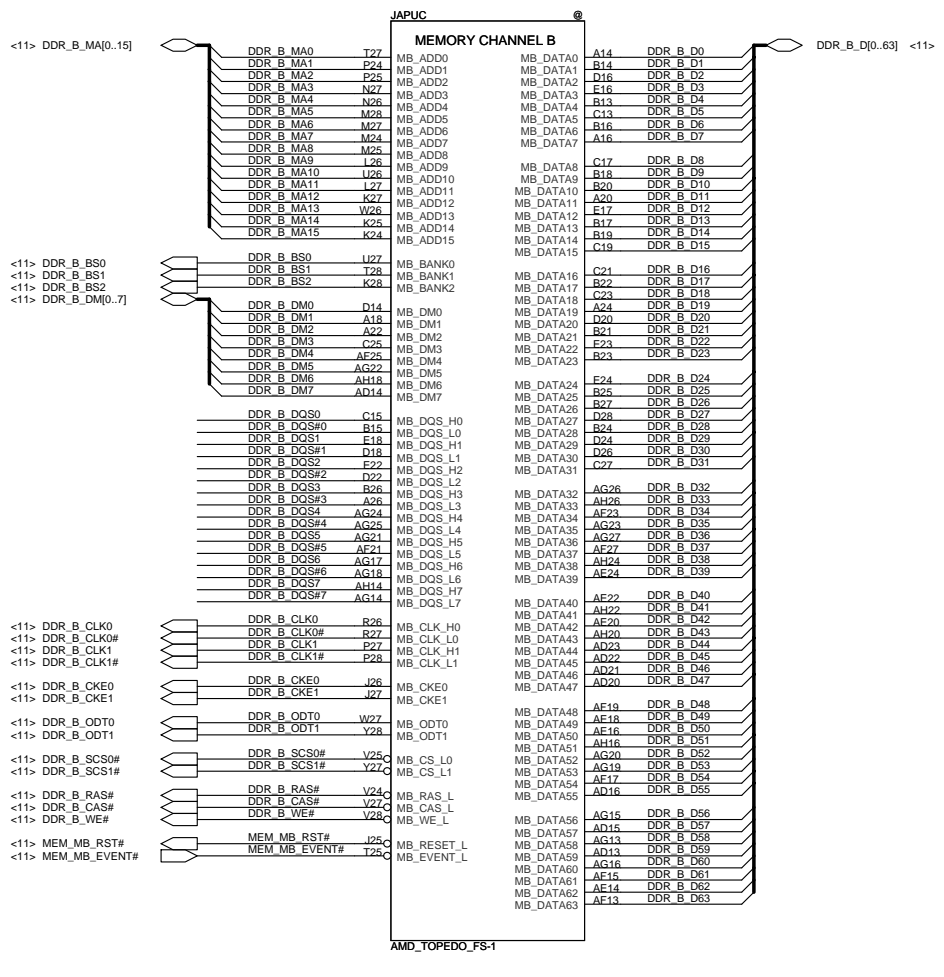
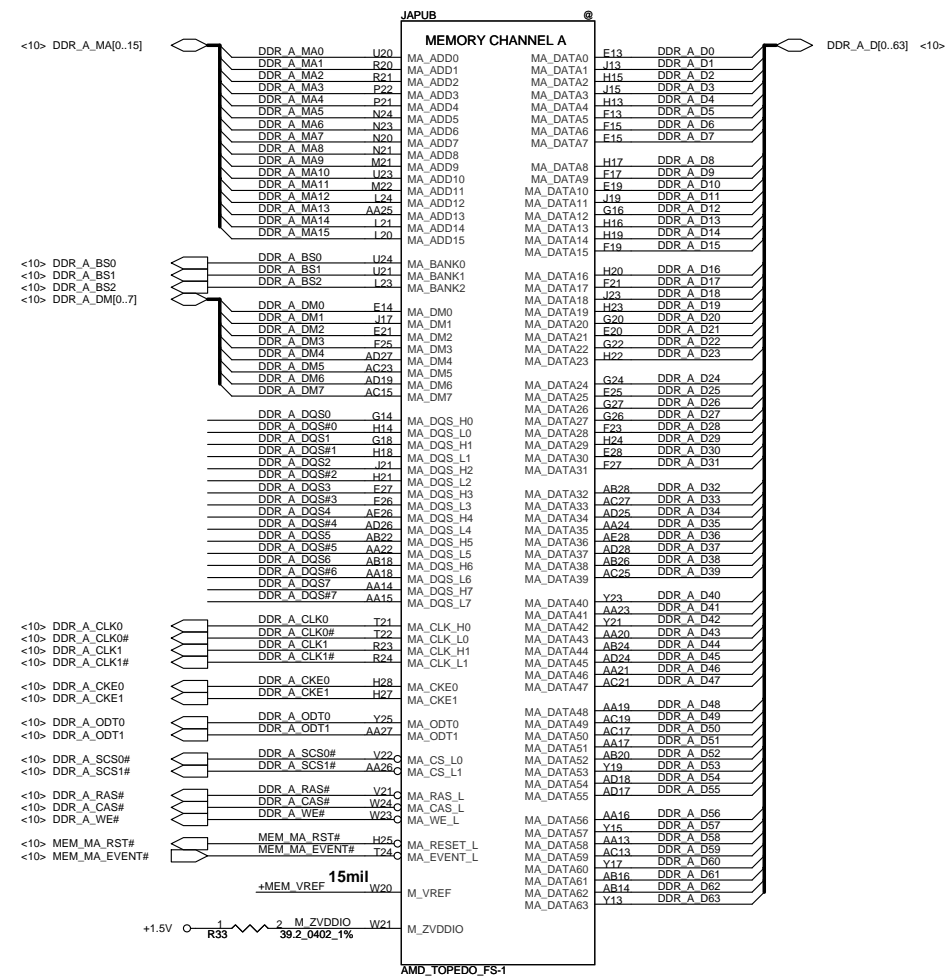
Security Classification		Compal Secret Data		Title	
Issued Date	2011/01/11	Deciphered Date	2011/11/11	SCHEMATIC MB A7571	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				4019D7	B
Date: Monday, May 09, 2011				Sheet	5 of 41

<10> DDR\_A\_DQS[0..7]

<10> DDR\_A\_DQS#[0..7]

<11> DDR\_B\_DQS[0..7]

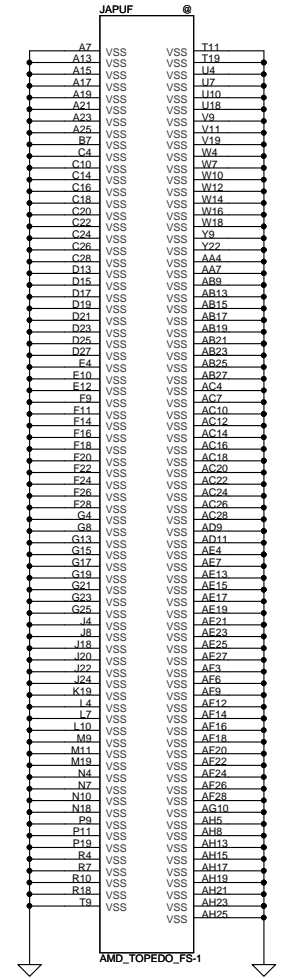
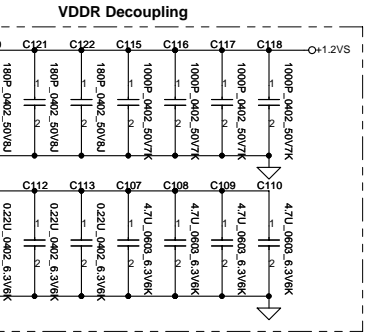
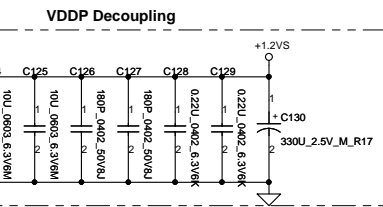
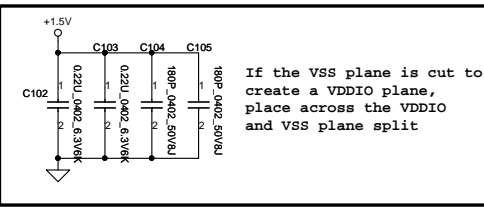
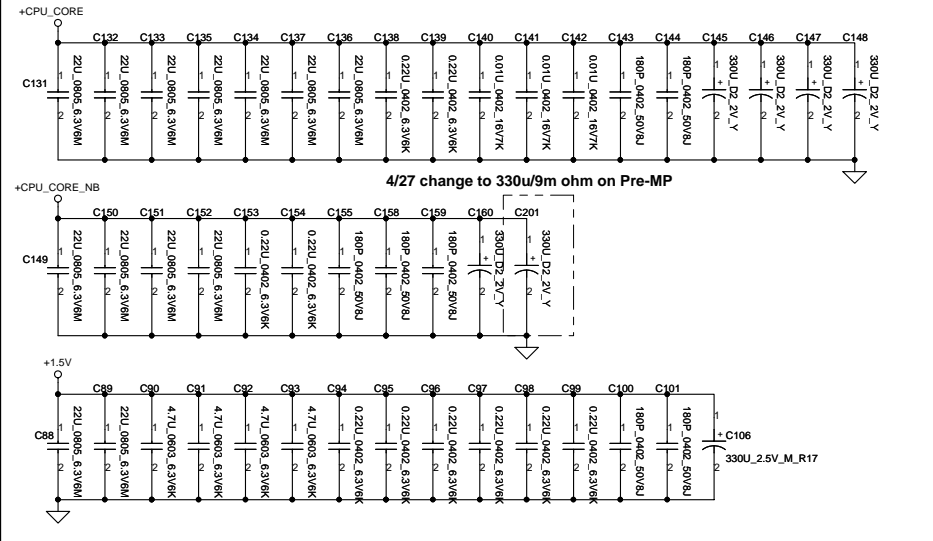
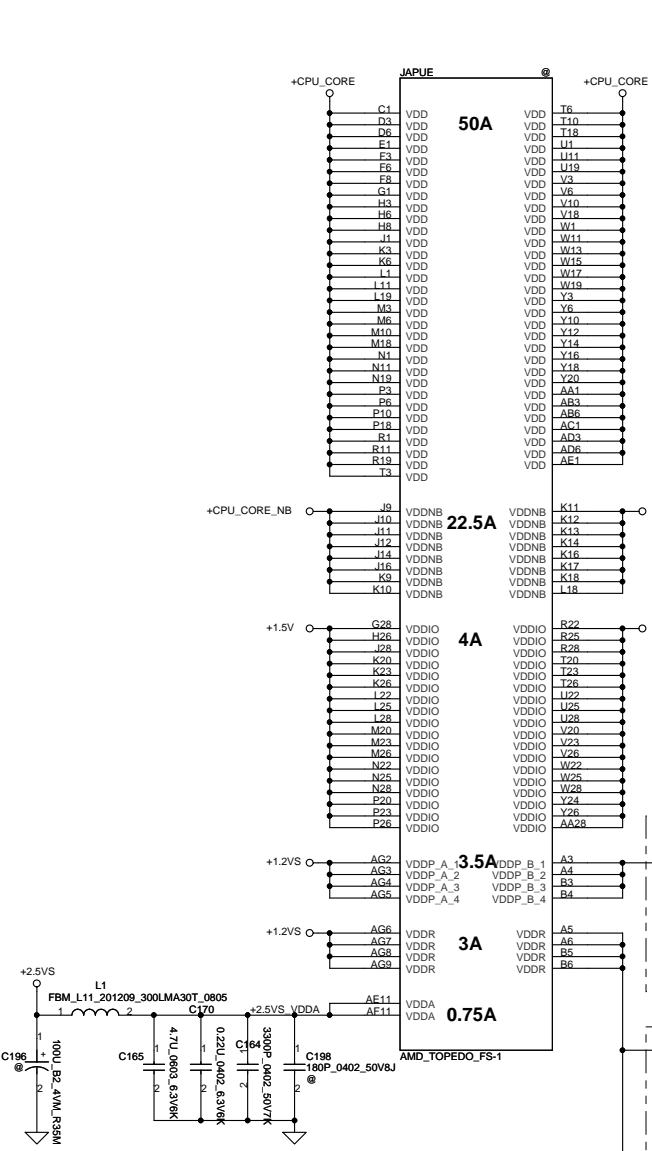
<11> DDR\_B\_DQS#[0..7]



Security Classification	Compal Secret Data		Title	Compal Electronics, Inc.	
Issued Date	2011/01/11	Deciphered Date	2011/11/11	SCHEMATIC MB A7571	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					
Rev	4019D7		Document Number	Rev B	
Date:	Monday, May 09, 2011	Sheet	6	of 41	



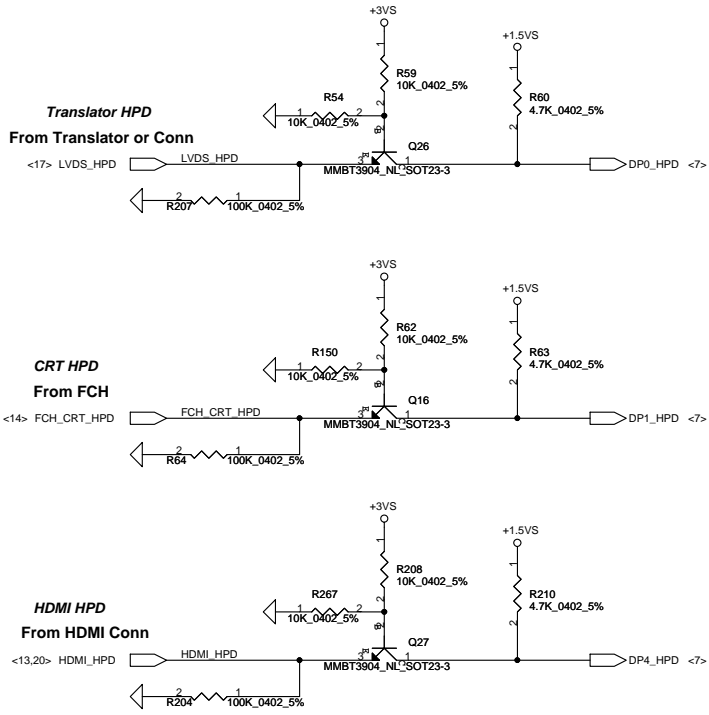
### CPU BOTTOM SIDE DECOUPLING



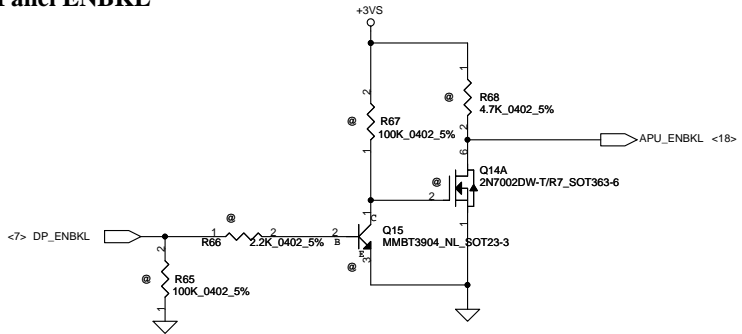
<b>VDD:</b> 470uF x 6 22uF x 9 0.22uF x 2 0.01uF x 3 180pF x 2	<b>VDDNB:</b> 470uF x 4 22uF x 6 0.22uF x 2 180pF x 3	<b>VDDIO:</b> 680uF x 1 330uF x 1 22uF x 2 4.7uF x 4 0.22uF x 8 180pF x 4	<b>VDDP/R_PWM:</b> 470uF x 2 10uF x 1	<b>VDDP:</b> 10uF x 3 0.22uF x 2 180pF x 2	<b>VDDR:</b> 4.7uF x 4 0.22uF x 4 1nF x 4 180pF x 4	<b>VDDA:</b> 100uF x 1 4.7uF x 1 0.22uF x 1 3.3nF x 1
---	---	---	---	---	---	---



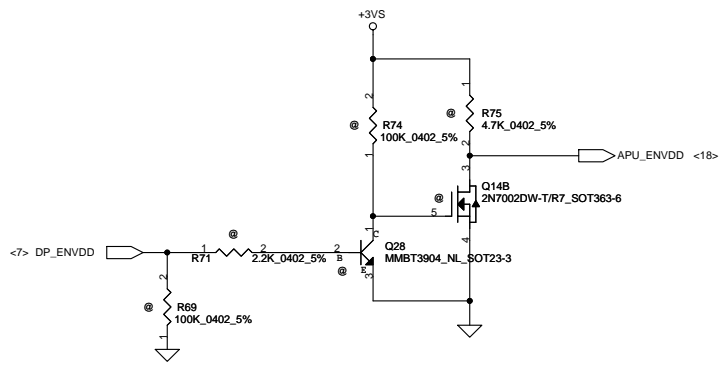
HPD



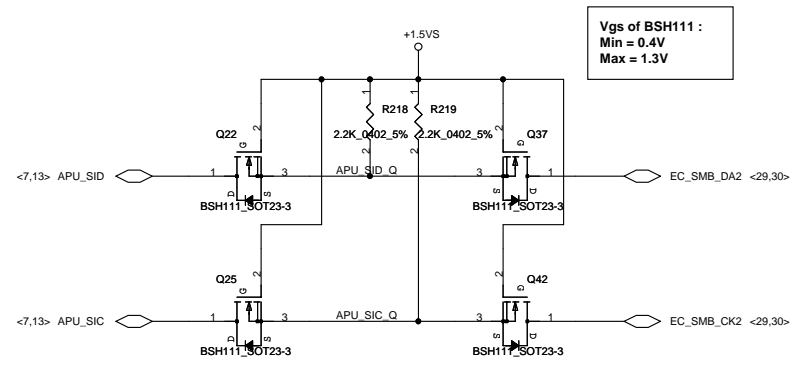
Panel ENBK L



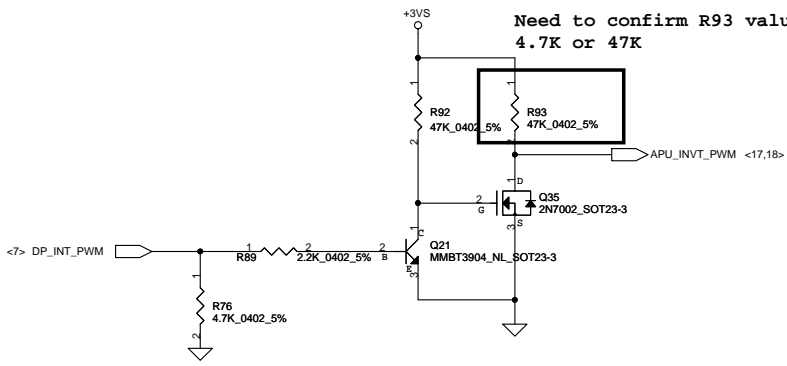
Panel ENVDD



SB-TSI

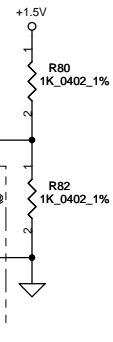
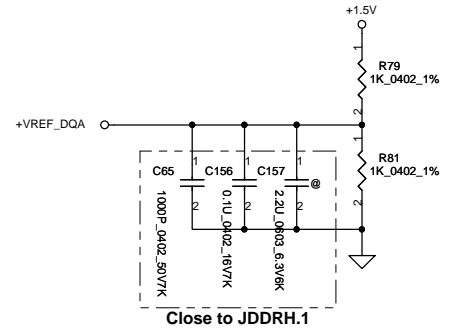
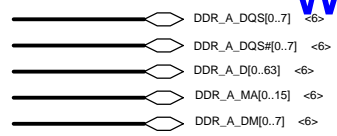
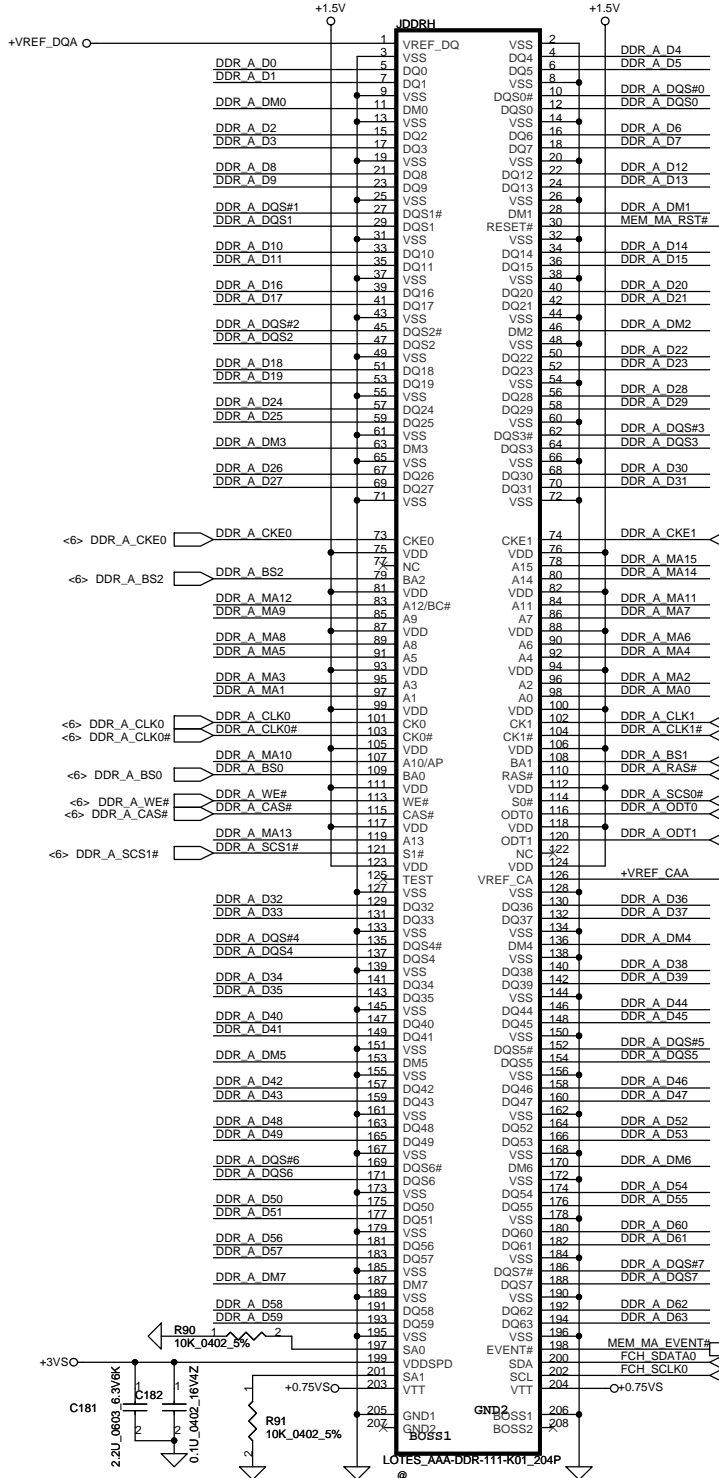


Panel PWM

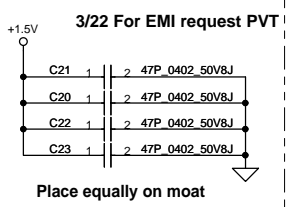
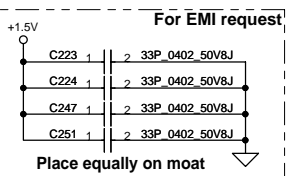
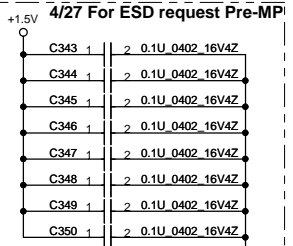
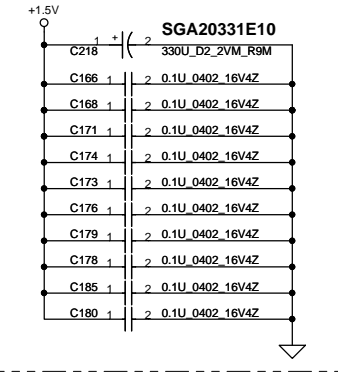


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/01/11	Deciphered Date	2011/11/11	Title	Schematic
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				4019D7	B
Date: Monday, May 09, 2011				Sheet	9 of 41

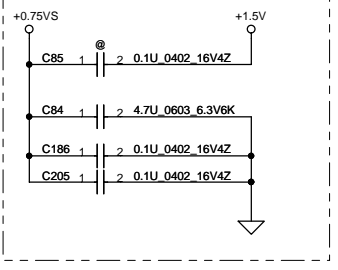
DDR3 SO-DIMM A Standard Type



Layout Note:  
Place near JDDRH



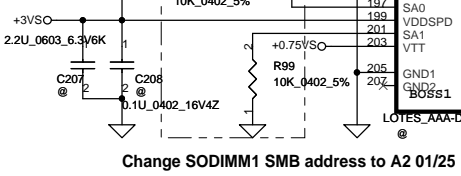
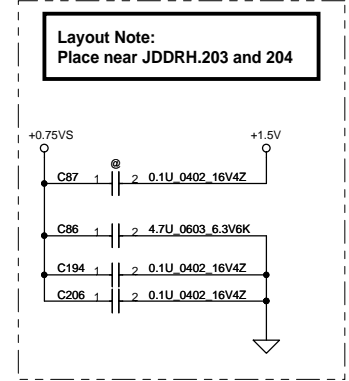
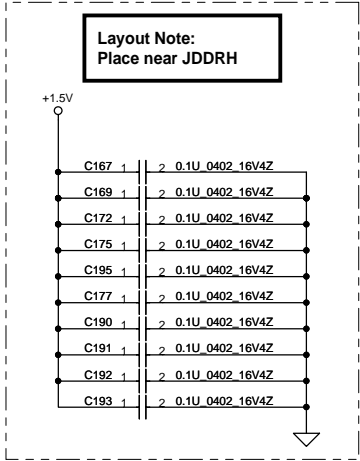
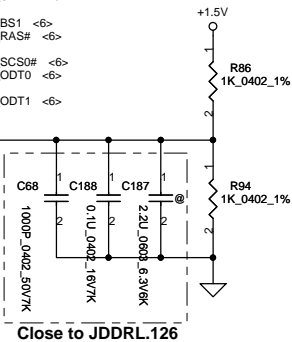
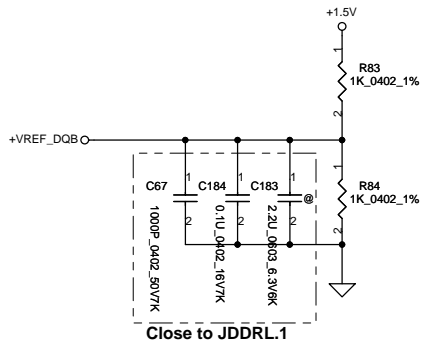
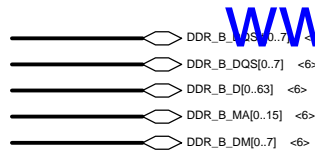
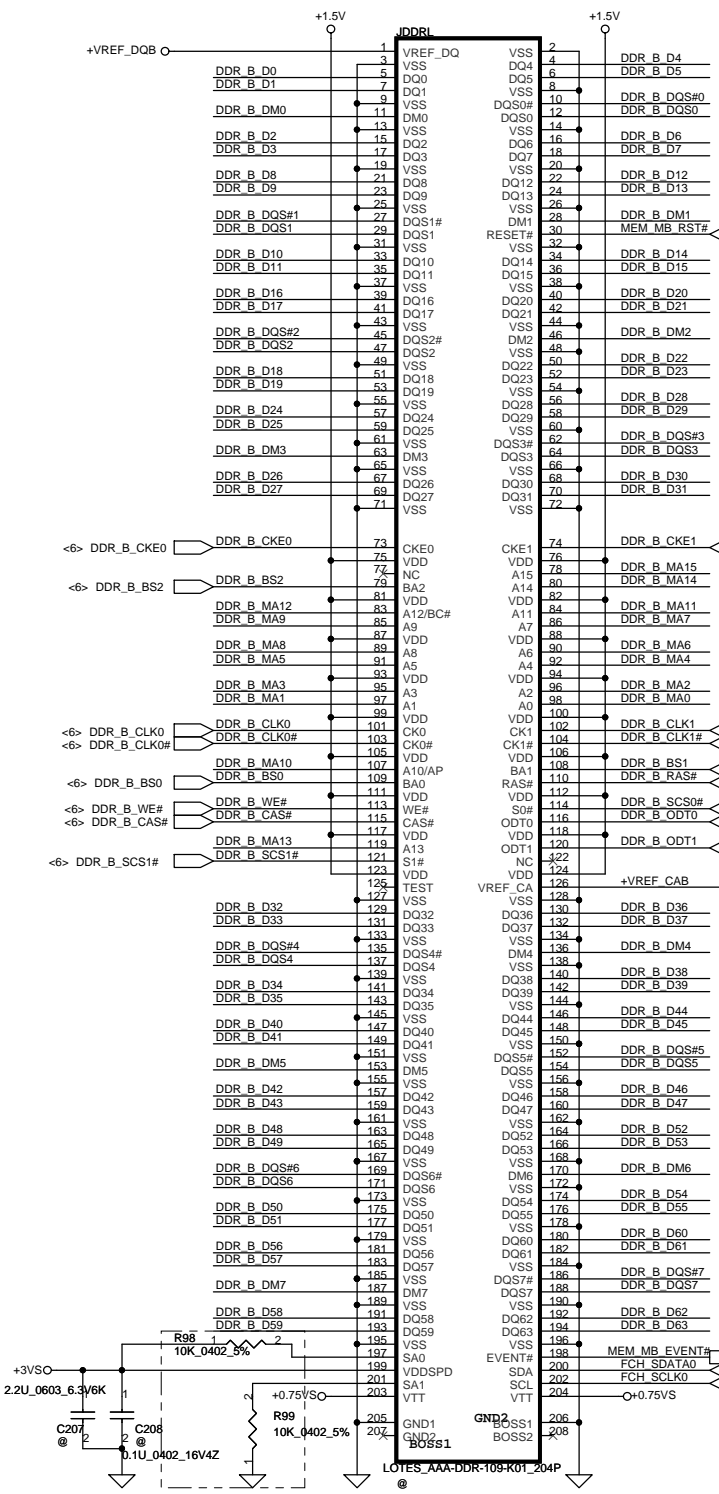
Layout Note:  
Place near JDDRH.203 and 204



Security Classification		Compal Secret Data	
Issued Date	2011/01/11	Deciphered Date	2011/11/11
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.			

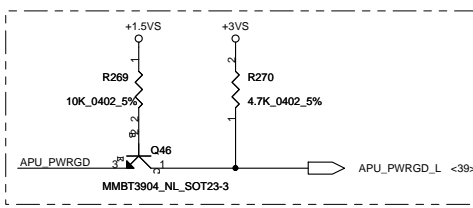
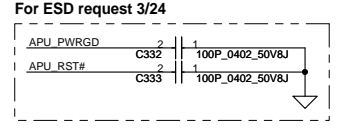
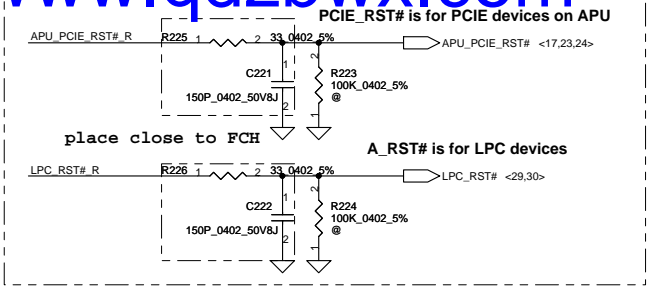
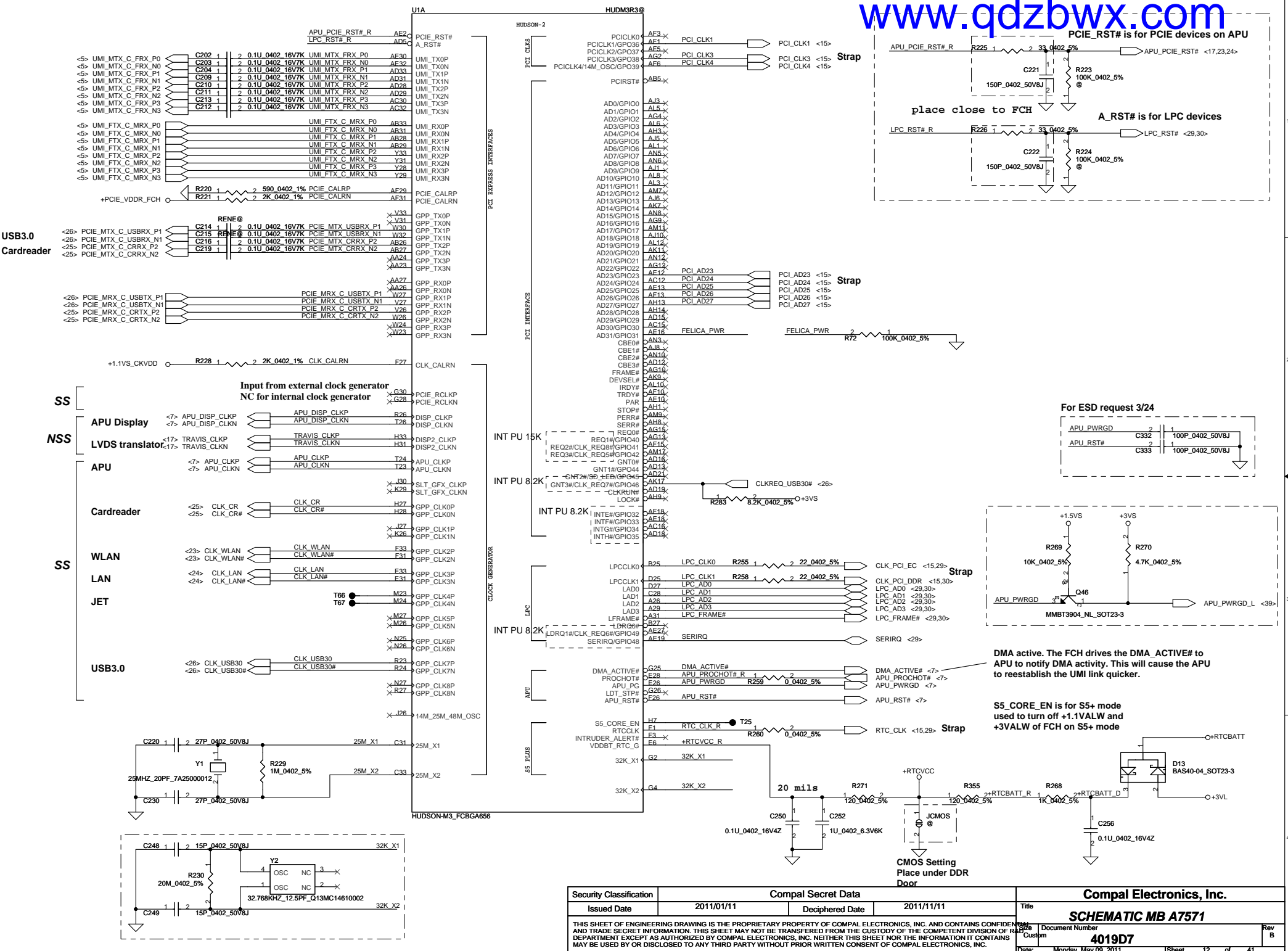
Compal Electronics, Inc.	
Title <b>SCHEMATIC MB A7571</b>	
Size Custom	Document Number <b>4019D7</b>
Date Monday, May 09, 2011	Rev B
Sheet 10	of 41

DDR3 SO-DIMM B Standard Type



Security Classification	Compal Secret Data	
Issued Date	2011/01/11	Deciphered Date
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		

Compal Electronics, Inc.	
Title	
SCHEMATIC MB A7571	
Size	Document Number
Custom	4019D7
Date	Monday, May 09, 2011
Sheet	11 of 41



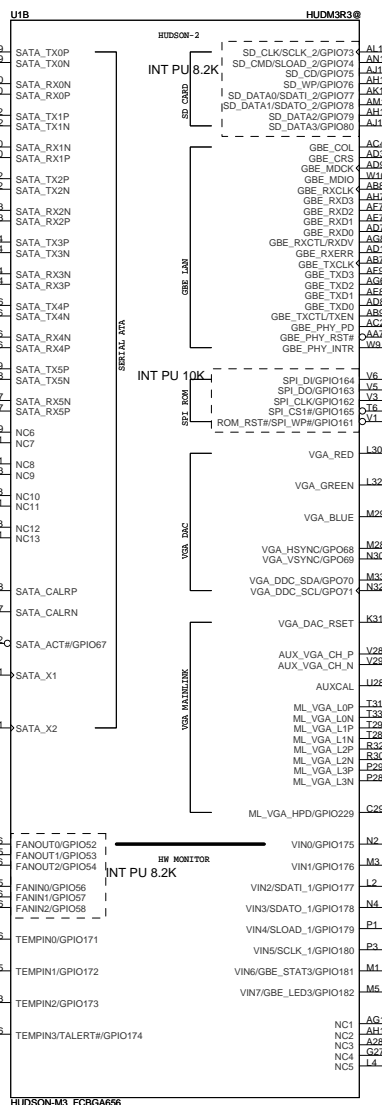
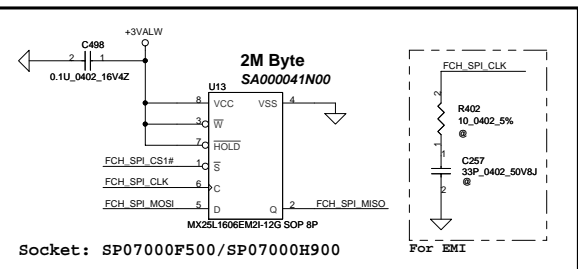
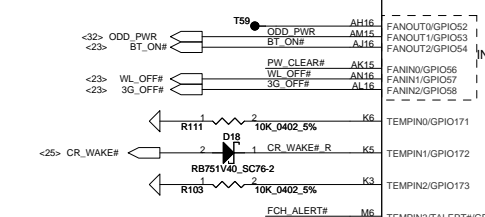
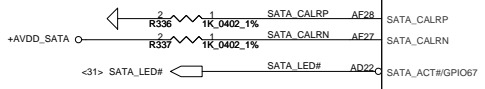
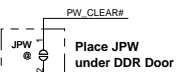
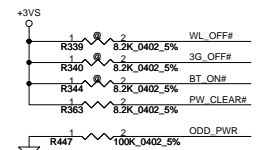
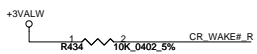
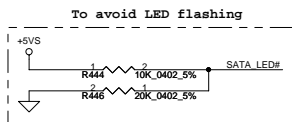
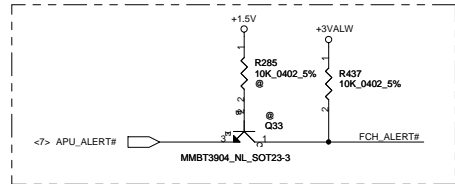
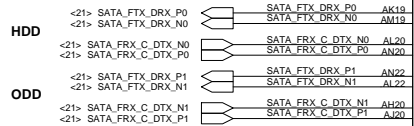
DMA active. The FCH drives the DMA\_ACTIVE# to APU to notify DMA activity. This will cause the APU to reestablish the UMI link quicker.

S5\_CORE\_EN is for S5+ mode used to turn off +1.1VALW and +3VALW of FCH on S5+ mode

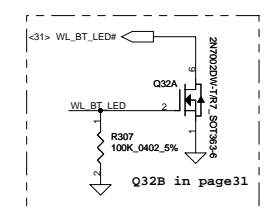
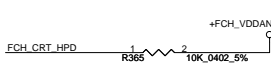
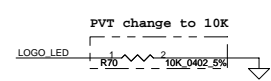
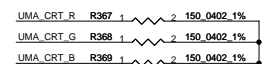
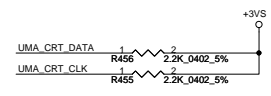
CMOS Setting Place under DDR Door

Security Classification			Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/01/11	Deciphered Date	2011/11/11	Title	SCHEMATIC MB A7571	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						
Rev	B	Customer	4019D7	Date:	Monday, May 09, 2011	Sheet 12 of 41



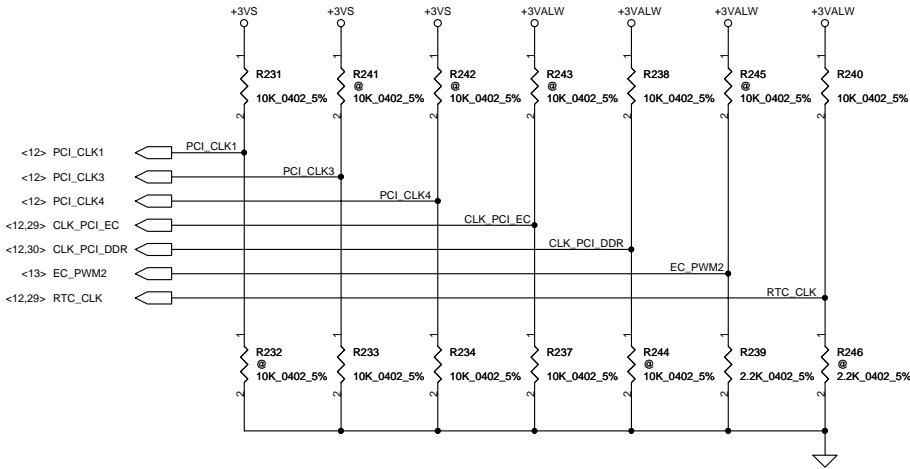


SKU	Non-HDMI SKU	HDMI SKU



**STRAP PINS**

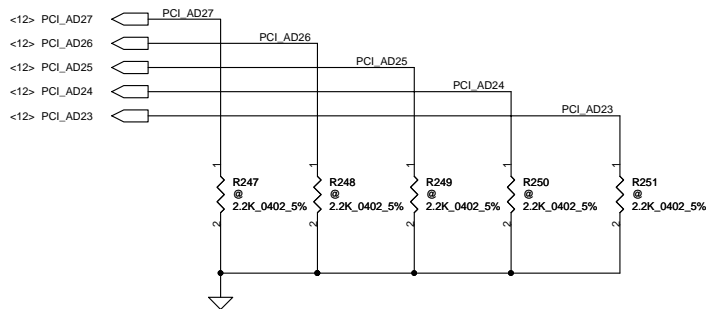
	PCI_CLK1	PCI_CLK3	PCI_CLK4	LPC_CLK0	LPC_CLK1	EC_PWM2	RTC_CLK
<b>PULL HIGH</b>	ALLOW PCIE GEN2 DEFAULT	ENABLE DEBUG STRAP	NON_FUSION CLOCK MODE	EC ENABLED	CLKGEN ENABLED DEFAULT	LPC ROM (INTERNAL 10K PULL-UP)	S5 PLUS MODE DISABLED DEFAULT
<b>PULL LOW</b>	FORCE PCIE GEN1	DISABLE DEBUG STRAP DEFAULT	FUSION CLOCK MODE DEFAULT	EC DISABLED DEFAULT	CLKGEN DISABLE	SPI ROM DEFAULT	S5 PLUS MODE ENABLED



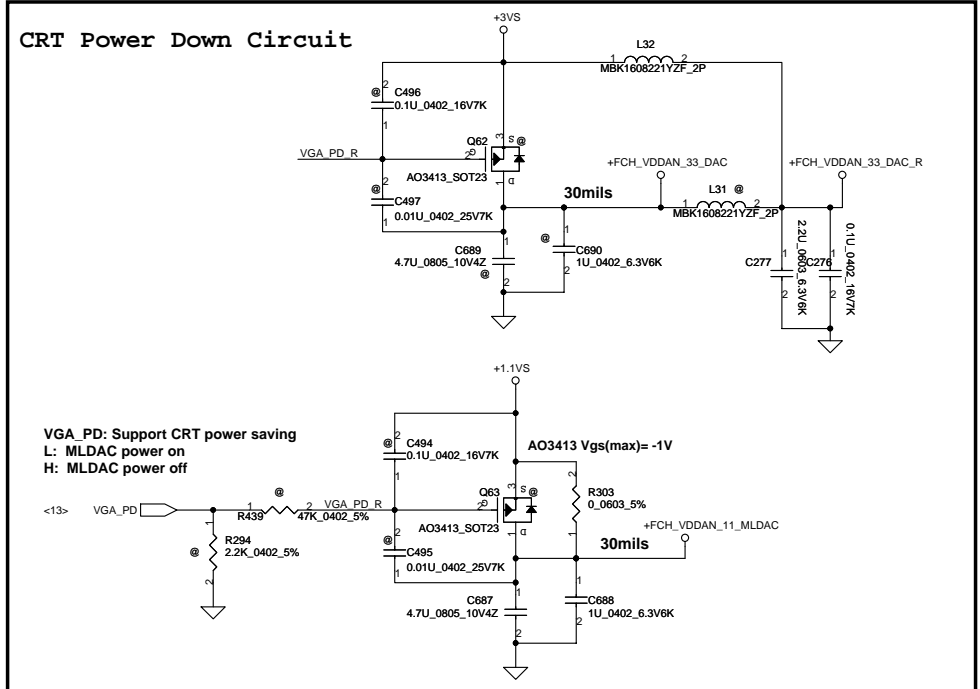
**DEBUG STRAPS**

FCH HAS 15K INTERNAL PU-UP FOR PCI\_AD[27:23]

	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
<b>PULL HIGH</b>	USE PCI PLL DEFAULT	DISABLE ILA AUTORUN DEFAULT	USE FC PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	DISABLE PCI MEM BOOT DEFAULT
<b>PULL LOW</b>	BYPASS PCI PLL	ENABLE ILA AUTORUN	BYPASS FC PLL	USE EEPROM PCIE STRAPS	ENABLE PCI MEM BOOT



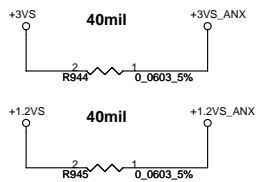
**CRT Power Down Circuit**



VGA\_PD: Support CRT power saving  
L: MLDAC power on  
H: MLDAC power off

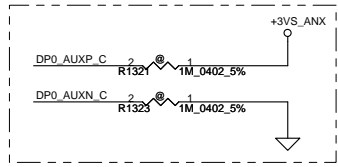
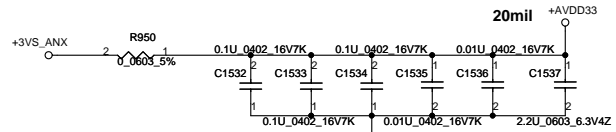
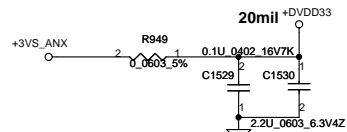
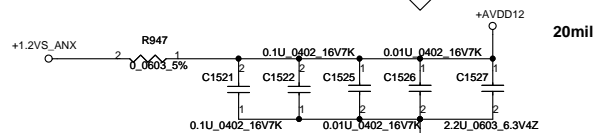
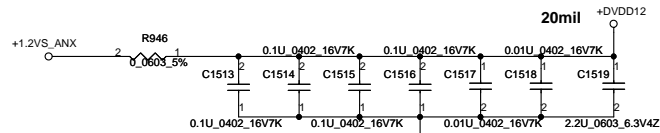
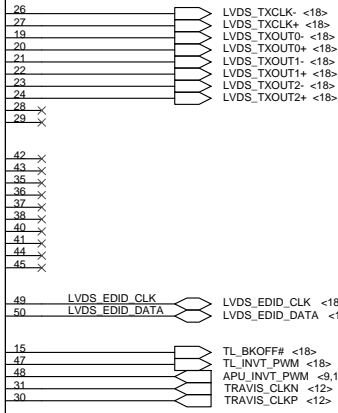
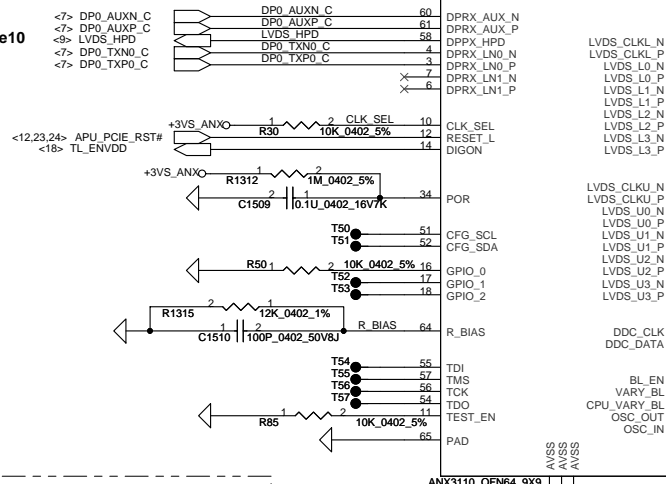






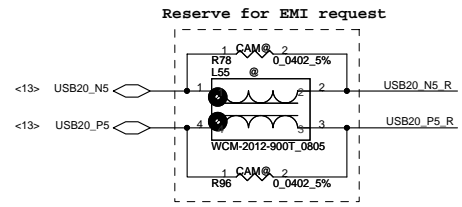
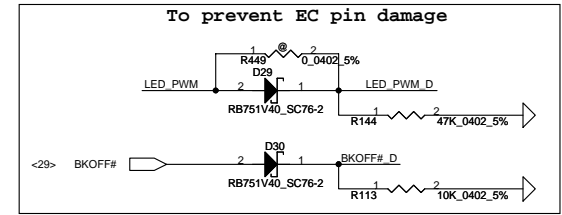
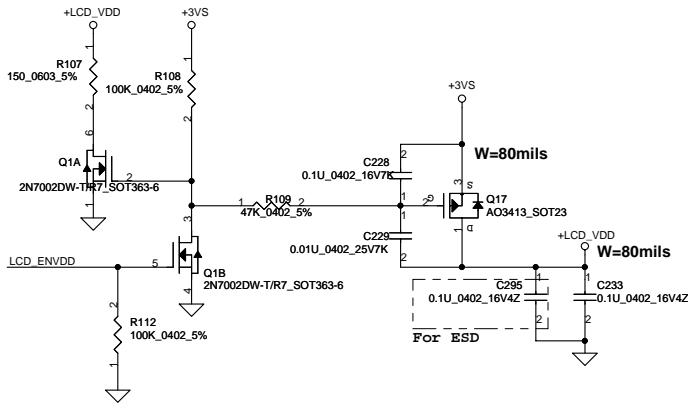
PD 100K at Page10

- <7> DP0\_AUXN\_C
- <7> DP0\_AUXP\_C
- <8> LVDS\_HPD\_C
- <7> DP0\_TXN0\_C
- <7> DP0\_TXP0\_C

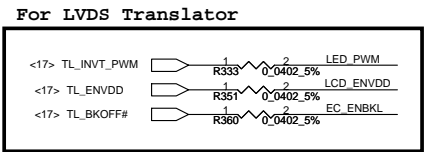
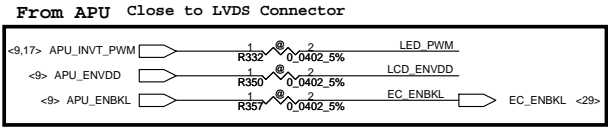
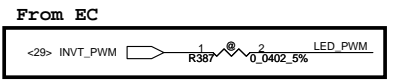
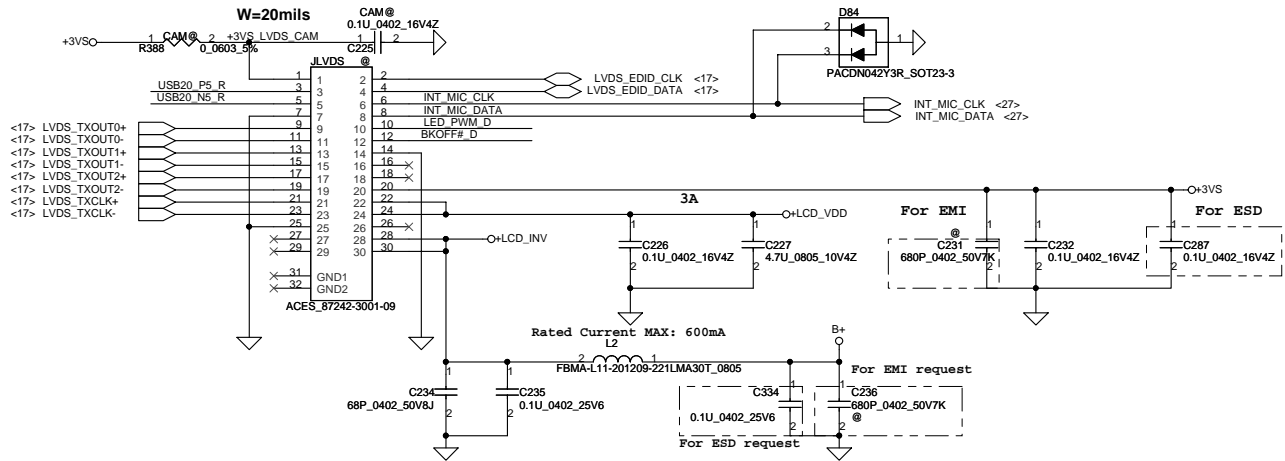


Place via on each trace bus and let resistor very close the via.

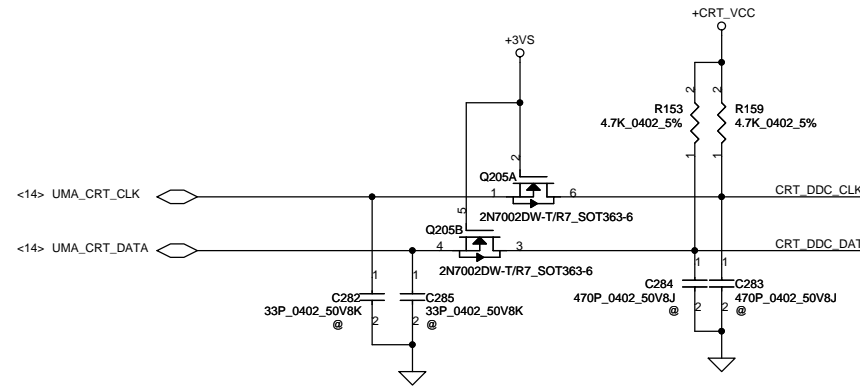
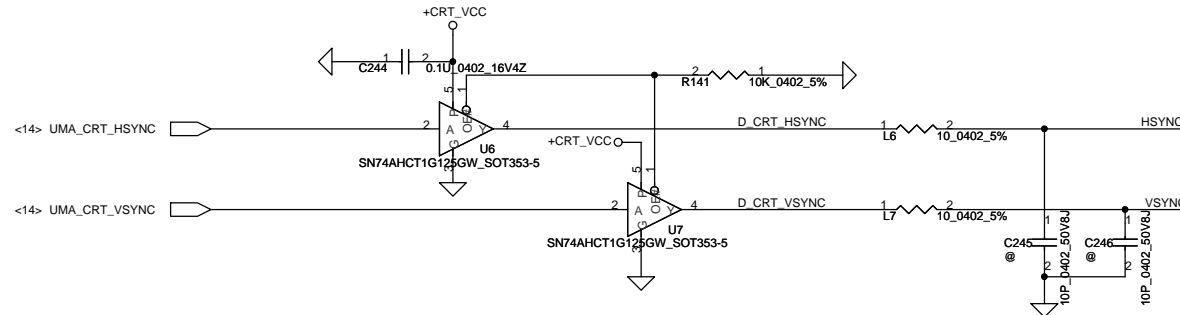
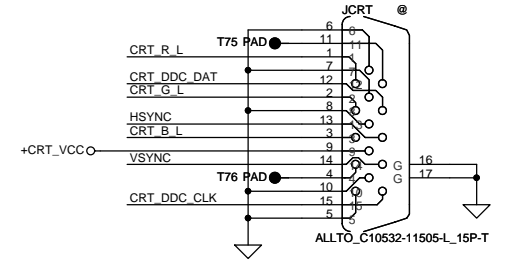
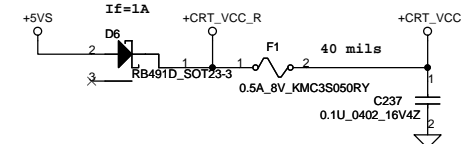
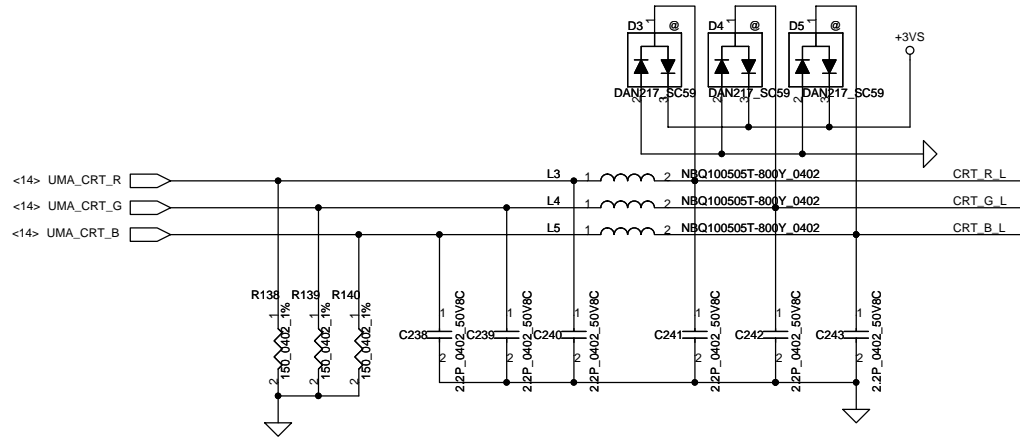
Security Classification	Compal Secret Data		Title	Compal Electronics, Inc.	
Issued Date	2011/01/11	Deciphered Date	2012/01/01	SCHEMATIC MB A7571	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.			Rev	B	
			Date	Monday, May 09, 2011	Sheet 17 of 41



LCD/PANEL BD. Conn.

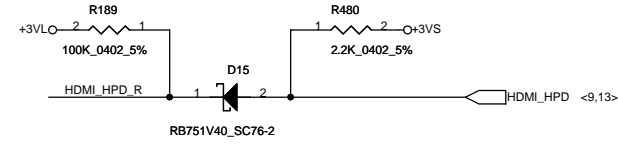
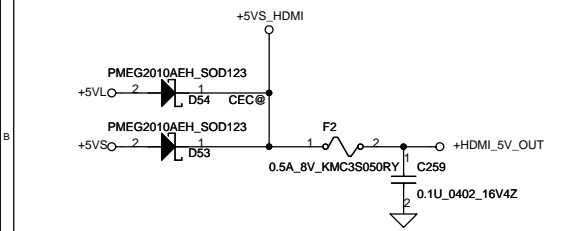
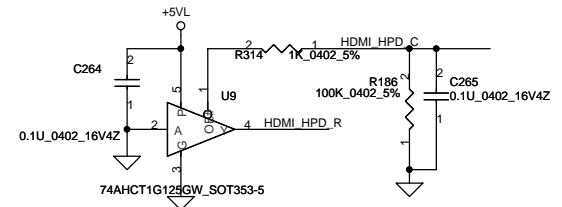
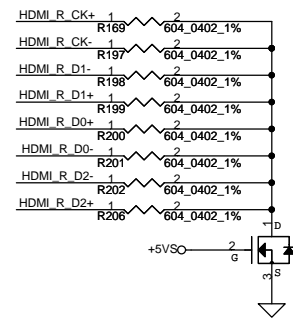
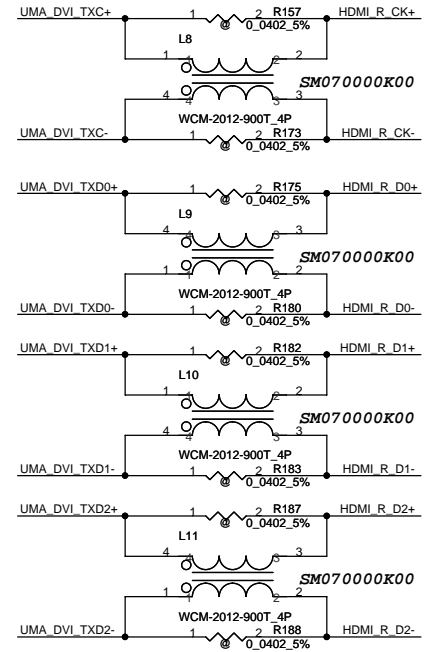
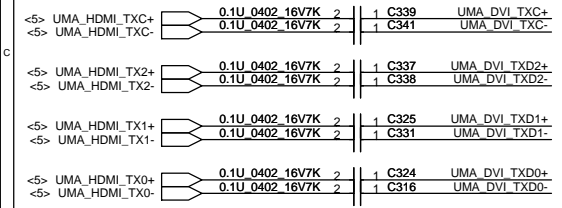
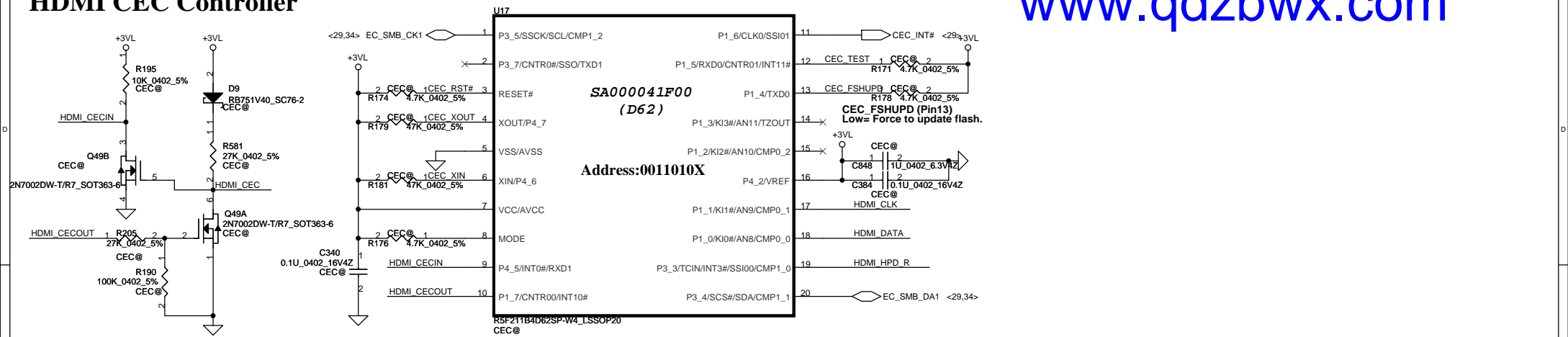


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/01/11	Deciphered Date	2011/11/11	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Document Number	B
				4019D7	
				Date	Monday, May 09, 2011
				Sheet	18 of 41

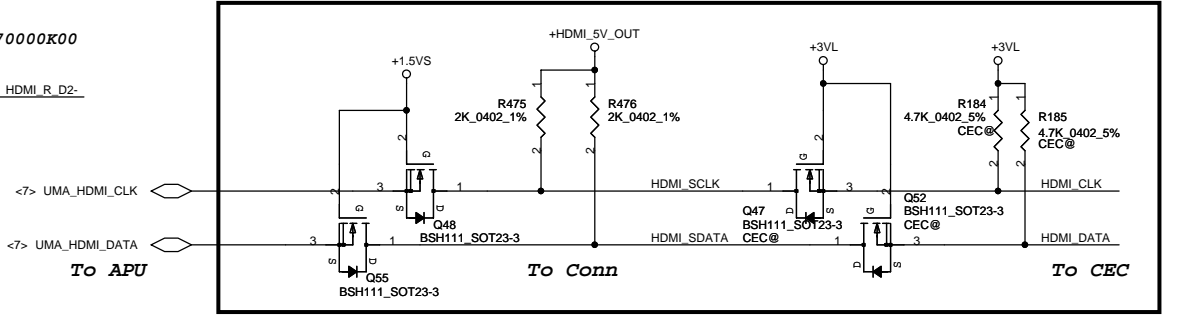
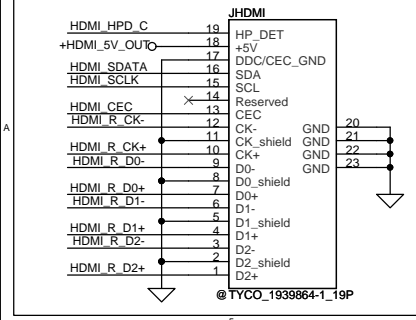


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/01/11	Deciphered Date	2011/11/11	Title	<b>SCHEMATIC MB A7571</b>
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					
Date:	Monday, May 09, 2011	Sheet	19	of	41

# HDMI CEC Controller

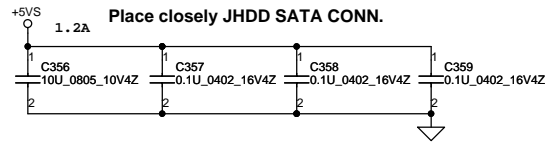


# HDMI Connector

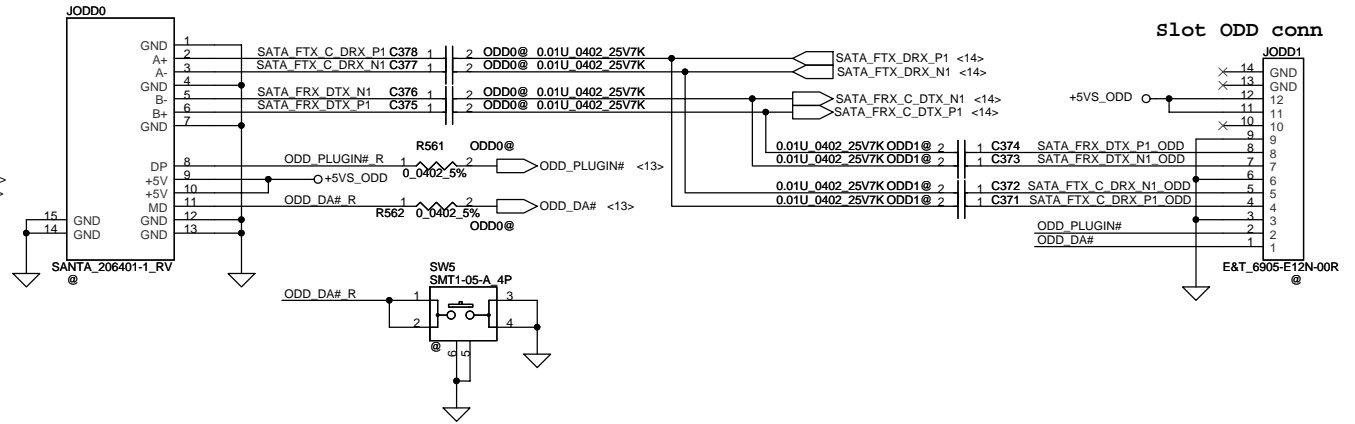
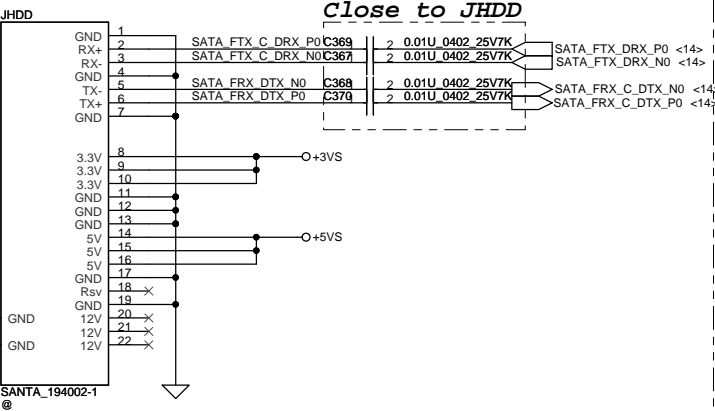
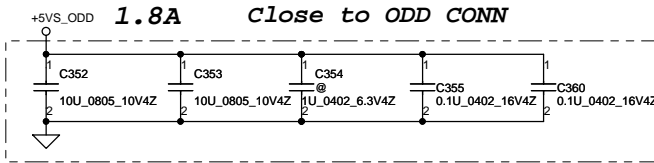


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/01/11	Deciphered Date	2011/11/11	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	
				4019D7	
				Date	Monday, May 09, 2011
				Sheet	20 of 41

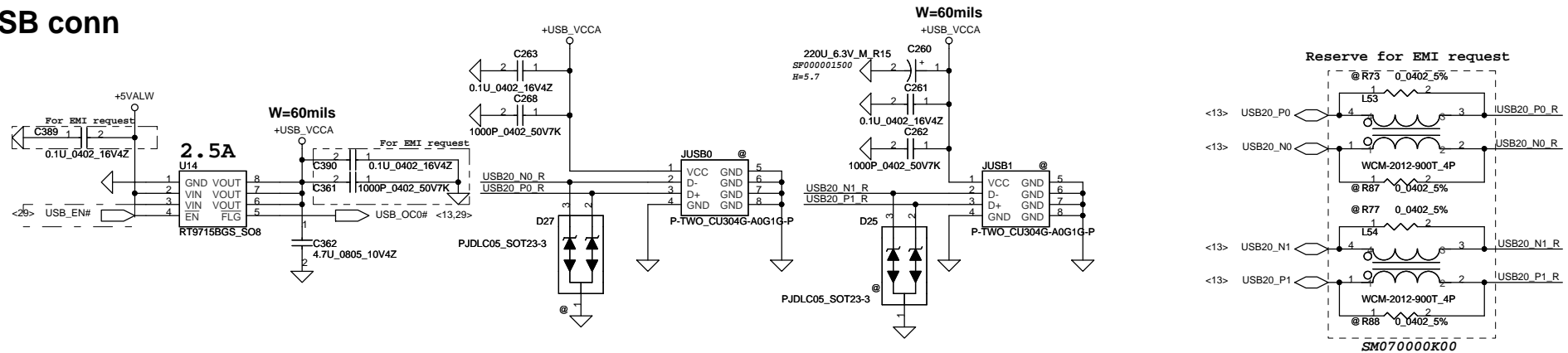
SATA HDD Conn.



SATA ODD Conn

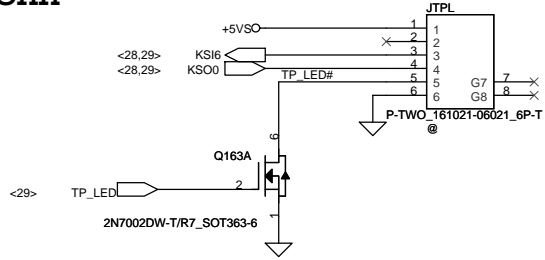


Left USB conn



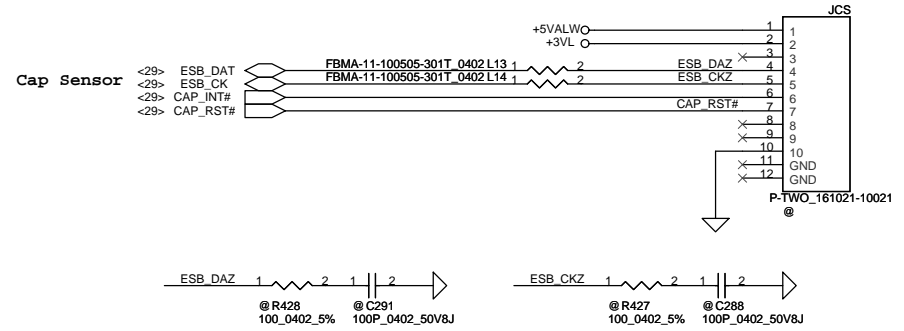
Security Classification		Compal Secret Data		Title	
Issued Date		Deciphered Date		Schematic MB A7571	
2011/01/11		2011/11/11		4019D7	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					
Date: Monday, May 09, 2011				Sheet 21 of 41	

# TP LED Conn

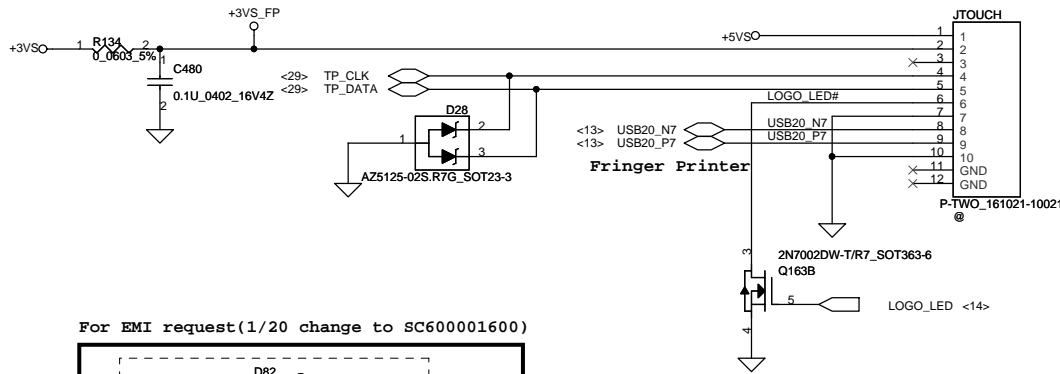


# Cap Sensor Conn

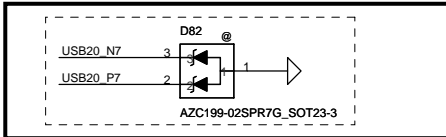
www.qdzbwx.com



# TP/B LED/B FP/B Conn

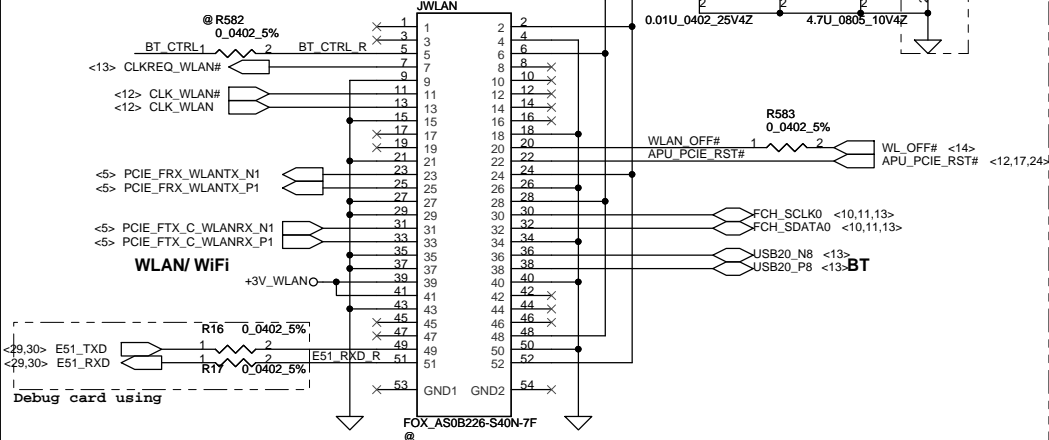
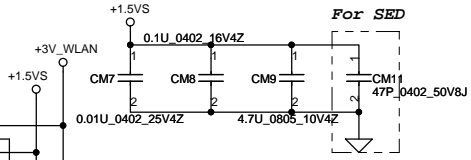
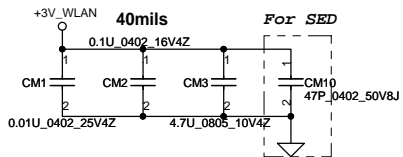
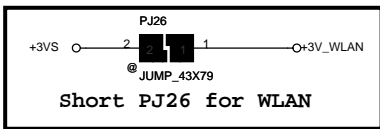


For EMI request(1/20 change to SC600001600)

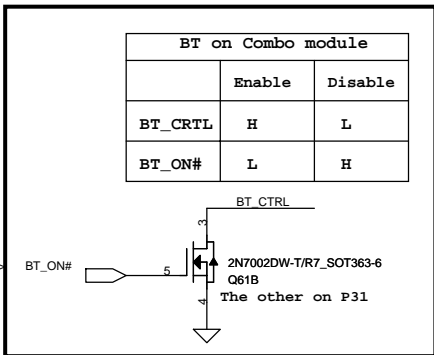
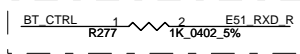


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/01/11	Deciphered Date	2011/11/11	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev B
				4019D7	
Date: Monday, May 09, 2011				Sheet	22 of 41

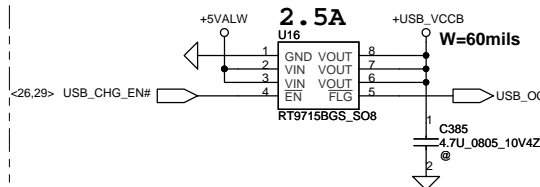
### PCIe Mini Card-WLAN



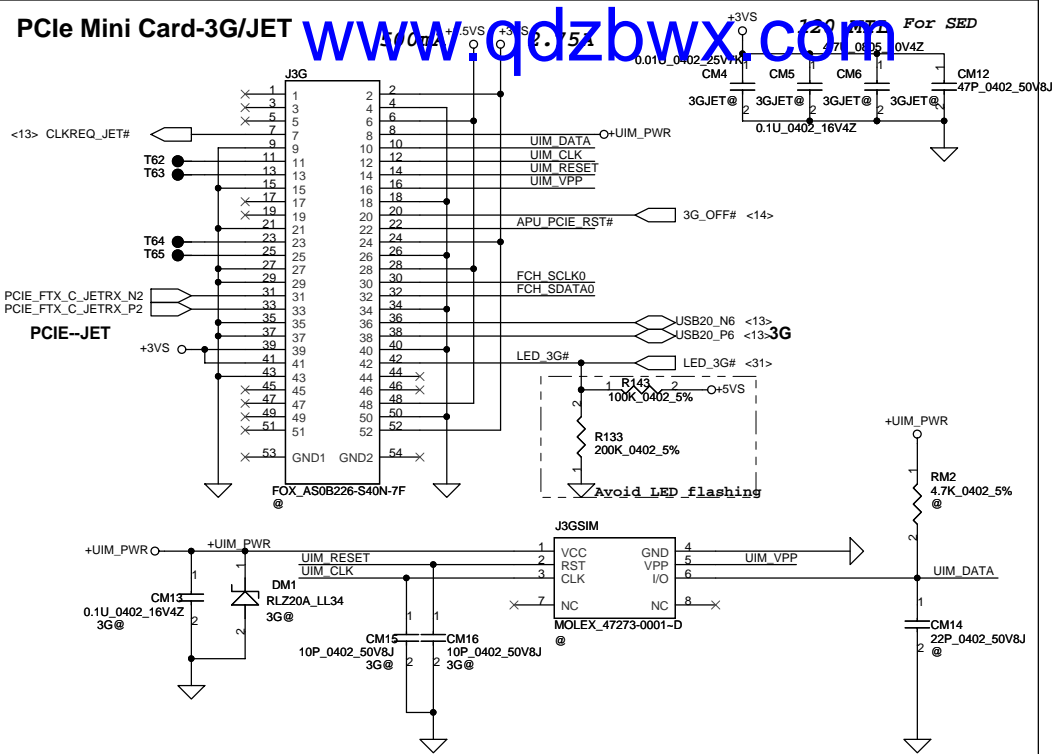
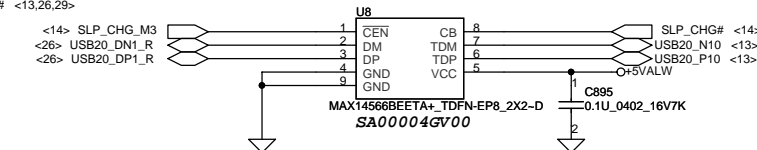
For Isolate Intel Rainbow Peak and Compal debug card

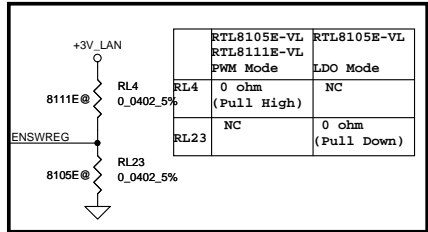
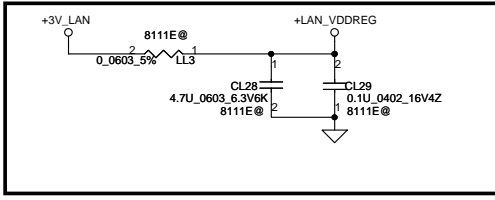
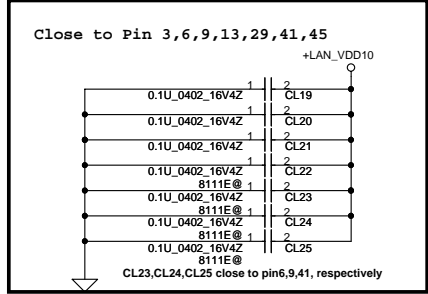
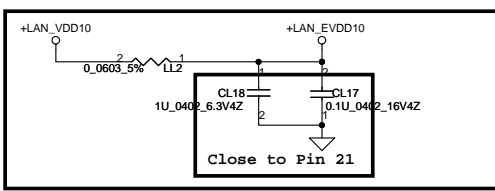
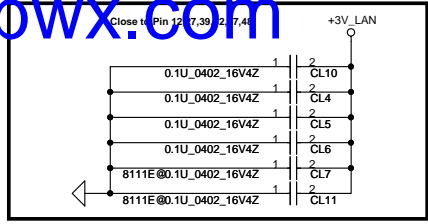
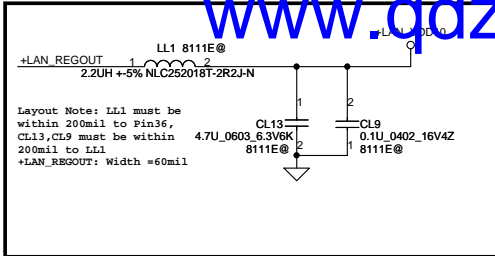
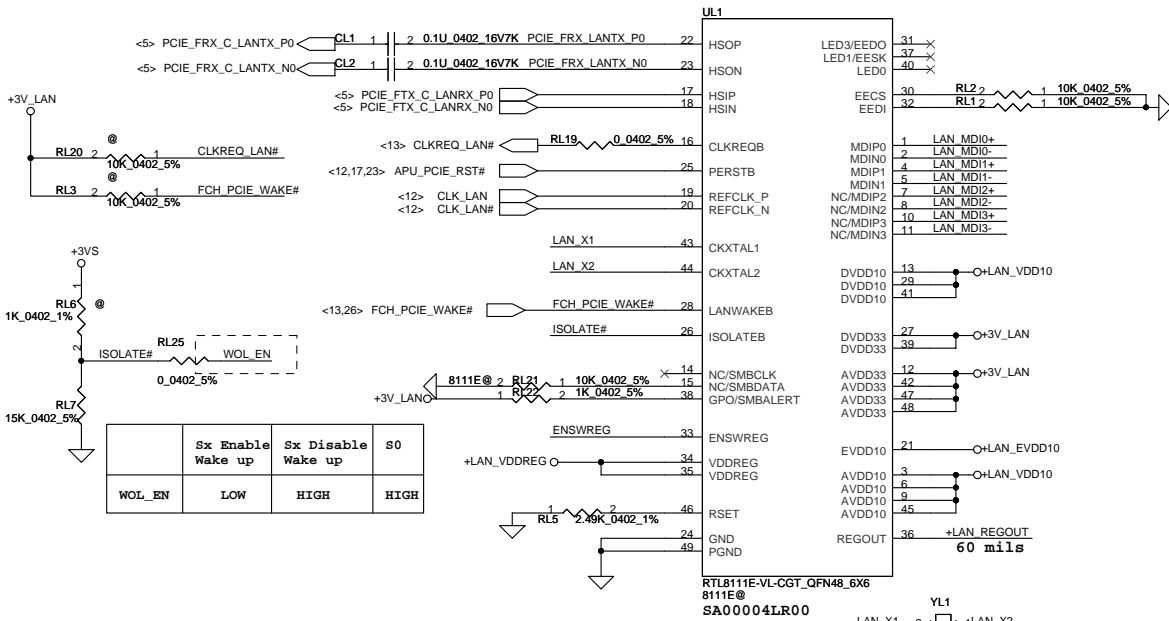


### USB Sleep & Charge Auto-Mode Mode3/Mode4

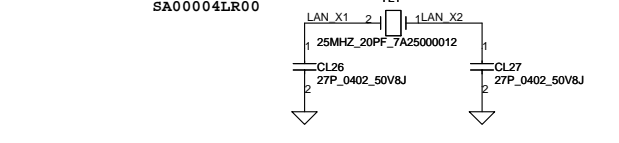
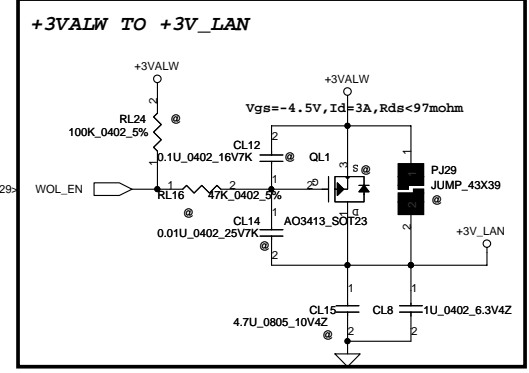


MAX14566B		
CB SLP_CHG_M4	CB1 (CEN#) SLP_CHG_M3	STATUS
0	0	AUTO MODE (Including Mode 4)
0	1	Force Dedicated charger mode (MODE3)
1	X	Pass-Through (USB) Mode: Connect DP/DM to TDP/TDM

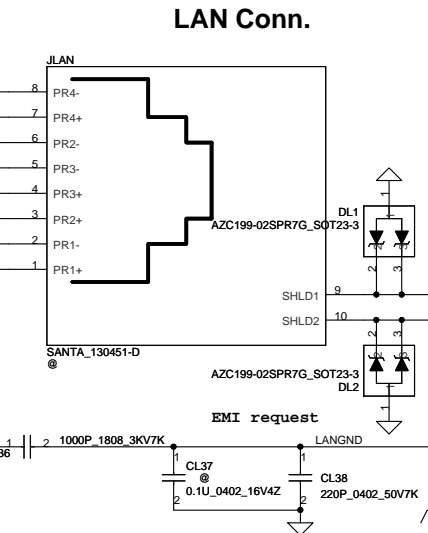
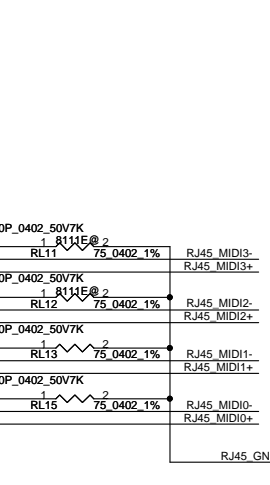
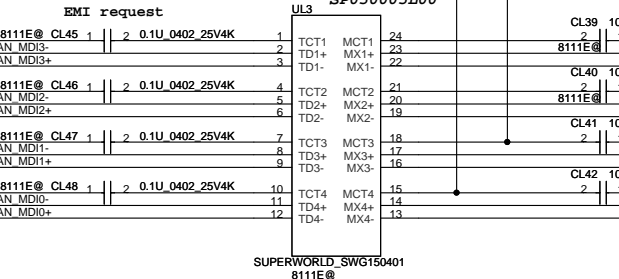
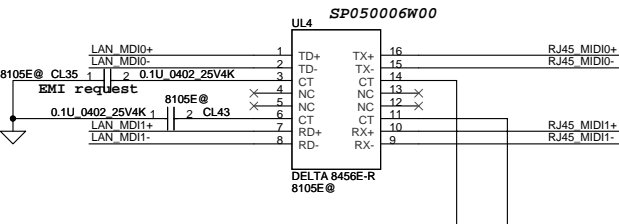
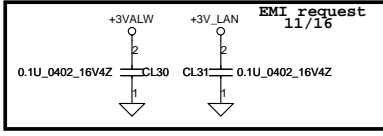
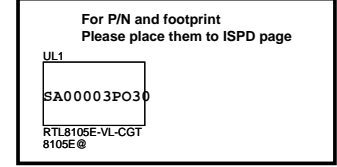




	RTL8105E	RTL8111E
Pin14	NC	NC
Pin15	NC	10K ohm PD
Pin38	1K ohm Pull-high	

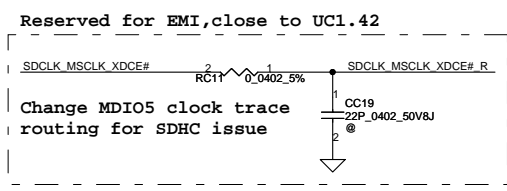
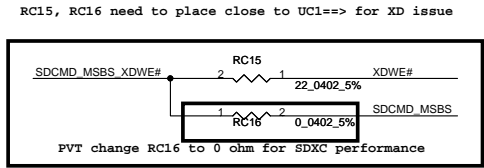
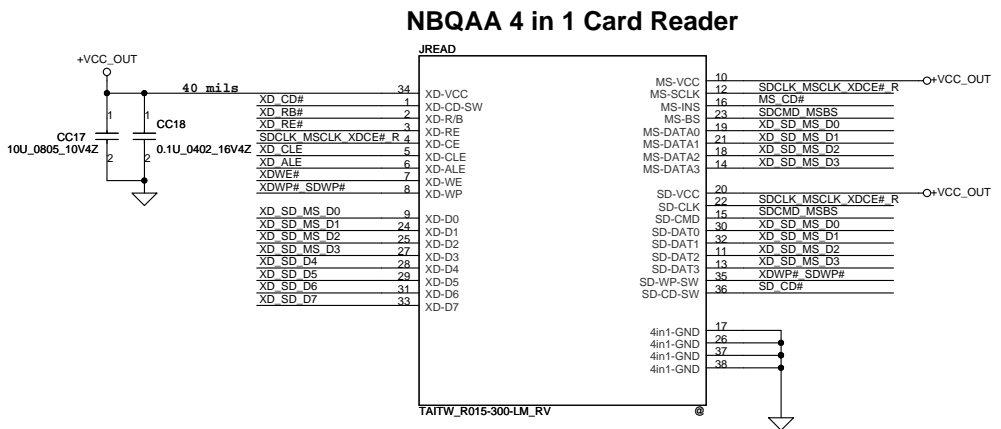
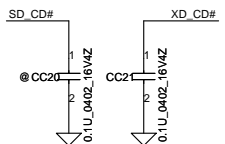
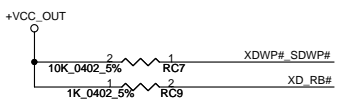
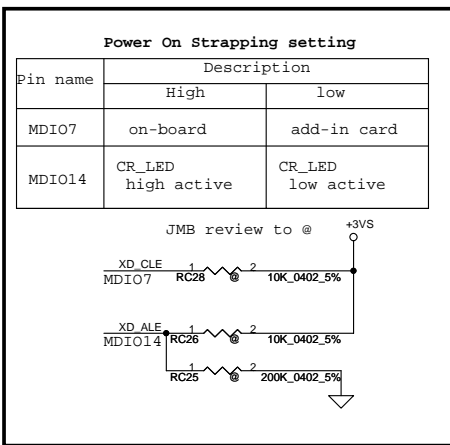
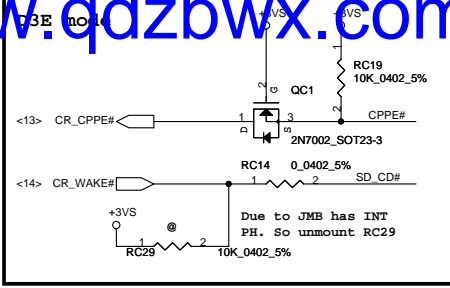
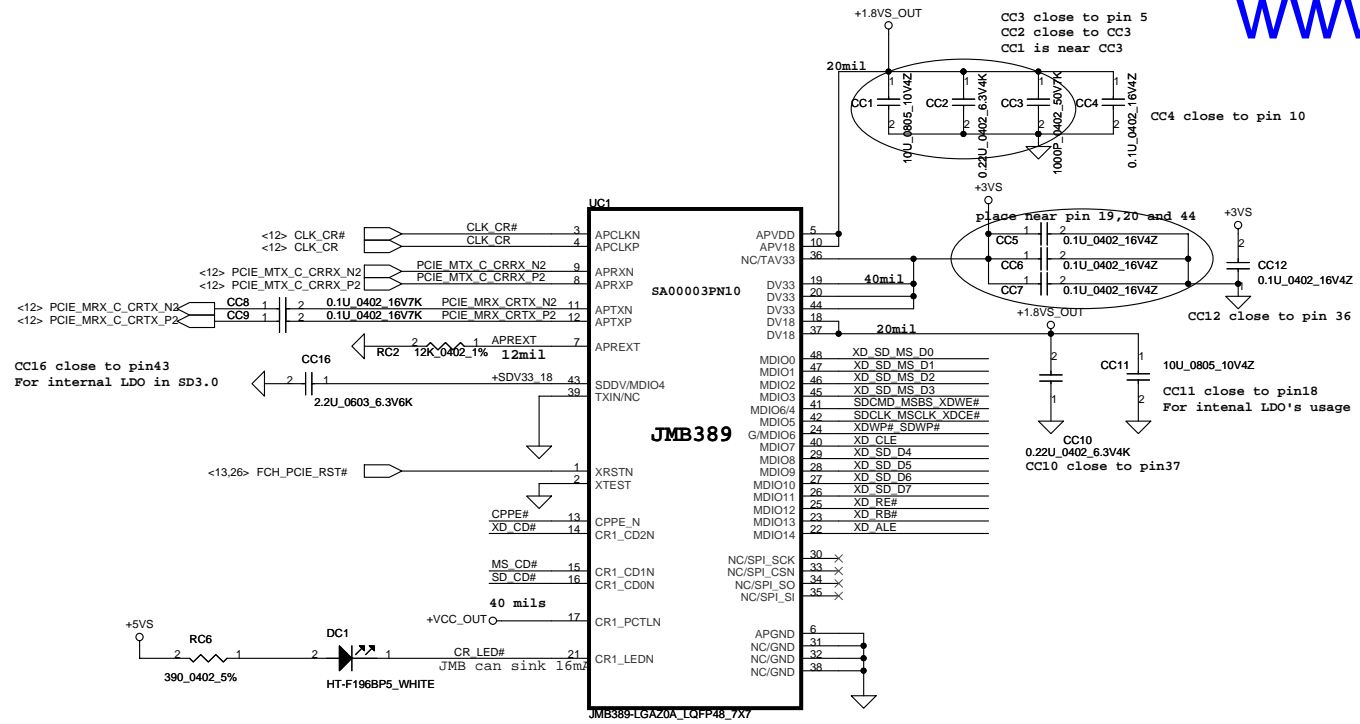


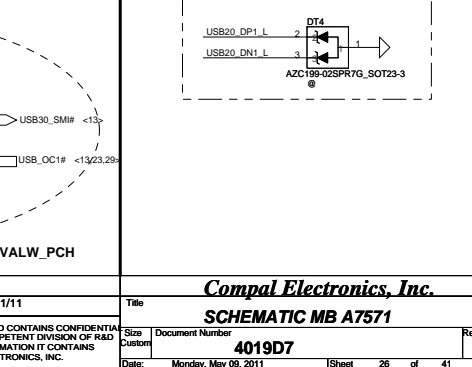
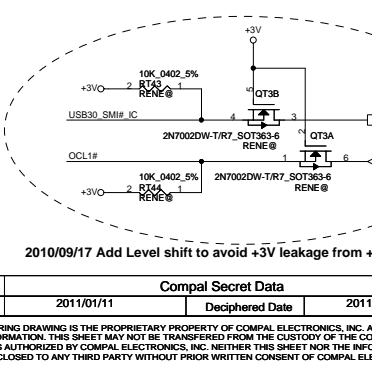
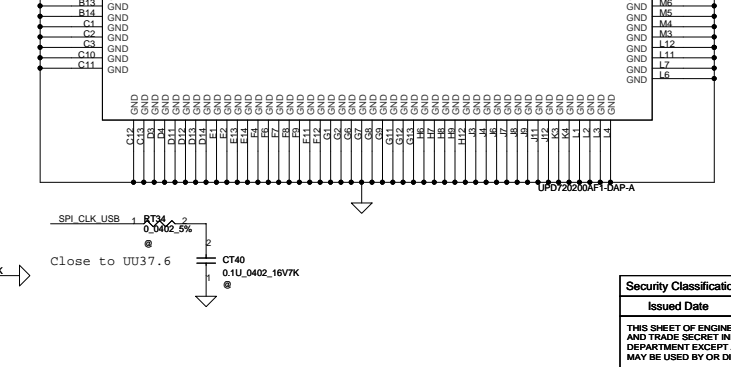
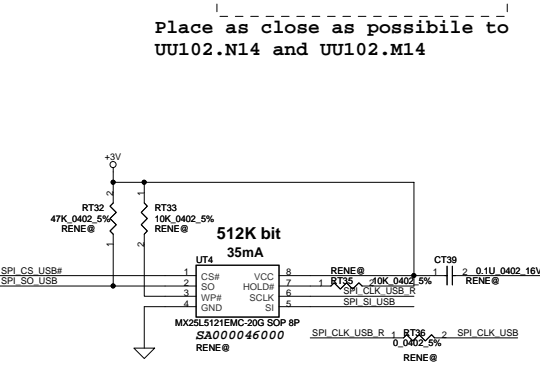
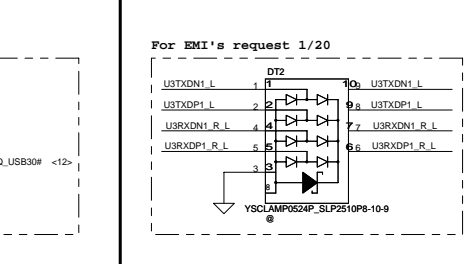
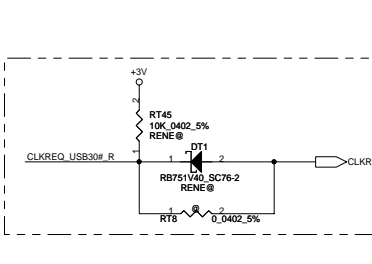
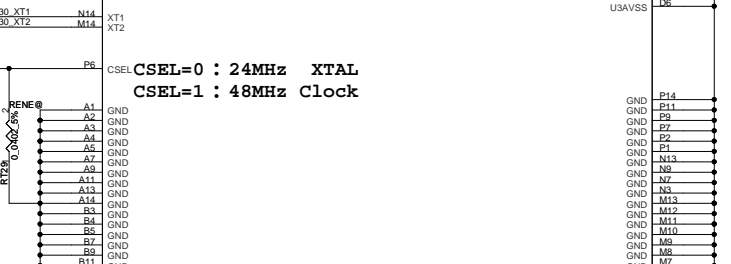
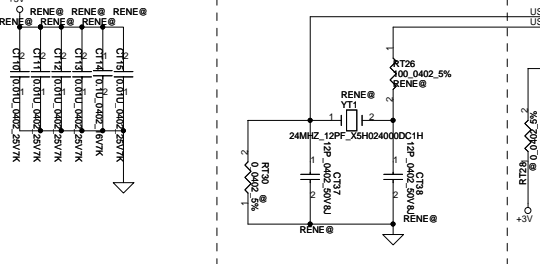
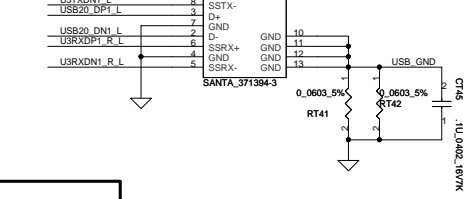
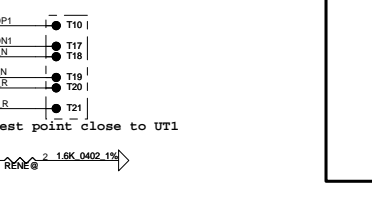
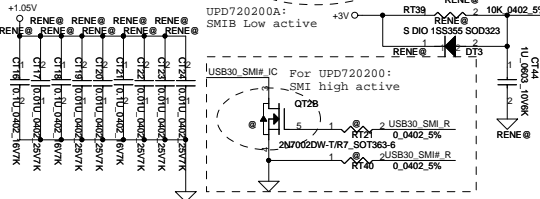
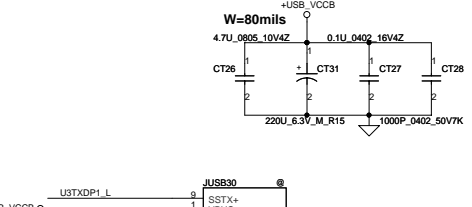
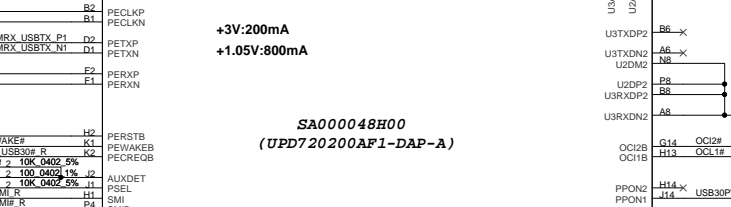
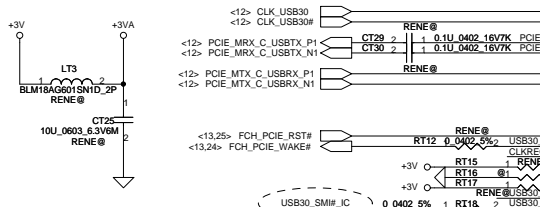
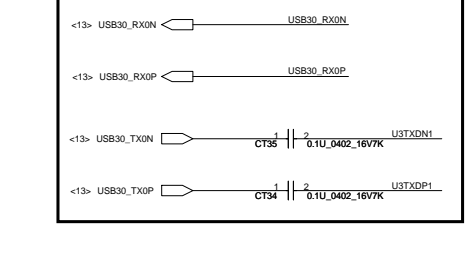
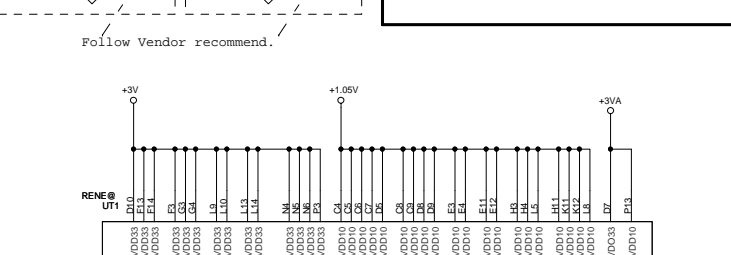
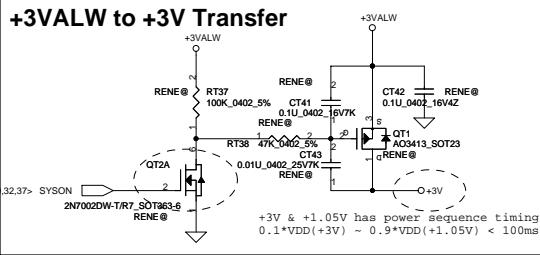
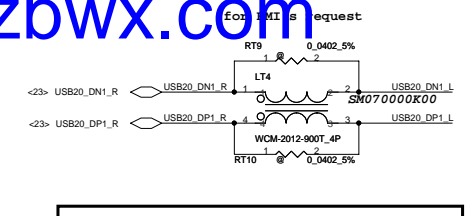
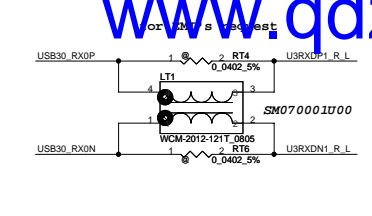
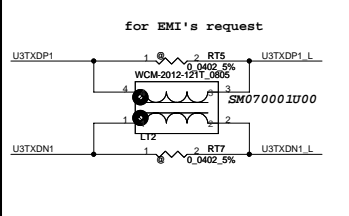
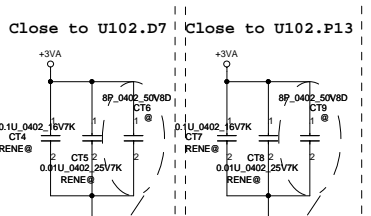
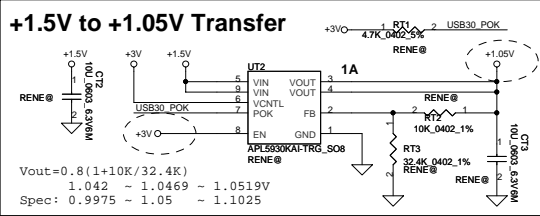
+3V\_LAN rising time (10%-90%) need > 1ms and < 100ms.



Security Classification	Compal Secret Data			Title	
Issued Date	2011/01/11	Deciphered Date	2011/11/11	Schematic MB A7571	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					
Size	Document Number	Rev	Date		
	4019D7	B	Monday, May 09, 2011	Sheet	24 of 41







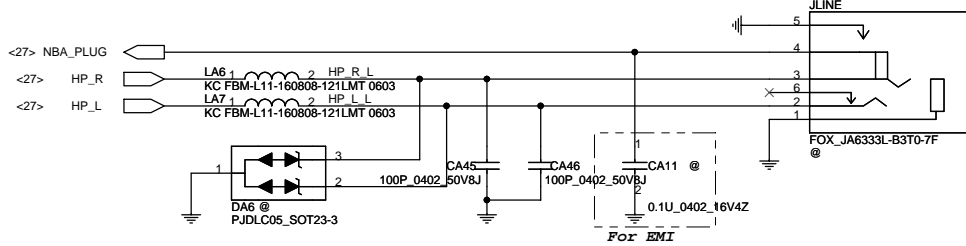
2010/09/17 Add Level shift to avoid +3V leakage from +3VALW\_PCH

Table with 4 columns: Security Classification, Issued Date, Deciphered Date, Title. Includes 'Compal Secret Data', '2011/01/11', '2011/11/11', and 'SCHEMATIC MB A7511'.

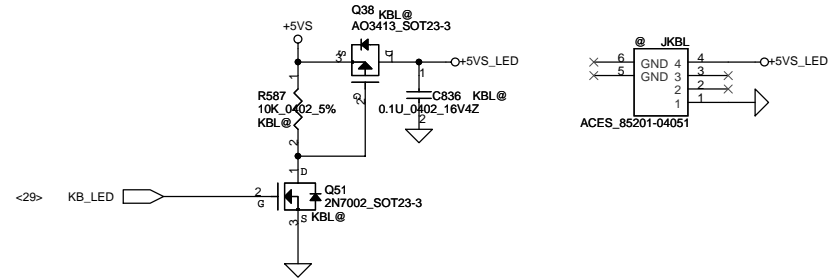
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



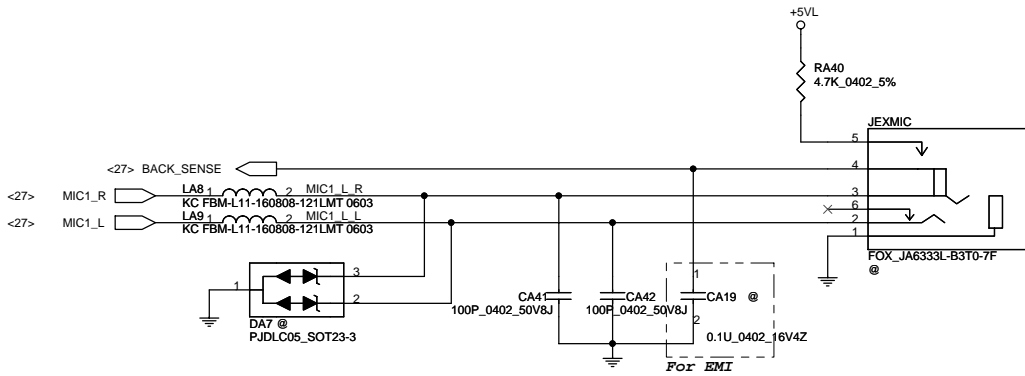
### HeadPhone/LINE Out JACK



### Keyboard LED

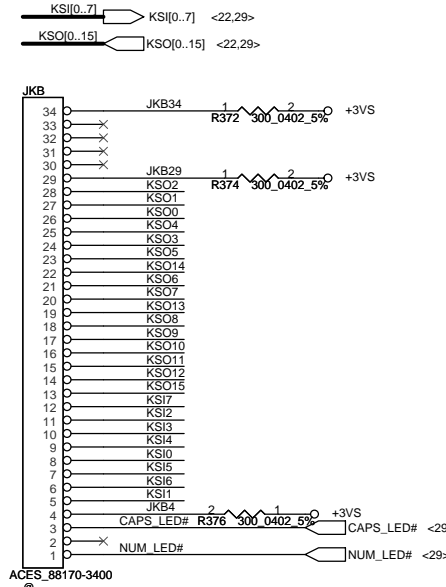


### Ext.MIC/LINE IN JACK



### KEYBOARD CONN.

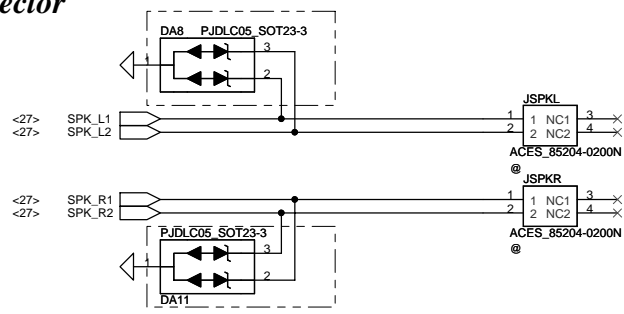
Notice: KB Connector Pin Definition Reversed with KB Membrane Pin Definition



please close to JKB

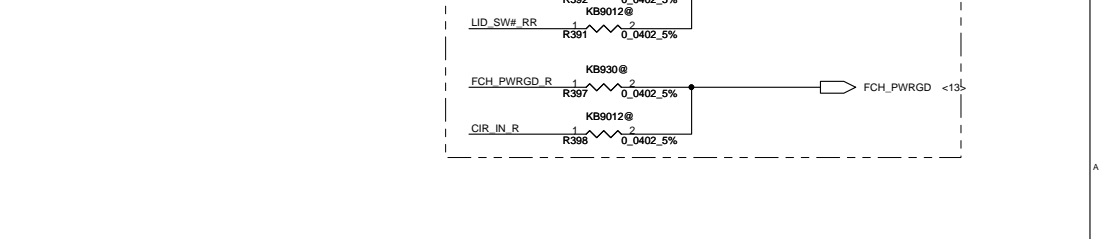
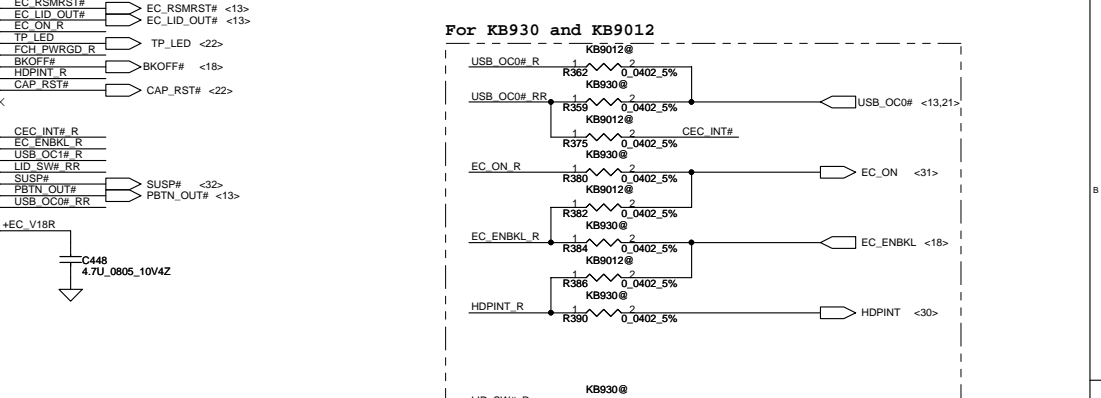
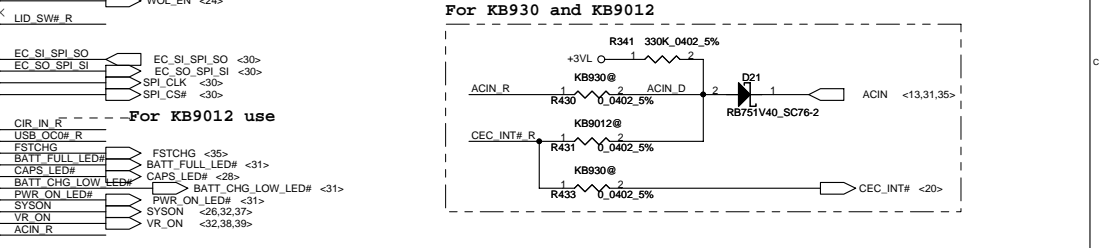
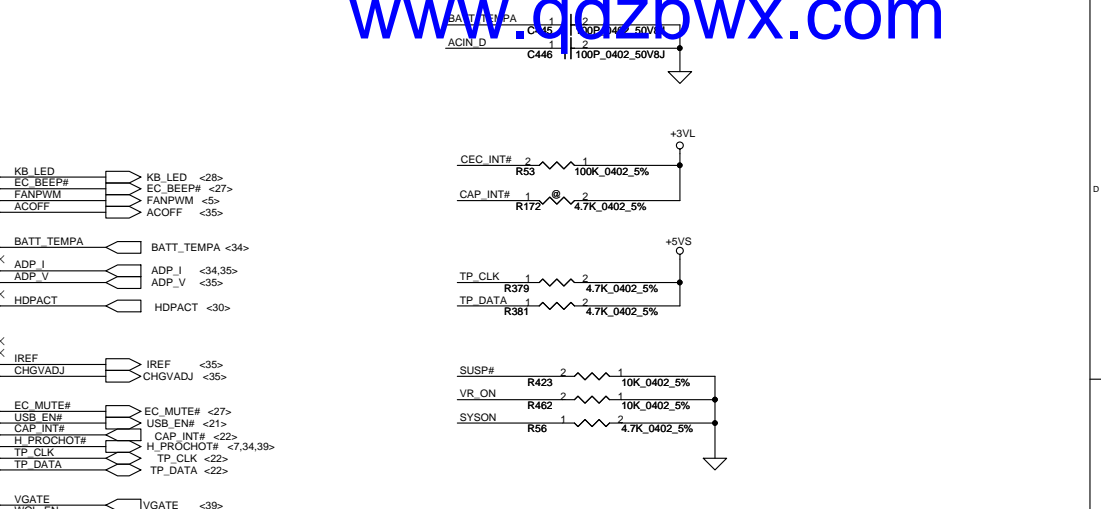
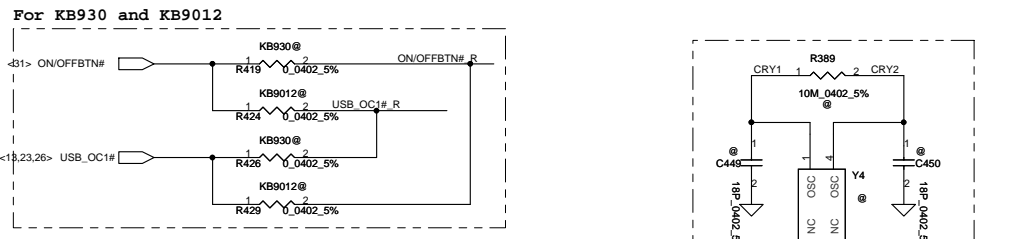
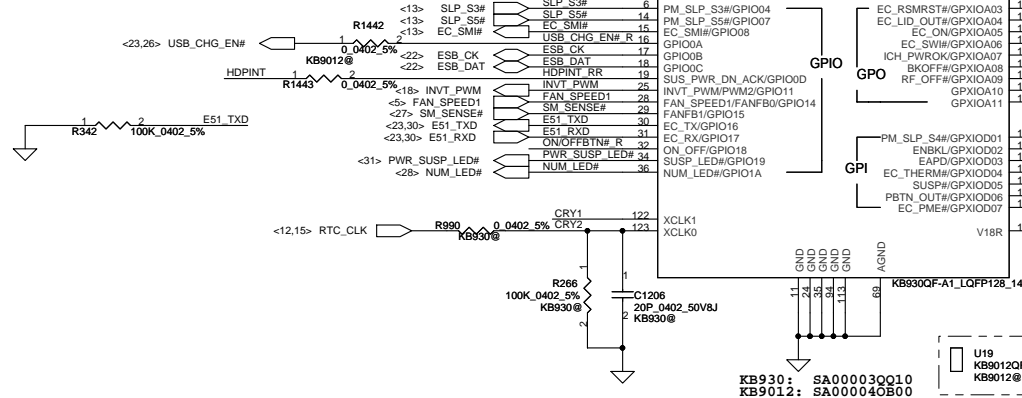
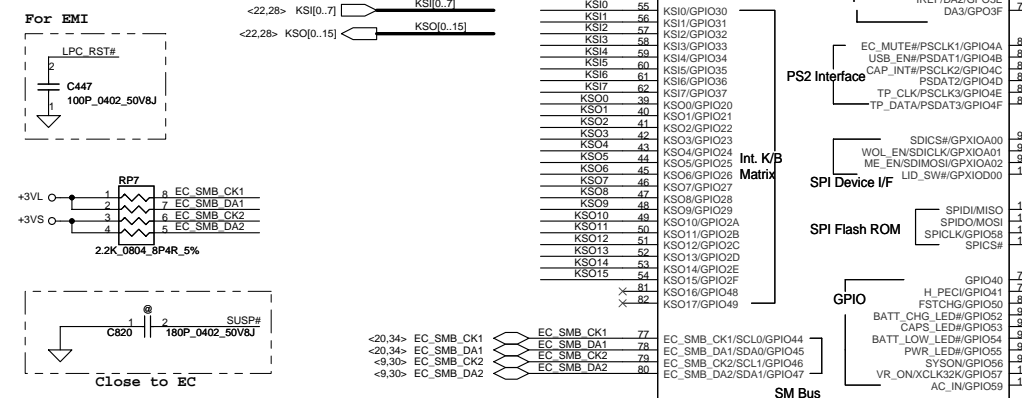
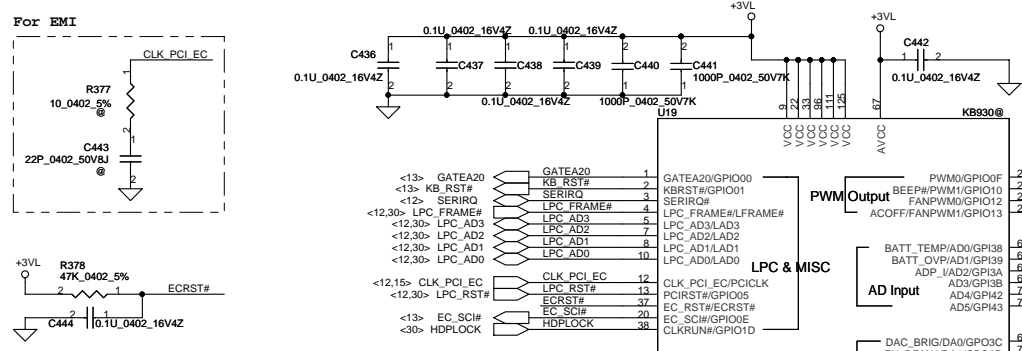
KSO2	C404	100P_0402_50V8J
KSO1	C405	100P_0402_50V8J
KSO0	C406	100P_0402_50V8J
KSO4	C407	100P_0402_50V8J
KSO3	C408	100P_0402_50V8J
KSO5	C409	100P_0402_50V8J
KSO14	C410	100P_0402_50V8J
KSO6	C411	100P_0402_50V8J
KSO7	C412	100P_0402_50V8J
KSO13	C413	100P_0402_50V8J
KSO8	C415	100P_0402_50V8J
KSO9	C416	100P_0402_50V8J
KSO10	C417	100P_0402_50V8J
KSO11	C418	100P_0402_50V8J
KSO12	C419	100P_0402_50V8J
KSO15	C420	100P_0402_50V8J
KSO17	C421	100P_0402_50V8J
KSI2	C422	100P_0402_50V8J
KSI3	C423	100P_0402_50V8J
KSI4	C424	100P_0402_50V8J
KSI0	C425	100P_0402_50V8J
KSI5	C427	100P_0402_50V8J
KSI6	C429	100P_0402_50V8J
KSI1	C431	100P_0402_50V8J
CAPS_LED#	C433	100P_0402_50V8J
NUM_LED#	C435	100P_0402_50V8J

### Speaker Connector



Need to update symbol

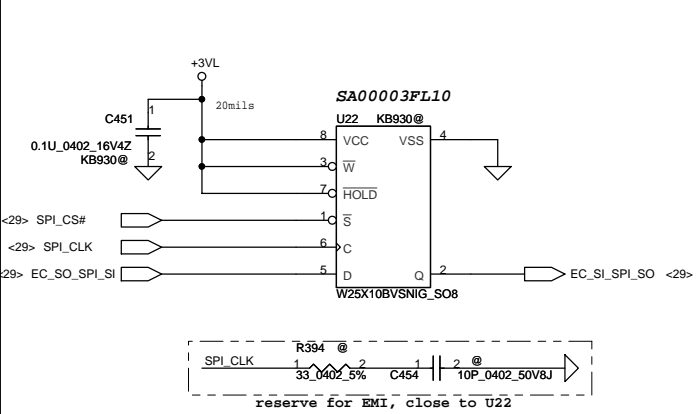
Security Classification	Compal Secret Data		Title	
Issued Date	2011/01/11	Deciphered Date	2011/11/11	SCHEMATIC MB A751
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number 4019D7
Date:	Monday, May 09, 2011	Sheet	28	of 41



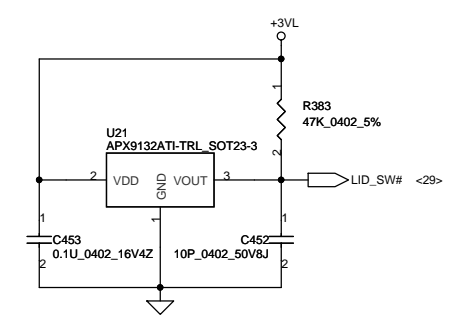
Security Classification	Compal Secret Data		2011/11/11		Title	
Issued Date	2011/01/11	Deciphered Date			4019D7	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Date: Monday, May 08, 2011		Sheet 29 of 41

Compal Electronics, Inc.  
**SCHEMATIC MB A751**  
 Document Number  
**4019D7**  
 Rev B

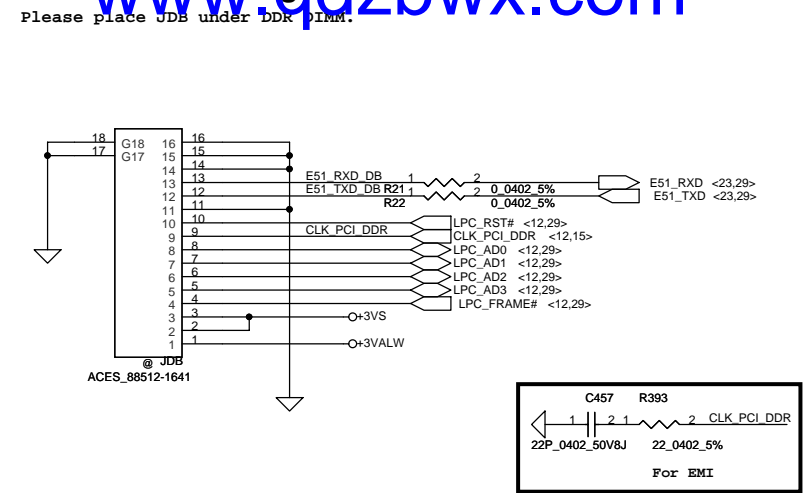
**SPI Flash (128KB)**



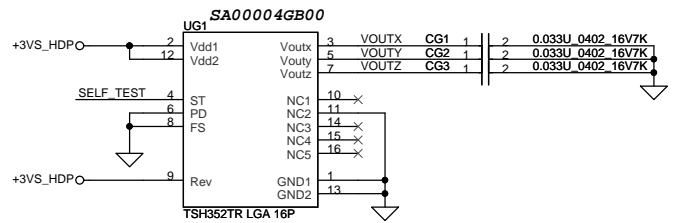
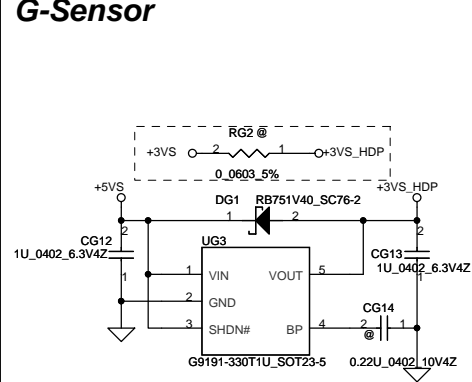
**Lid SW**



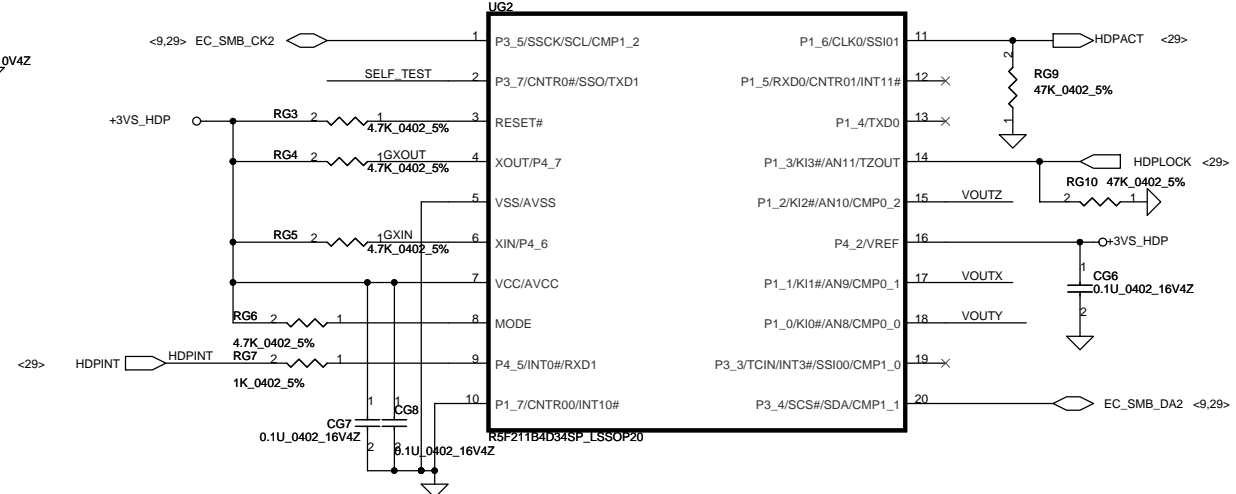
**LPC UART Debug Port**



**G-Sensor**

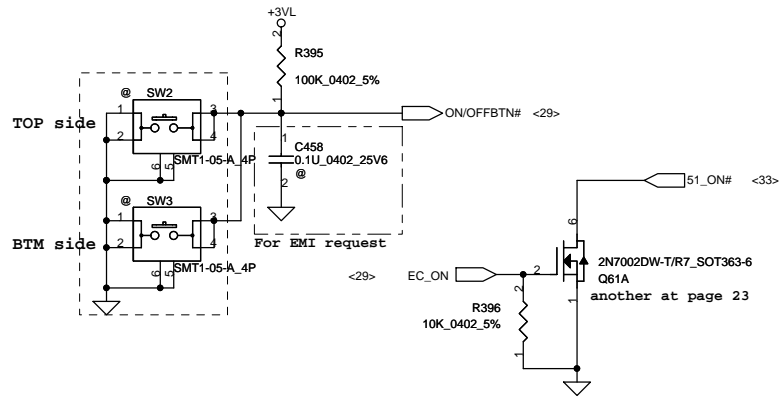


**SA00003A600 Supports ODD unexpected tray eject prevention**

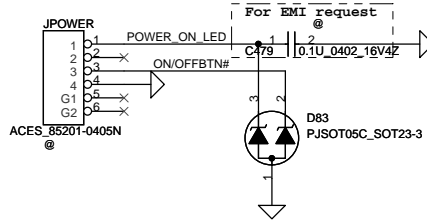


Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/01/11	Deciphered Date	2011/11/11	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RESEARCH AND DEVELOPMENT DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				4019D7
Date: Monday, May 09, 2011				Sheet 30 of 41

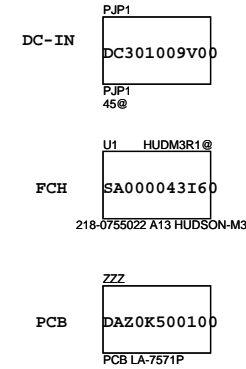
# Power Button



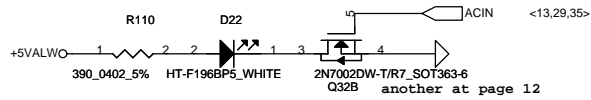
# PWR/B Conn



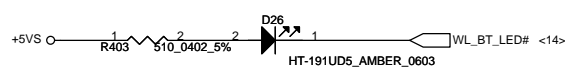
www.qdzbwx.com



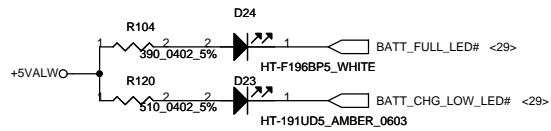
# DC-IN LED



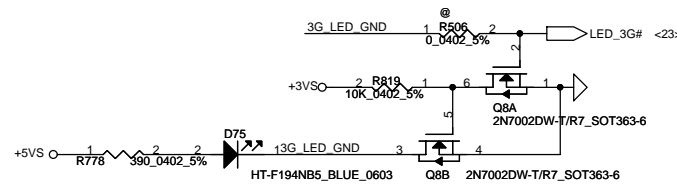
# WL/BT LED



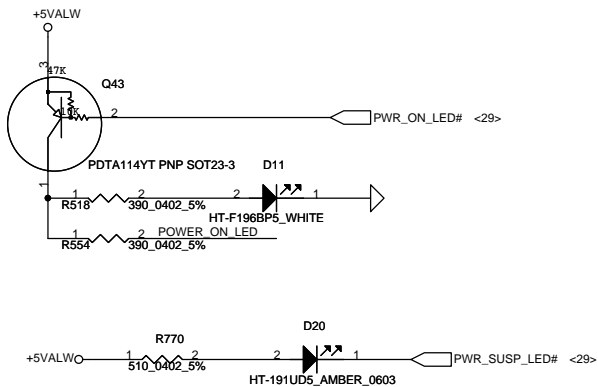
# BATT CHARGE/FULL LED



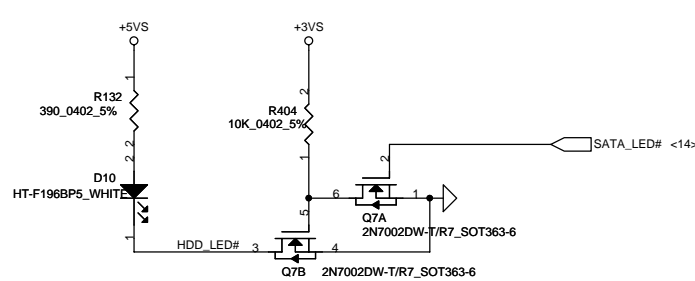
# WiMAX/3G LED



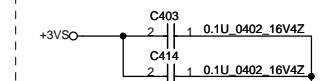
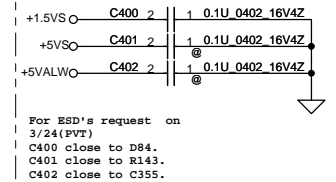
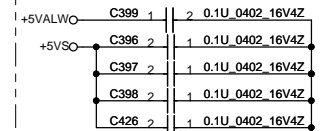
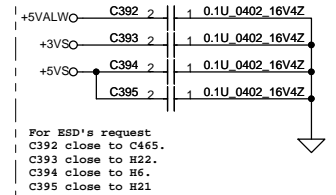
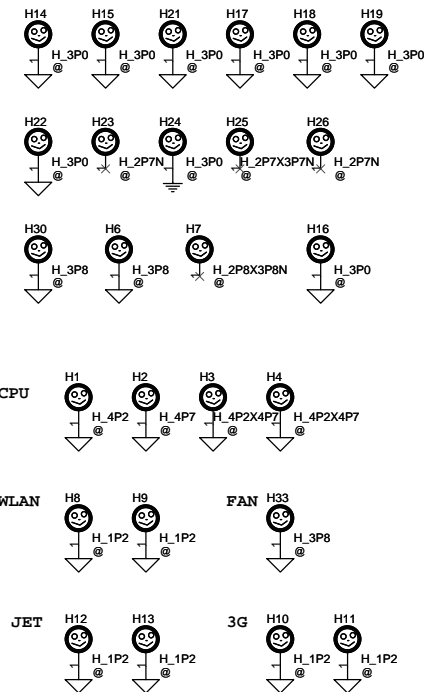
# POWER/SUSPEND LED



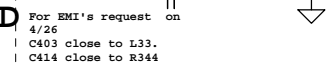
# HDD LED



# Screw Hole

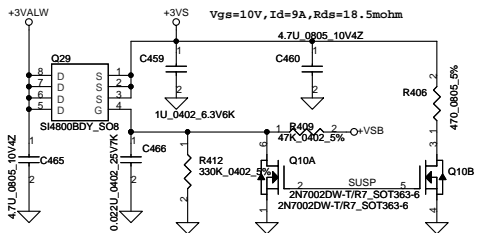


# PCB Federal Mark PAD

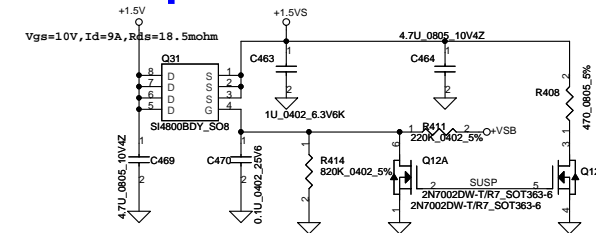
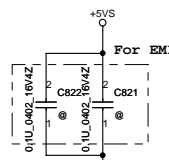
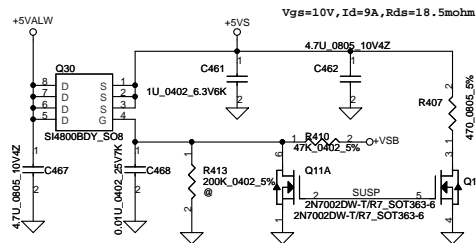


Security Classification		Compal Secret Data		Title	
Issued Date		Deciphered Date		SCHEMATIC MB A7571	
2011/01/11		2011/11/11		4019D7	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					
Date: Monday, May 09, 2011				Sheet 31 of 41	

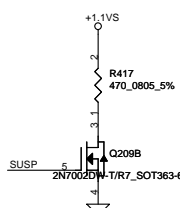
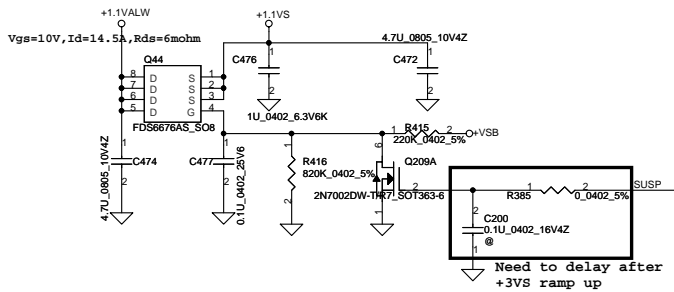
**+3VALW TO +3VS**



**+5VALW TO +5VS**

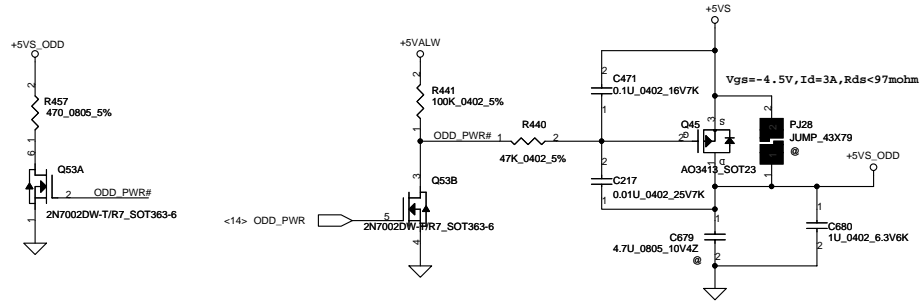


**+1.1VALW to +1.1VS**

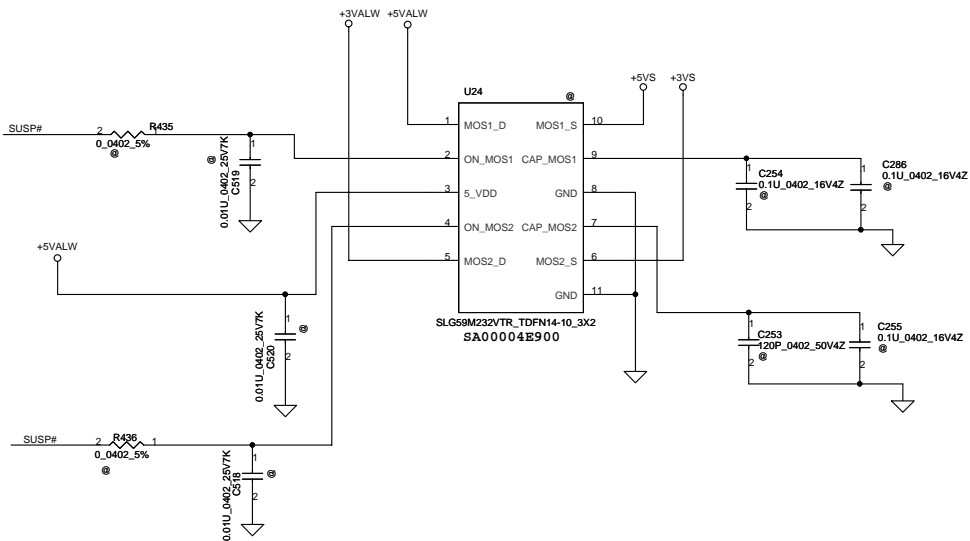


Need to delay after +3VS ramp up

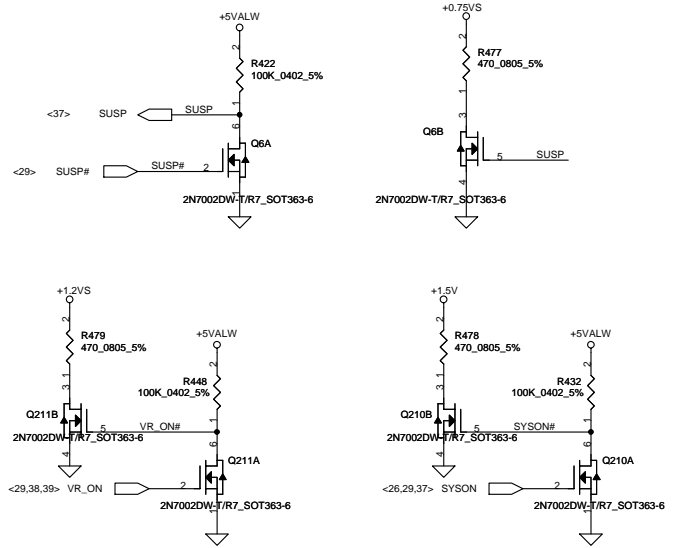
**+5VS TO +5VS\_ODD**



Each 250pF on CAP\_MOS1 (2) will make Slew Rate(uS/V) increase of 100uS/V

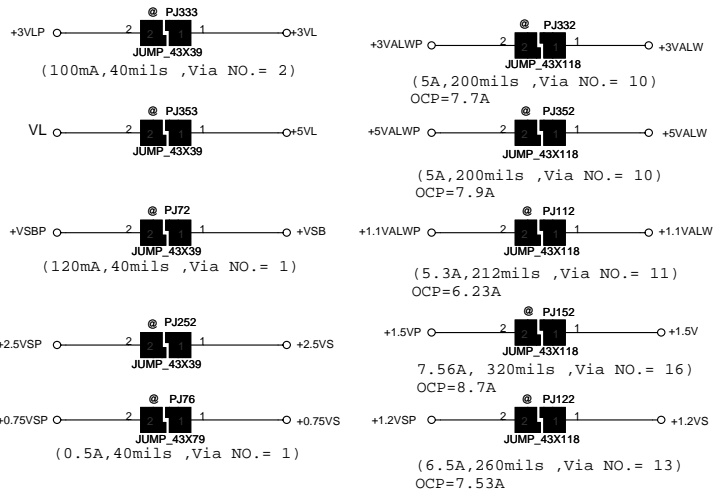
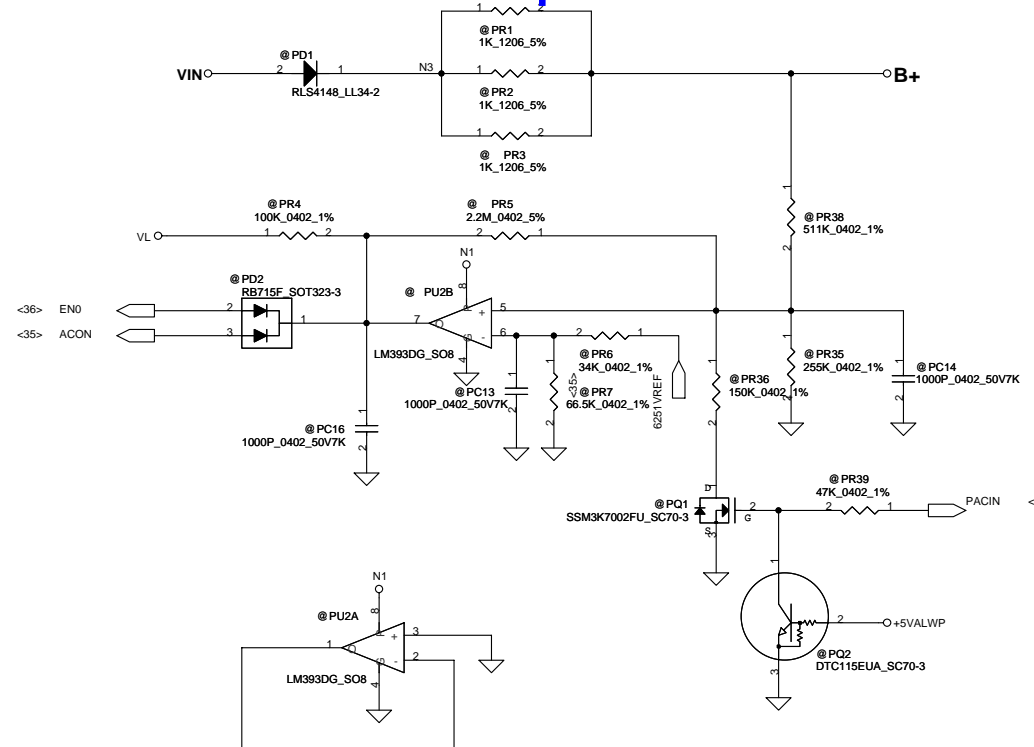
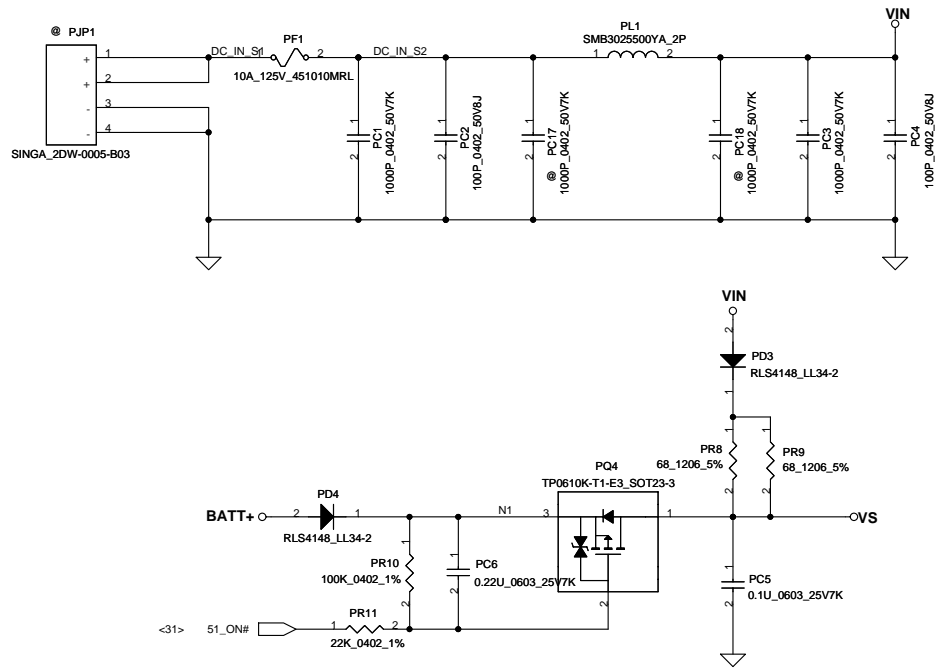


**Discharge**

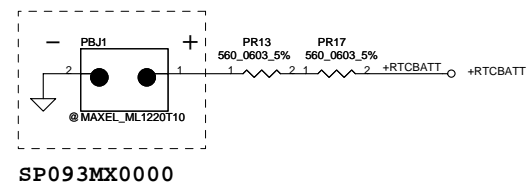


Security Classification	Compal Secret Data		Title	
Issued Date	2011/01/11	Deciphered Date	2011/11/11	Compal Electronics, Inc.
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				4019D7
Date: Monday, May 08, 2011				Rev B
Sheet 32 of 41				



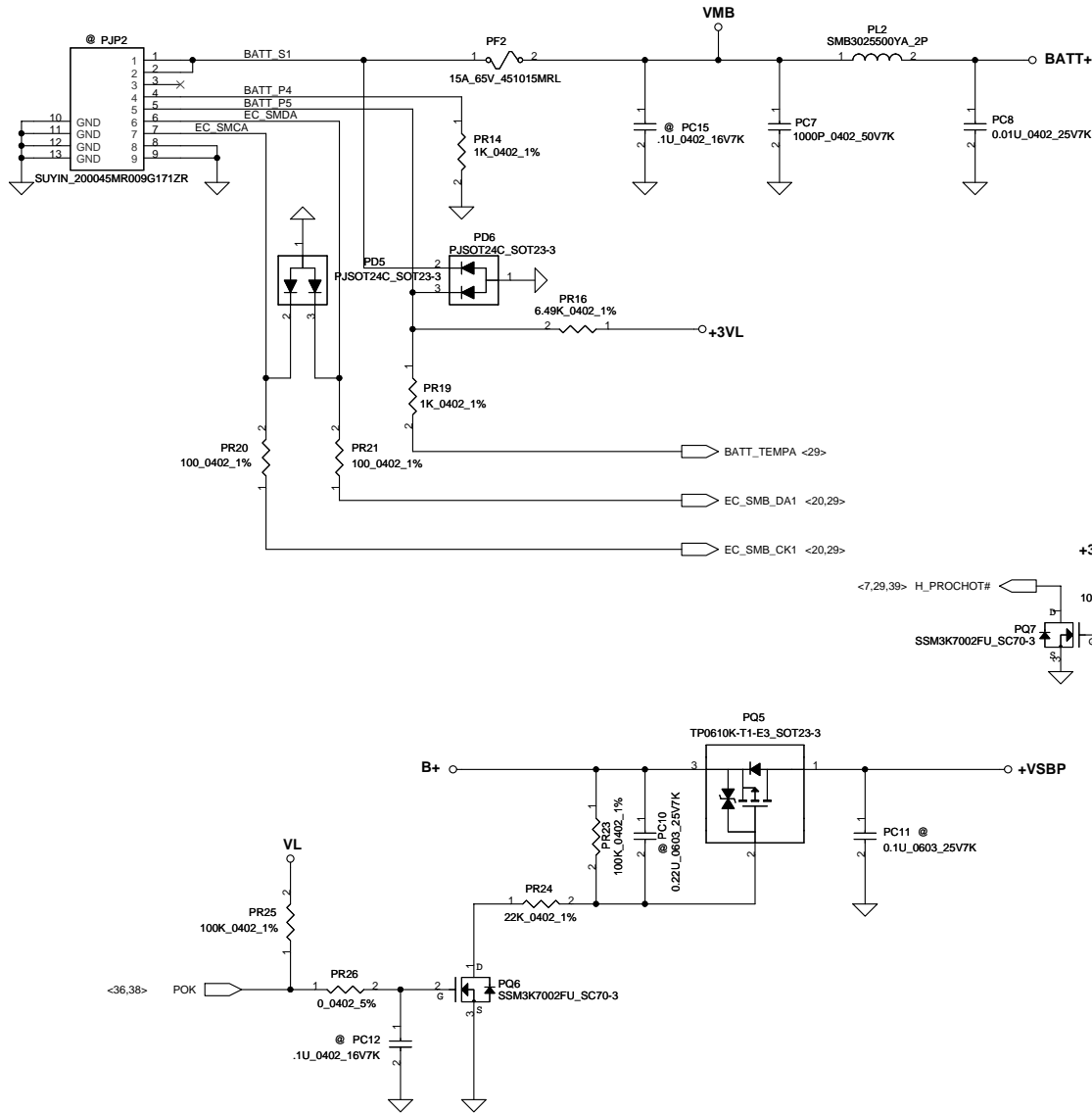


RTC Battery



SP093MX0000

ACIN			
Precharge detector			
	Min.	typ.	Max
H-->L	14.42V	14.74V	15.23V
L-->H	15.39V	15.88V	16.39V

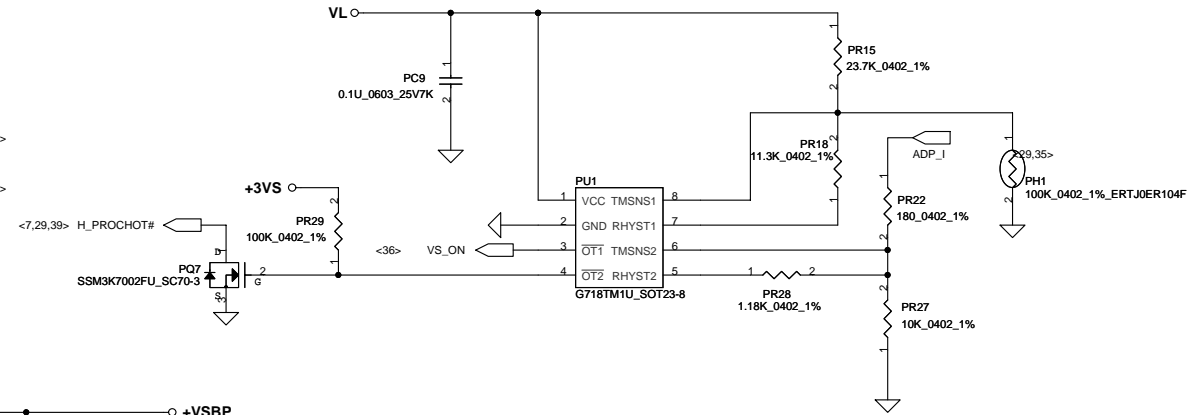


PH1 under CPU botten side :  
 CPU thermal protection at 90 degree C  
 Recovery at 56 degree C

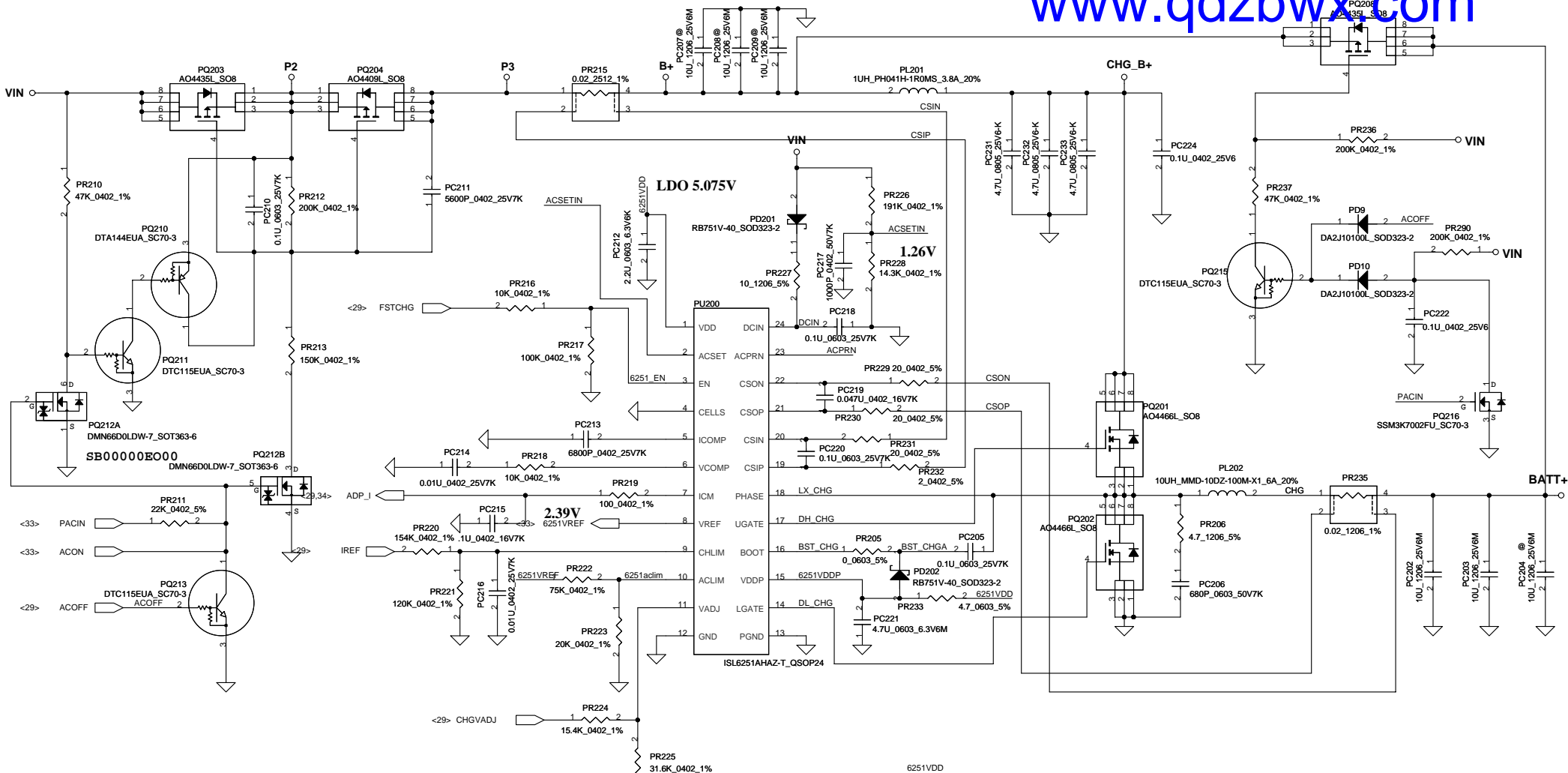
$$R_{set} = 3 * R_{tmh}$$

$$R_{hyst} = (R_{set} * R_{tml}) / (3 * R_{tml} - R_{set})$$

Rtmh at 90C = 7.87K, Rtml at 56C = 26.1K  
 Rset = 3 \* 7.87K = 23.61K ==> 23.7K  
 Rhyst = (23.7K \* 26.1K) / (3 \* 26.1K - 23.7K) = 11.33K ==> 11.3K

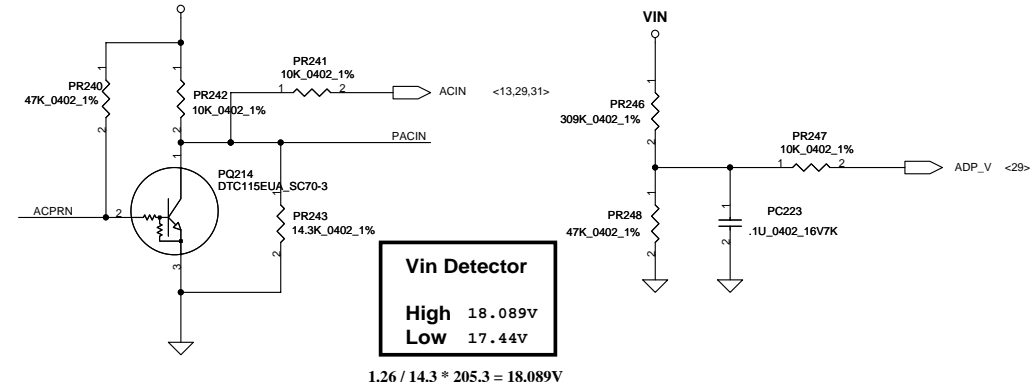


Adapter	CPSETIN	Throttle Watt	Recovery Watt	Throttle Point	Recovery Point
65W_UMA	no use	71.8W	62.45W	1.504V	1.308V



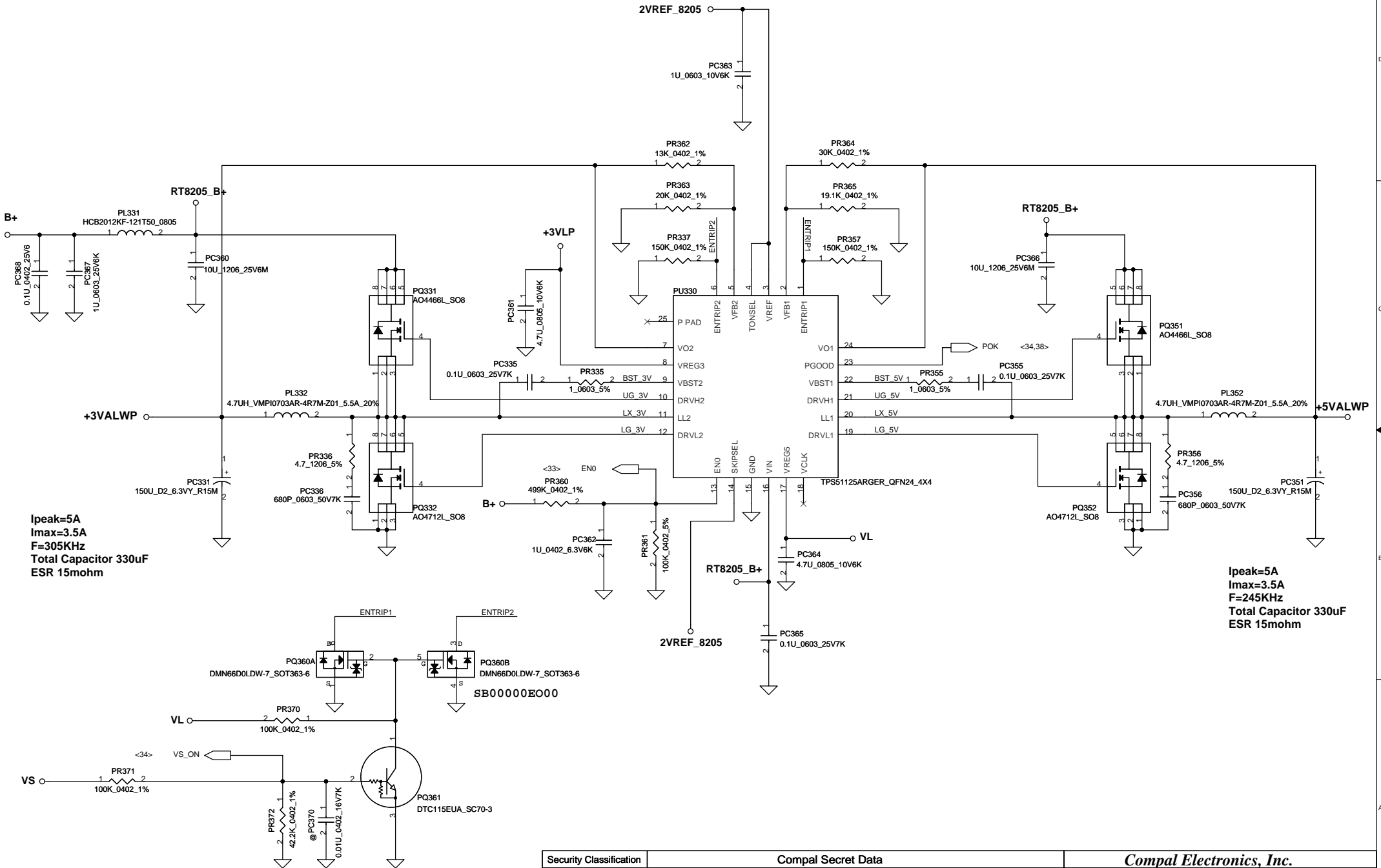
CC=0.25A~3.6A	
IREF=0.9133*Icharge	
IREF=0.228V~3.288V	
VCHLIM need over 88mV	
CHGVADJ=(Vcell-4)*9.445	
Vcell	CHGVADJ
4V	0V
4.2V	1.889V
4.35V	3.30575V

CP mode CP= 92%*Iada	
UMA	
Iada=0~3.947A(75W)	CP=3.63A
Vaclim=1.1V(75W)	PR222=24k, PR223=20k, PR215=0.02

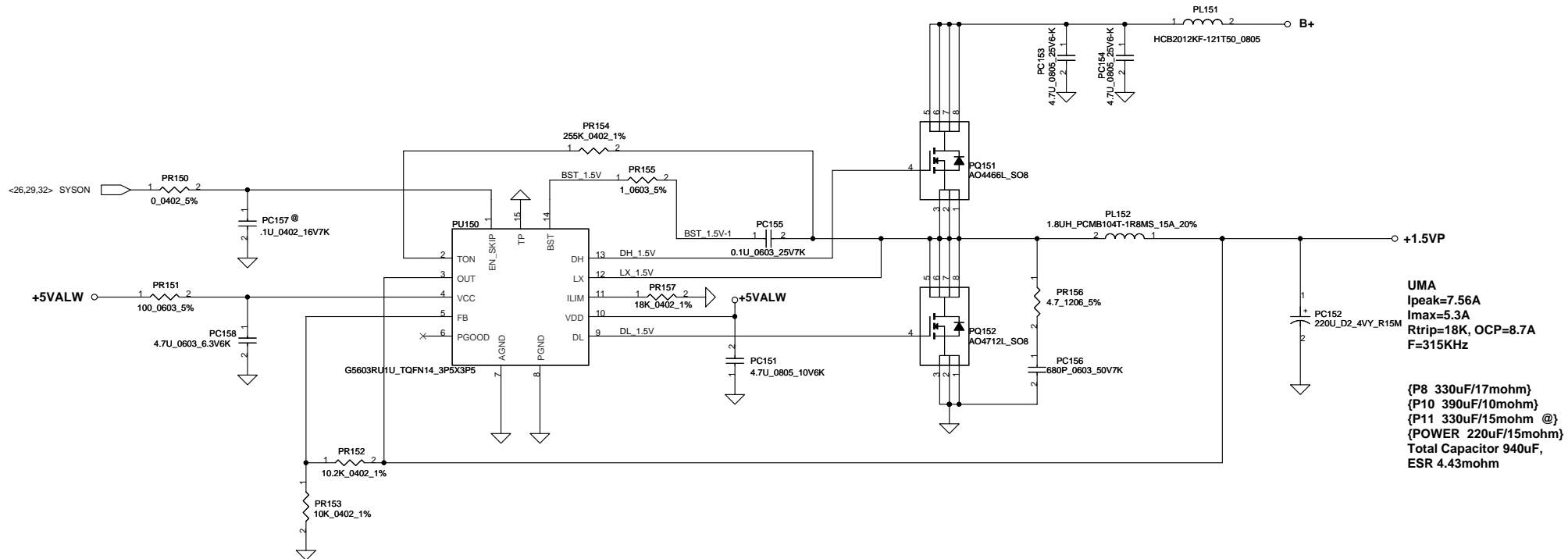


**Vin Detector**  
 High 18.089V  
 Low 17.44V  
 1.26 / 14.3 \* 205.3 = 18.089V

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2010/11/11	Deciphered Date	2011/11/11	Title	SCHEMATIC MB A7571
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					
Date: Monday, May 09, 2011				Sheet	35 of 41

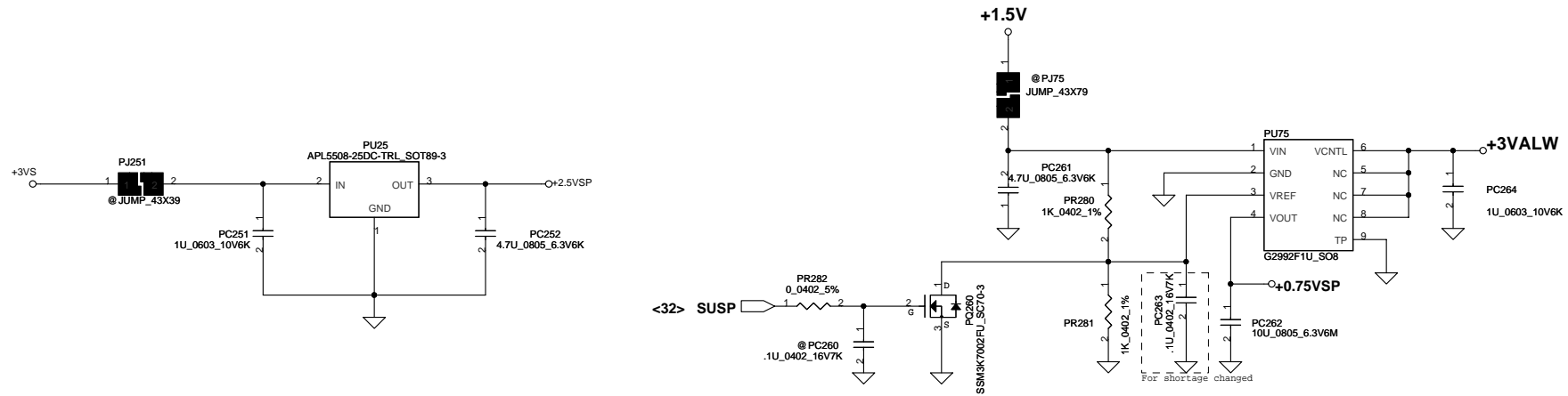


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2010/11/11	Deciphered Date	2011/11/11	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				4019D7	B
Date: Monday, May 09, 2011				Sheet	36 of 41

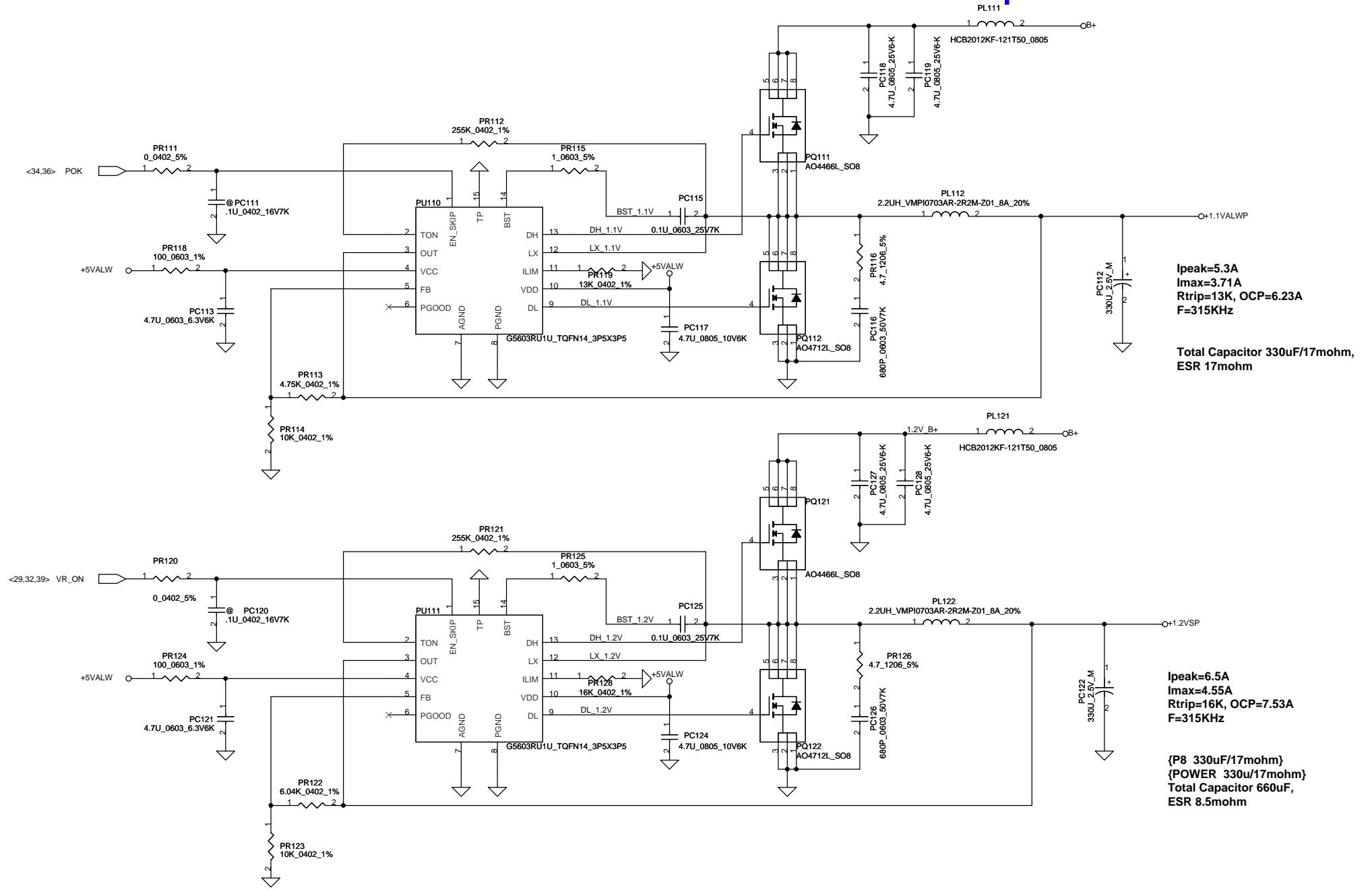


UMA  
I<sub>peak</sub>=7.56A  
I<sub>max</sub>=5.3A  
R<sub>trip</sub>=18K, OCP=8.7A  
F=315KHz

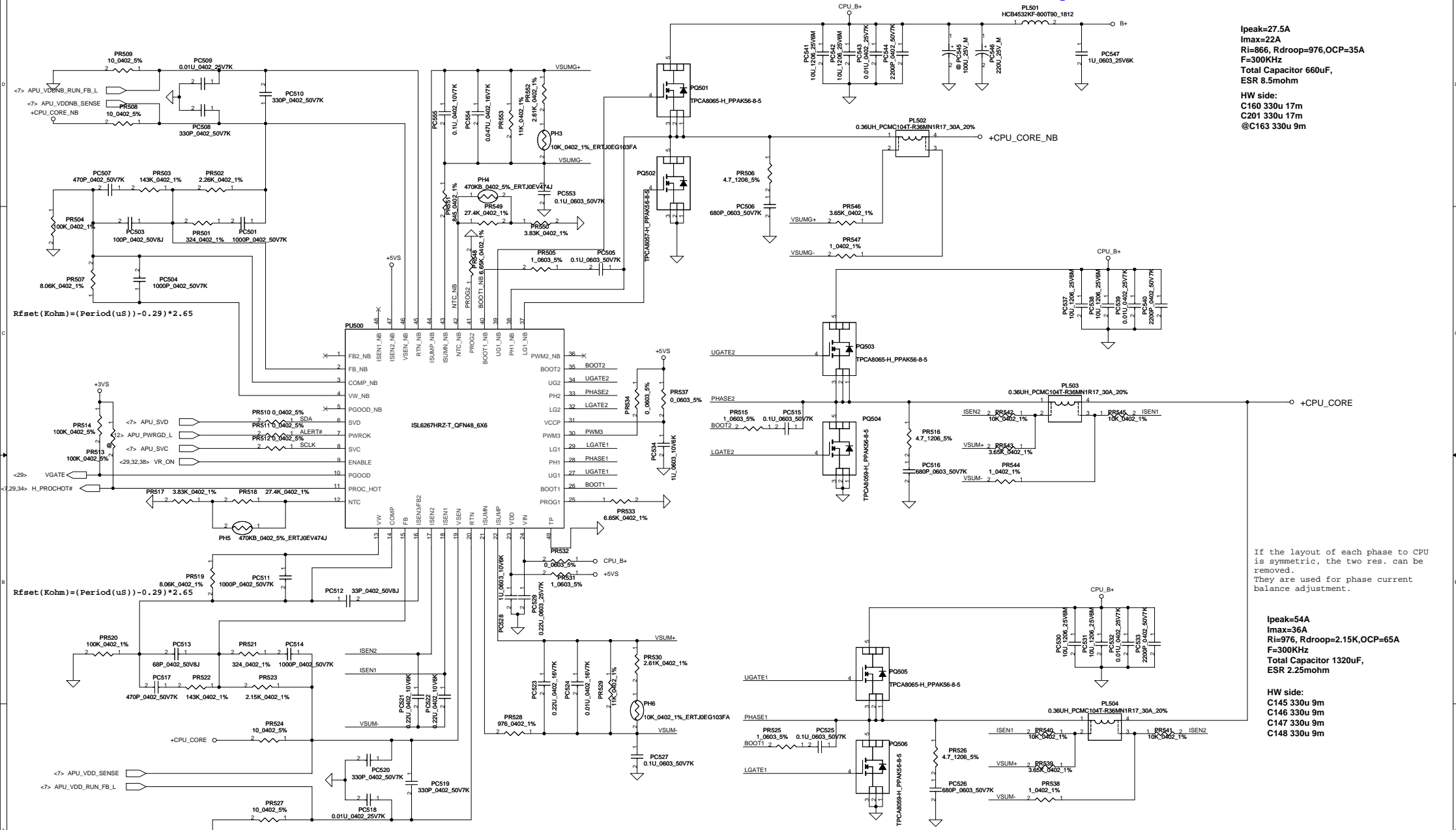
{P8 330uF/17mohm}  
{P10 390uF/10mohm}  
{P11 330uF/15mohm @}  
Total Capacitor 940uF,  
ESR 4.43mohm



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2010/11/11	Deciphered Date	2011/11/11	Title	<b>SCHMATIC MB A7571</b>
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					
				Document Number	<b>4019D7</b>
				Date:	Monday, May 09, 2011
				Sheet	37 of 41
				Rev	B



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2010/11/11	Deciphered Date	2011/11/11	Title	SCHEMATIC MB A7571
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	4019D7
Date:	Monday, May 09, 2011	Sheet	38	of	41



Ipeak=27.5A  
 Imax=22A  
 Ri=866, Rdroop=976, OCP=35A  
 F=300KHz  
 Total Capacitor 660uF,  
 ESR 8.5mohm  
 HW side:  
 C160 330u 17m  
 C201 330u 17m  
 @C163 330u 9m

If the layout of each phase to CPU is symmetric, the two res. can be removed. They are used for phase current balance adjustment.

Ipeak=54A  
 Imax=36A  
 Ri=976, Rdroop=2.15K, OCP=65A  
 F=300KHz  
 Total Capacitor 1320uF,  
 ESR 2.25mohm  
 HW side:  
 C145 330u 9m  
 C146 330u 9m  
 C147 330u 9m  
 C148 330u 9m

Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2010/11/11	Deciphered Date	2011/11/11	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				4019D7
				Rev B
				Monday, May 08, 2011
				Sheet 39 of 41

NO DATE	PAGE	MODIFICATION LIST	PURPOSE
DVT to PVT			
2011/03/16	Page38	Change PR119/PR128 to 13K/16Kohm	For 1.1VALW/1.2VS OCP design point
2011/03/17	Page39	Change PR523 from 2.26K to 2.15Kohm	For match loadline SPEC
2011/03/21	Page37	Change PR152 from 10K to 10.2Kohm	For HW request to increase 1.5V voltage level
2011/03/21	Page35	Change PR236 & PR237 to 200K & 47Kohm	For increase BAT P-MOS divide voltage level
2011/03/21	Page34	Change PR22 & PR28 to 180 & 1.18Kohm	For 65W adapter protection point(H_PROCHOT#)
2011/03/21	Page35	Change PR222 to 75Kohm	For 65W CP protection design point
2011/03/21	Page35&36	Add PC224 & PC368 two 0.1uF/25V_0402 to GND	For EMI solution
2011/03/21	Page35-39	Add all power snuber and change boostrape R to 1ohm	For EMI solution
2011/03/21	Page35	Change PQ208 from A04407 to A04435	For 65W system
2011/03/21	Page35	Change PL202 to moding package	For short risk by QAD 8KG test

Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2010/11/11	Deciphered Date	2011/11/11	
<small>THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&amp;D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.</small>			Title	<b>SCHEMATIC MB A7571</b>
			Size Custom	Document Number <b>4019D7</b>
			Date: Monday, May 09, 2011	Rev B
			Sheet 40 of 41	



# HW PIR (Product Improve Record)

QOQAE LA-7571P SCHEMATIC CHANGE LIST  
 REVISION CHANGE: 0.2 TO 0.3

NO	DATE	PAGE	MODIFICATION LIST	PURPOSE
1.	03/15	26	Add DT4	For ESD's request
2.	03/15	13	Delete ODD_PLUGIN#_FCH level shift since ODD has INT 1k PD	ODD has internal 1K PD
3.	03/15	25	Add level shift for CR_CPPE# leakage issue	For leakage issue
4.	03/15	13	Change HDMI_HPD to GEVENT10#	GEVENT9# has issue
5.	03/15	24	Change LOGO_LED to GPIO176 and WL_BT_LED to GPIO177	For LED flash issue
6.	03/17	25	Change RC16 from 22 ohm to 0 ohm for SDXC performance issue	For SDXC performance issue
7.	03/17	12	Change JCOMS to JCMOS	For naming rule
8.	03/17	07	Delete HDT signals	For ESD's request
9.	03/22	10	Add C21/C22/C23	For EMI's request
10.	03/22	31	Add C396/C397/C398/C399	For ESD's request
11.	03/22	21	Add C390 on +USB_VCCA	For ESD's request
12.	03/24	29	Delete net KS016 and KS017	W/O support 10 key
13.	03/24	07	Delete R127/R118/R119	For ESD's request
14.	03/24	31	Add C400/C40/C402	For ESD's request
15.	03/24	12	Add C332/C333	For ESD's request
16.	03/24	18	Add C287/C295	For ESD's request
17.	03/25	30	Change LPC debug port	For SW request
18.	03/28	18	Add C334	For ESD request
19.	03/28	12	Change C248 and C249 to 15PF	For RTC issue

REVISION CHANGE: 0.3 TO 1.0

NO	DATE	PAGE	MODIFICATION LIST	PURPOSE
1.	04/26	16	Add C521,C342,C335 and C336	For EMI request
2.	04/26	16	Delete Q56 and add R371	No need for AMD A13
3.	04/26	31	Add C403 and C414	For EMI request
4.	04/26	31	Change SW2 and SW3 to 0	For Debug use
5.	04/27	08	Change C201 to 330/9mohm ))))))))	For Dynamic CPU_CORE_NB fail
6.	04/27	14	Delete 450 of U13	For Debug use
7.	04/27	10	Add C343/C344/C345/C346/C347/C348/C349/C350	For ESD request
8.	04/27	31	Add C426	For ESD request
9.	04/29	10	Change C343/C344/C345/C346/C347/C348/C349/C350 from 1u to 0.1u	For ESD request
10.	05/03	12)	Add C333 and C332	For ESD request
11.	05/03	13	Add C363	For ESD request

Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/01/11	Deciphered Date	2011/11/11	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				4019D7
				Rev B
Date: Monday, May 09, 2011				Sheet 41 of 41