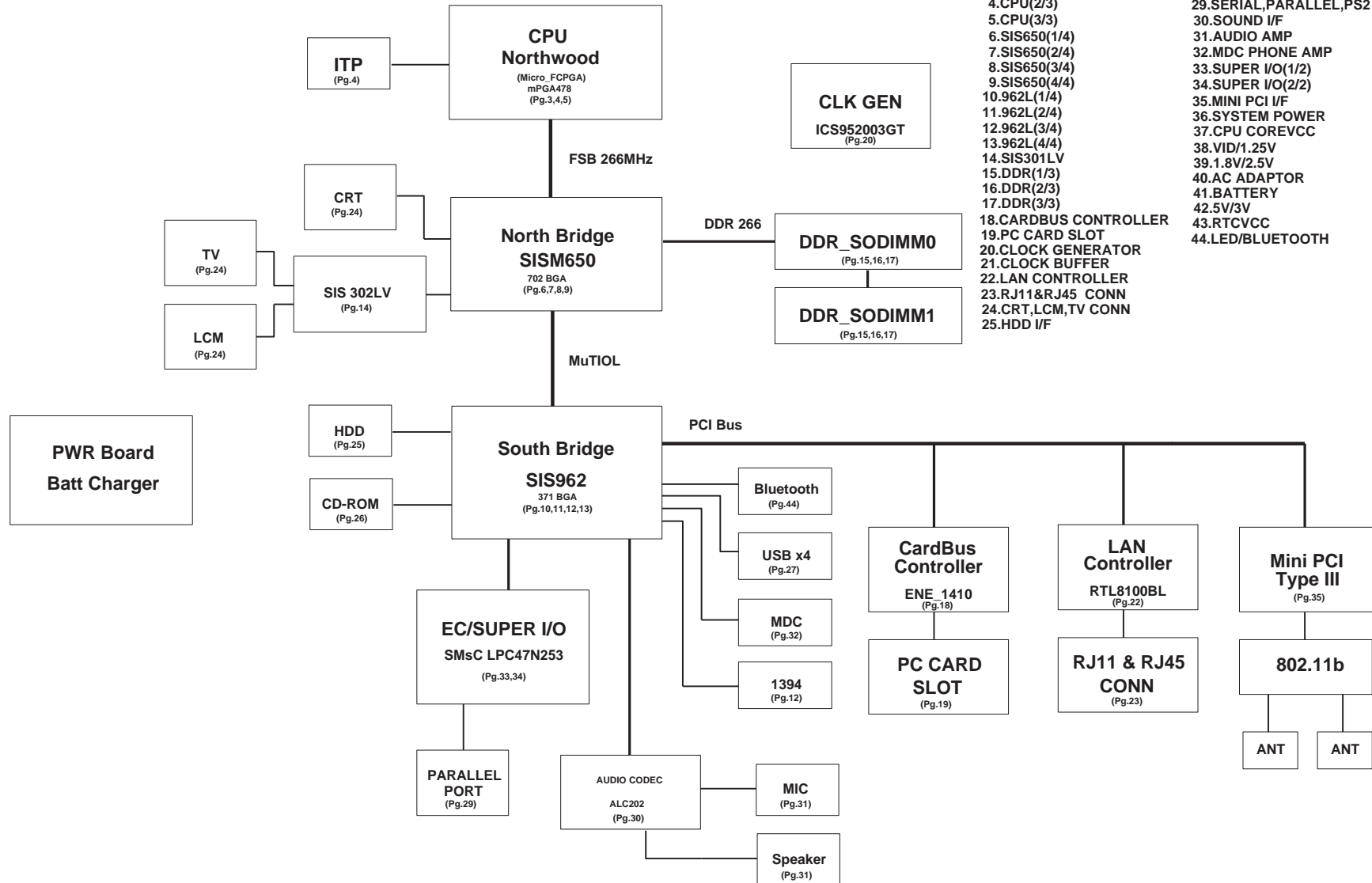


<http://hobi-elektronika.net>

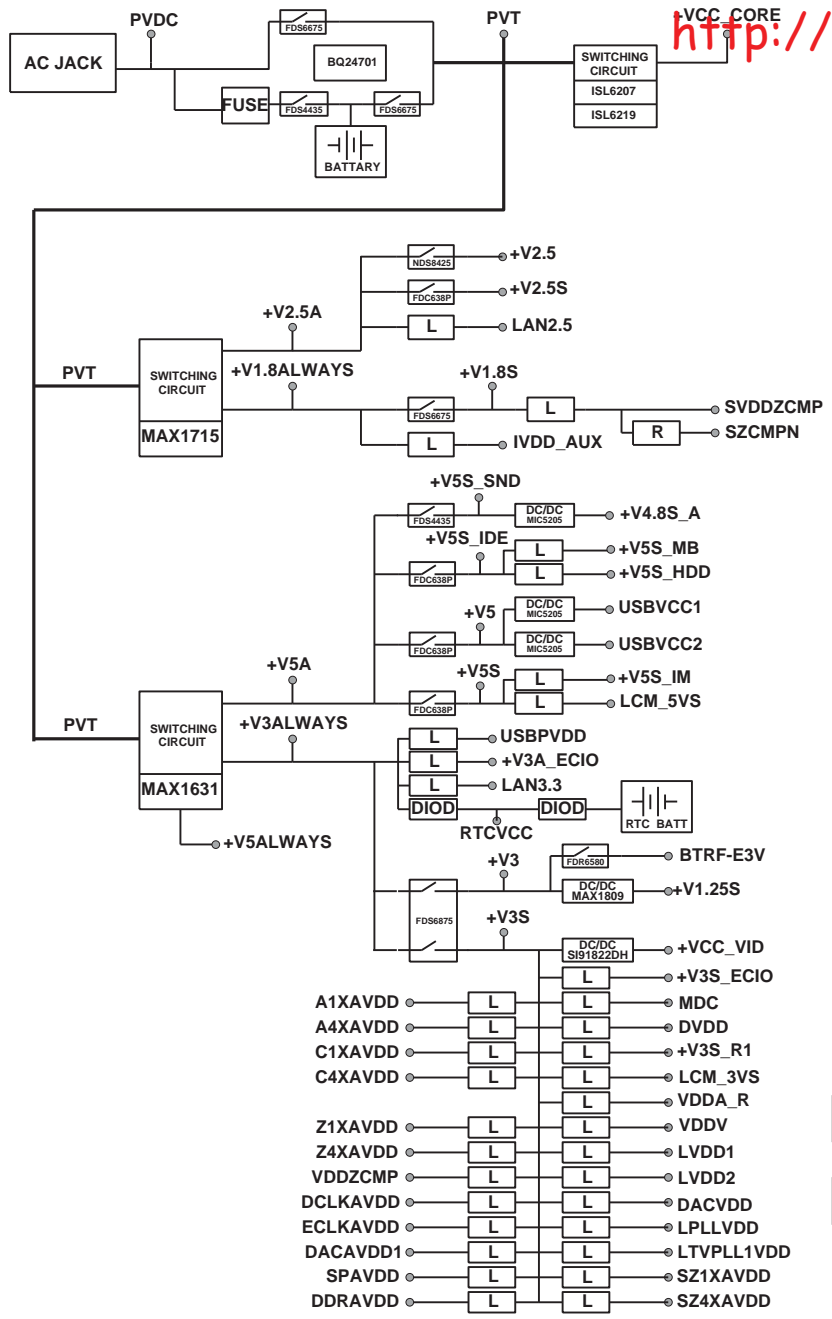


1. BLOCK DIAGRAM
2. POWER DIAGRAM
3. CPU(1/3)
4. CPU(2/3)
5. CPU(3/3)
6. SIS650(1/4)
7. SIS650(2/4)
8. SIS650(3/4)
9. SIS650(4/4)
10. 962L(1/4)
11. 962L(2/4)
12. 962L(3/4)
13. 962L(4/4)
14. SIS301LV
15. DDR(1/3)
16. DDR(2/3)
17. DDR(3/3)
18. CARDBUS CONTROLLER
19. PC CARD SLOT
20. CLOCK GENERATOR
21. CLOCK BUFFER
22. LAN CONTROLLER
23. RJ11&RJ45 CONN
24. CRT, LCM, TV CONN
25. HDD I/F
26. CDROM/FDD
27. USB I/F
28. KB I/F
29. SERIAL, PARALLEL, PS2 PORTS
30. SOUND I/F
31. AUDIO AMP
32. MDC PHONE AMP
33. SUPER I/O(1/2)
34. SUPER I/O(2/2)
35. MINI PCI I/F
36. SYSTEM POWER
37. CPU COREVCC
38. VID/1.25V
39. 1.8V/2.5V
40. AC ADAPTOR
41. BATTERY
42. 5V/3V
43. RTCVCC
44. LED/BLUETOOTH

Engineer INVENTEC	INVENTEC
Drawn by <i>Drawn Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE TOPAZ 1.1 BLOCK DIAGRAM	Size A3

Changed by EES	Date Changed Thursday, October 31, 2002	Time Changed 4:22:07 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 1 of 45
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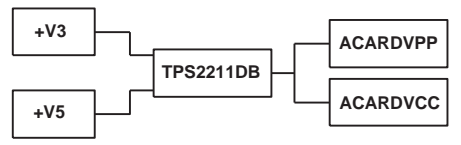
<http://hobi-elektronika.net>



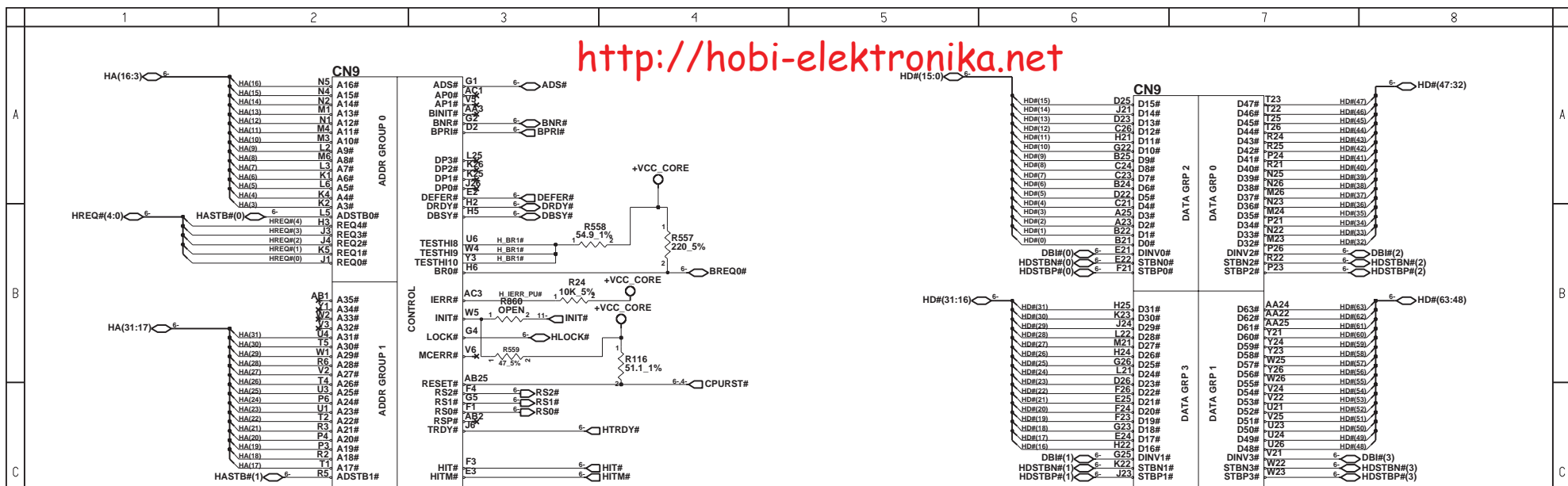
	+V1.8S +V1.25S +VCC_CORE +V5S +V3S	+V2.5 +V5 +V3	+V1.8ALWAYS +V2.5A +V5A +V5ALWAYS +V3ALWAYS
S4 , S5	0	0	1
S3	0	1	1
S0	1	1	1

S0: Full on
S1: Power on suspend
S3: Suspend to RAM
S4: Suspend to Disk
S5: Soft off

+VCC_CORE	South Bridge(P.13)	North Bridge(P.9)	CPU
+V2.5A +V2.5 LAN2.5	DDR-RAM(P.15)	North Bridge(P.9)	
+V1.8ALWAYS	South Bridge(P.13)	North Bridge(P.9)	
+V1.8S	South Bridge(P.10)(P.13)	North Bridge(P.9)	
+V5ALWAYS			
+V5A +V5 USBVCC1 USBVCC2	USB#1(P.27) USB#2(P.27)		
+V5S +V5S_IM LCM_5VS	PS2(P.29) Touch PAD(P.28) LCM(P.24)		
+V5S_SND +V4.8S_A	AUDIO(P.31) SOUND(P.30) AUDIO(P.31)		
+V5S_IDE +V5S_MB +V5S_HDD	CDROM(P.26) H.D.D.(P.25)		
+V3ALWAYS	BIOS(P.35) KBC(P.28) South Bridge(P.13) North Bridge(P.9)		
USBPVDD +V3A_ECIO LAN3.3	USB(P.27) SUPER I/O(P.34) LAN(P.22)		
+V3 BTRF-E3V +V1.25S	KBC(P.28) BLUE TOOTH(P.45) DDR-RAM(P.15)	North Bridge(P.9) CardBUS-CNTR(P.18)	
+V3S +VCC_VID +V3S_ECIO MDC +V3S_R1 VDDA_R DVDD VDDV SZ1XAVDD SZ4XAVDD	South Bridge(P.13) CPU SUPER I/O(P.34) SOUND-AC97(P.32) CLOCK Generator(P.20) CLOCK Generator(P.20) LCM-D/A(P.14) LCM-D/A(P.14) South Bridge(P.10) South Bridge(P.10)	North Bridge(P.9) MiniPCI(P.36) S/P(P.29) Thermal Sensor(P.21)	IR(P.35) RGB-D/A(P.24) SiS310LV(P.14)
+V3 ACARDVCC BCARDVCC ACARDVPP BCARDVPP	CardBUS-CNTR(P.18) CardBUS-CNTR(P.18) PCMCIA(P.19) PCMCIA(P.19)		



Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 Size: A3
Drawn by Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

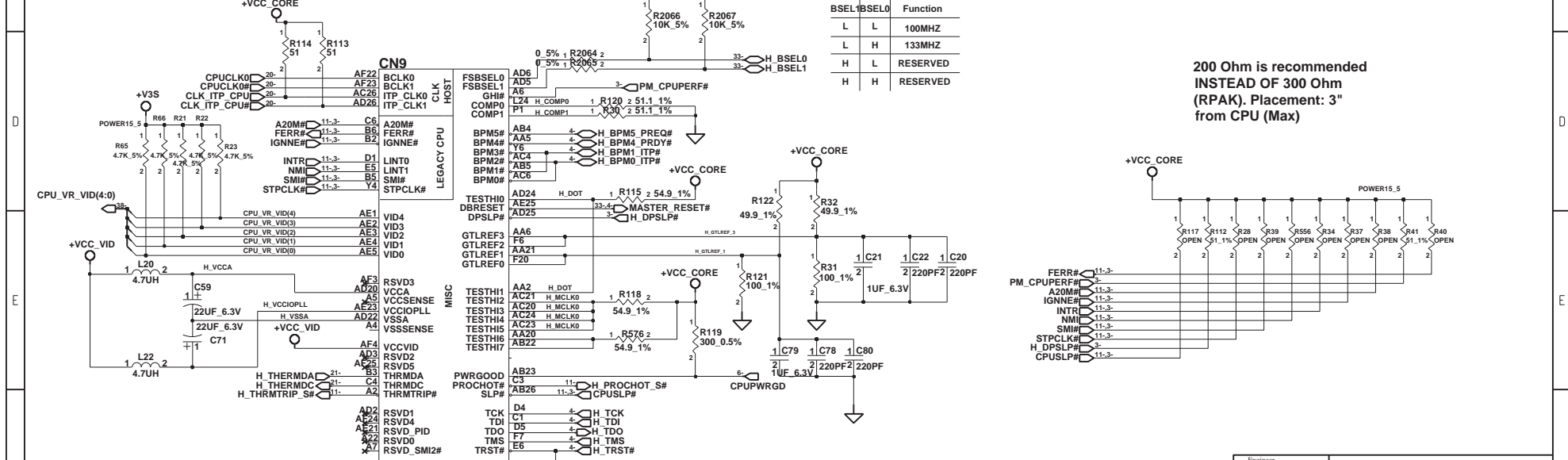


FOX_MPGA478B_PZ47803_2748_1_478P
(ITL_NORTHWOOD_1200_733MHZ_512K_FC_PGA2_478P)

FOX_MPGA478B_PZ47803_2748_1_478P
(ITL_NORTHWOOD_1200_733MHZ_512K_FC_PGA2_478P)

BSEL#	BSEL0	Function
L	L	100MHZ
L	H	133MHZ
H	L	RESERVED
H	H	RESERVED

200 Ohm is recommended
INSTEAD OF 300 Ohm
(RPAK). Placement: 3"
from CPU (Max)

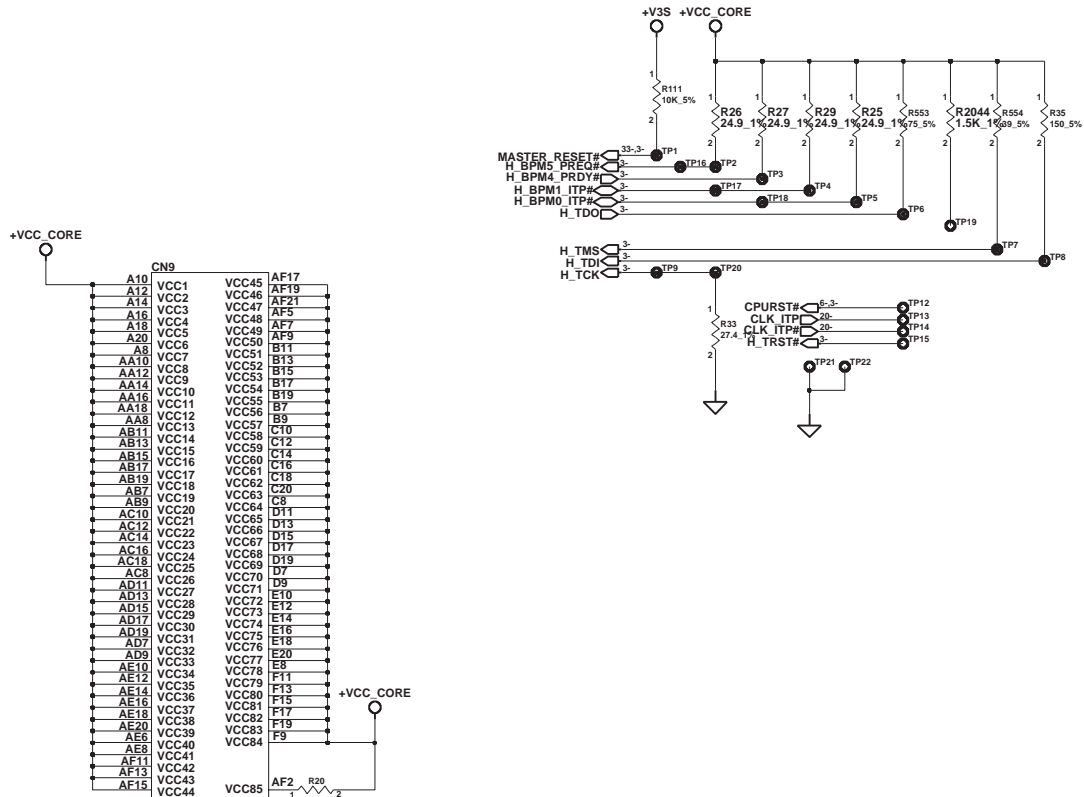


FOX_MPGA478B_PZ47803_2748_1_478P
(ITL_NORTHWOOD_1200_733MHZ_512K_FC_PGA2_478P)

NORTHWOOD 1 OF 3

Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 CPU(1/3) Size: A3
Drawn by DRAWER	
Rev'd CHK	
DOC CTRL CHK	
MFG ENGR CHK	VER: 1.2 Model Number: PC8677 Sheet 3 of 45

CN9		
H1	VSS1	C19
H4	VSS2	C2
H23	VSS3	C22
H26	VSS4	C25
A11	VSS5	C5
A13	VSS6	C7
A15	VSS7	C9
A17	VSS8	D10
A19	VSS9	D12
A21	VSS10	D14
A24	VSS11	D18
A26	VSS12	D20
A3	VSS13	D24
A9	VSS14	D3
AA1	VSS15	D6
AA11	VSS16	D8
AA13	VSS17	E1
AA15	VSS18	E11
AA19	VSS19	E13
AA23	VSS20	E15
AA26	VSS21	E17
AA4	VSS22	E23
AA7	VSS23	E26
AA9	VSS24	E7
AA9	VSS25	E8
AB10	VSS26	E9
AB12	VSS27	E16
AB14	VSS28	E18
AB16	VSS29	E19
AB18	VSS30	F12
AB20	VSS31	F14
AB21	VSS32	F16
AB24	VSS33	F18
AB3	VSS34	F2
AB6	VSS35	F22
AB8	VSS36	F25
AC11	VSS37	F8
AC13	VSS38	F8
AC15	VSS39	G21
AC17	VSS40	G24
AC19	VSS41	G3
AC2	VSS42	G8
AC22	VSS43	J2
AC26	VSS44	J22
AC9	VSS45	J25
AC9	VSS46	J5
AC9	VSS47	J5
AD1	VSS48	K21
AD10	VSS49	K24
AD12	VSS50	K3
AD14	VSS51	K6
AD16	VSS52	L1
AD18	VSS53	L23
AD21	VSS54	L26
AD23	VSS55	L4
AD4	VSS56	M2
AD8	VSS57	M22
AE11	VSS58	M25
AE13	VSS59	M5
AE15	VSS60	N21
AE17	VSS61	N24
AE19	VSS62	N3
AE22	VSS63	N6
AE24	VSS64	N6
AE26	VSS65	P22
AE7	VSS66	P25
AE9	VSS67	P5
AF1	VSS68	R23
AF10	VSS69	R26
AF12	VSS70	R4
AF14	VSS71	R71
AF16	VSS72	T24
AF18	VSS73	T3
AF20	VSS74	T6
AF26	VSS75	U2
AF8	VSS76	U22
AF8	VSS77	U25
B10	VSS78	U5
B12	VSS79	V23
B14	VSS80	V26
B16	VSS81	V4
B18	VSS82	W1
B20	VSS83	W1
B23	VSS84	W24
B26	VSS85	W3
B4	VSS86	W6
BB	VSS87	W6
C11	VSS88	Y22
C13	VSS89	Y25
C15	VSS90	Y5
C17	VSS91	Y5



FOX_MPGA478B_PZ47803_2748_1_478P
(ITL_NORTHWOOD_1200_733MHZ_512K_FC_PGA2_478P)

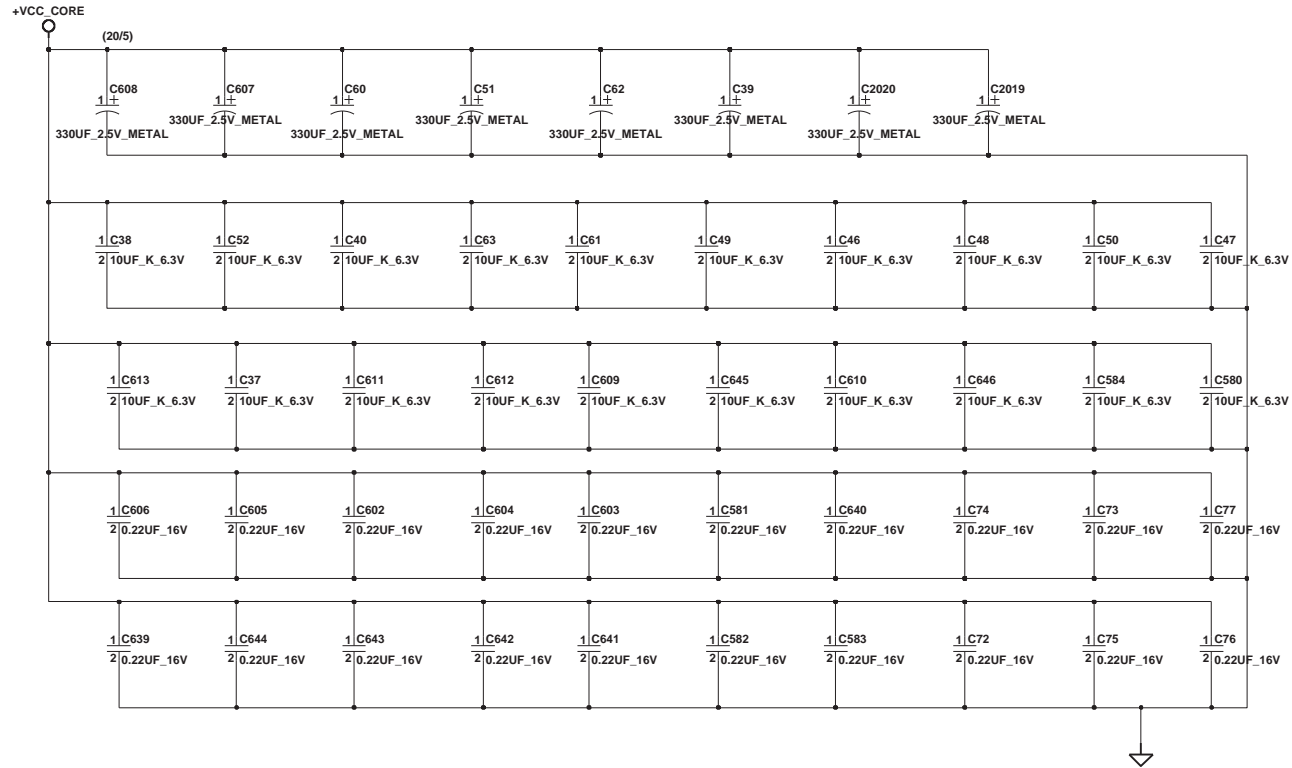
NORTHWOOD 2 OF 3

FOX_MPGA478B_PZ47803_2748_1_478P
(ITL_NORTHWOOD_1200_733MHZ_512K_FC_PGA2_478P)

Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 CPU(2/3) Model Number: PC8677	Size A3
Drawn by Drafter Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

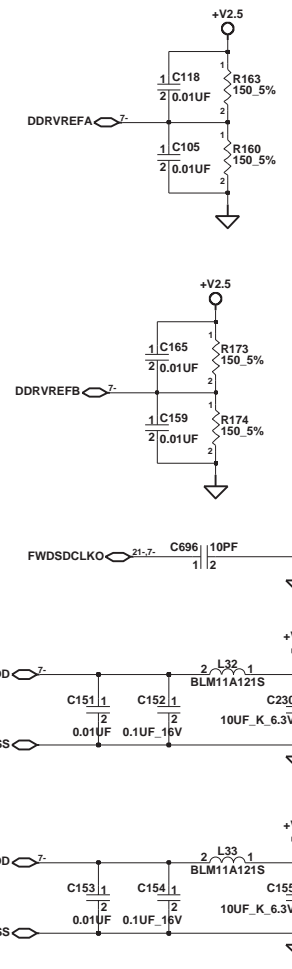
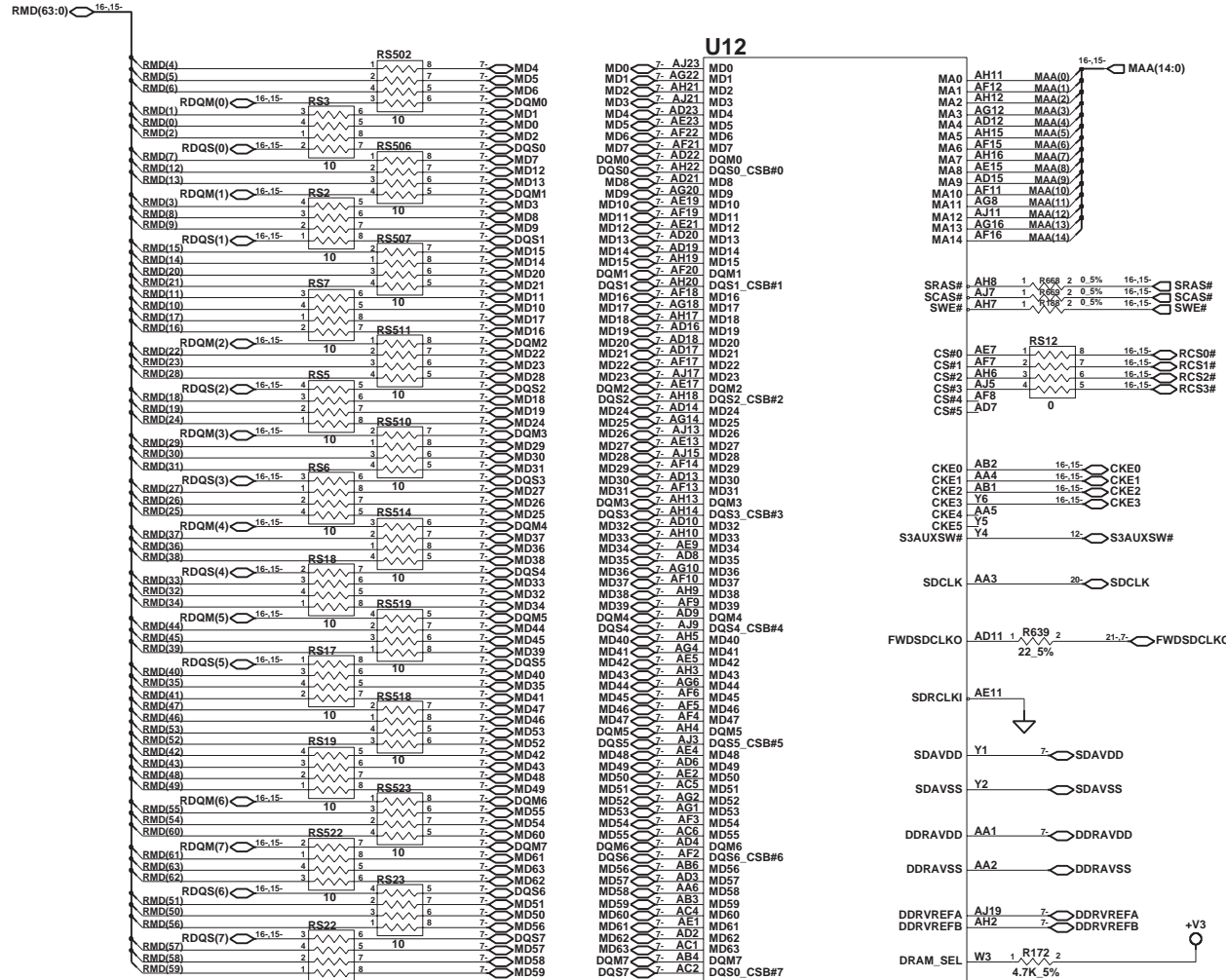
Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:42:51 pm	QA CHK	VER 1.2	Sheet 4 of 45
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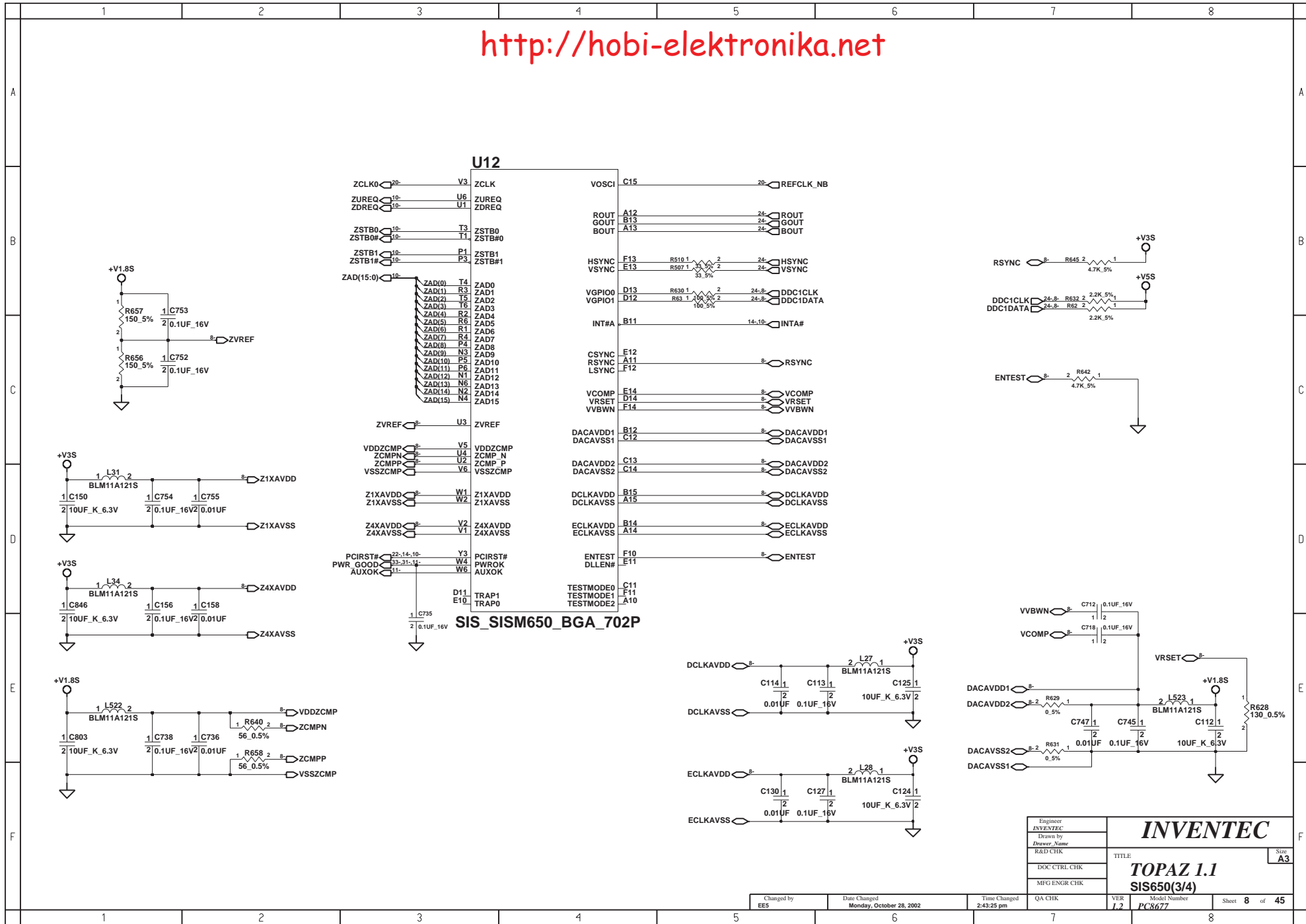
Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 CPU(3/3) Size: A3					
Drawn by <i>Drawer Name</i>						
R&D CHK						
DOC CTRL CHK						
MFG ENGR CHK						
Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:43:14 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 5 of 45

<http://hobi-elektronika.net>



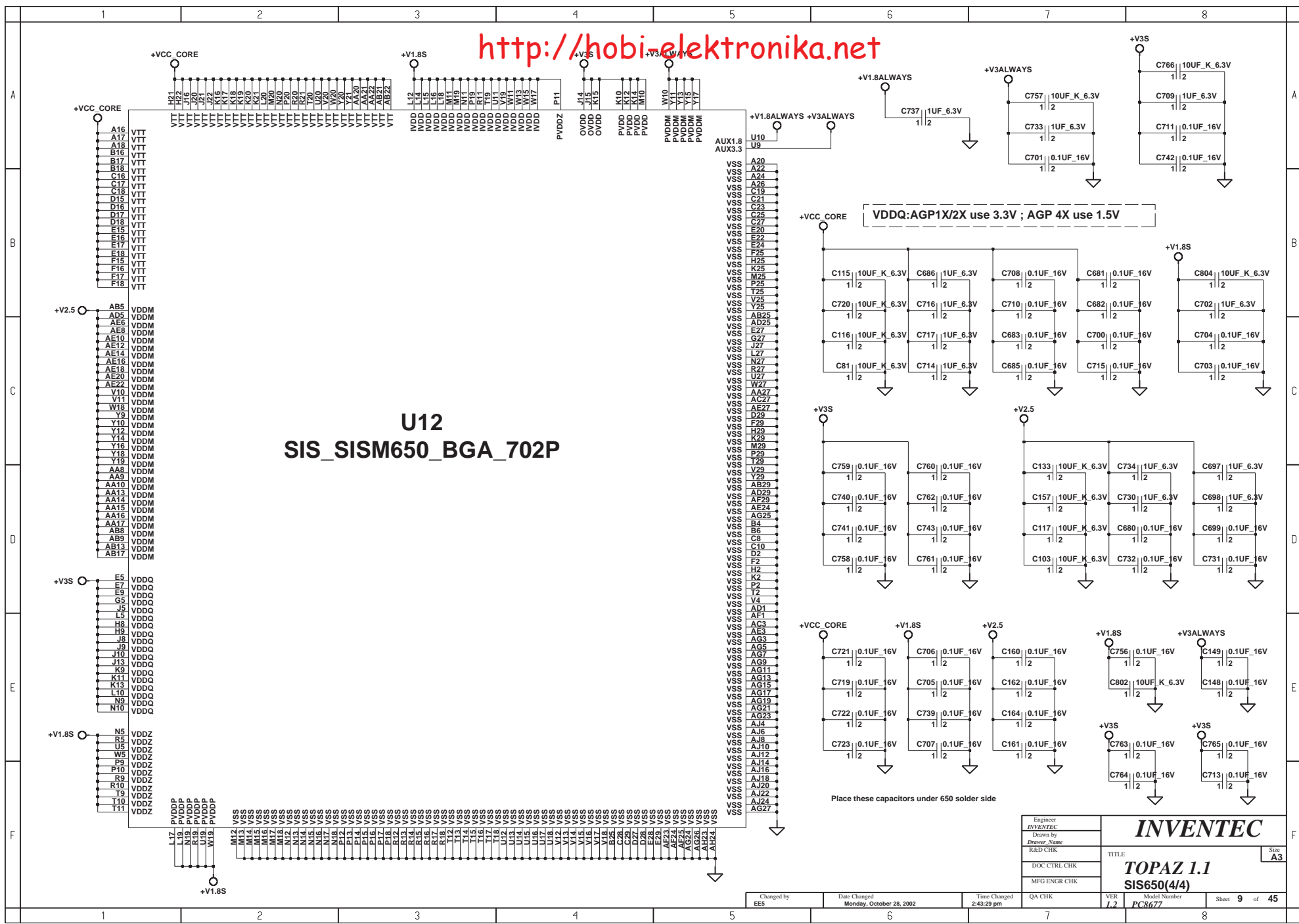
Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 SIS650(2/4) Size: A3
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:43:22 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 7 of 45
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Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 SIS650(3/4)	Size A3
Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

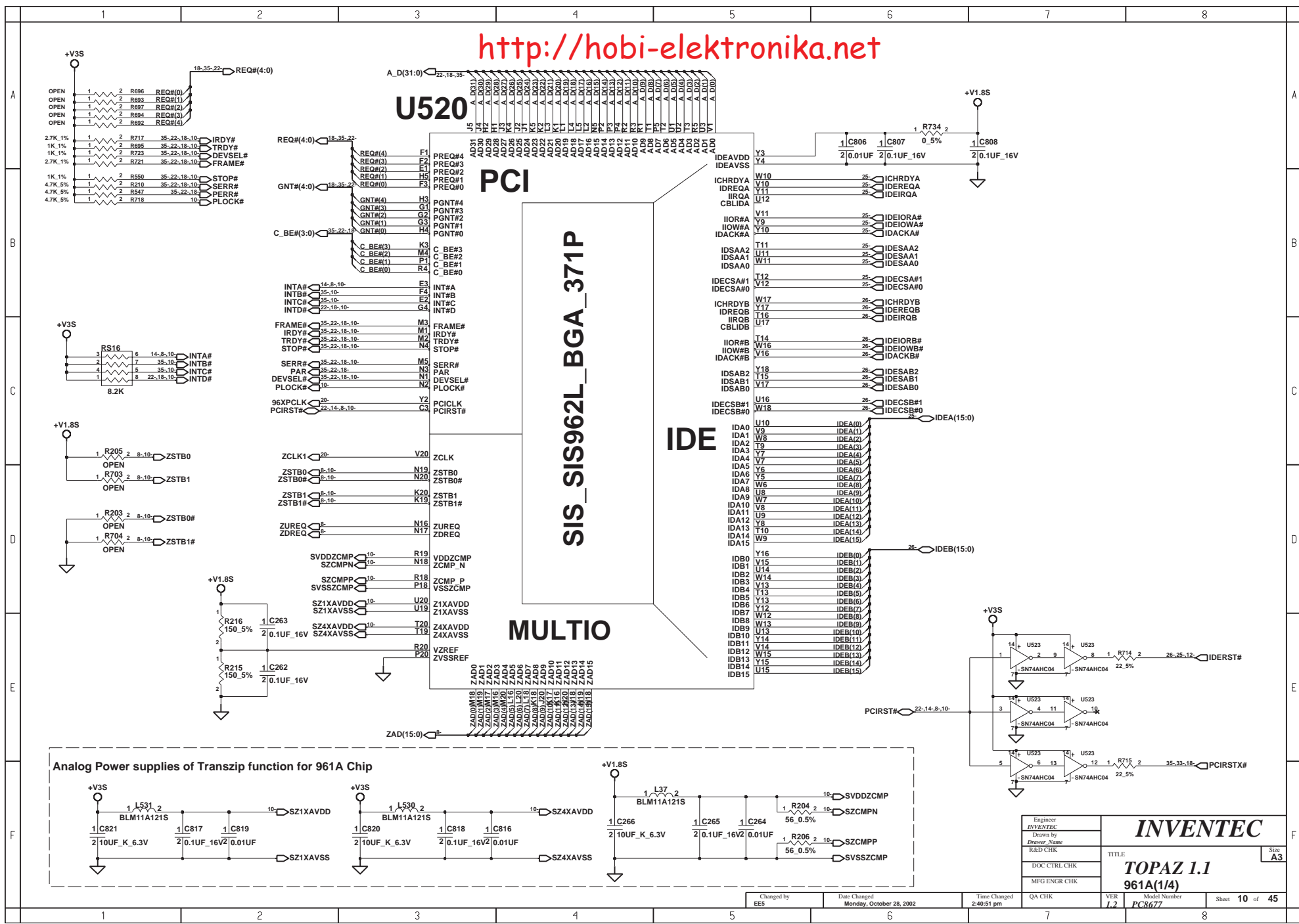
<http://hobi-elektronika.net>



U12
SIS_SISM650_BGA_702P

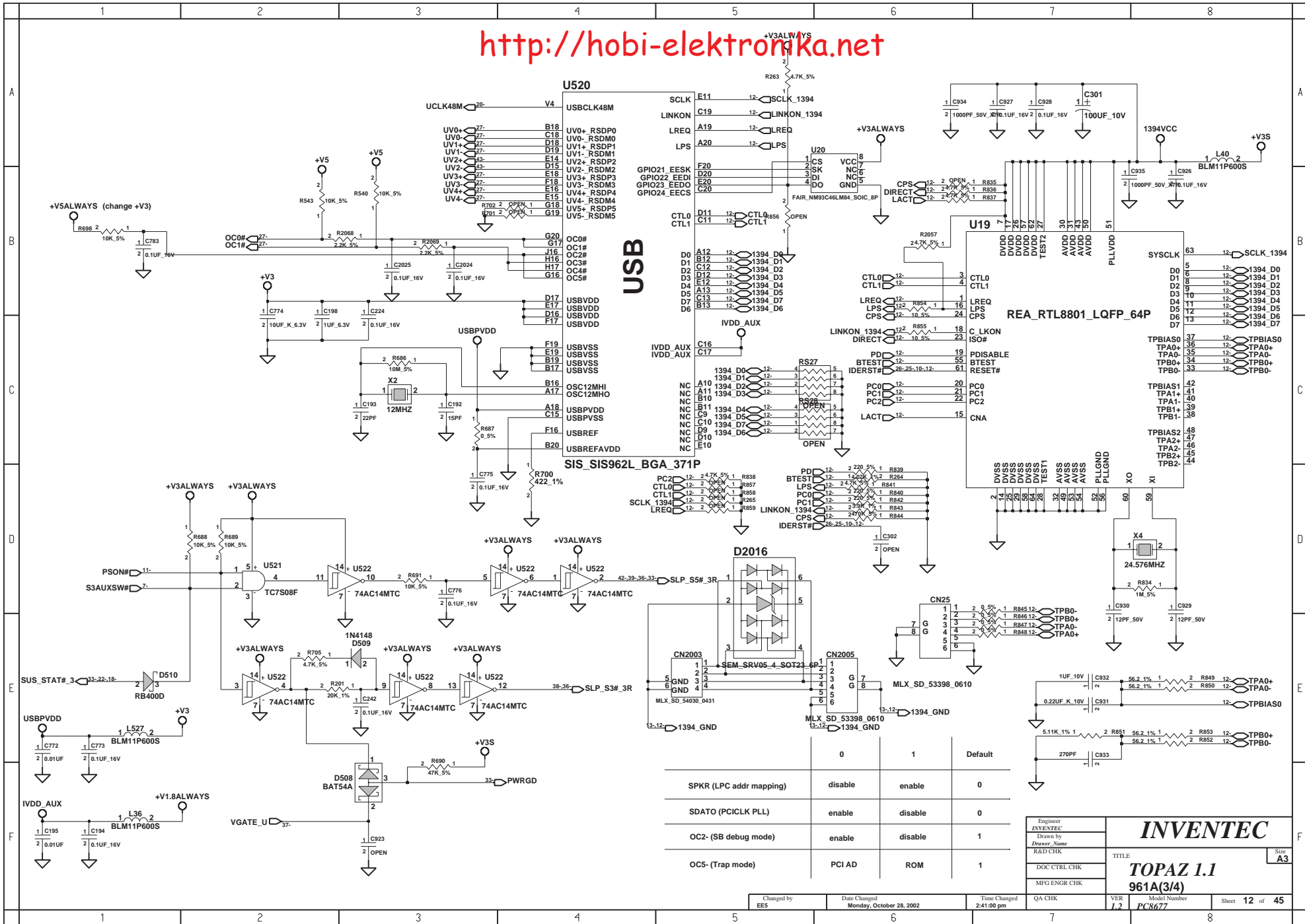
Engineer INVENTEC	<p align="center">INVENTEC</p> <p align="center">TITLE: TOPAZ 1.1</p> <p align="center">SIS650(4/4)</p>	Model Number PC8677	Sheet 9 of 45
Drawn by <i>Drawer Name</i>			
Rev'd CHK			
DOC CTRL CHK			
MFG ENGR CHK			

Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:43:29 pm	QA CHK	VER 1.2
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Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 961A(1/4)	Size A3
Drawn by		
Doc Ctrl. Chk		
MFG ENGR CHK		
VER 1.2		Model Number PC8677

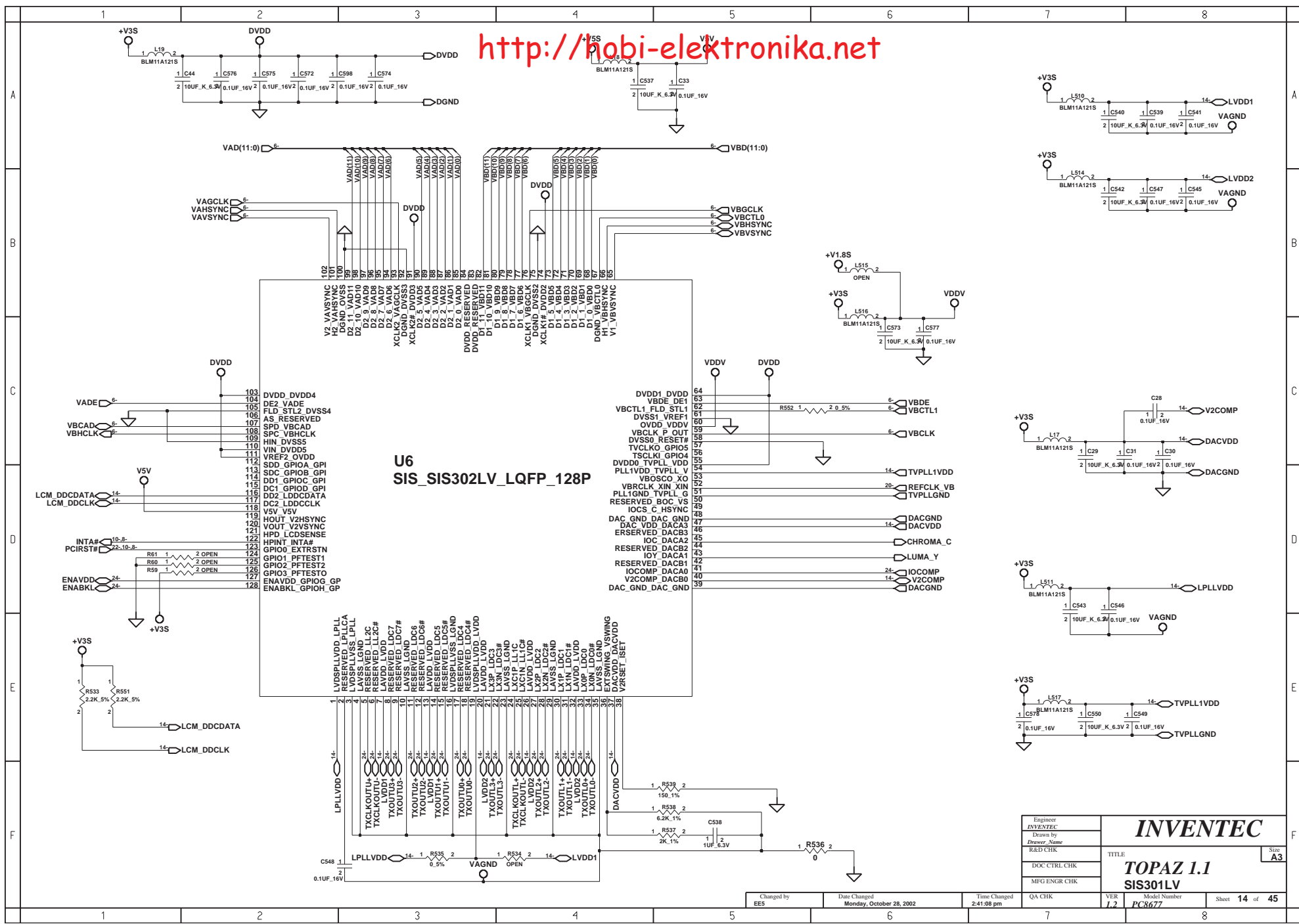
<http://hobi-elektronika.net>



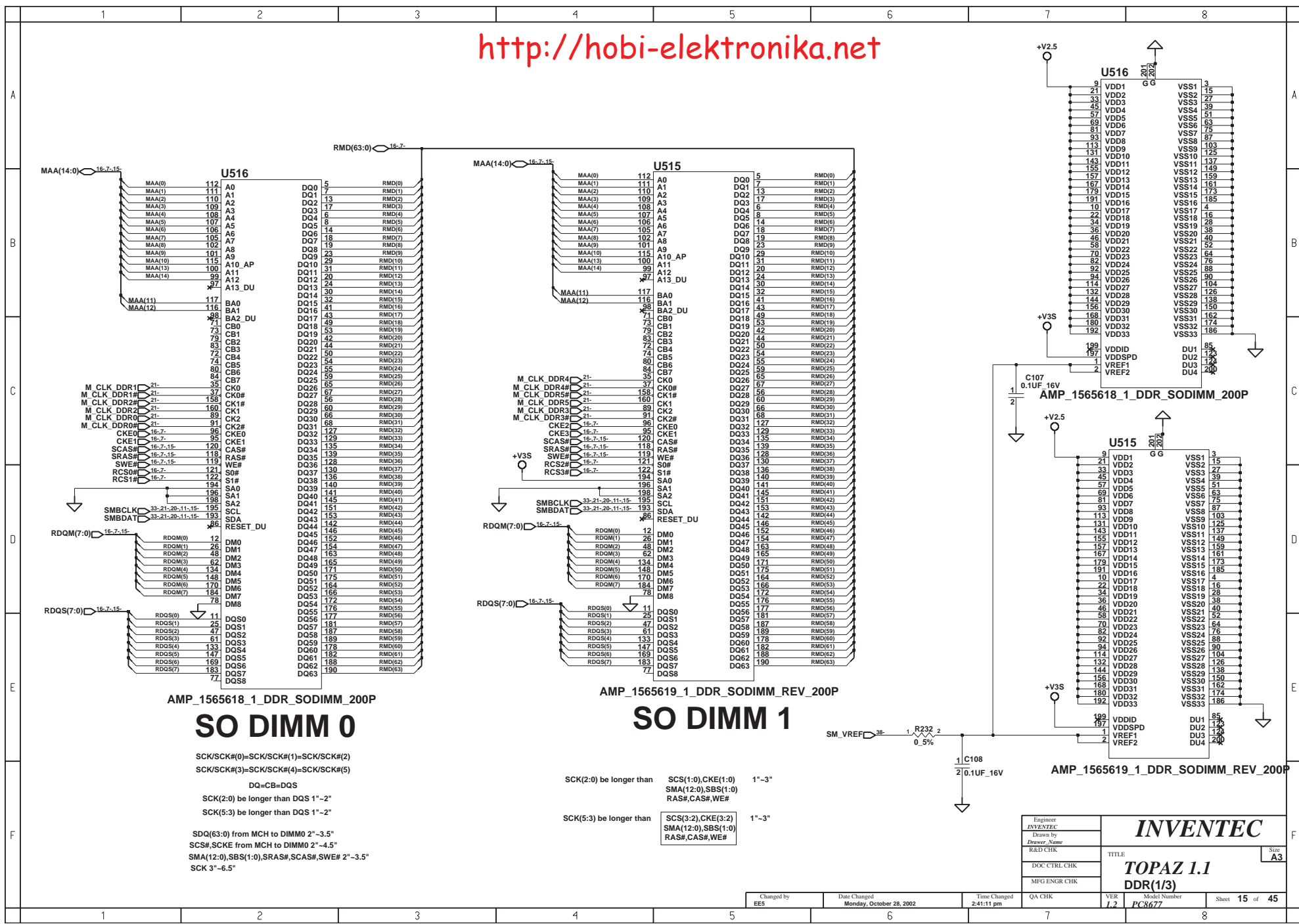
	0	1	Default
SPKR (LPC addr mapping)	disable	enable	0
SDATO (PCICLK PLL)	enable	disable	0
OC2- (SB debug mode)	enable	disable	1
OC5- (Trap mode)	PCI AD	ROM	1

Engineer INVENTEC	INVENTEC	Size A3
Drawn by Drafter Name		TITLE TOPAZ 1.1
RxD CHK		Model Number 961A(3/4)
DOC CTRL CHK		Sheet 12 of 45
MFG ENGR CHK	VER 1.2	Model Number PC8677

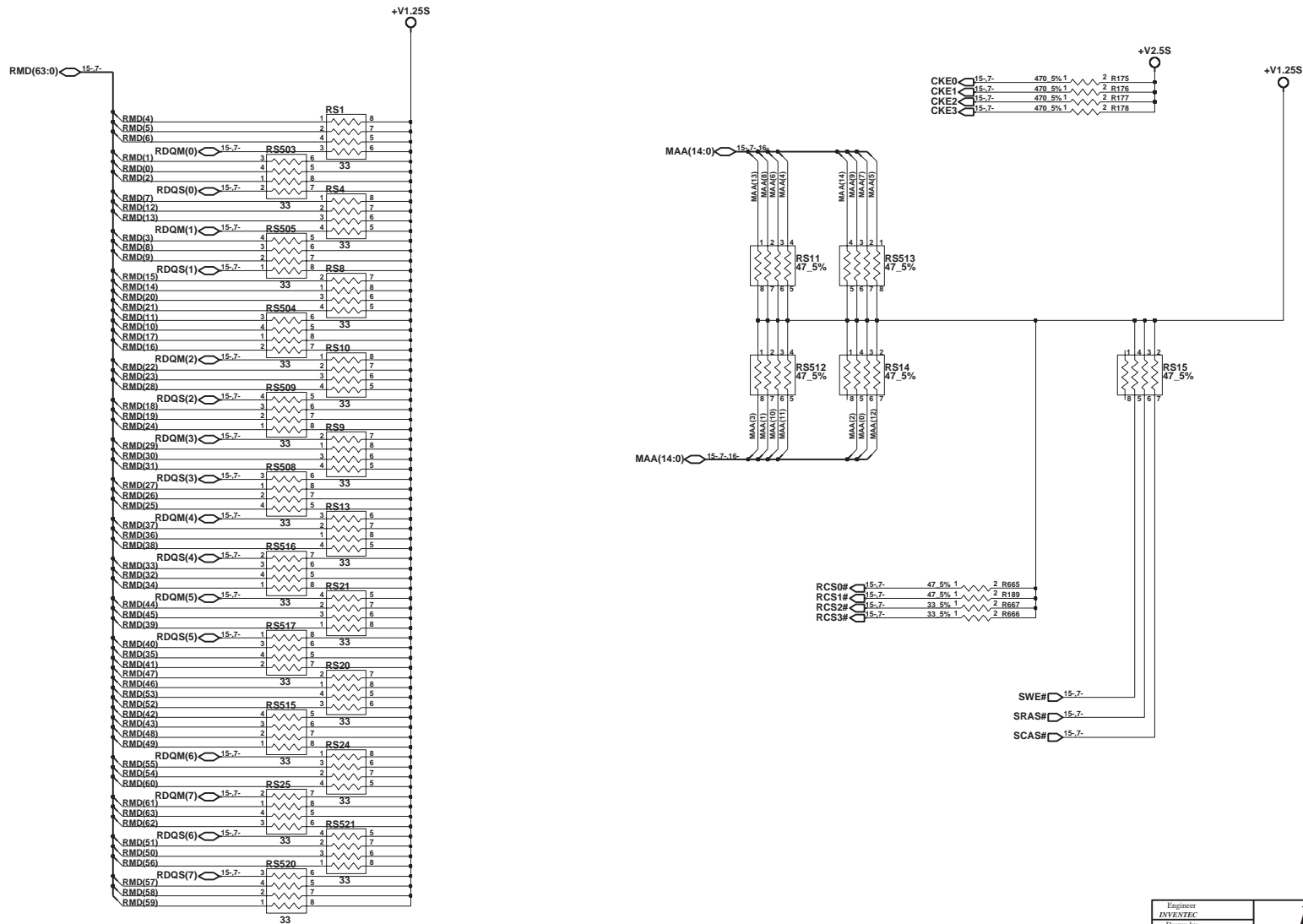
<http://mobi-elektronika.net>



Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 SIS301LV Model Number PC8677	Size A3
Drawn by R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		
VER 1.2		



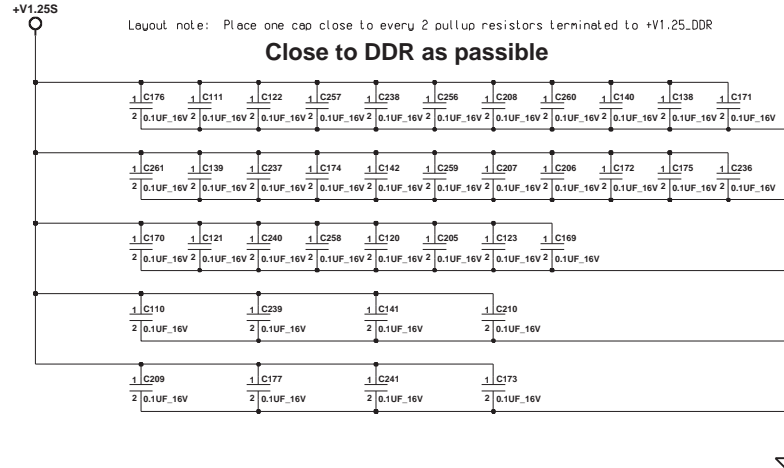
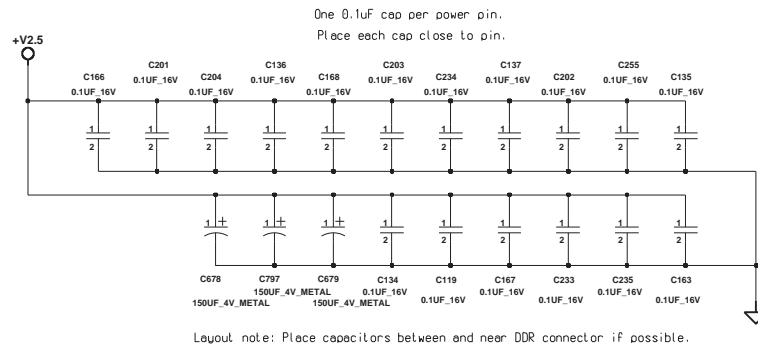
<http://hobi-elektronika.net>



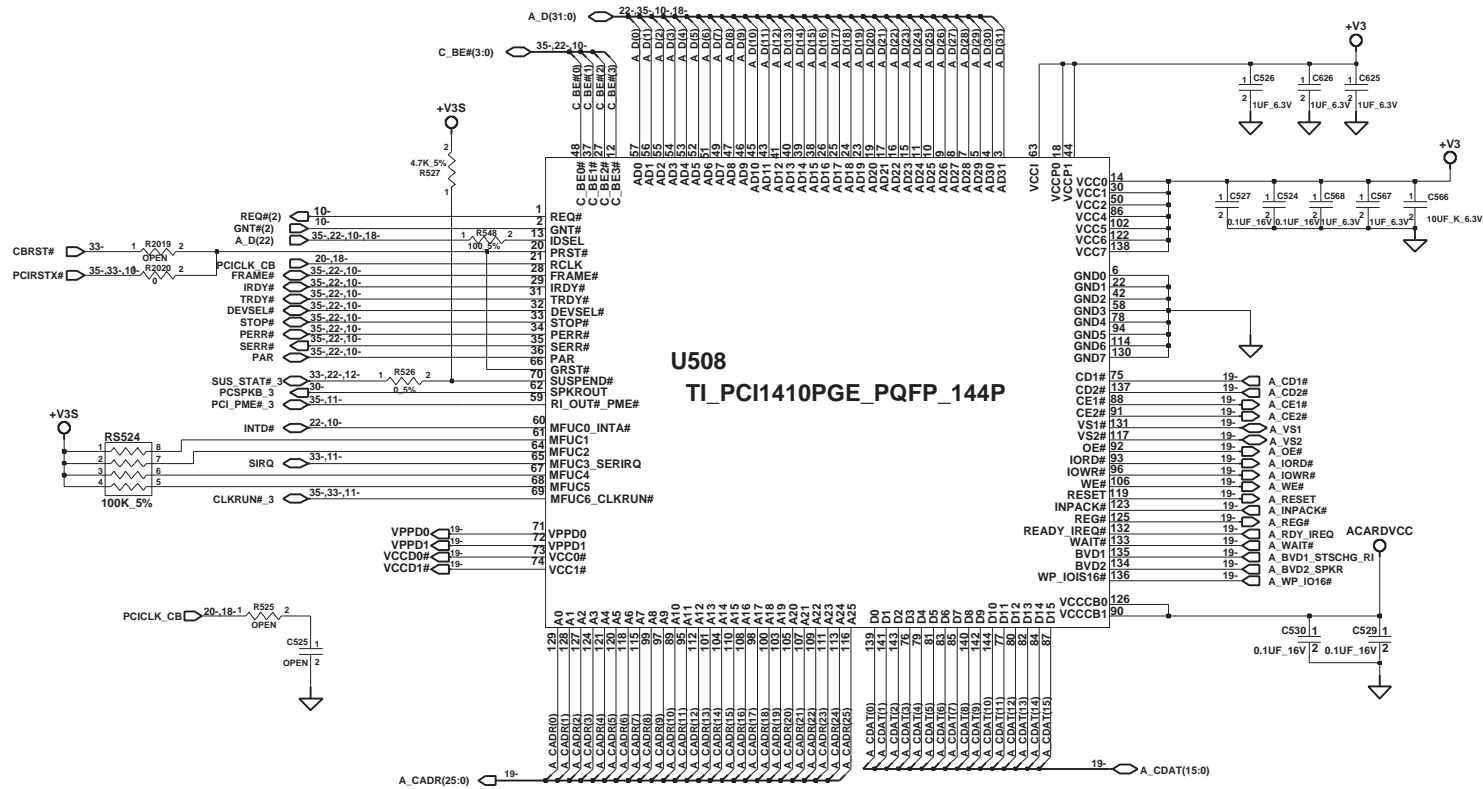
Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 DDR(2/3)	Size A3
Drawn by <i>Drawer Name</i>		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:41:15 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 16 of 45
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<http://hobi-elektronika.net>

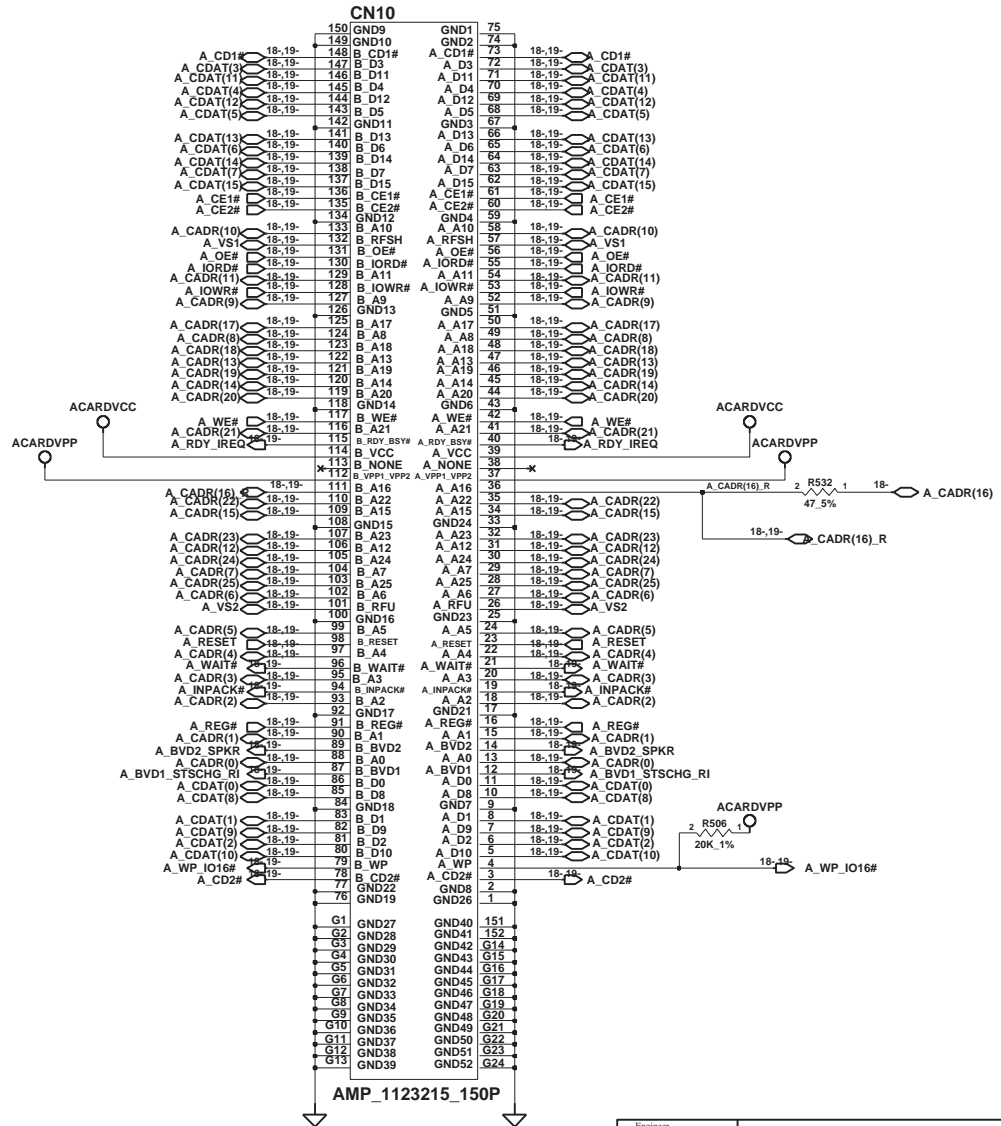
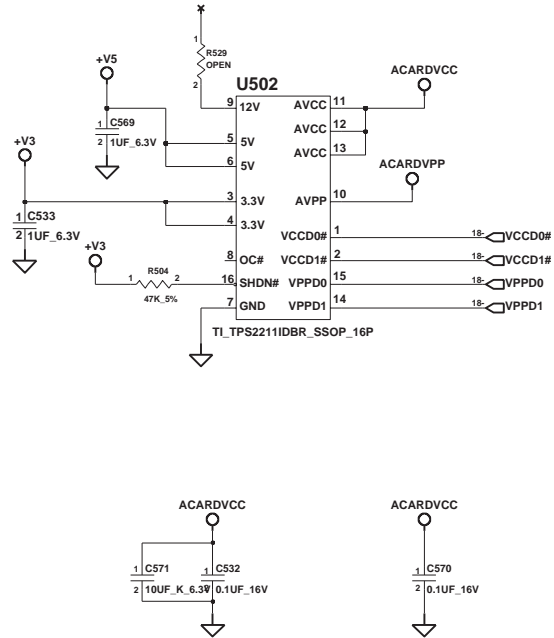


Engineer <i>INVENTEC</i>	INVENTEC TITLE: TOPAZ 1.1 DDR(3/3)	Size A3				
Drawn by <i>Drawer Name</i>						
R&D CHK						
DOC CTRL CHK						
MFG ENGR CHK						
Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:41:19 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 17 of 45



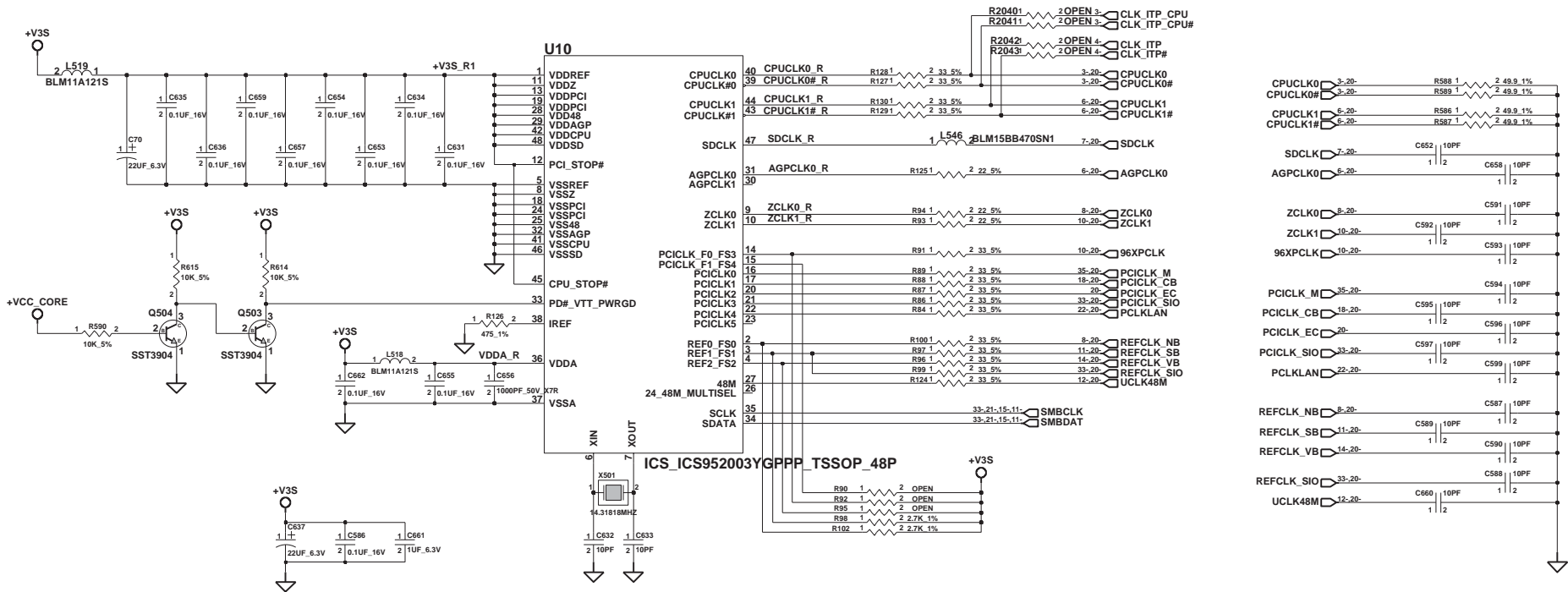
Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 CARBUS CONTROLLER Model Number: PC8677	Size: A3
Drawn by <i>Drawn Name</i>		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		
Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:41:22 pm
QA CHK	VER 1.2	Sheet 18 of 45

<http://hobi-elektronika.net>



Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 PC CARD SLOT Size: A3
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

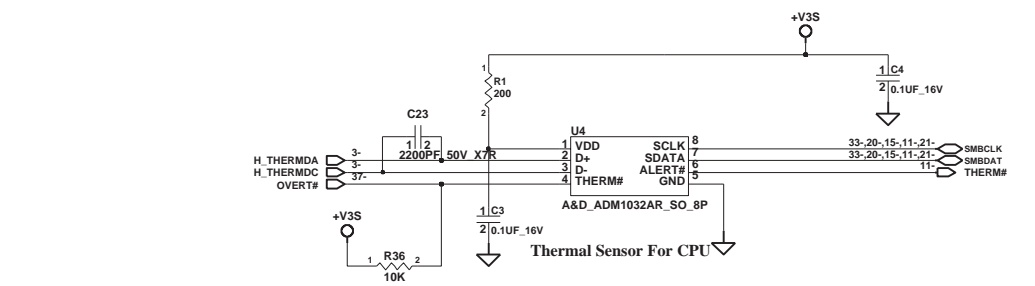
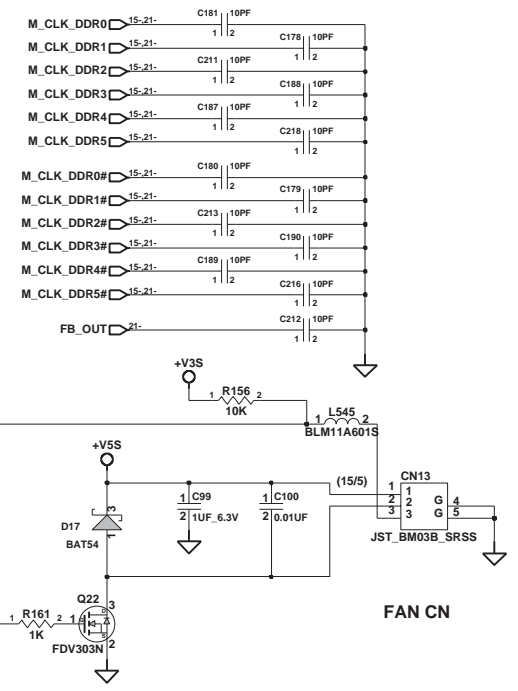
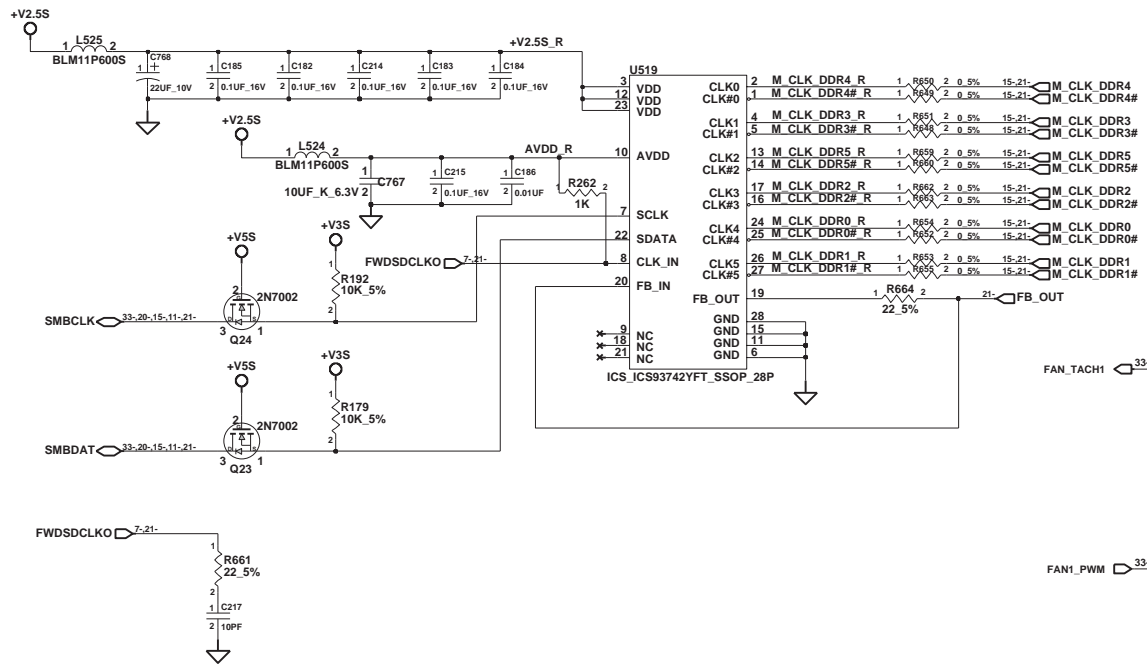
Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:41:26 pm	QA CHK	VER L2	Model Number PC8677	Sheet 19 of 45
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FS4	FS3	FS2	FS1	FS0	CPU	DRAM
0	0	0	1	1	100	133
1	0	1	1	0	133	133

Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 CLOCK GENERATOR Size A3
Drawn by Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

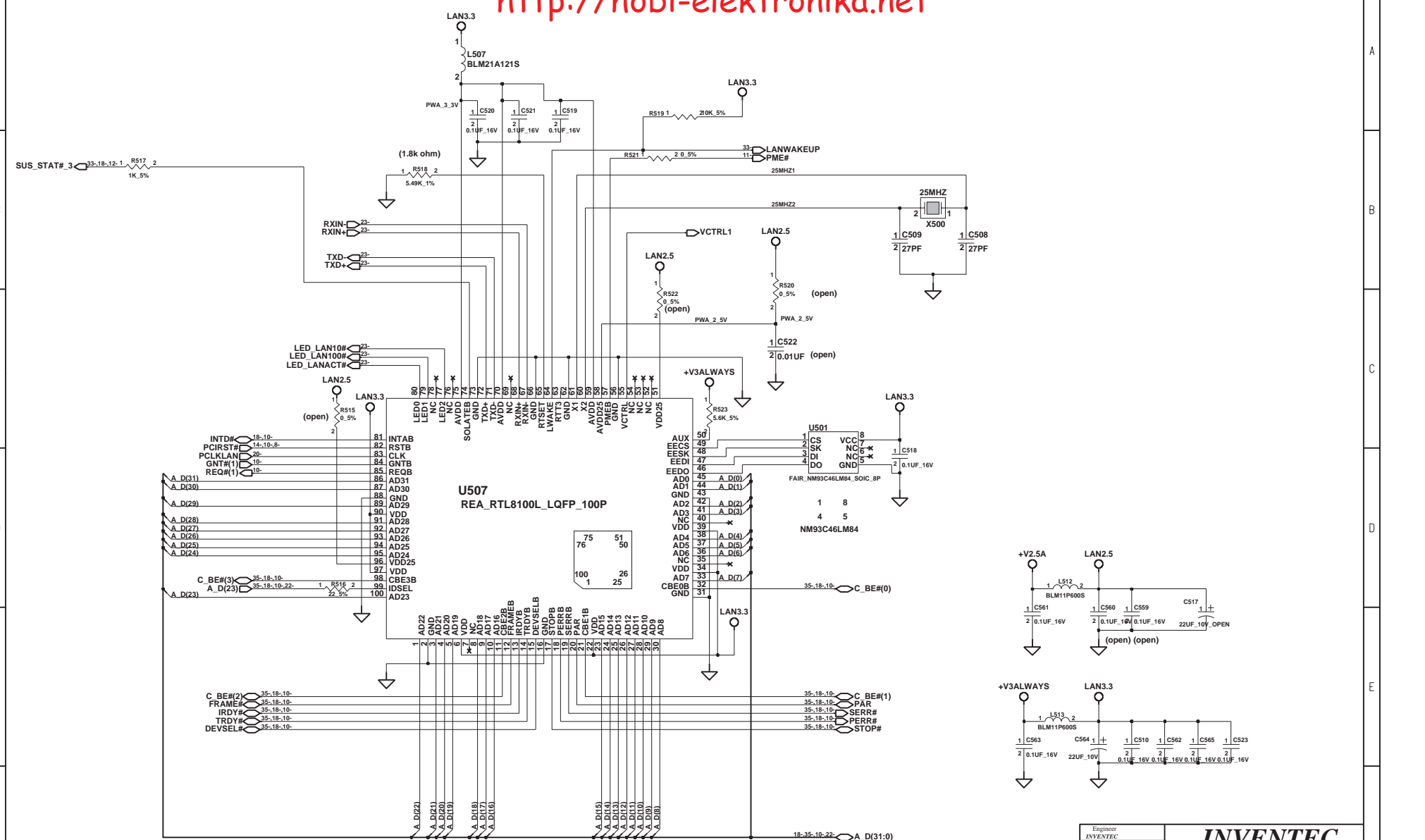
<http://hobi-elektronika.net>



Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 CLOCK BUFFER Size: A3
Drawn by Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

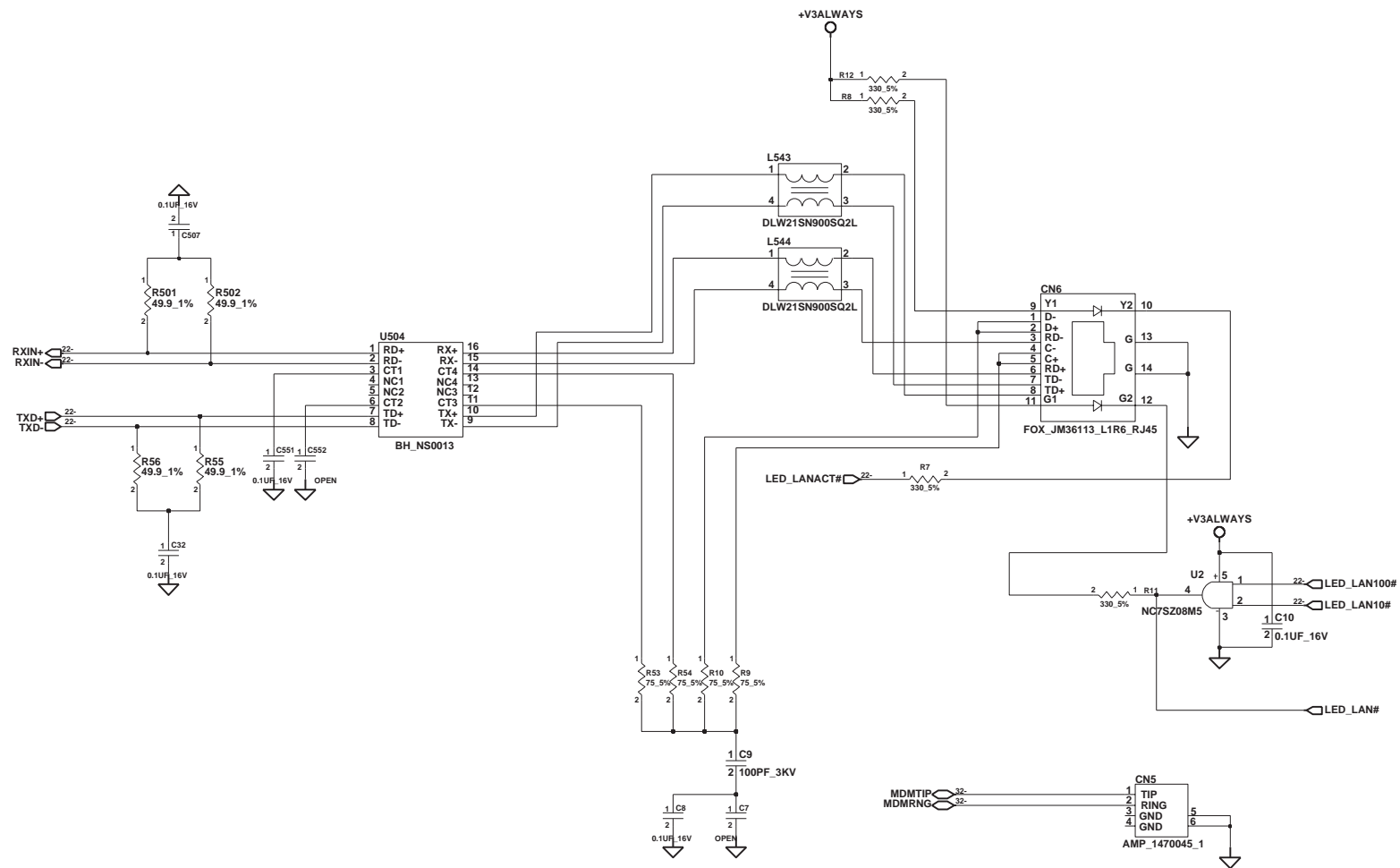
Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:41:36 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 21 of 45
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<http://hobi-elektronika.net>



Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 LAN CONTROLLER Size: A3
Drawn by Drawer Name	
R&D CHK BLM11P6008	
DOC CTRL CHK	
MFG ENGR CHK	

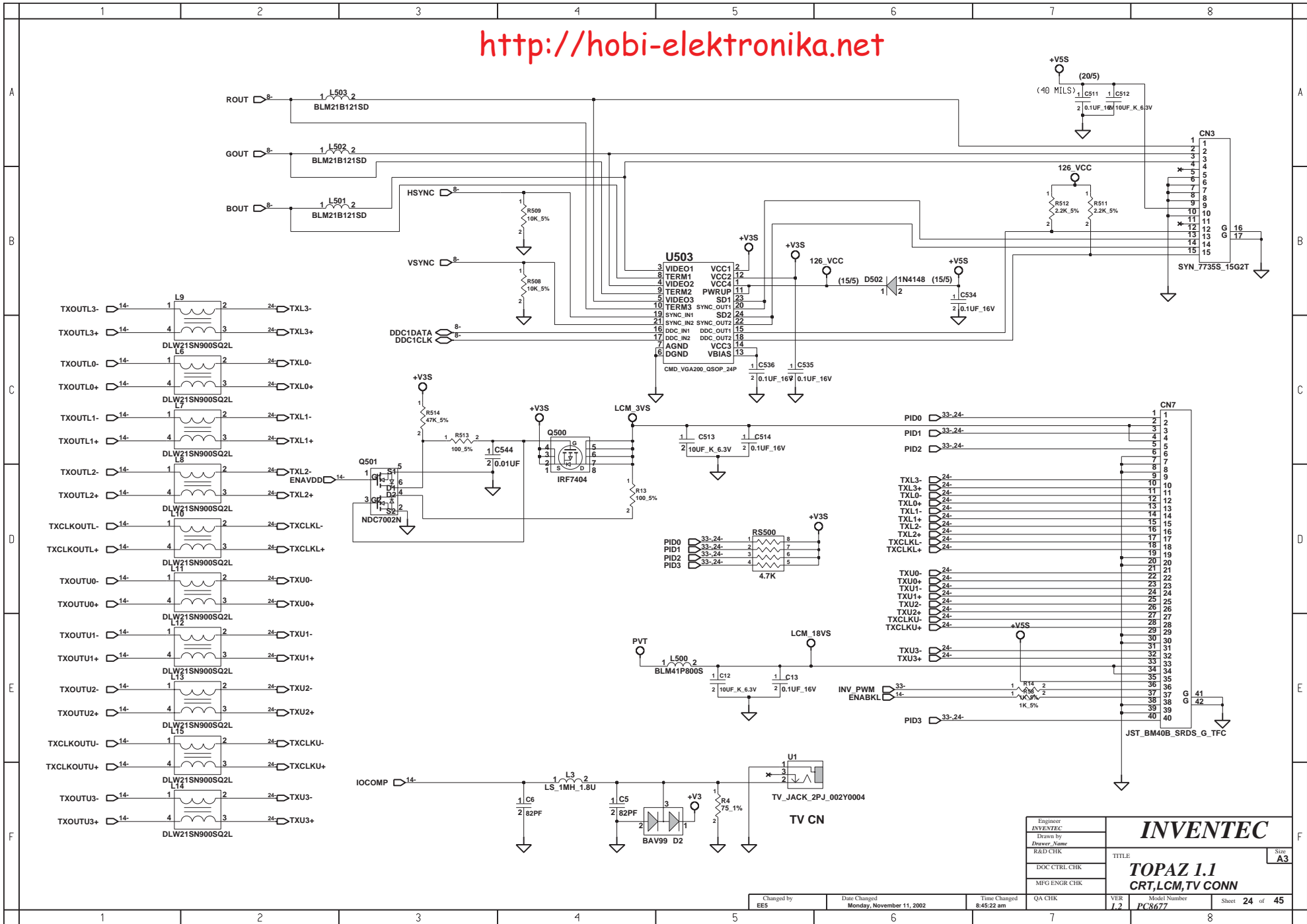
<http://hobi-elektronika.net>



Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 RJ11&RJ45 CONN	Size A3
Drawn by <i>Drawer Name</i>		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:41:43 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 23 of 45
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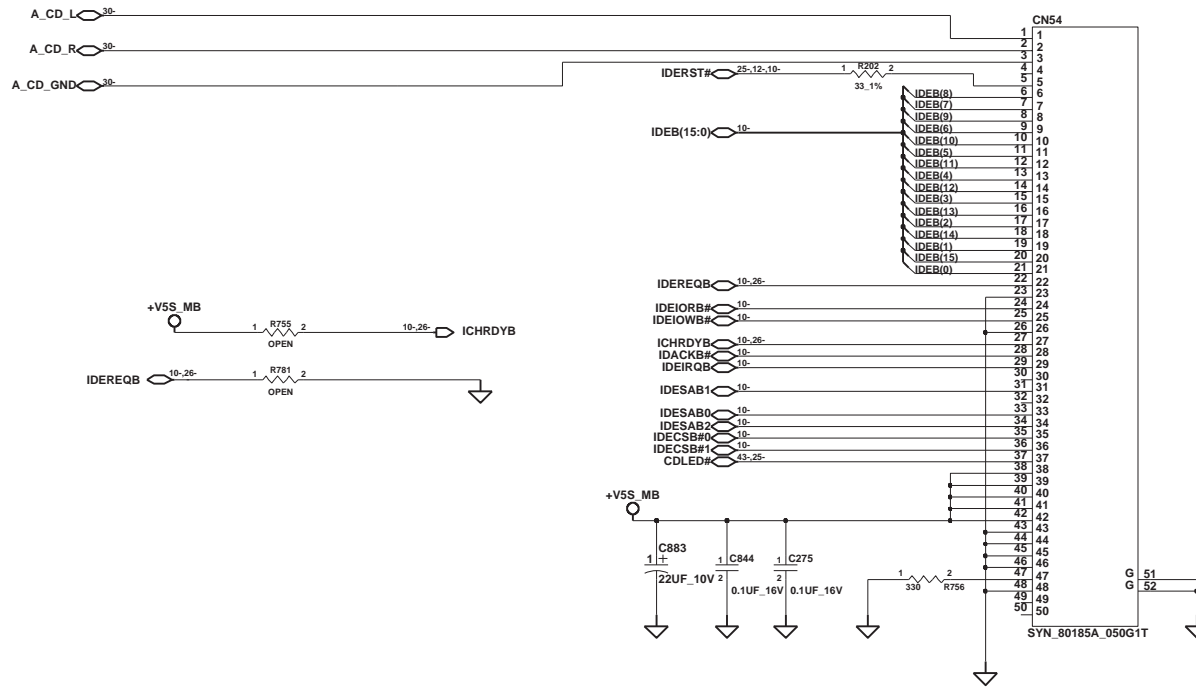
<http://hobi-elektronika.net>



Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 CRT, LCM, TV CONN Size: A3
Drawn by Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

Changed by EES	Date Changed Monday, November 11, 2002	Time Changed 8:45:22 am	QA CHK	VER 1.2	Model Number PC8677	Sheet 24 of 45
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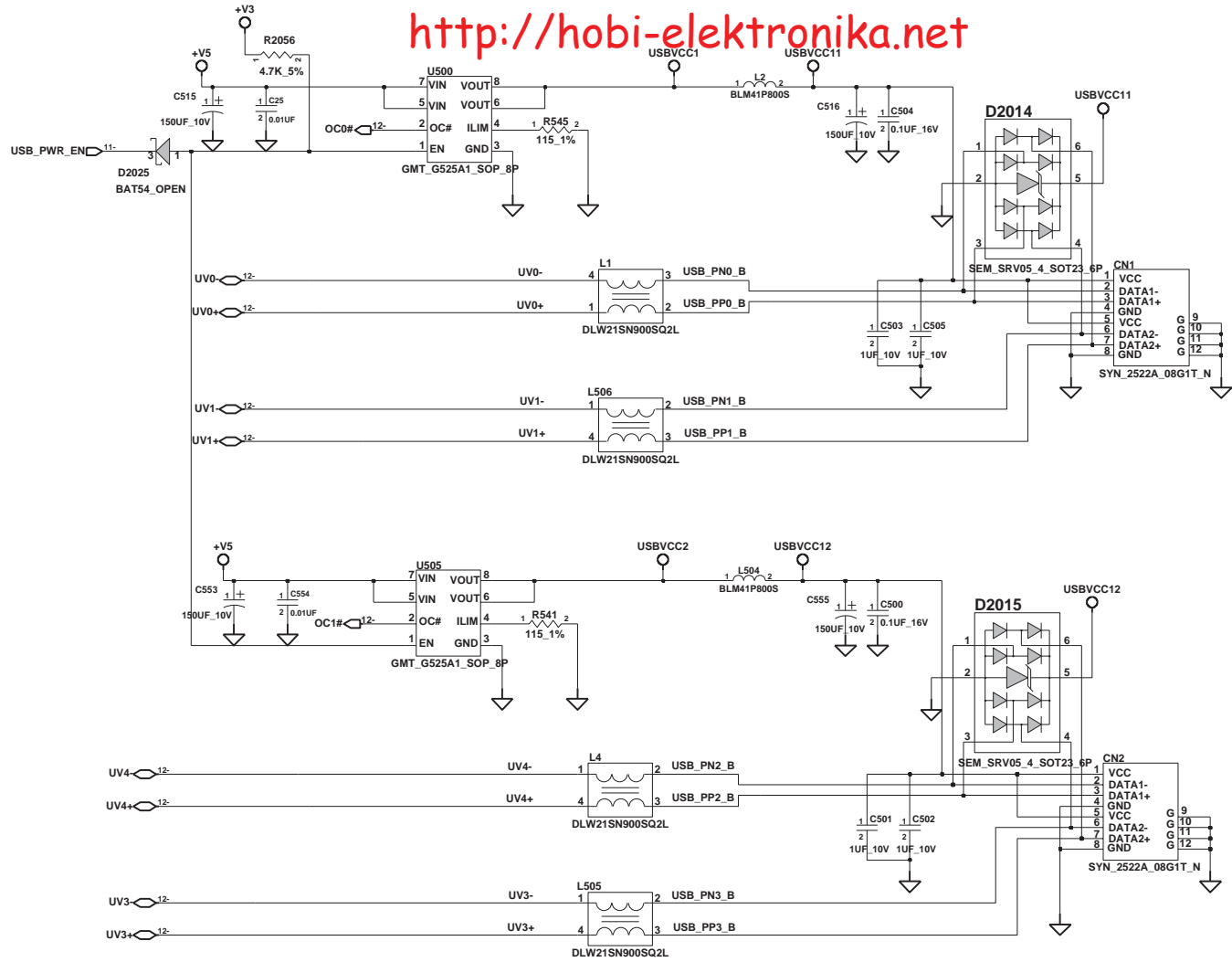
<http://hobi-elektronika.net>



Engineer INVENTEC	INVENTEC	
Drawn by <i>Drawer Name</i>		
R&D CHK		TITLE: TOPAZ 1.1
DOC CTRL CHK		Size A3
MFG ENGR CHK		SELECT BAY I/F

Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:41:53 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 26 of 45
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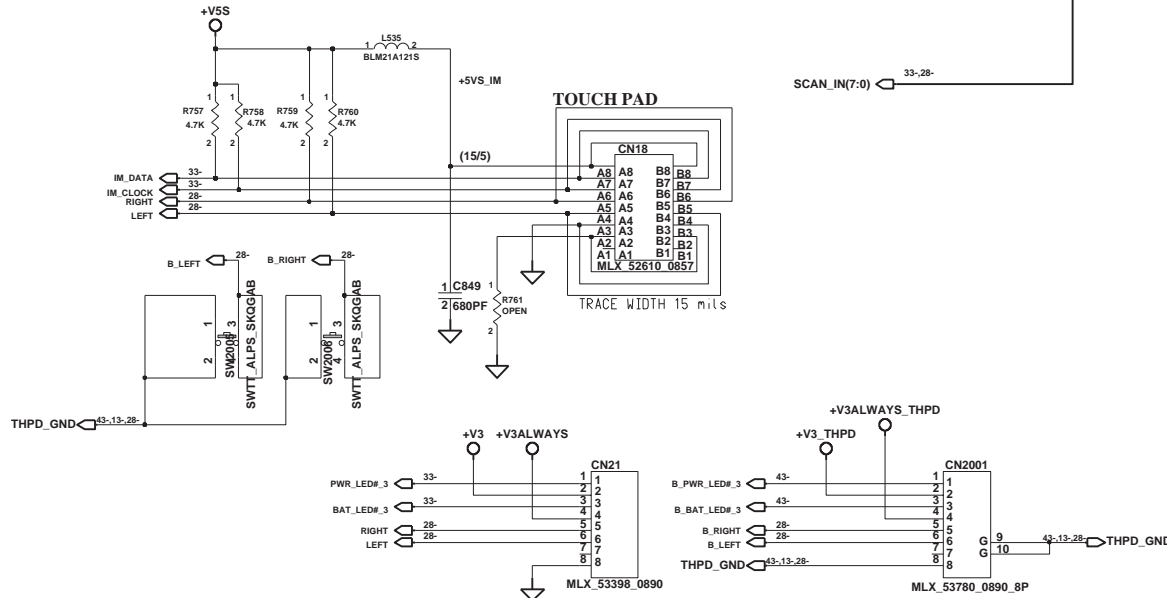
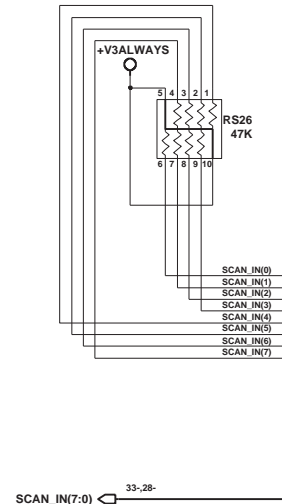
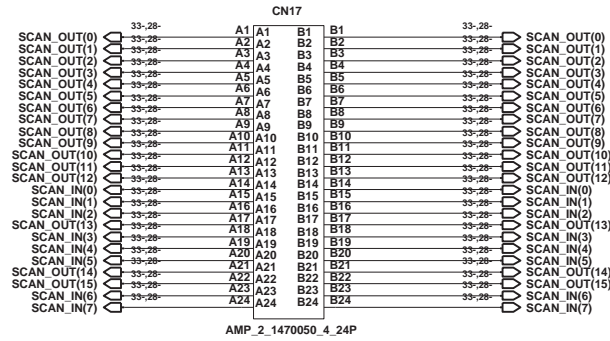
<http://hobi-elektronika.net>



Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 USB I/F Size: A3
Drawn by Drawer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:41:56 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 27 of 45
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<http://hobi-elektronika.net>

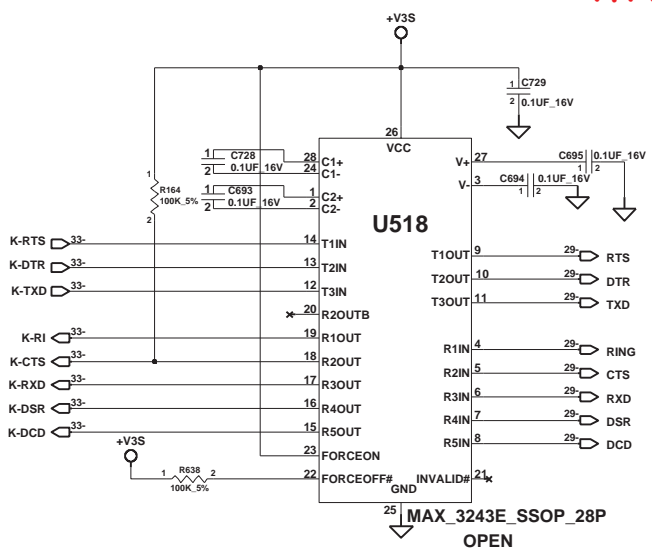


Engineer	INVENTEC
Drawn by	Drawer Name
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

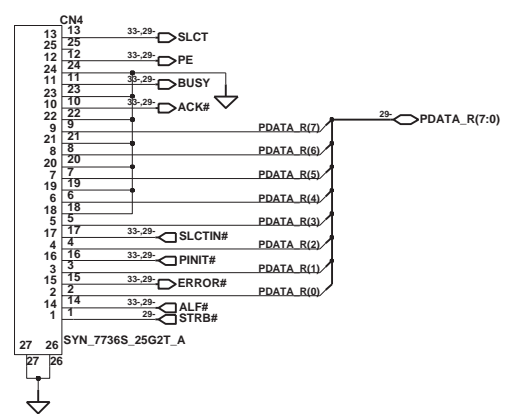
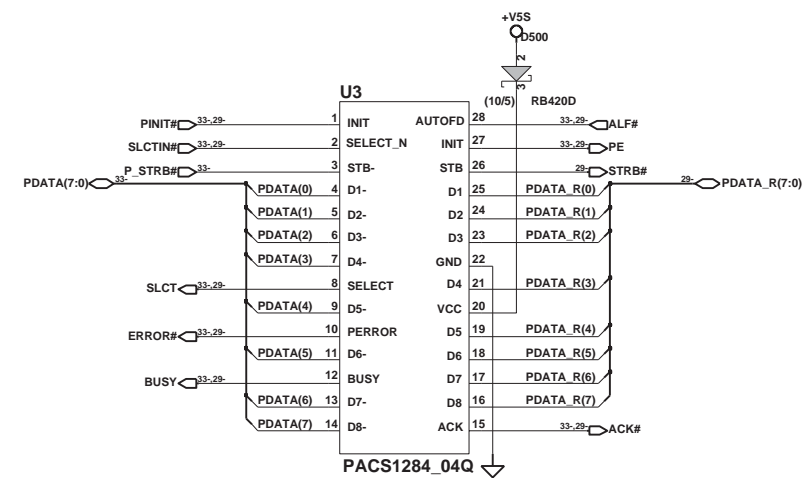
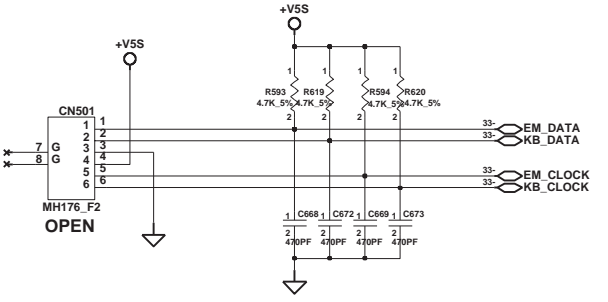
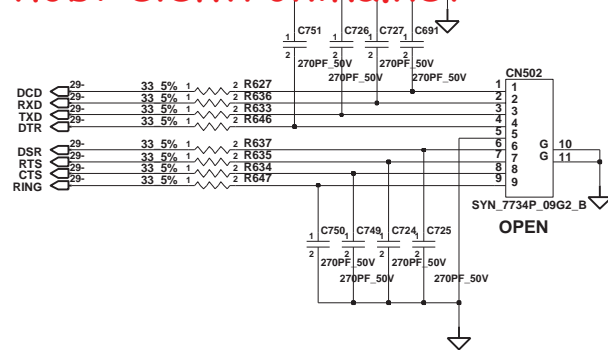
TITLE	TOPAZ 1.1	Size	A3
	KB I/F		

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
EES	Monday, October 28, 2002	2:42:00 pm		L2	PC8677	28 of 45

<http://hobi-elektronika.net>

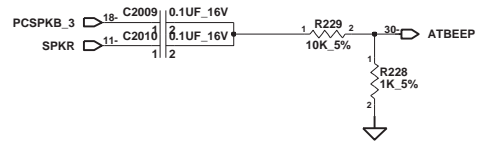
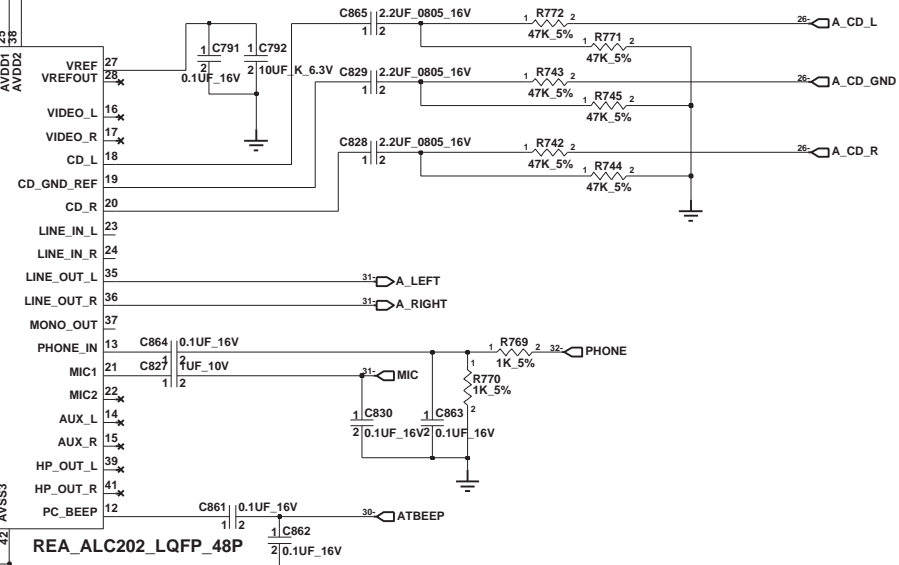
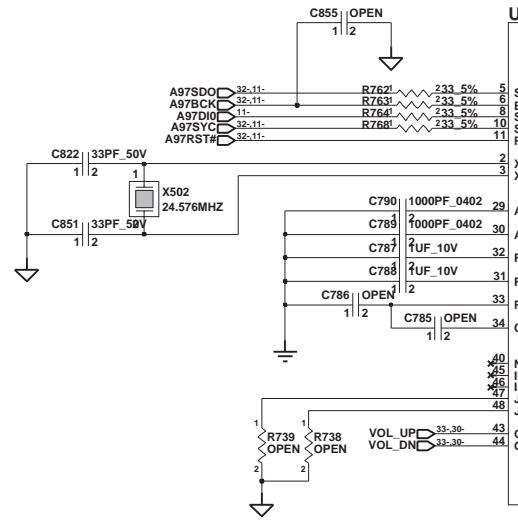
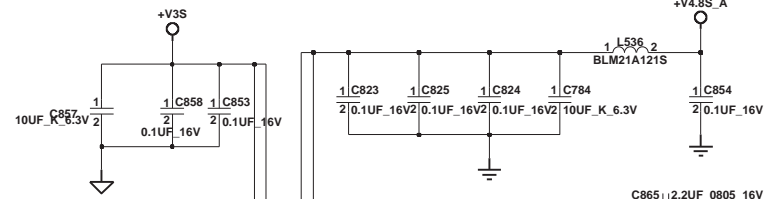
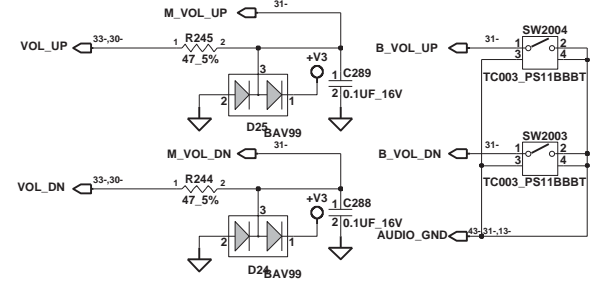
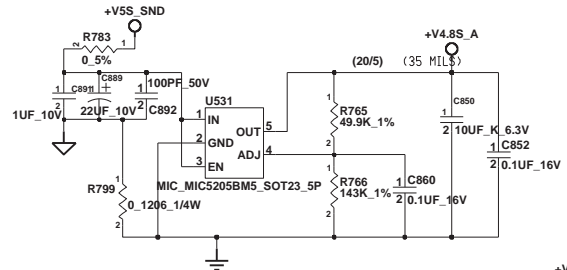


Reserved for BIOS DEBUG



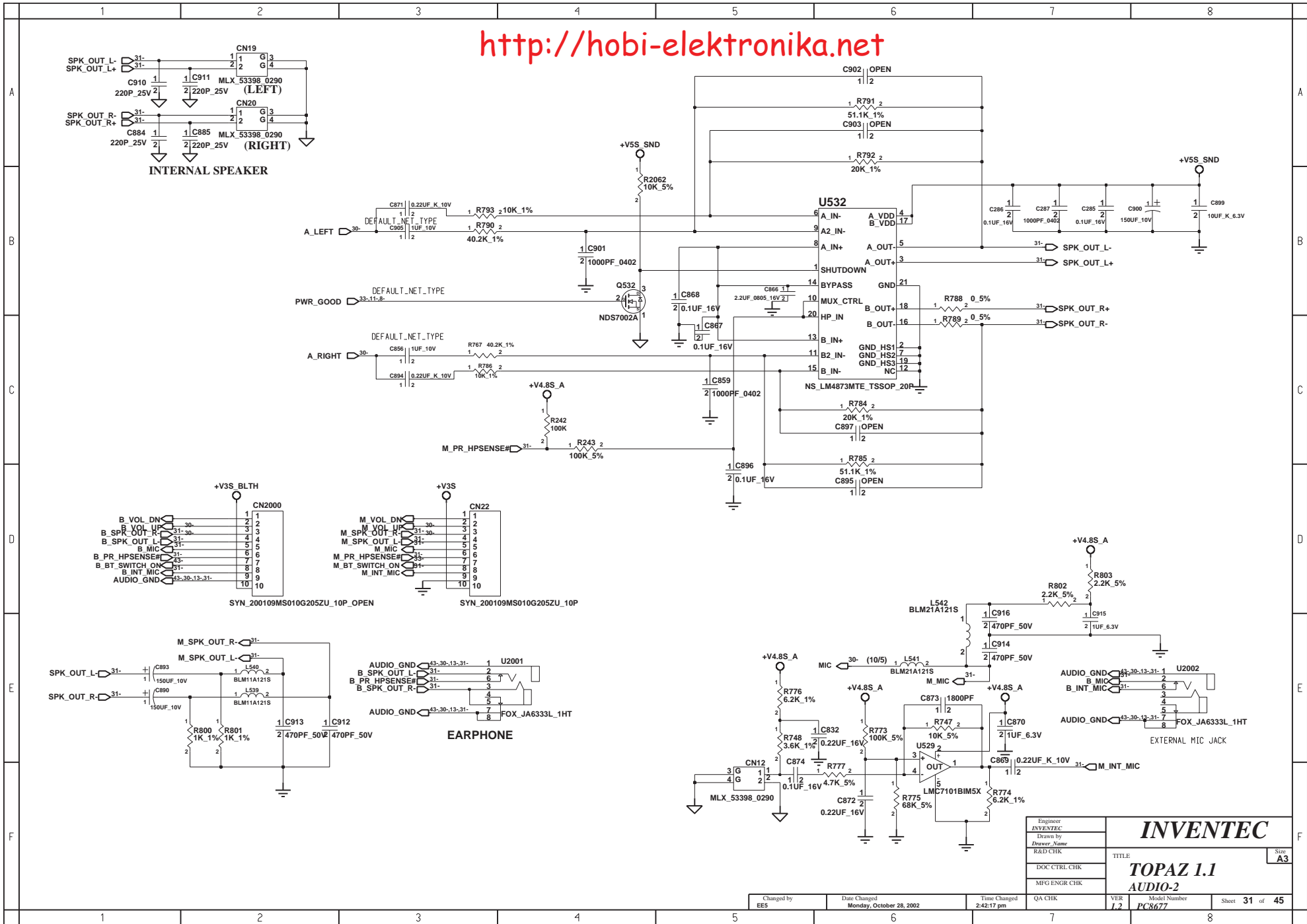
Engineer INVENTEC	INVENTEC		TITLE: TOPAZ 1.1	Size A3
Drawn by <i>Drawn Name</i>				
R&D CHK	SERIAL, PARALLEL, PS2 PORTS			
DOC CTRL CHK				
MFG ENGR CHK				
Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:42:04 pm	QA CHK	VER 1.2
			Model Number PC8677	Sheet 29 of 45

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Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 Sound I/F Model Number PC8677	Size A3
Drawn by Drawn Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

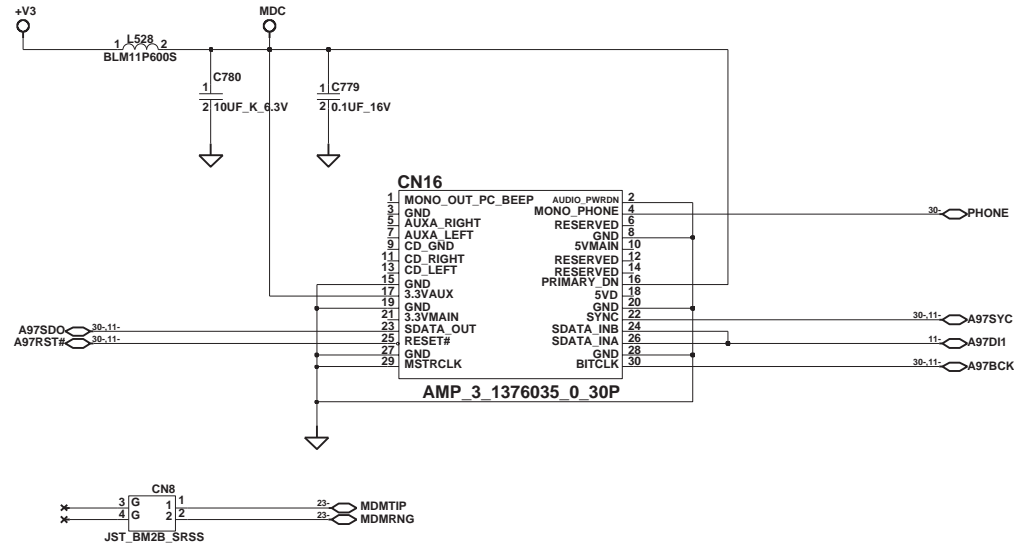
<http://hobi-elektronika.net>



Engineer INVENTEC	TITLE: TOPAZ 1.1 AUDIO-2	Size A3
Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:42:17 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 31 of 45
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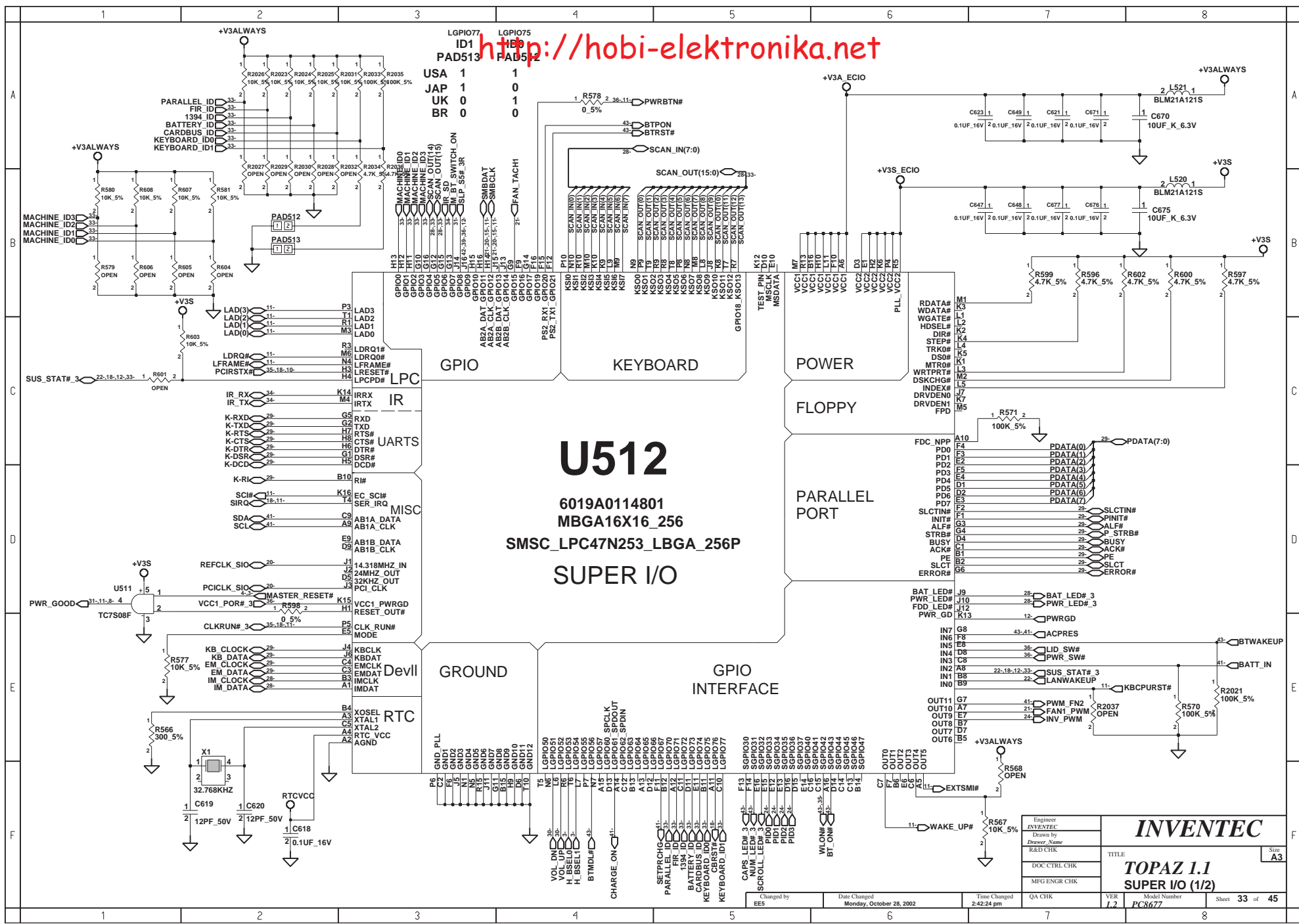
<http://hobi-elektronika.net>



Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 MDC PHONE AMP Size: A3
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:42:20 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 32 of 45
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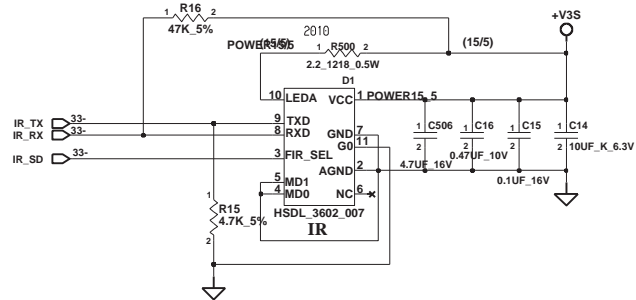
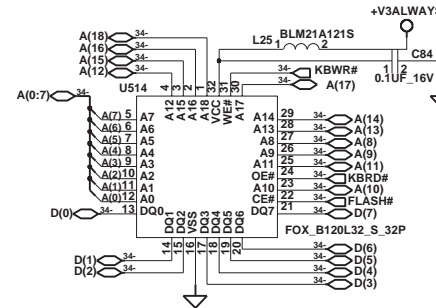
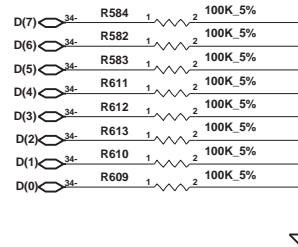
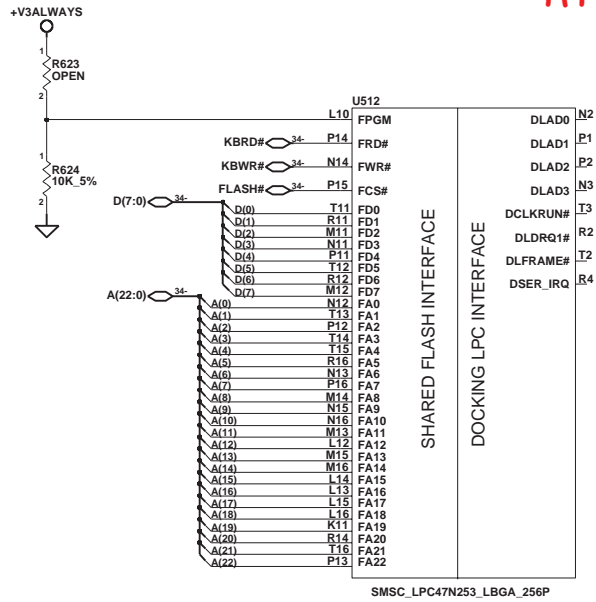
<http://hobi-elektronika.net>



U512
6019A0114801
MBGA16X16_256
SMSC_LPC47N253_LBGA_256P
SUPER I/O

Engineer INVENTEC	TITLE TOPAZ 1.1 SUPER I/O (1/2)	Size A3
Drawn by R&D CHK	Model Number PC8677	Sheet 33 of 45
DOC CTRL CHK	VER 1.2	
MFG ENGR CHK	QA CHK	
Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:42:24 pm

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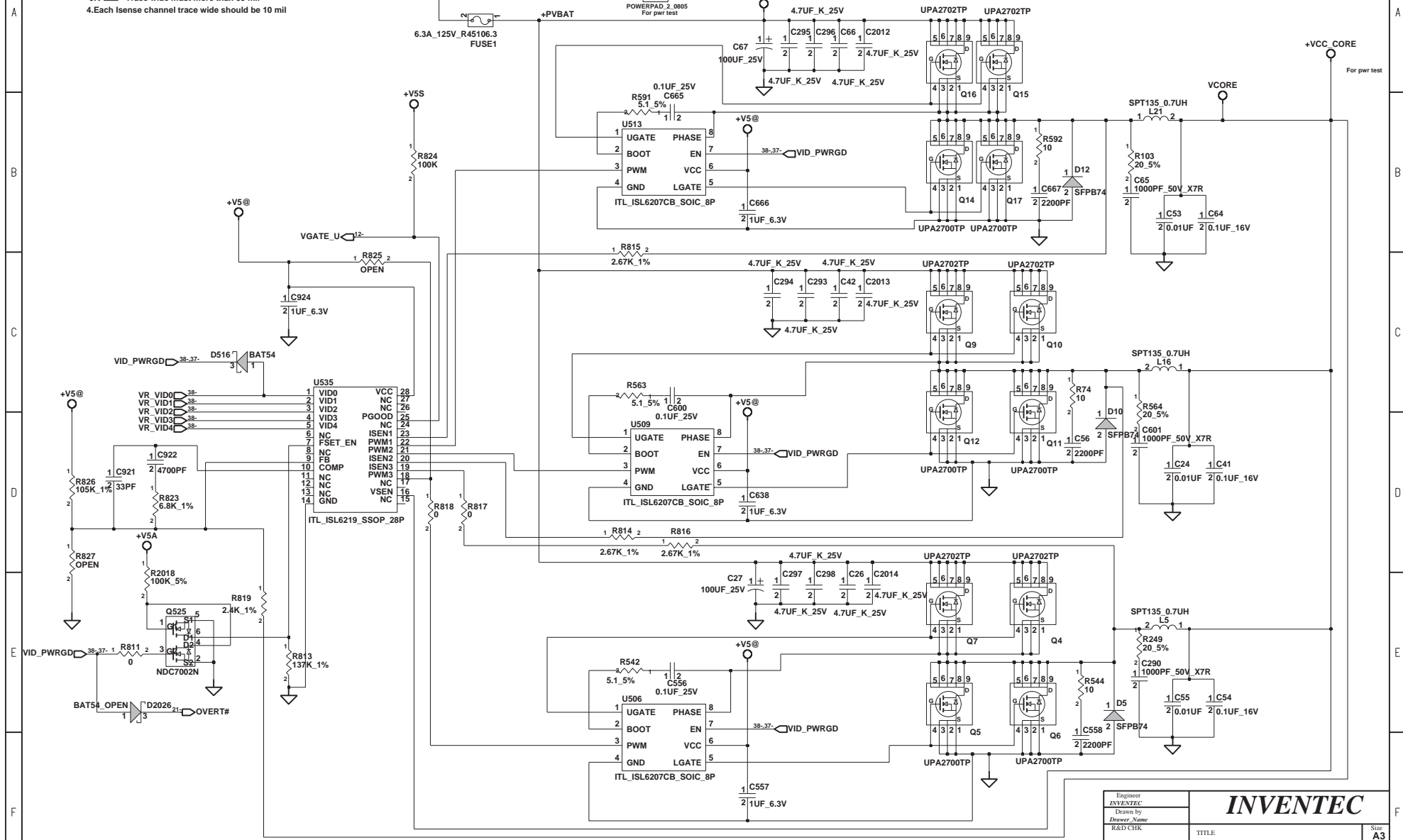


Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 SUPER I/O (2/2)	Size A3
Drawn by <i>Drawer Name</i>		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:42:28 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 34 of 45
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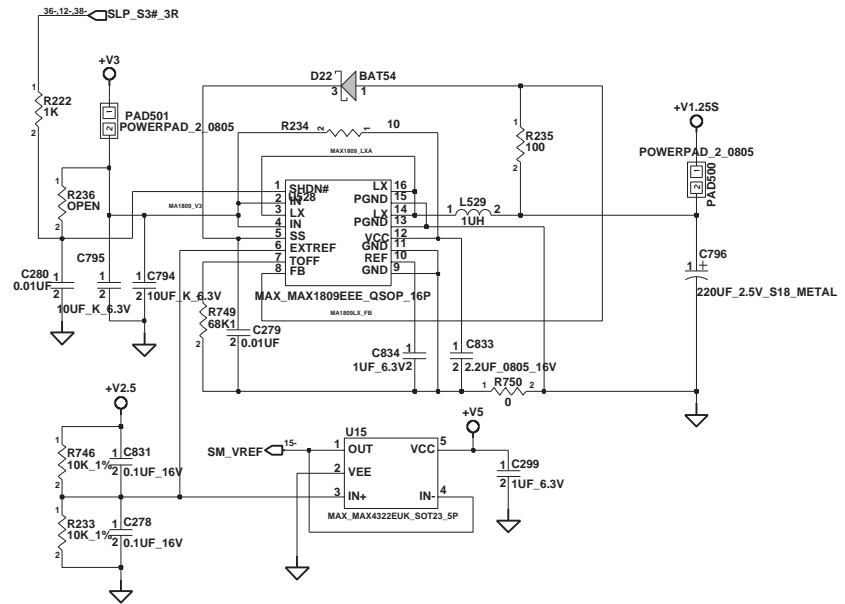
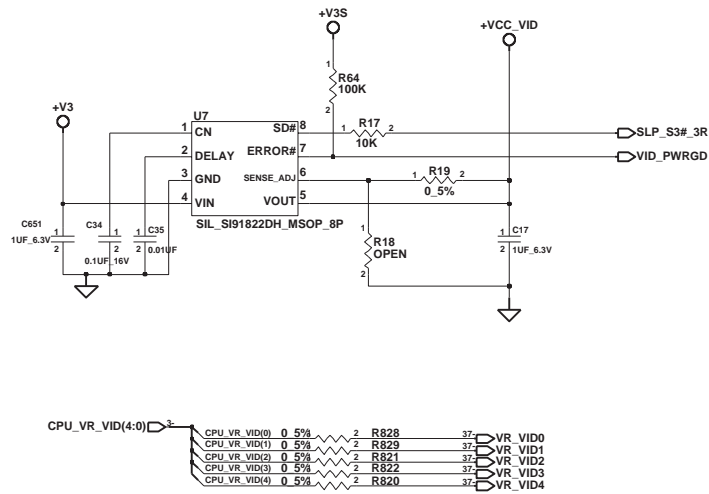
- 1.Enable three phase :Short R112 R119 , Open R111
- 2.Disable three phase :Short R111 , Open R112 R119
3. : Trace wide must more than 35 mil
- 4.Each Isense channel trace wide should be 10 mil



Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 CPU COREVCC Size: A3
Drawn by Drafter Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

Changed by EES	Date Changed Wednesday, November 13, 2002	Time Changed 4:56:37 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 37 of 45
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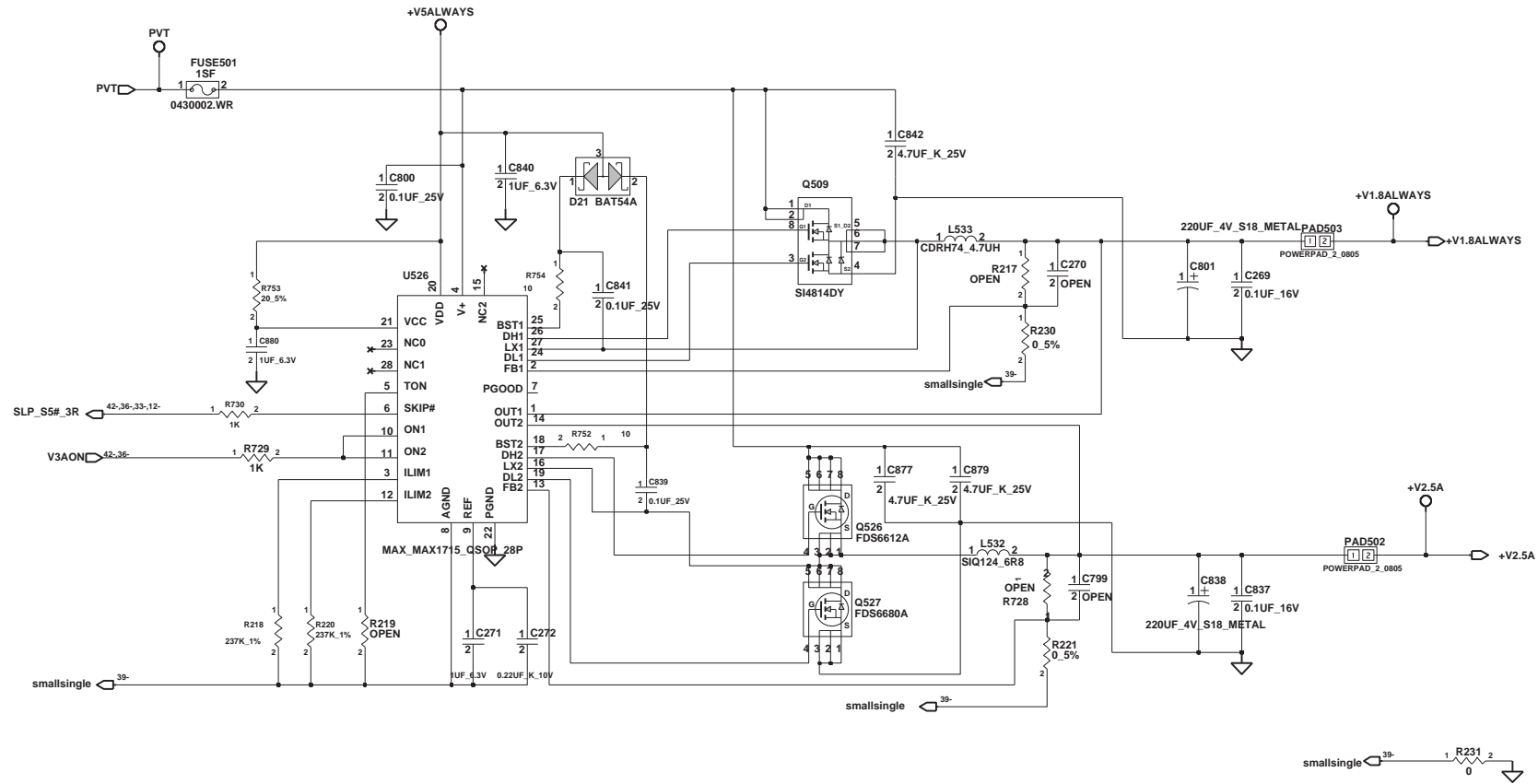
<http://hobi-elektronika.net>



Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 VIDVCC1.25V Size A3
Drawn by Designer Name	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:42:44 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 38 of 45
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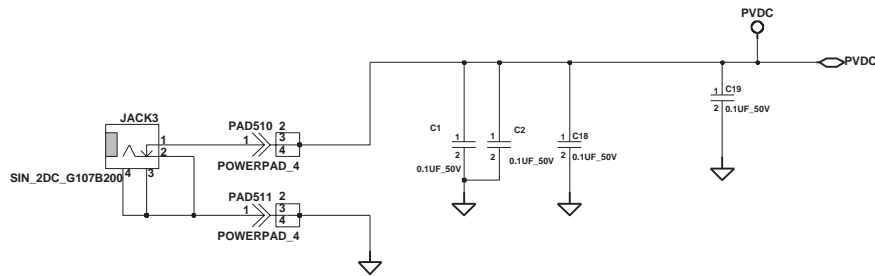
<http://hobi-elektronika.net>



Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 1.8V/2.5V	Size A3
Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:42:47 pm	QA CHK	VER 1.2	Model Number PC8677	Sheet 39 of 45
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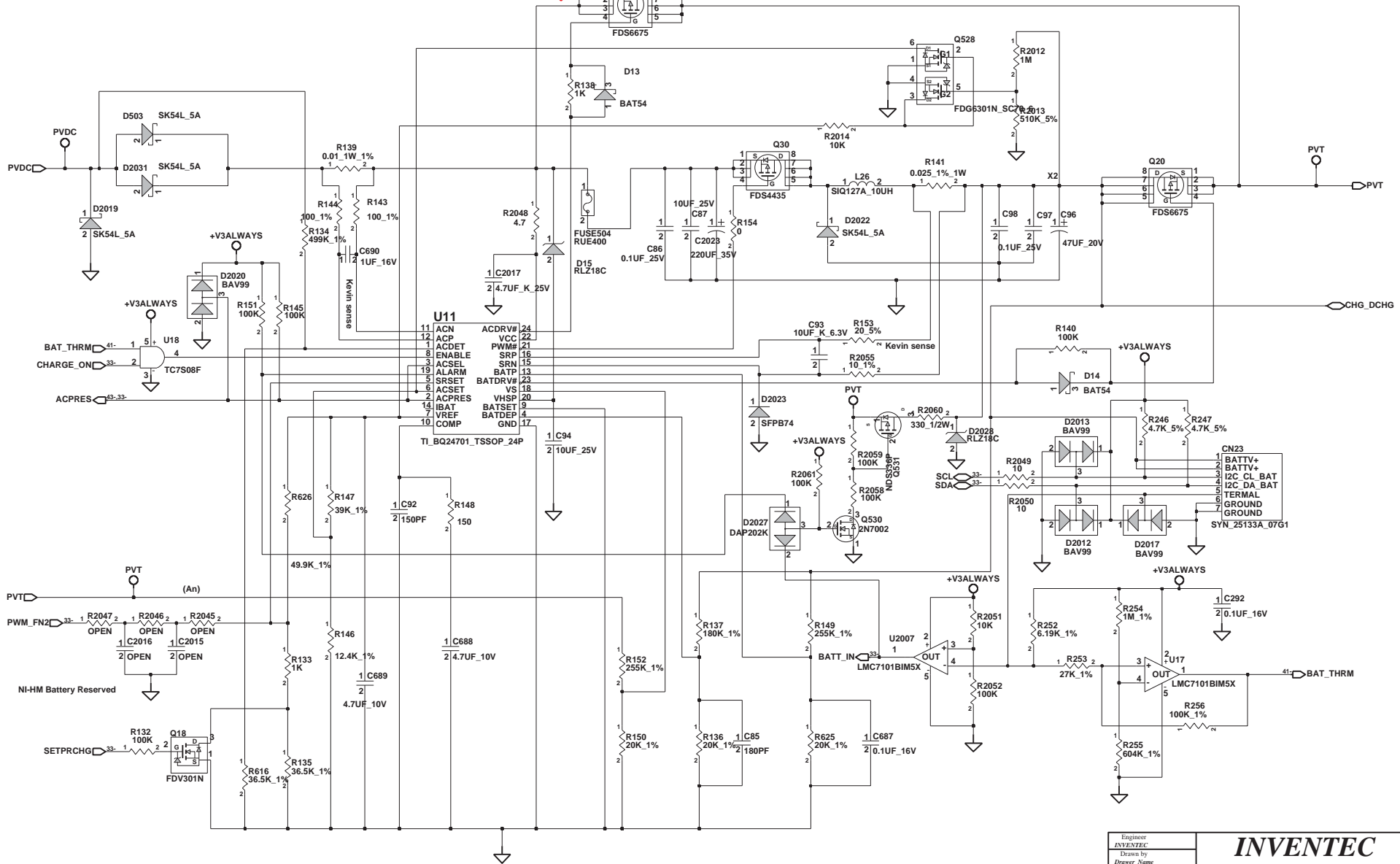
<http://hobi-elektronika.net>



Engineer INVENTEC	INVENTEC	
Drawn by <i>Drawer Name</i>		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		
VER 1.2	TITLE TOPAZ 1.1 AC ADAPTOR	Size A3
Model Number PC8677	Sheet 40 of 45	

Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:42:54 pm	QA CHK
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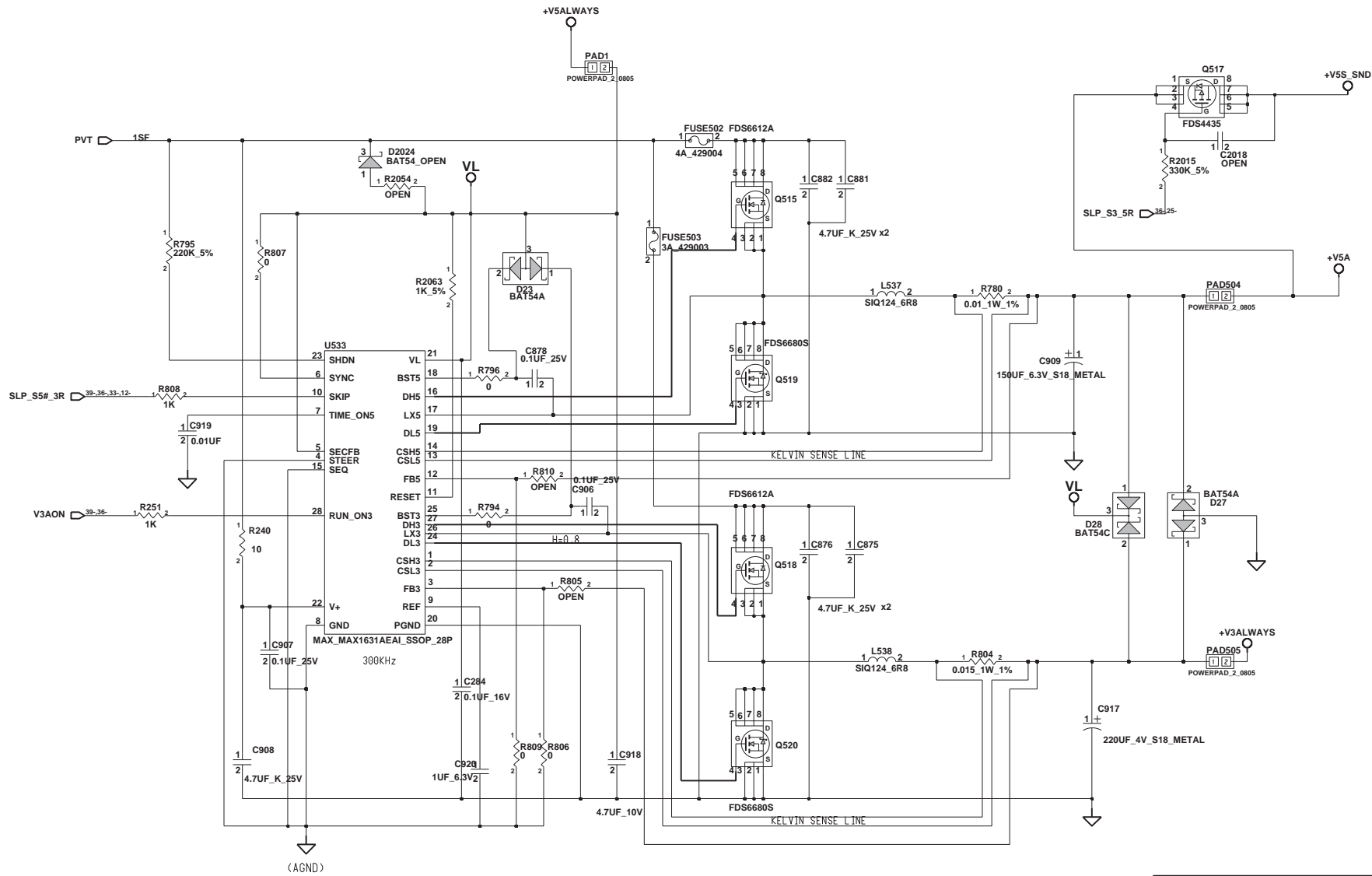
<http://hobi-elektronika.net>



Engineer INVENTEC	INVENTEC	TITLE: TOPAZ 1.1	Size A3
Drawn by Drewor Name			
R&D CHK			
DOC CTRL CHK			
MFG ENGR CHK			
Model Number PC8677	Sheet 41 of 45		

Changed by EES	Date Changed Wednesday, November 20, 2002	Time Changed 4:28:45 pm	QA CHK	VER 1.2
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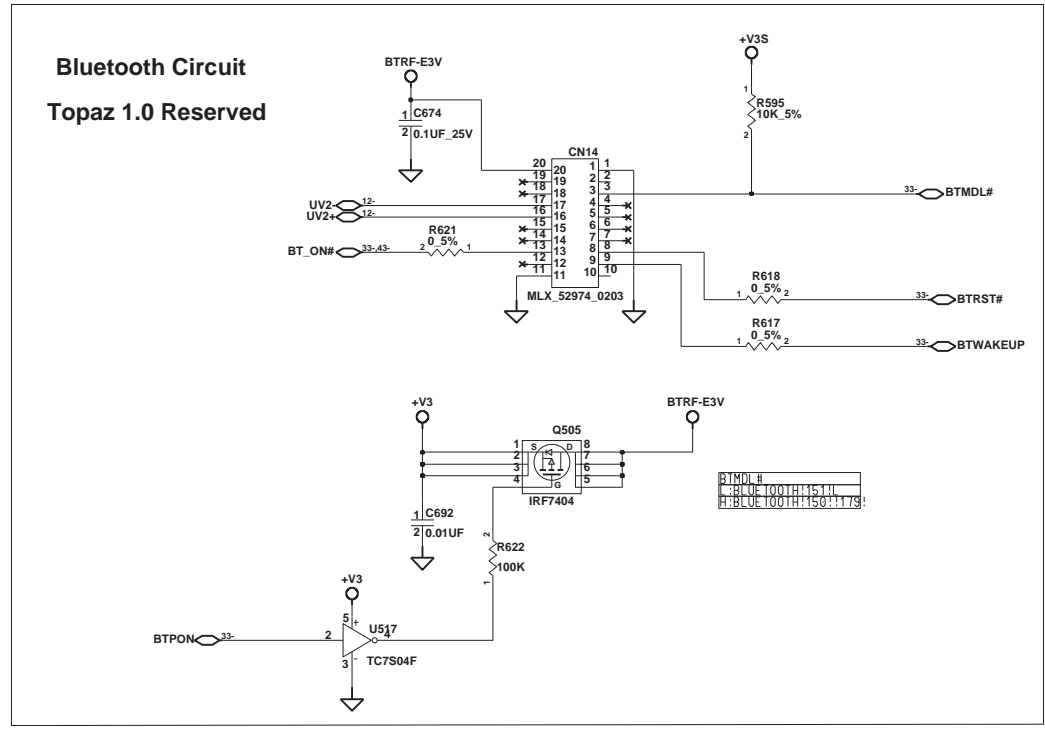
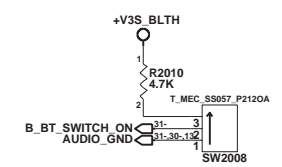
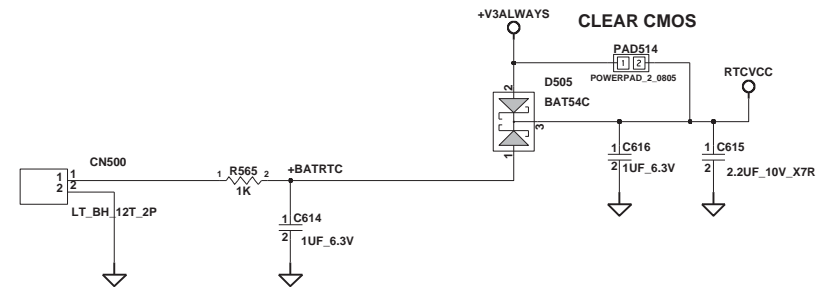
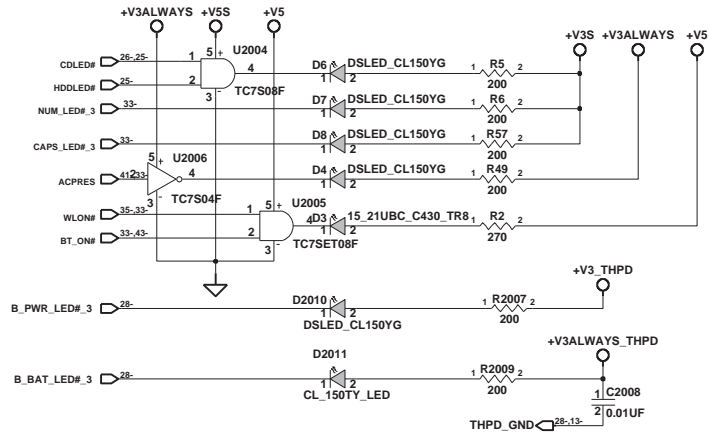
<http://hobi-elektronika.net>



Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 5V/3V Model Number PC8677	Size A3
Drawn by Drafter Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by EES	Date Changed Monday, October 28, 2002	Time Changed 2:43:02 pm	QA CHK	VER 1.2	Sheet 42 of 45
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Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 LED/BLUETOOTH Model Number PC8677	Size A3
Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

NOT-USED FUNCTION

<http://hobi-elektronika.net>

OPTIONAL FUNCTION

SERIAL

REMOVE
CN502
C691
C693
C694
C695
C724
C725
C726
C727
C728
C729
C749
C750
C751
R164
R627
R633
R634
R635
R636
R637
R638
R647
R646
U518

INT MIC

REMOVE
CN12
C832
C869
C870
C872
C873
C874
R747
R748
R773
R774
R775
R776
R777
U529

BLTH

REMOVE
C674
C692
CN14
Q505
R595
R617
R618
R621
R622
R2010
SW2008
U517

PS2

REMOVE
CN501
C668
C669
C672
C673
R593
R594
R619
R620

IEEE 1394

NOT-USED	USED
REMOVE	
U19	RS27
U20	RS28
CN25	R856
CN2003	R857
CN2005	R858
L40	R859
X4	R265
C301	R2030
C302*	R835*
C934	C302*
C927	
C928	
R834	
R835*	
R836	
R837	
R838	
R2057	
R854	
R855	
R845	
R846	
R847	
R848	
R849	
R850	
R851	
R852	
R853	
R839	
R264	
R840	
R841	
R842	
R263	
R844	
C930	
C929	
C926	
C931	
C932	
C933	
C935	
R2024	

CARDBUS

NOT-USED	USED
REMOVE	
U502	R2032
U508	R2019*
CN10	C525*
R548	R525*
R2019*	R529*
R2020	
RS524	
C526	
C625	
C626	
C566	
C567	
C568	
C524	
C525*	
C527	
C529	
C530	
C571	
C532	
C570	
R504	
C533	
C569	
R532	
R506	
R2031	
R525*	
R529*	

FIR

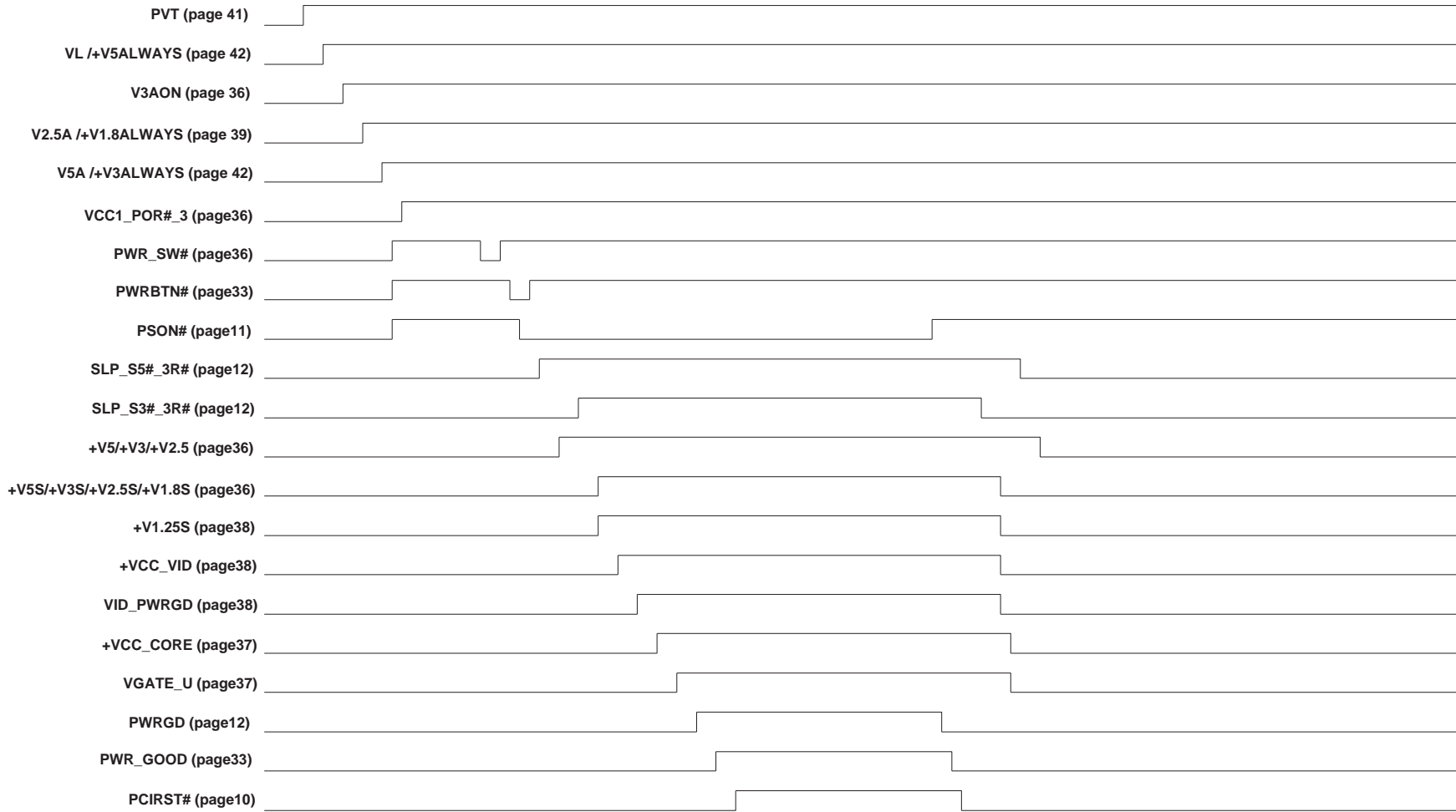
NOT-USED	USED
REMOVE	
D1	R2029
R500	
R15	
C14	
C15	
C16	
C506	
R2023	

MINIPCI

NOT-USED	USED
REMOVE	
Q502	R198*
R198*	R573*
R199	R585*
R562	
R572	
R573*	
R531	
R585*	
R2	
D3	
L508	
C36	
C45	
C630	
C650	
C663	
C664	
CN11	
U2005	

Engineer INVENTEC	<h1>INVENTEC</h1> <p>TITLE: TOPAZI.1</p> <p>OPTIONAL PARTS LIST</p>	Size A3
Drawn by Drawer Name		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

<http://hobi-elektronika.net>



Engineer INVENTEC	INVENTEC TITLE: TOPAZ 1.1 Power Sequential Model Number: PC8677 Sheet 45 of 45
Drawn by <i>Drawer Name</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	Size A3

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