

COMPAL CONFIDENTIAL

MODEL NAME : QXW00
PCB NO : LA-7901P (DA60000PM00)
BOM P/N : 4619F631L01 / L02
GPIO MAP: E4_VC_GPIO_map_rev_1.1

Korbel 14 UMA--Non vPRO Ivy/Sandy Bridge + Panther POINT(HM77w/DASH)

2012-03-03

REV : 1.0 (A00)

@ : Nopop Component
CONN@ : Connector Component


MB Type	BOM P/N	
TPM	43*	1@ 3@ 5@
TCM		2@ 4@ 5@
TPM DIS		2@ 3@
HM77 w/o Vpro		
QM77 w/ Vpro		
PCH XDP		PXDP@
HDMI LOGO		46@

MB PCB	
Part Number	Description
DA60000PM00	PCB OLH LA-7901P REV0 M/S UMA

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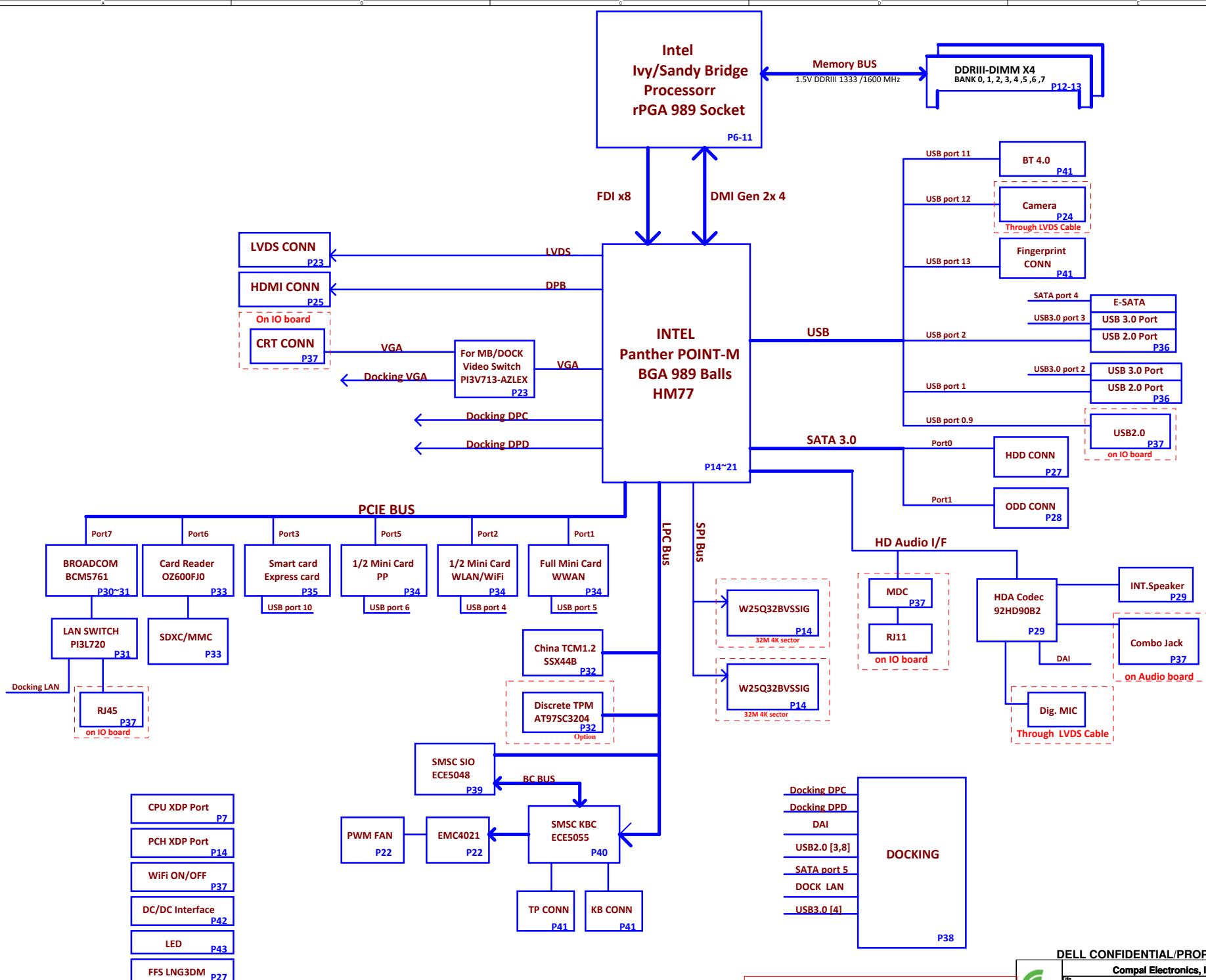
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 Title: **Cover Sheet**

Size: Document Number: **LA-7901P** Rev: 1.0

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POWER STATES

Signal State	SLP S3#	SLP S4#	SLP S5#	SLP A#	ALWAYS PLANE	M PLANE	SUS PLANE	RUN PLANE	CLOCKS
S0 (Full ON) / M0	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON	ON
S3 (Suspend to RAM) / M3	LOW	HIGH	HIGH	HIGH	ON	ON	ON	OFF	OFF
S4 (Suspend to DISK) / M3	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF	OFF
S5 (SOFT OFF) / M3	LOW	LOW	LOW	HIGH	ON	ON	OFF	OFF	OFF
S3 (Suspend to RAM) / M-OFF	LOW	HIGH	HIGH	LOW	ON	OFF	ON	OFF	OFF
S4 (Suspend to DISK) / M-OFF	LOW	LOW	HIGH	LOW	ON	OFF	OFF	OFF	OFF
S5 (SOFT OFF) / M-OFF	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF	OFF

PM TABLE

power plane State	+15V_ALW +5V_ALW +3.3V_ALW_PCH +3.3V_RTC_LDO	+3.3V_SUS +1.5V_MEM	+5V_RUN +3.3V_RUN +1.8V_RUN +1.5V_RUN +0.75V_DDR_VTT +VCC_CORE +1.05V_RUN_VTT +1.05V_RUN	+3.3V_M +1.05V_M	+3.3V_M +1.05V_M (M-OFF)
S0	ON	ON	ON	ON	ON
S3	ON	ON	OFF	ON	OFF
S5 S4/AC	ON	OFF	OFF	ON	OFF
S5 S4/AC don't exist	OFF	OFF	OFF	OFF	OFF

need to update Power Status and PM Table

USB 3.0 PORT#	Connetion
1	NA
2	JUSB1 (Left side)
3	JUSB2 (Left side)
4	DOCKING

PCH	USB PORT#	DESTINATION
	0	JUSB (Right side-IO/B)
	1	JUSB (Left side)
	2	JESA1 (Leftt side ESATA)
	3	MLK DOCK
	4	WLAN
	5	WWAN
	*1 6	JMINI3(Flash)-for w/ Vpro
	*1 7	DOCKING
	8	NA
	9	JUSB (Right side-Audio/B)
	10	Express card/Smart Card
	11	Bluetooth
	12	Camera
13	BIO	


*1: HM76 don't support port 6,7

SATA	DESTINATION
SATA 0	HDD
SATA 1	ODD/ E3 Module Bay
SATA 2	NA
SATA 3	NA
SATA 4	ESATA
SATA 5	Dock

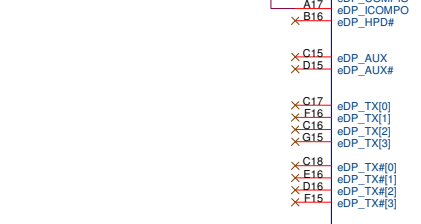
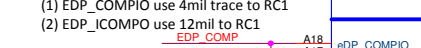
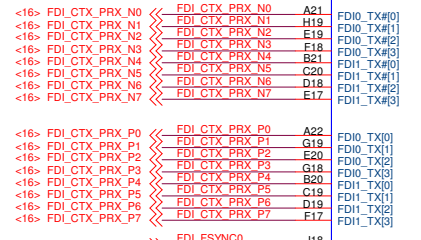
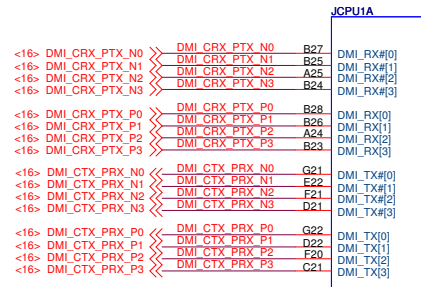
UMA DP/HDMI Port	Connetion
Port B	MB HDMI Conn
Port C	Dock DP port 2
Port D	Dock DP port 1

PCI EXPRESS	DESTINATION
Lane 1	MINI CARD-1 WWAN
Lane 2	MINI CARD-2 WLAN
Lane 3	Express card
Lane 4	None
Lane 5	1/2vMINI CARD-3 PCIE
Lane 6	MMI
Lane 7	10/100/1G LOM
Lane 8	None

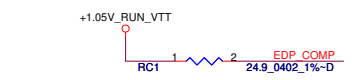
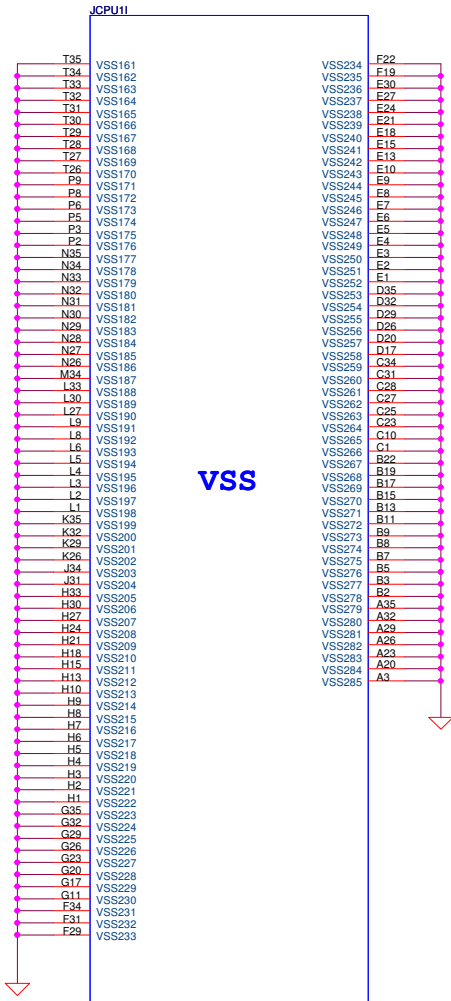
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Title		Index and Config.	
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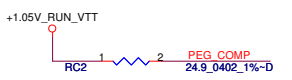
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(1) PEG_RCOMPO (H22) use 4mil connect to PEG_ICOMPI, then use 4mil connect to RC2.
 (2) PEG_ICOMPO use 12mil connect to RC2



DP Compensation
 eDP_COMPIO and ICOMPO signals should be shorted near balls and routed with typical impedance <25 mohms



PEG Compensation
 PEG_ICOMPI and RCOMPO signals should be shorted and routed with - max length = 500 mils - typical impedance = 43 mohms
 PEG_ICOMPO signals should be routed with - max length = 500 mils - typical impedance = 14.5 mohms

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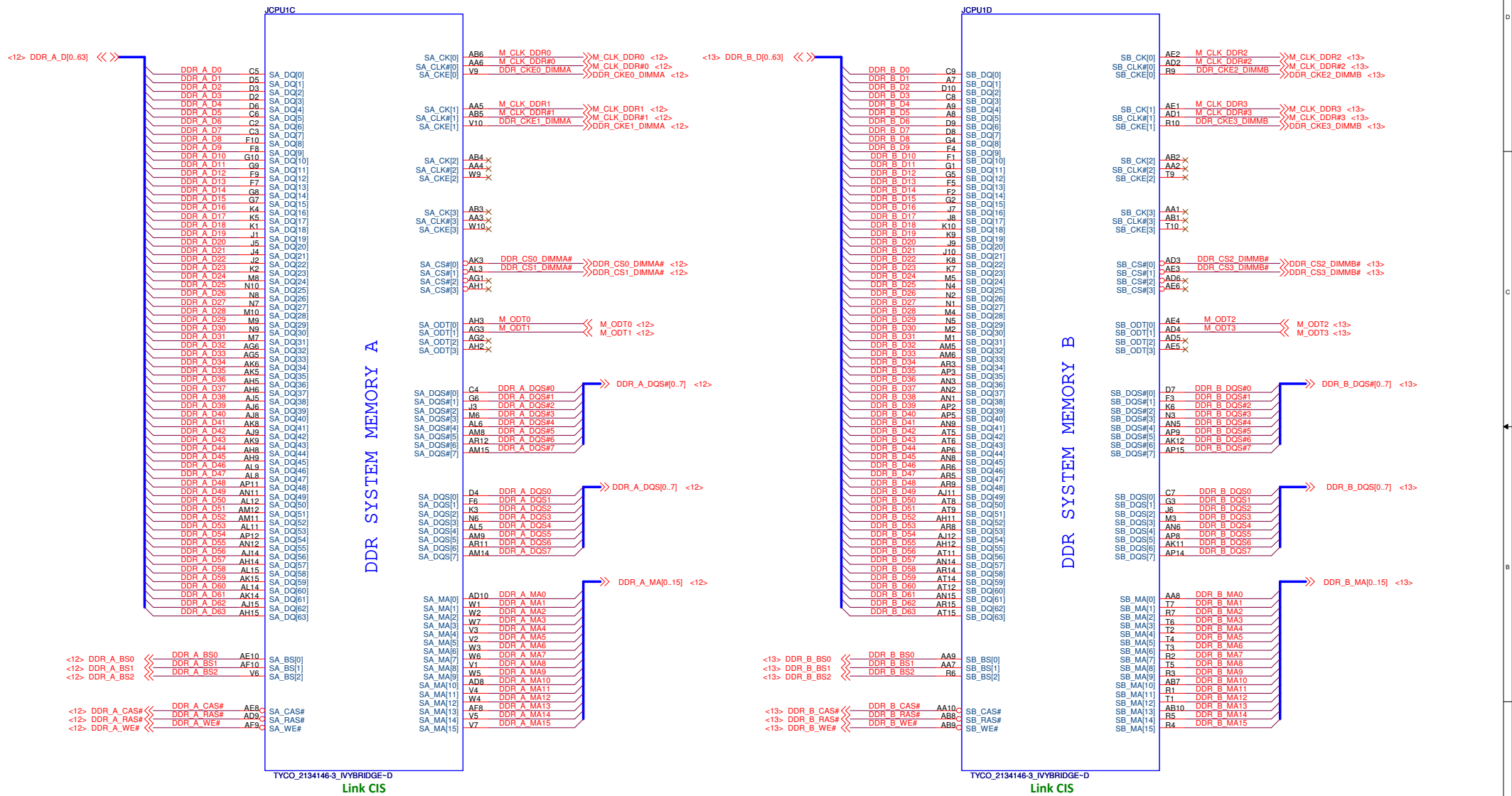
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Ivy/Sandt Bridge (1/6)

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Ivy/Sandy Bridge (3/6)

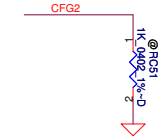
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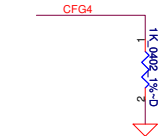
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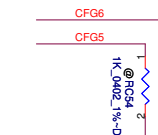
CFG Straps for Processor



PEG Static Lane Reversal - CFG2 is for the 16x	
CFG2	1:(Default) Normal Operation; Lane # definition matches socket pin map definition 0: Lane Reversed



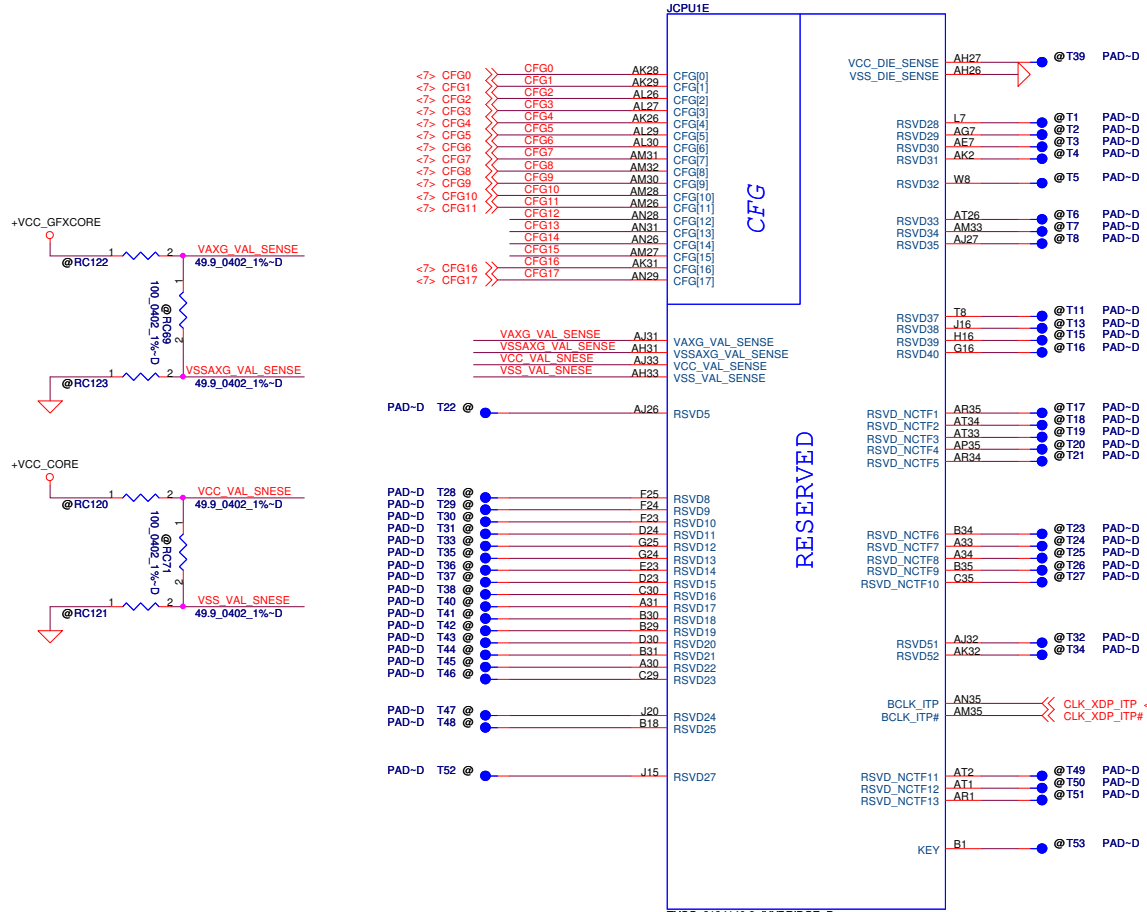
Display Port Presence Strap	
CFG4	1 : Disabled; No Physical Display Port attached to Embedded Display Port 0 : Enabled; An external Display Port device is connected to the Embedded Display Port



PCIe Port Bifurcation Straps	
CFG[6:5]	11: (Default) x16 - Device 1 functions 1 and 2 disabled 10: x8, x8 - Device 1 function 1 enabled ; function 2 disabled 01: Reserved - (Device 1 function 1 disabled ; function 2 enabled) 00: x8,x4,x4 - Device 1 functions 1 and 2 enabled



PEG DEFER TRAINING	
CFG7	1: (Default) PEG Train immediately following xxRESETB de assertion 0: PEG Wait for BIOS for training



CFG

RESERVED

TYCO_2134146-3_IVYBRIDGE-D
Link CIS

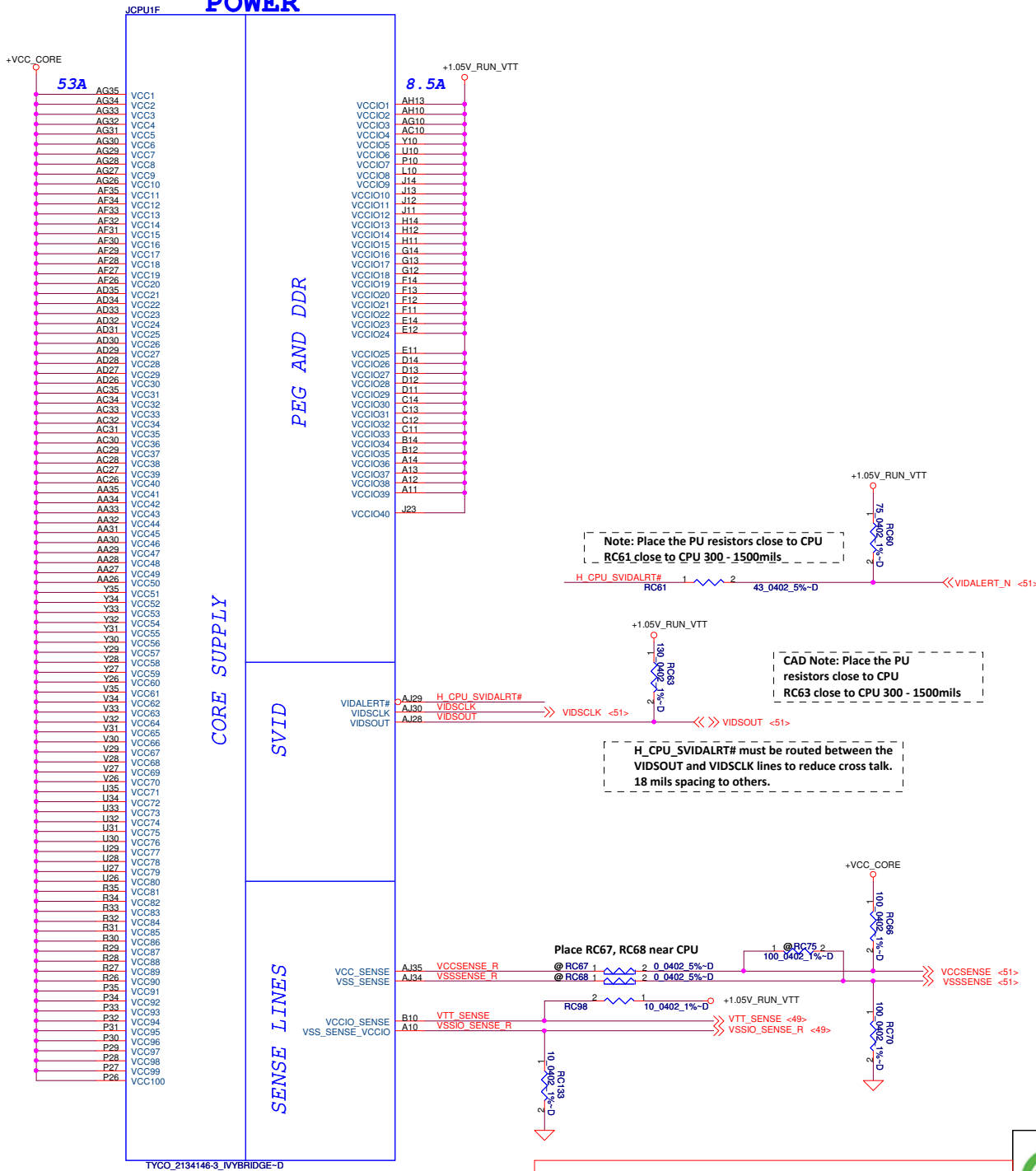
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Title Ivy/Sandy Bridge (4/6)			
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POWER



Note: Place the PU resistors close to CPU
 RC61 close to CPU 300 - 1500mils

CAD Note: Place the PU resistors close to CPU
 RC63 close to CPU 300 - 1500mils

H_CPU_SVIDALRT# must be routed between the
 VIDSOUT and VIDSCLK lines to reduce cross talk.
 18 mils spacing to others.

Iccmax current changed for PDDG Rev.0.7

CPU Power Rail Table		
Voltage Rail	Voltage	S0 Iccmax Current (A)
VCC	0.65-1.3	53
VCCIO	1.05	8.5
VAXG	0.0-1.1	26
VCCPLL	1.8	3
VDDQ	1.5	5
VCCSA	0.65-0.9	6
+1.5V_MEM	1.5	12-16

5A to Mem controller(+1.5V_CPU_VDDQ)
 5-6A to 2 DIMMs/channel
 2-5A to +1.5V_RUN & +0.75V_DDR_VTT

TYCO_2134146-3_IVYBRIDGE-D
 Link CIS

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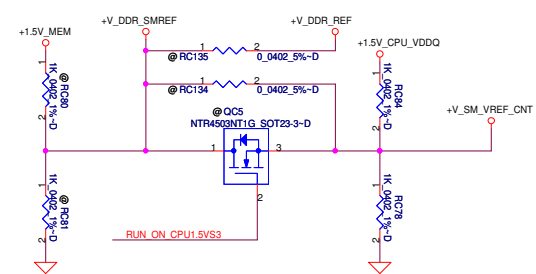
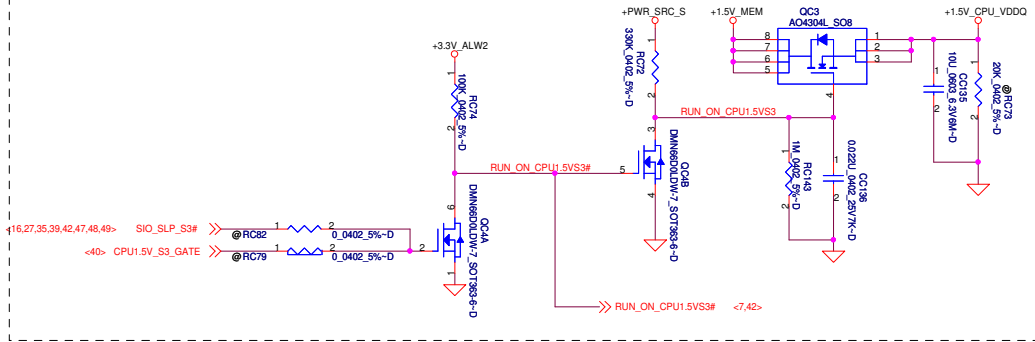
Title: Ivy/Sandy Bridge (5/6)

Size: Document Number LA-7903P Rev 1.0

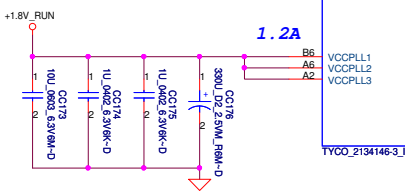
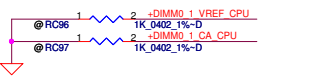
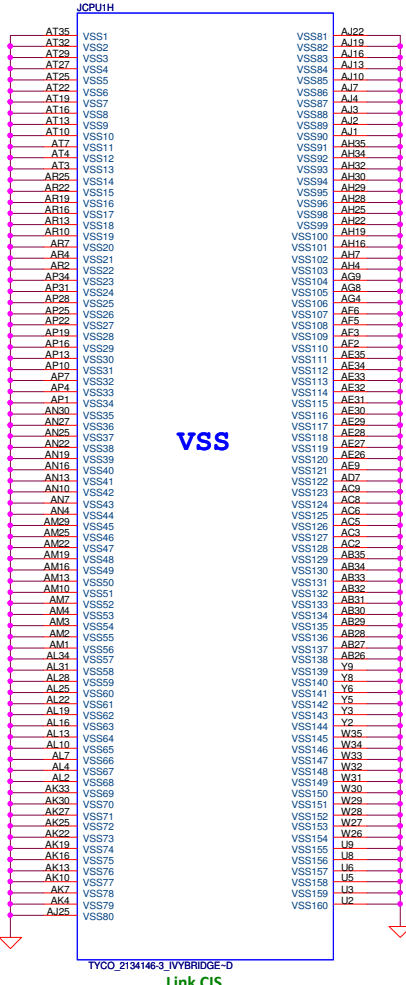
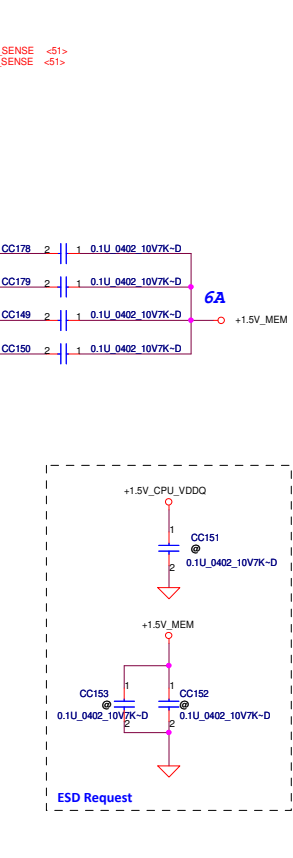
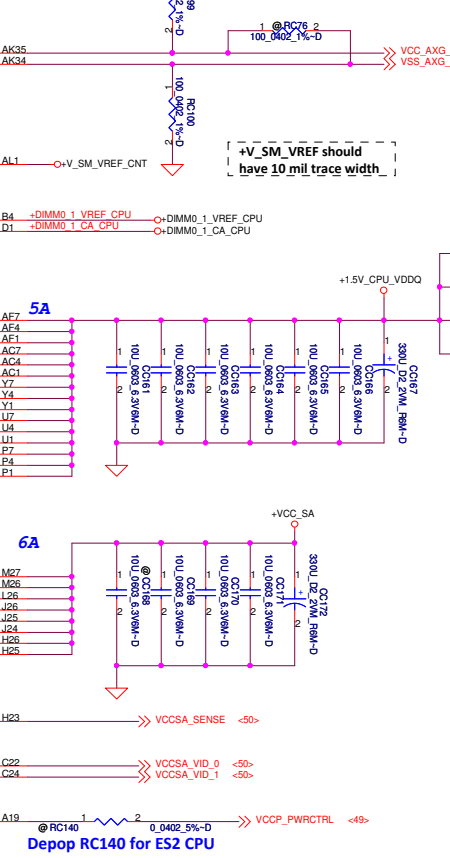
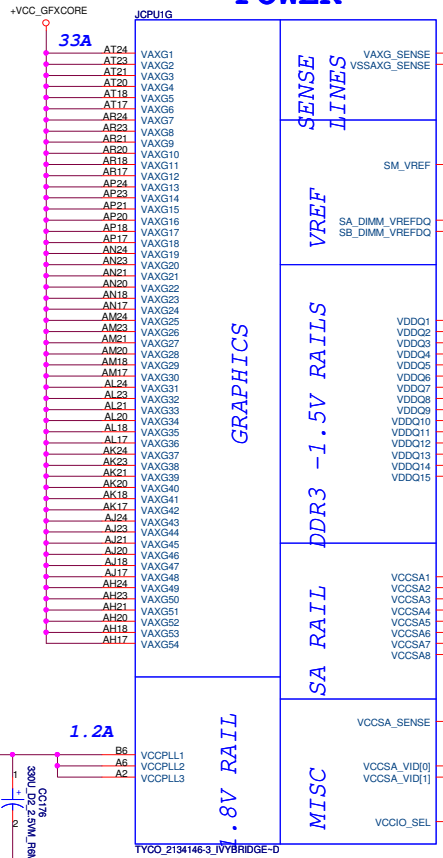
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+1.5V_CPU_VDDQ Source



POWER



Link CIS

Link CIS

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Ivy/Sandy Bridge (6/6)

LA-7901P

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JDIMM2 Rev Type H=4

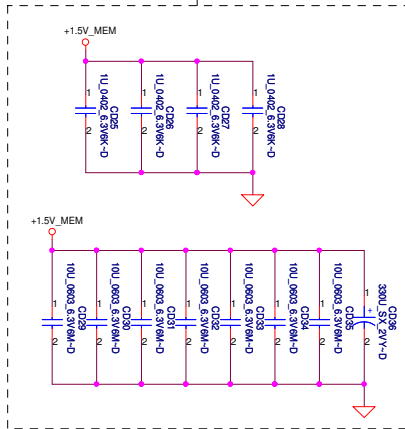
2-3A to 1 DIMMs/channel



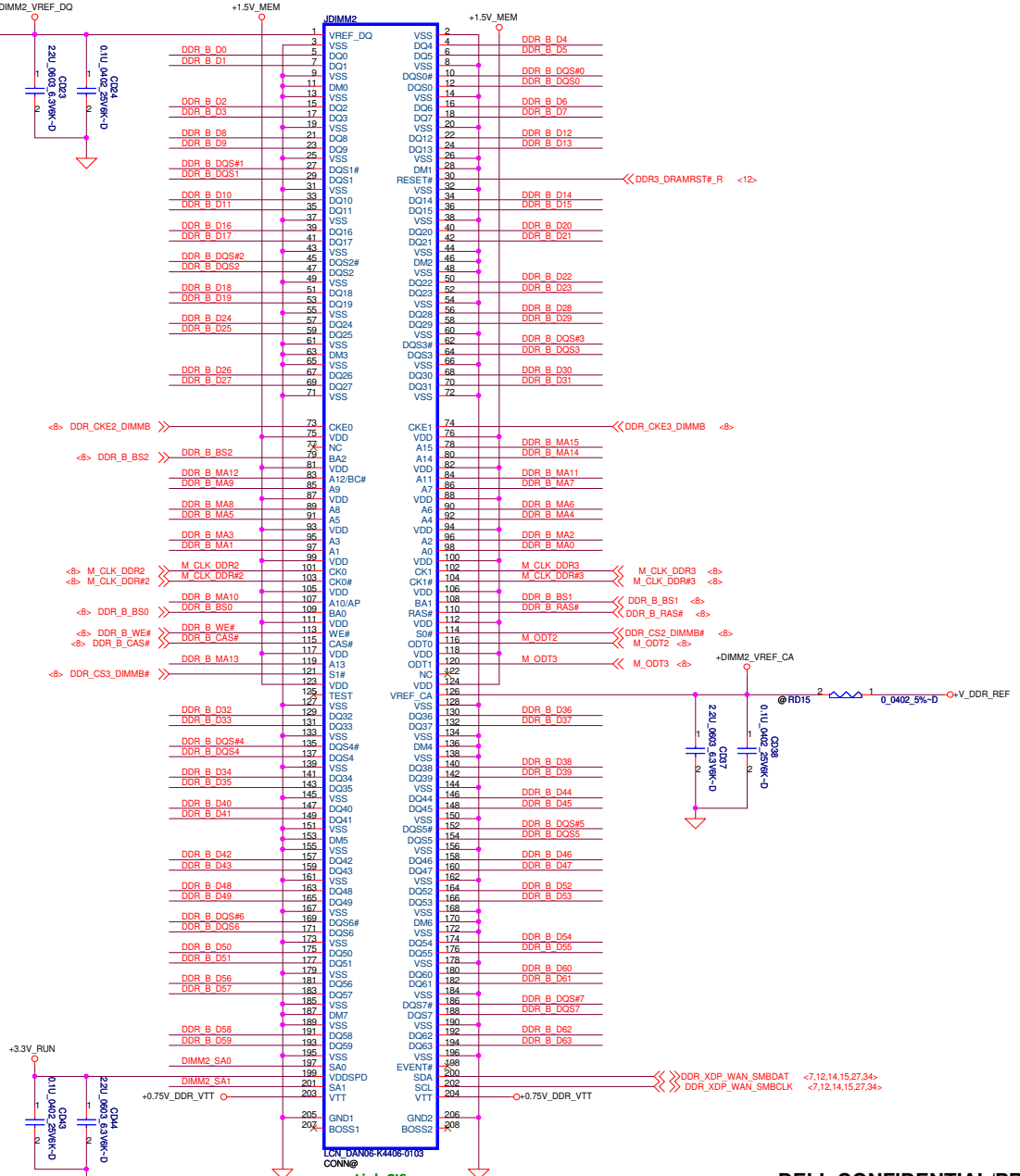
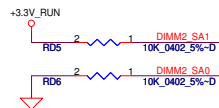
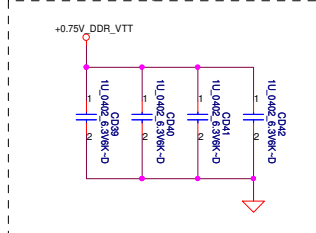
All VREF traces should have 10 mil trace width

Populate RD4, De-Populate RD8 for Intel DDR3 VREFDQ multiple methods M1
Populate RD8, De-Populate RD4 for Intel DDR3 VREFDQ multiple methods M3

Layout Note:
Place near JDIMM2



Layout Note:
Place near JDIMM2.203,204



Link CS

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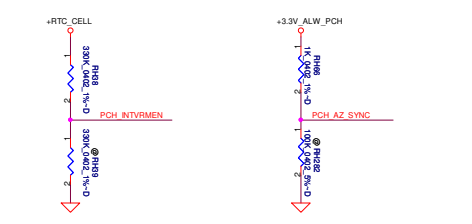
DDRIII-SODIMM SLOT2

LA-7901P

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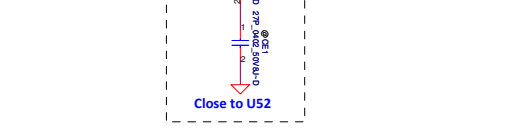
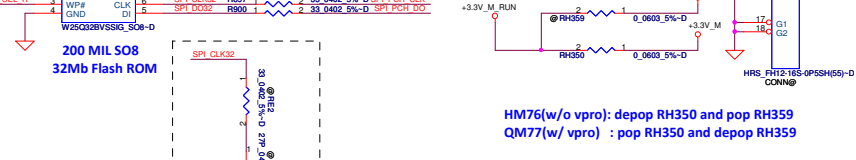
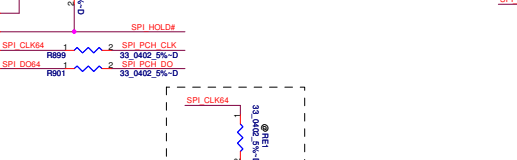
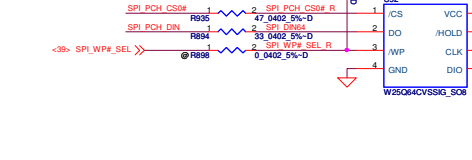
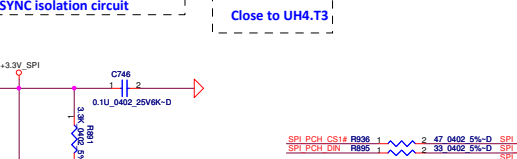
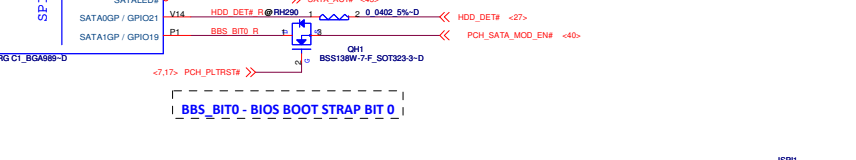
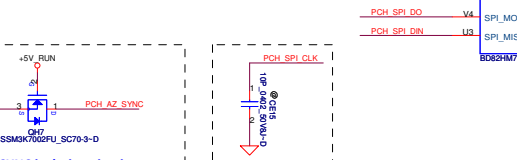
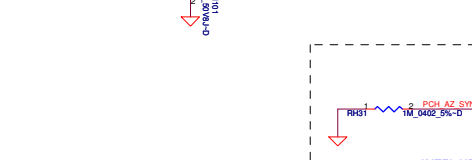
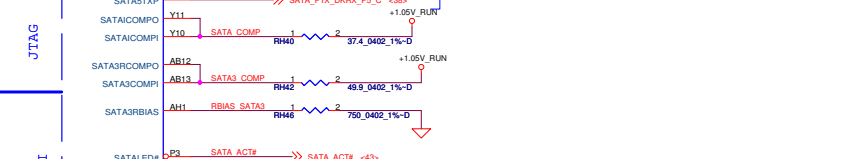
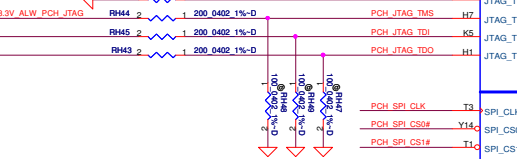
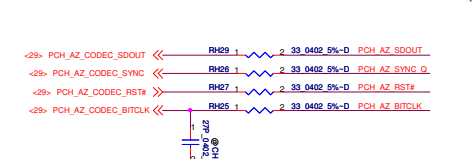
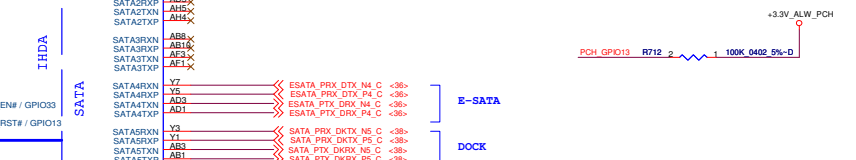
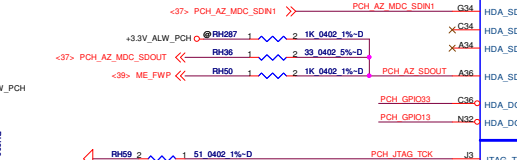
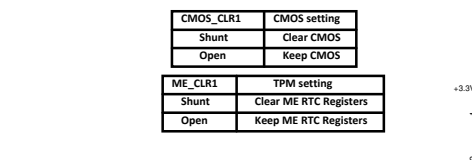
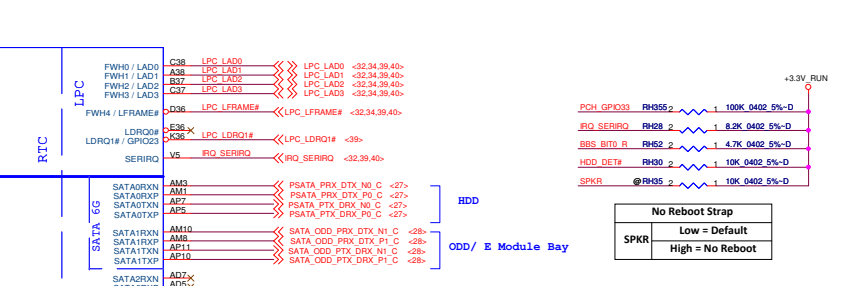
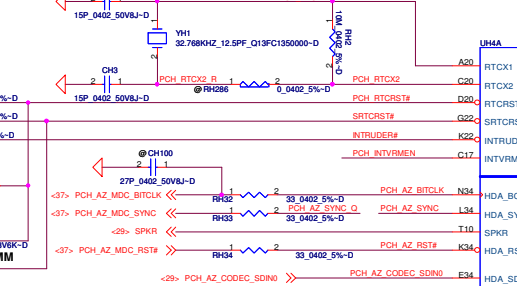
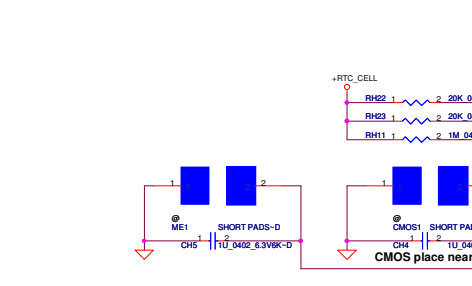
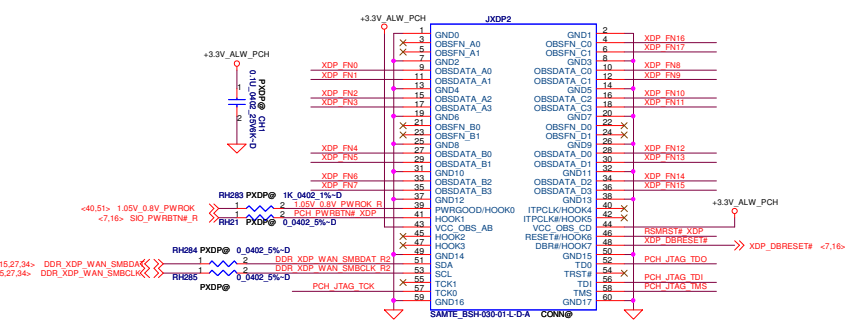
Title	DDRIII-SODIMM SLOT2	
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INTVRMEN-Integrated SUS 1.1V VRM Enable
 High - Enable Internal VRs
 Low - Enable External VRs

PCH_AZ_SYNC is sampled at the rising edge of RSMRST# pin.
 So signal should be PU to the ALWAYS rail.

USB_OC1#_R	PXDP#	RH1	2	33	0402 5%-D	XDP FN0
USB_OC1#_R	PXDP#	RH2	2	33	0402 5%-D	XDP FN1
USB_OC1#_R	PXDP#	RH3	2	33	0402 5%-D	XDP FN2
USB_OC1#_R	PXDP#	RH4	2	33	0402 5%-D	XDP FN3
USB_OC1#_R	PXDP#	RH5	2	33	0402 5%-D	XDP FN4
USB_OC1#_R	PXDP#	RH6	2	33	0402 5%-D	XDP FN5
USB_OC1#_R	PXDP#	RH7	2	33	0402 5%-D	XDP FN6
USB_OC1#_R	PXDP#	RH8	2	33	0402 5%-D	XDP FN7
USB_OC1#_R	PXDP#	RH9	2	33	0402 5%-D	XDP FN8
USB_OC1#_R	PXDP#	RH10	2	33	0402 5%-D	XDP FN9
USB_OC1#_R	PXDP#	RH11	2	33	0402 5%-D	XDP FN10
USB_OC1#_R	PXDP#	RH12	2	33	0402 5%-D	XDP FN11
USB_OC1#_R	PXDP#	RH13	2	33	0402 5%-D	XDP FN12
USB_OC1#_R	PXDP#	RH14	2	33	0402 5%-D	XDP FN13
USB_OC1#_R	PXDP#	RH15	2	33	0402 5%-D	XDP FN14
USB_OC1#_R	PXDP#	RH16	2	33	0402 5%-D	XDP FN15
USB_OC1#_R	PXDP#	RH17	2	33	0402 5%-D	XDP FN16
USB_OC1#_R	PXDP#	RH18	2	33	0402 5%-D	XDP FN17
USB_OC1#_R	PXDP#	RH19	2	33	0402 5%-D	XDP FN18
USB_OC1#_R	PXDP#	RH20	2	33	0402 5%-D	XDP FN19
USB_OC1#_R	PXDP#	RH21	2	33	0402 5%-D	XDP FN20
USB_OC1#_R	PXDP#	RH22	2	33	0402 5%-D	XDP FN21
USB_OC1#_R	PXDP#	RH23	2	33	0402 5%-D	XDP FN22
USB_OC1#_R	PXDP#	RH24	2	33	0402 5%-D	XDP FN23
USB_OC1#_R	PXDP#	RH25	2	33	0402 5%-D	XDP FN24
USB_OC1#_R	PXDP#	RH26	2	33	0402 5%-D	XDP FN25
USB_OC1#_R	PXDP#	RH27	2	33	0402 5%-D	XDP FN26
USB_OC1#_R	PXDP#	RH28	2	33	0402 5%-D	XDP FN27
USB_OC1#_R	PXDP#	RH29	2	33	0402 5%-D	XDP FN28
USB_OC1#_R	PXDP#	RH30	2	33	0402 5%-D	XDP FN29
USB_OC1#_R	PXDP#	RH31	2	33	0402 5%-D	XDP FN30
USB_OC1#_R	PXDP#	RH32	2	33	0402 5%-D	XDP FN31
USB_OC1#_R	PXDP#	RH33	2	33	0402 5%-D	XDP FN32
USB_OC1#_R	PXDP#	RH34	2	33	0402 5%-D	XDP FN33
USB_OC1#_R	PXDP#	RH35	2	33	0402 5%-D	XDP FN34
USB_OC1#_R	PXDP#	RH36	2	33	0402 5%-D	XDP FN35
USB_OC1#_R	PXDP#	RH37	2	33	0402 5%-D	XDP FN36
USB_OC1#_R	PXDP#	RH38	2	33	0402 5%-D	XDP FN37
USB_OC1#_R	PXDP#	RH39	2	33	0402 5%-D	XDP FN38
USB_OC1#_R	PXDP#	RH40	2	33	0402 5%-D	XDP FN39
USB_OC1#_R	PXDP#	RH41	2	33	0402 5%-D	XDP FN40
USB_OC1#_R	PXDP#	RH42	2	33	0402 5%-D	XDP FN41
USB_OC1#_R	PXDP#	RH43	2	33	0402 5%-D	XDP FN42
USB_OC1#_R	PXDP#	RH44	2	33	0402 5%-D	XDP FN43
USB_OC1#_R	PXDP#	RH45	2	33	0402 5%-D	XDP FN44
USB_OC1#_R	PXDP#	RH46	2	33	0402 5%-D	XDP FN45
USB_OC1#_R	PXDP#	RH47	2	33	0402 5%-D	XDP FN46
USB_OC1#_R	PXDP#	RH48	2	33	0402 5%-D	XDP FN47
USB_OC1#_R	PXDP#	RH49	2	33	0402 5%-D	XDP FN48
USB_OC1#_R	PXDP#	RH50	2	33	0402 5%-D	XDP FN49
USB_OC1#_R	PXDP#	RH51	2	33	0402 5%-D	XDP FN50
USB_OC1#_R	PXDP#	RH52	2	33	0402 5%-D	XDP FN51
USB_OC1#_R	PXDP#	RH53	2	33	0402 5%-D	XDP FN52
USB_OC1#_R	PXDP#	RH54	2	33	0402 5%-D	XDP FN53
USB_OC1#_R	PXDP#	RH55	2	33	0402 5%-D	XDP FN54
USB_OC1#_R	PXDP#	RH56	2	33	0402 5%-D	XDP FN55
USB_OC1#_R	PXDP#	RH57	2	33	0402 5%-D	XDP FN56
USB_OC1#_R	PXDP#	RH58	2	33	0402 5%-D	XDP FN57
USB_OC1#_R	PXDP#	RH59	2	33	0402 5%-D	XDP FN58
USB_OC1#_R	PXDP#	RH60	2	33	0402 5%-D	XDP FN59
USB_OC1#_R	PXDP#	RH61	2	33	0402 5%-D	XDP FN60
USB_OC1#_R	PXDP#	RH62	2	33	0402 5%-D	XDP FN61
USB_OC1#_R	PXDP#	RH63	2	33	0402 5%-D	XDP FN62
USB_OC1#_R	PXDP#	RH64	2	33	0402 5%-D	XDP FN63
USB_OC1#_R	PXDP#	RH65	2	33	0402 5%-D	XDP FN64
USB_OC1#_R	PXDP#	RH66	2	33	0402 5%-D	XDP FN65
USB_OC1#_R	PXDP#	RH67	2	33	0402 5%-D	XDP FN66
USB_OC1#_R	PXDP#	RH68	2	33	0402 5%-D	XDP FN67
USB_OC1#_R	PXDP#	RH69	2	33	0402 5%-D	XDP FN68
USB_OC1#_R	PXDP#	RH70	2	33	0402 5%-D	XDP FN69
USB_OC1#_R	PXDP#	RH71	2	33	0402 5%-D	XDP FN70
USB_OC1#_R	PXDP#	RH72	2	33	0402 5%-D	XDP FN71
USB_OC1#_R	PXDP#	RH73	2	33	0402 5%-D	XDP FN72
USB_OC1#_R	PXDP#	RH74	2	33	0402 5%-D	XDP FN73
USB_OC1#_R	PXDP#	RH75	2	33	0402 5%-D	XDP FN74
USB_OC1#_R	PXDP#	RH76	2	33	0402 5%-D	XDP FN75
USB_OC1#_R	PXDP#	RH77	2	33	0402 5%-D	XDP FN76
USB_OC1#_R	PXDP#	RH78	2	33	0402 5%-D	XDP FN77
USB_OC1#_R	PXDP#	RH79	2	33	0402 5%-D	XDP FN78
USB_OC1#_R	PXDP#	RH80	2	33	0402 5%-D	XDP FN79
USB_OC1#_R	PXDP#	RH81	2	33	0402 5%-D	XDP FN80
USB_OC1#_R	PXDP#	RH82	2	33	0402 5%-D	XDP FN81
USB_OC1#_R	PXDP#	RH83	2	33	0402 5%-D	XDP FN82
USB_OC1#_R	PXDP#	RH84	2	33	0402 5%-D	XDP FN83
USB_OC1#_R	PXDP#	RH85	2	33	0402 5%-D	XDP FN84
USB_OC1#_R	PXDP#	RH86	2	33	0402 5%-D	XDP FN85
USB_OC1#_R	PXDP#	RH87	2	33	0402 5%-D	XDP FN86
USB_OC1#_R	PXDP#	RH88	2	33	0402 5%-D	XDP FN87
USB_OC1#_R	PXDP#	RH89	2	33	0402 5%-D	XDP FN88
USB_OC1#_R	PXDP#	RH90	2	33	0402 5%-D	XDP FN89
USB_OC1#_R	PXDP#	RH91	2	33	0402 5%-D	XDP FN90
USB_OC1#_R	PXDP#	RH92	2	33	0402 5%-D	XDP FN91
USB_OC1#_R	PXDP#	RH93	2	33	0402 5%-D	XDP FN92
USB_OC1#_R	PXDP#	RH94	2	33	0402 5%-D	XDP FN93
USB_OC1#_R	PXDP#	RH95	2	33	0402 5%-D	XDP FN94
USB_OC1#_R	PXDP#	RH96	2	33	0402 5%-D	XDP FN95
USB_OC1#_R	PXDP#	RH97	2	33	0402 5%-D	XDP FN96
USB_OC1#_R	PXDP#	RH98	2	33	0402 5%-D	XDP FN97
USB_OC1#_R	PXDP#	RH99	2	33	0402 5%-D	XDP FN98
USB_OC1#_R	PXDP#	RH100	2	33	0402 5%-D	XDP FN99
USB_OC1#_R	PXDP#	RH101	2	33	0402 5%-D	XDP FN100



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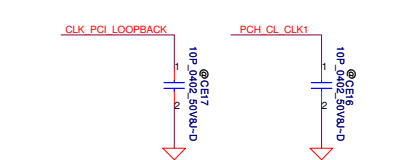
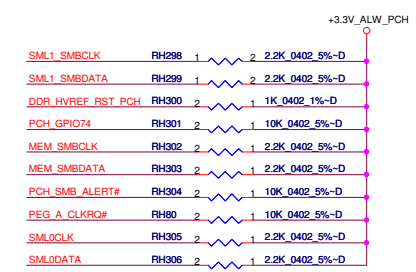
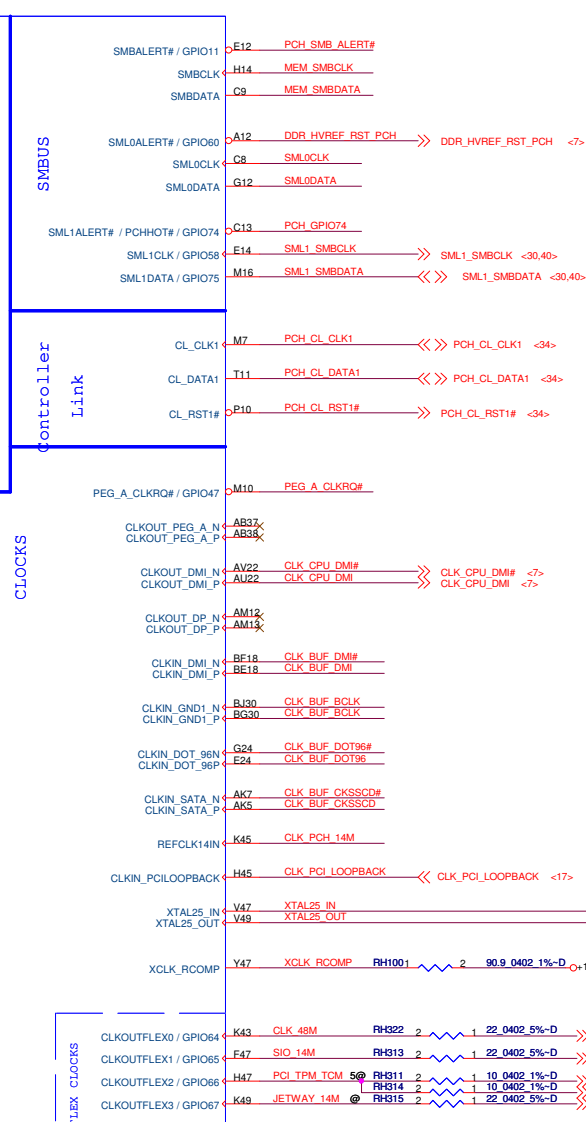
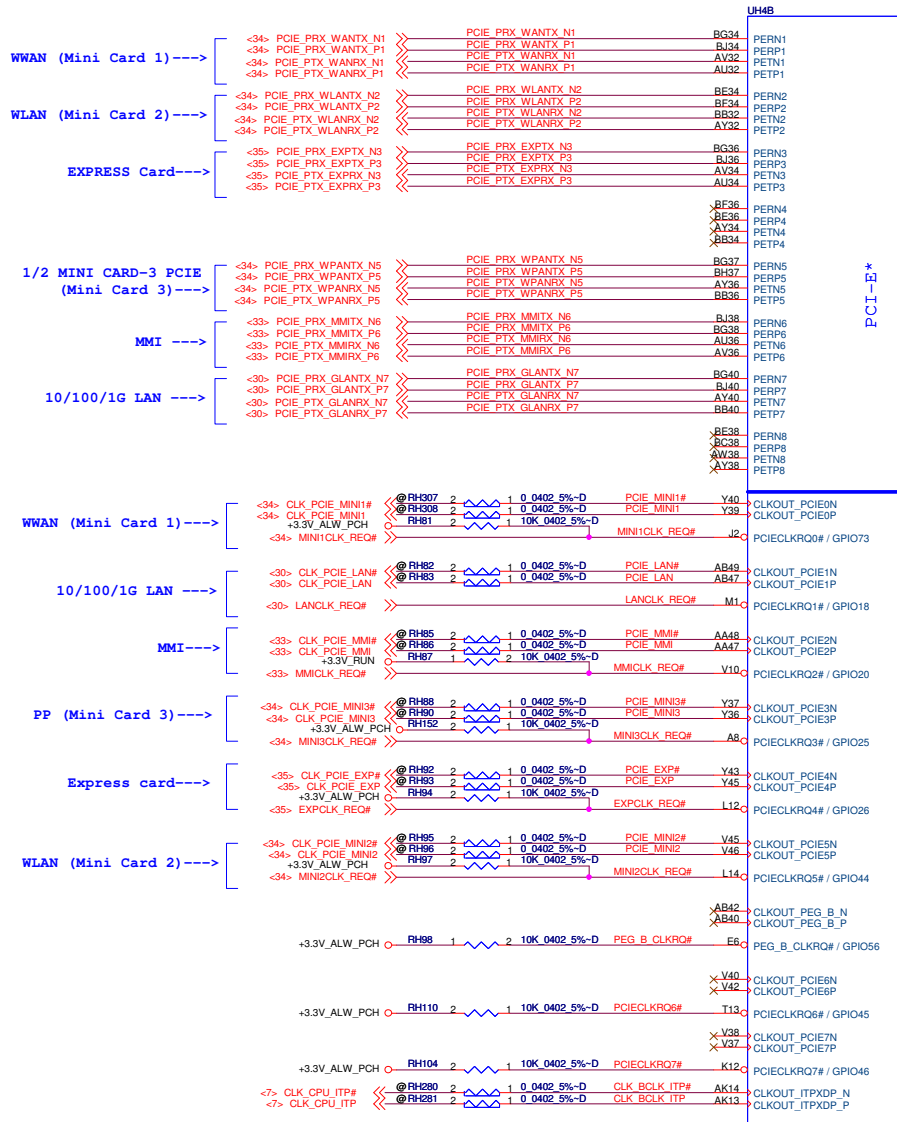
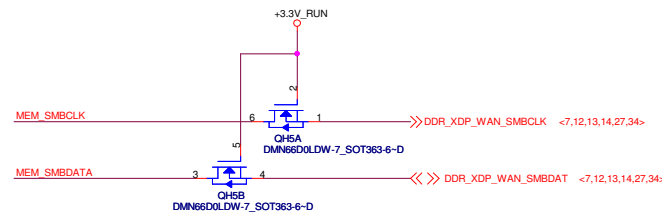
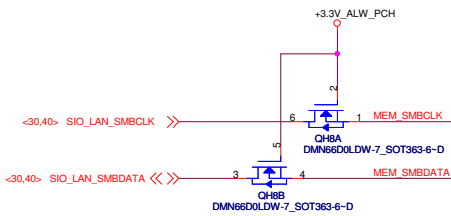
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File: **PCH (1/8)**

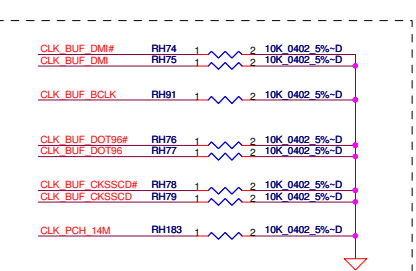
Doc: **LA-7901P**

Date: Saturday, March 1st, 2012

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RF review in 0629



CLOCK TERMINATION for FCIM and need close to PCH

PCIE REQ power rail:
suspend: 0 3 4 5 6 7
core: 1 2

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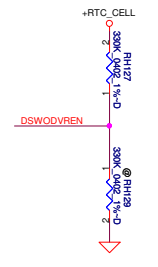
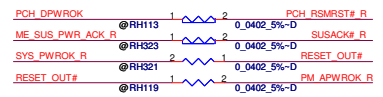
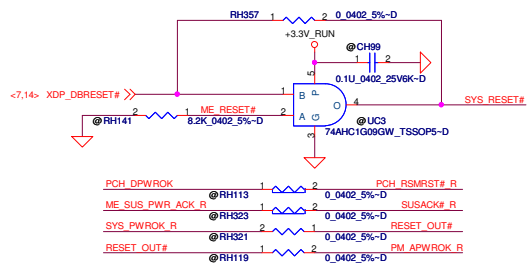
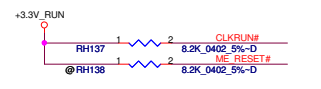
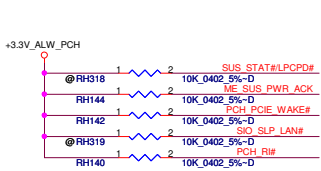
Compal Electronics, Inc.

Title: **PCH (2/8)**

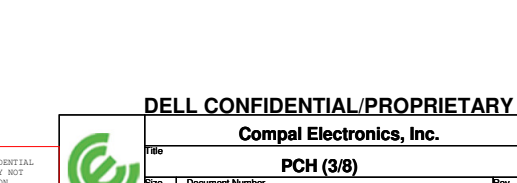
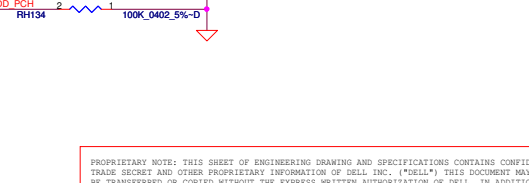
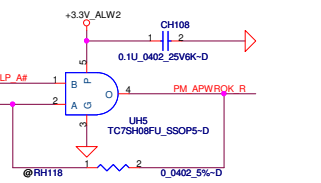
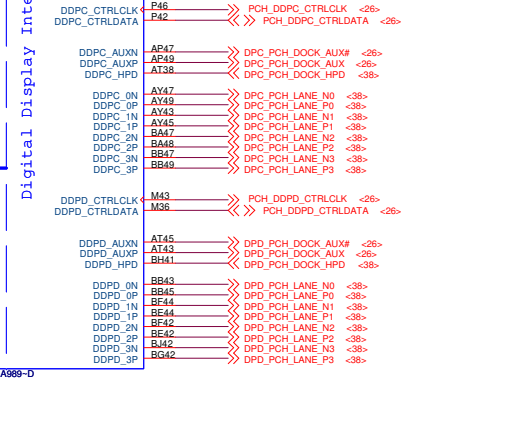
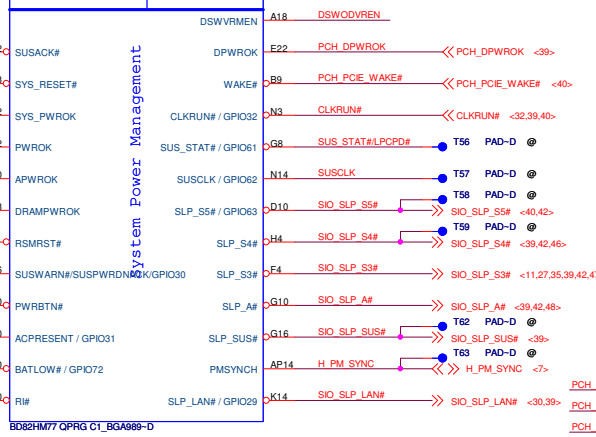
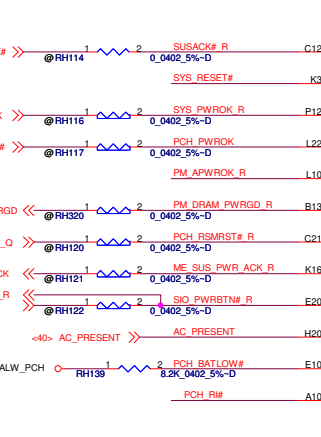
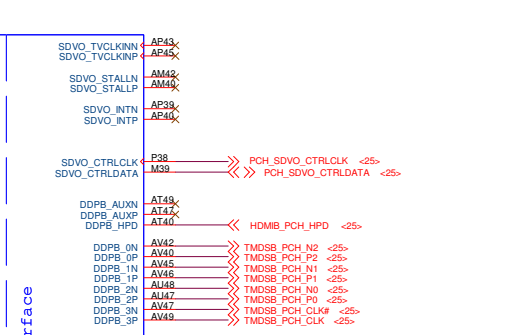
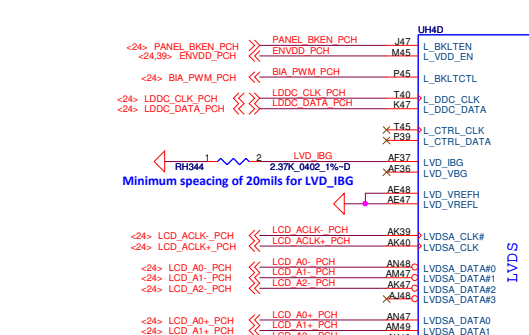
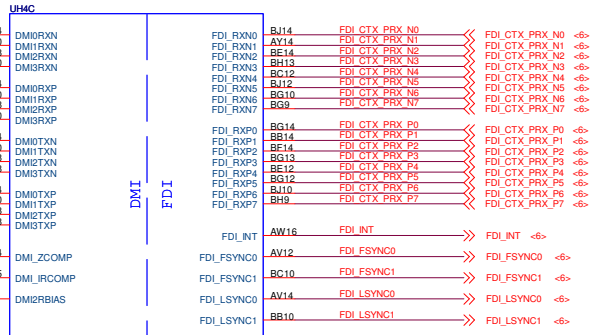
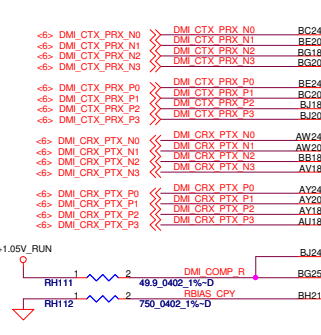
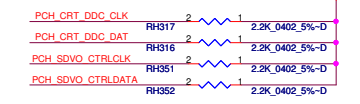
Size: **LA-7901P**

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DSWODVREN - On Die VSR Enable
Enabled (DEFAULT)
HIGH: RH127 STUFFED, RH129 UNSTUFFED
Disabled
LOW: RH129 STUFFED, RH127 UNSTUFFED



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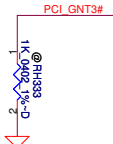
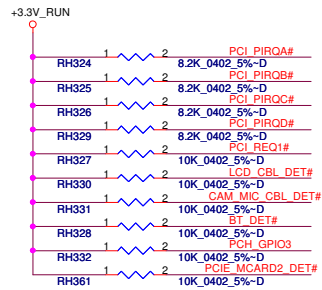
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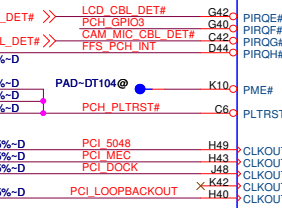
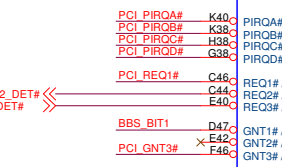
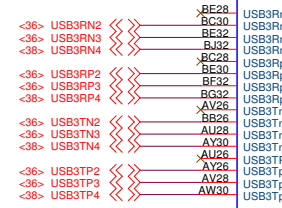
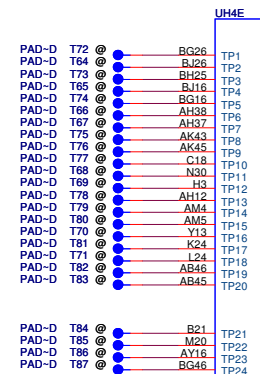
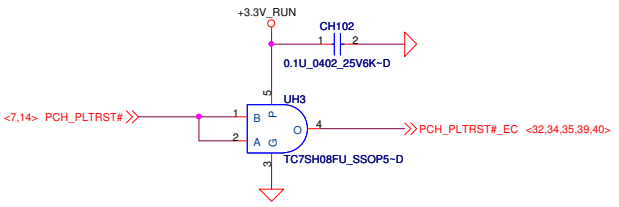
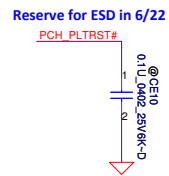
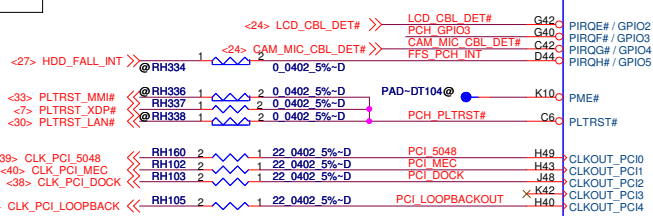
PCH (3/8)

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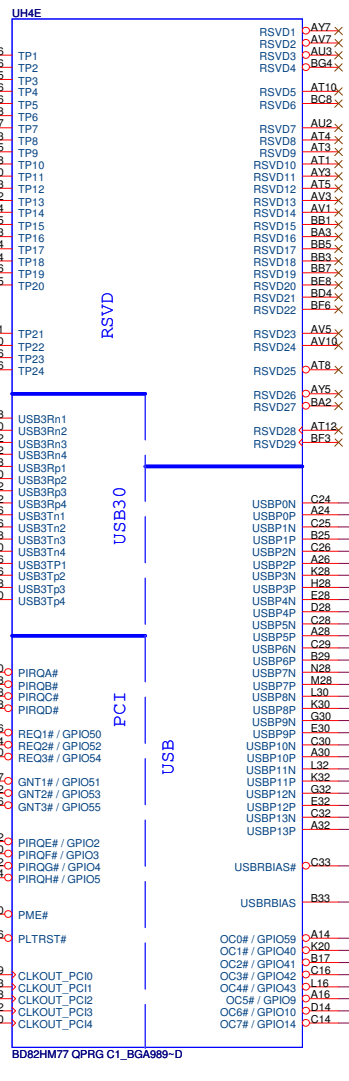
A16 swap override Strap/Top-Block Swap Override jumper	
PCI_GNT#3	Low = A16 swap High = Default



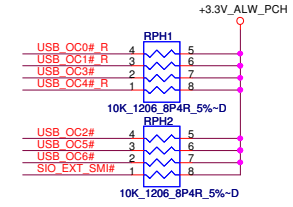
BD82HM77 OPRG C1_BGA989-D

Boot BIOS Strap		
BBS_BIT1	SATA_SLPD (BBS_BIT0)	Boot BIOS Location
0	0	LPC
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI

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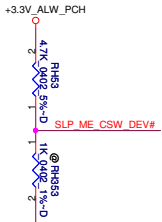
- >Back Right--IO
- >Left Side
- >Left side E-SATA
- >MLK DOCK
- >WLAN/WIMAX
- >WWAN/UWB
- >Flash
- >DOCK
- >Non used
- >Right side--IO
- >Express Card
- >Blue Tooth
- >Camera
- >BIO



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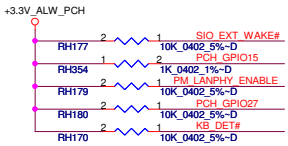


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Title PCH (4/8)		
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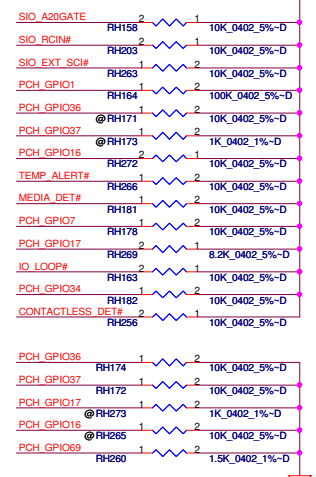
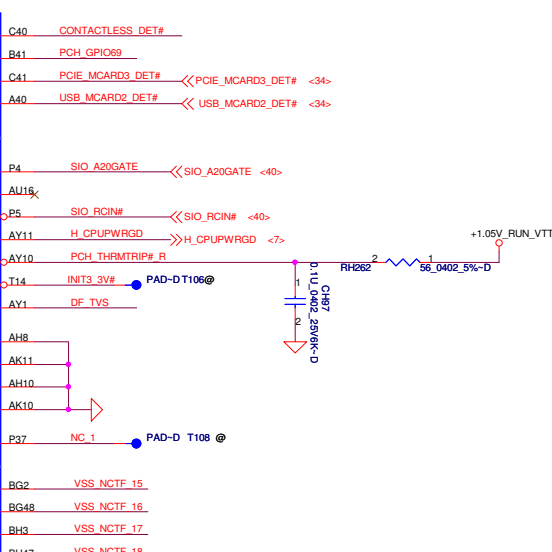
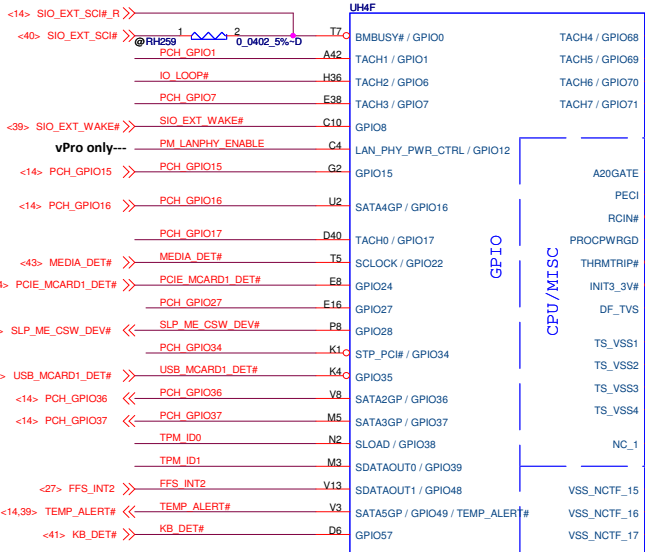


Note: PCH has internal pull up 20k ohm on E3_PAID_TS_DET# (GPIO27)

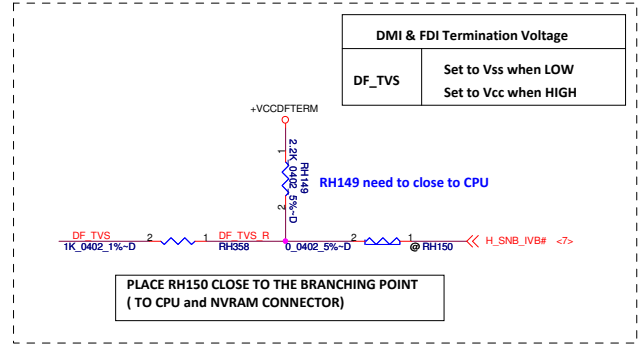
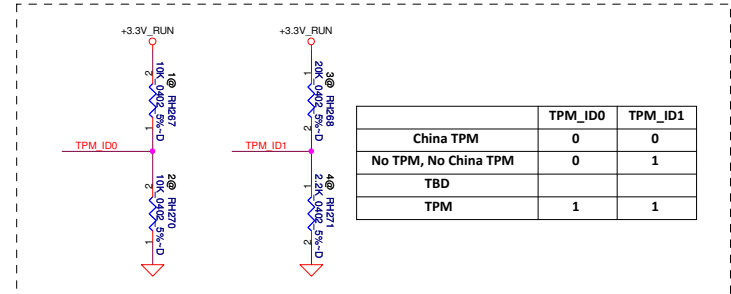
SLP_ME_CSW_DEV# PLL ON DIE VR ENABLE	
ENABLED	HIGH (DEFAULT)
DISABLED	LOW



Layout note:
Trace wide 10mil & length 30mil
All NCTF pins should have thick traces at 45° from the pad.



Layout note:
Trace wide 10mil & length 30mil
All NCTF pins should have thick traces at 45° from the pad.

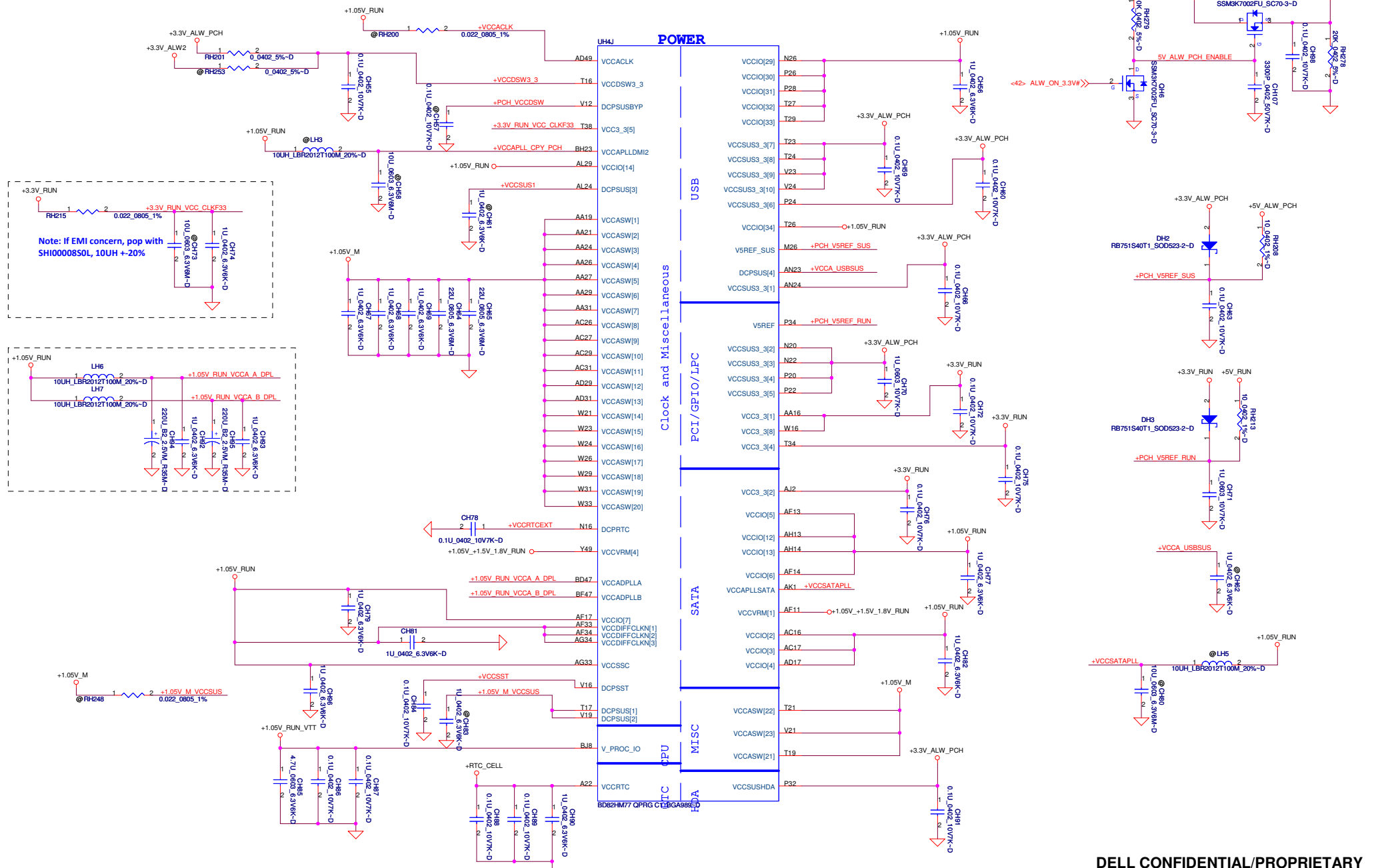


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Note: If EMI concern, pop with SH100008S0L, 10UH +20%

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UH4H		UH4H	
H5	VSS[0]		
AA17	VSS[1]	VSS[80]	AK38
AA2	VSS[2]	VSS[81]	AK4
AA3	VSS[3]	VSS[82]	AK42
AA33	VSS[4]	VSS[83]	AK46
AA34	VSS[5]	VSS[84]	AK8
AB11	VSS[6]	VSS[85]	AL1
AB14	VSS[7]	VSS[86]	AL19
AB39	VSS[8]	VSS[87]	AL2
AB4	VSS[9]	VSS[88]	AL21
AB43	VSS[10]	VSS[89]	AL23
AB5	VSS[11]	VSS[90]	AL28
AB7	VSS[12]	VSS[91]	AL27
AC19	VSS[13]	VSS[92]	AL31
AC2	VSS[14]	VSS[93]	AL33
AC21	VSS[15]	VSS[94]	AL34
AC24	VSS[16]	VSS[95]	AL48
AC33	VSS[17]	VSS[96]	AM11
AC34	VSS[18]	VSS[97]	AM14
AC48	VSS[19]	VSS[98]	AM36
AD10	VSS[20]	VSS[99]	AM39
AD11	VSS[21]	VSS[100]	AM43
AD12	VSS[22]	VSS[101]	AM45
AD13	VSS[23]	VSS[102]	AM46
AD19	VSS[24]	VSS[103]	AM7
AD24	VSS[25]	VSS[104]	AN2
AD26	VSS[26]	VSS[105]	AN29
AD27	VSS[27]	VSS[106]	AN3
AD33	VSS[28]	VSS[107]	AN31
AD34	VSS[29]	VSS[108]	AP12
AD36	VSS[30]	VSS[109]	AP19
AD37	VSS[31]	VSS[110]	AP28
AD38	VSS[32]	VSS[111]	AP12
AD4	VSS[33]	VSS[112]	AP32
AD43	VSS[34]	VSS[113]	AP38
AD40	VSS[35]	VSS[114]	AP4
AD42	VSS[36]	VSS[115]	AP24
AD43	VSS[37]	VSS[116]	AP46
AD45	VSS[38]	VSS[117]	AP8
AD46	VSS[39]	VSS[118]	AR2
AD8	VSS[40]	VSS[119]	AR48
AE2	VSS[41]	VSS[120]	AT11
AE3	VSS[42]	VSS[121]	AT13
AF10	VSS[43]	VSS[122]	AT18
AF12	VSS[44]	VSS[123]	AT22
AD14	VSS[45]	VSS[124]	AT26
AD16	VSS[46]	VSS[125]	AT28
AF19	VSS[47]	VSS[126]	AT30
AF24	VSS[48]	VSS[127]	AT38
AF26	VSS[49]	VSS[128]	AT39
AF27	VSS[50]	VSS[129]	AT42
AF29	VSS[51]	VSS[130]	AT46
AF29	VSS[52]	VSS[131]	AT7
AF31	VSS[53]	VSS[132]	AU24
AF38	VSS[54]	VSS[133]	AU30
AF4	VSS[55]	VSS[134]	AV16
AF42	VSS[56]	VSS[135]	AV20
AF46	VSS[57]	VSS[136]	AV24
AF5	VSS[58]	VSS[137]	AV30
AF7	VSS[59]	VSS[138]	AV4
AF8	VSS[60]	VSS[139]	AV43
AG19	VSS[61]	VSS[140]	AV8
AG2	VSS[62]	VSS[141]	AW14
AG31	VSS[63]	VSS[142]	AW18
AG48	VSS[64]	VSS[143]	AW2
AH11	VSS[65]	VSS[144]	AW22
AH3	VSS[66]	VSS[145]	AW26
AH36	VSS[67]	VSS[146]	AW28
AH39	VSS[68]	VSS[147]	AW32
AH40	VSS[69]	VSS[148]	AW34
AH42	VSS[70]	VSS[149]	AW36
AH46	VSS[71]	VSS[150]	AW40
AH7	VSS[72]	VSS[151]	AW48
AJ19	VSS[73]	VSS[152]	AV11
AJ21	VSS[74]	VSS[153]	AY12
AJ24	VSS[75]	VSS[154]	AY22
AJ3	VSS[76]	VSS[155]	AY28
AJ34	VSS[77]	VSS[156]	
AK12	VSS[78]	VSS[157]	
AK3	VSS[79]	VSS[158]	

BD82HM77 QPRG C1_BGA989-D

UH4I		UH4I	
AY4	VSS[159]	VSS[259]	H46
AY42	VSS[160]	VSS[260]	K18
AY46	VSS[161]	VSS[261]	K26
AY9	VSS[162]	VSS[262]	K38
B11	VSS[163]	VSS[263]	K46
B15	VSS[164]	VSS[264]	K7
B19	VSS[165]	VSS[265]	L18
B23	VSS[166]	VSS[266]	L2
B27	VSS[167]	VSS[267]	L20
B31	VSS[168]	VSS[268]	L26
B35	VSS[169]	VSS[269]	L28
B39	VSS[170]	VSS[270]	L36
B7	VSS[171]	VSS[271]	L48
F45	VSS[172]	VSS[272]	M12
BB12	VSS[173]	VSS[273]	P16
AL17	VSS[174]	VSS[274]	M18
BB20	VSS[175]	VSS[275]	M22
BB22	VSS[176]	VSS[276]	M24
BB24	VSS[177]	VSS[277]	M30
BB28	VSS[178]	VSS[278]	M32
BB30	VSS[179]	VSS[279]	M34
BB38	VSS[180]	VSS[280]	M38
BB4	VSS[181]	VSS[281]	M4
BB46	VSS[182]	VSS[282]	M42
BC14	VSS[183]	VSS[283]	M46
BC18	VSS[184]	VSS[284]	M8
BC2	VSS[185]	VSS[285]	N18
BC22	VSS[186]	VSS[286]	P30
BC26	VSS[187]	VSS[287]	N47
BC32	VSS[188]	VSS[288]	P11
BC34	VSS[189]	VSS[289]	P18
BC36	VSS[190]	VSS[290]	T33
BC40	VSS[191]	VSS[291]	P40
BC42	VSS[192]	VSS[292]	P43
BC48	VSS[193]	VSS[293]	P47
BD46	VSS[194]	VSS[294]	P7
AN3	VSS[195]	VSS[295]	P2
BE2	VSS[196]	VSS[296]	R48
BE26	VSS[197]	VSS[297]	T12
BE40	VSS[198]	VSS[298]	T31
BF10	VSS[199]	VSS[299]	T37
BF12	VSS[200]	VSS[300]	T4
BF16	VSS[201]	VSS[301]	W34
BF20	VSS[202]	VSS[302]	T46
BF22	VSS[203]	VSS[303]	T47
BF24	VSS[204]	VSS[304]	T9
BF26	VSS[205]	VSS[305]	V17
BF28	VSS[206]	VSS[306]	V26
BD3	VSS[207]	VSS[307]	V27
BF38	VSS[208]	VSS[308]	V29
BF40	VSS[209]	VSS[309]	V31
BF8	VSS[210]	VSS[310]	V36
BF8	VSS[211]	VSS[311]	V39
BG21	VSS[212]	VSS[312]	V43
BG33	VSS[213]	VSS[313]	V7
BG44	VSS[214]	VSS[314]	W17
BC38	VSS[215]	VSS[315]	W19
BH11	VSS[216]	VSS[316]	W2
BH19	VSS[217]	VSS[317]	W27
BH15	VSS[218]	VSS[318]	W48
BH17	VSS[219]	VSS[319]	Y12
BH19	VSS[220]	VSS[320]	Y38
H10	VSS[221]	VSS[321]	Y4
BH27	VSS[222]	VSS[322]	Y42
BH31	VSS[223]	VSS[323]	Y46
BH33	VSS[224]	VSS[324]	Y8
AV20	VSS[225]	VSS[325]	BG29
BH39	VSS[226]	VSS[326]	N24
BH43	VSS[227]	VSS[327]	AJ3
BH7	VSS[228]	VSS[328]	AD47
AV38	VSS[229]	VSS[329]	B43
D3	VSS[230]	VSS[330]	BE10
D12	VSS[231]	VSS[331]	BG41
D16	VSS[232]	VSS[332]	G14
D18	VSS[233]	VSS[333]	H16
D22	VSS[234]	VSS[334]	T36
D24	VSS[235]	VSS[335]	BG22
D26	VSS[236]	VSS[336]	BG24
D30	VSS[237]	VSS[337]	C22
D32	VSS[238]	VSS[338]	AP13
D34	VSS[239]	VSS[339]	M14
D38	VSS[240]	VSS[340]	AP3
D42	VSS[241]	VSS[341]	AP1
D8	VSS[242]	VSS[342]	BE16
E18	VSS[243]	VSS[343]	BC16
E26	VSS[244]	VSS[344]	BG28
G18	VSS[245]	VSS[345]	B128
G20	VSS[246]	VSS[346]	
G26	VSS[247]	VSS[347]	
G28	VSS[248]	VSS[348]	
G36	VSS[249]	VSS[349]	
G48	VSS[250]	VSS[350]	
H12	VSS[251]	VSS[351]	
H18	VSS[252]	VSS[352]	
H22	VSS[253]		
H24	VSS[254]		
H26	VSS[255]		
H30	VSS[256]		
H32	VSS[257]		
H34	VSS[258]		
F3	VSS[259]		

BD82HM77 QPRG C1_BGA989-D

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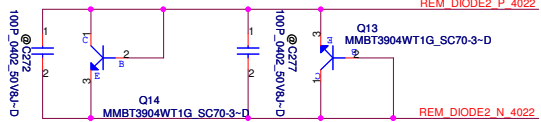
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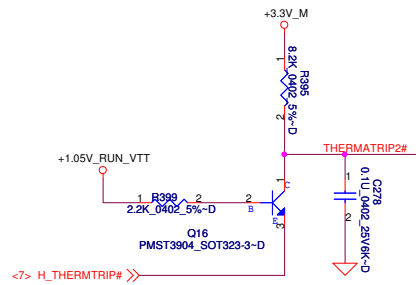
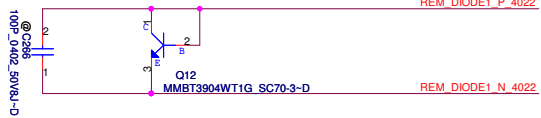
Title			PCH (8/8)		
Size			Document Number		
			LA-7901P		
Date			Saturday, March 03, 2012		
			Sheet 21 of 61		
			Rev 1.0		

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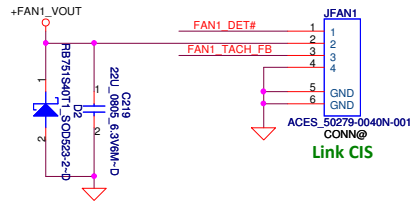
- (1) DP3/DN3 for SODIMM on Q14, place Q14 close to SODIMM and C272 close to Q14
- (2) DP5/DN5 for Skin on Q13, place Q13 close to Vcore VR choke.



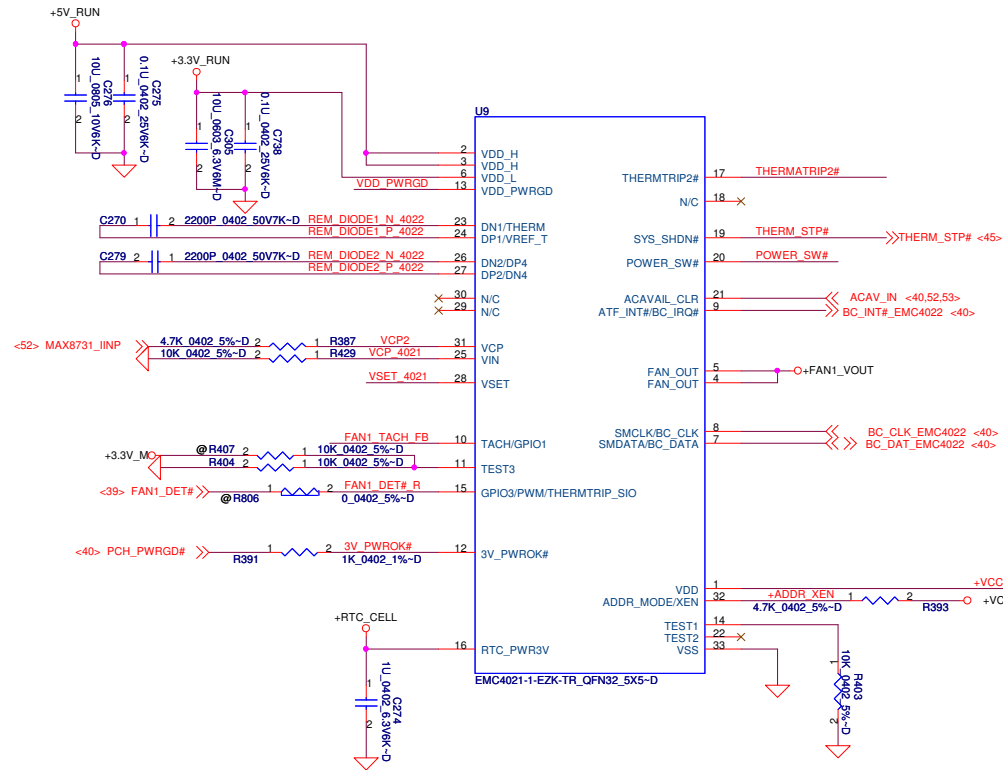
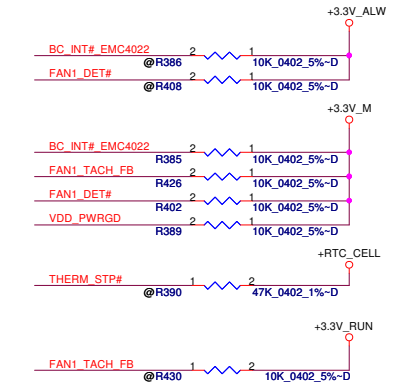
Place under CPU
Place C266 close to the Q12 as possible



<-> H_THERMATRIP# >>



HM76(w/o vpro): depop R385 and pop R386
QM77(w/ vpro) : pop R385 and depop R386



<52> MAX8731_IINP >> 4.7k 0402 5%-D 2 R387 VCP2
10k 0402 5%-D 2 R429 VCP_4021

3.3V_M @R407 2 10k 0402 5%-D 1 R404 2 10k 0402 5%-D 1

<39> FAN1_DET# >> @R806 1 10k 0402 5%-D 2

<40> PCH_PWRGD# >> R391 1 3V_PWROK# 2 1k 0402 1%-D

+RTC_CELL 1 1uF 0402 25V/6k-C C274

VSET 4021 1 1.24k 0402 1%-D R406 2 0.1uF 0402 25V/6k-C C282

+RTC_CELL 1 1uF 0402 25V/6k-C C274

VSET 4021 1 1.24k 0402 1%-D R406 2 0.1uF 0402 25V/6k-C C282

+RTC_CELL 1 1uF 0402 25V/6k-C C274

VSET 4021 1 1.24k 0402 1%-D R406 2 0.1uF 0402 25V/6k-C C282

+RTC_CELL 1 1uF 0402 25V/6k-C C274

VSET 4021 1 1.24k 0402 1%-D R406 2 0.1uF 0402 25V/6k-C C282

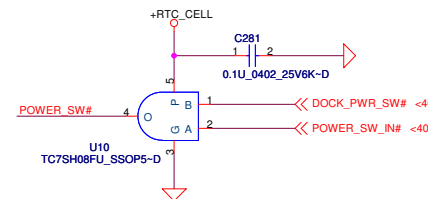
+RTC_CELL 1 1uF 0402 25V/6k-C C274

VSET 4021 1 1.24k 0402 1%-D R406 2 0.1uF 0402 25V/6k-C C282

+RTC_CELL 1 1uF 0402 25V/6k-C C274

VSET 4021 1 1.24k 0402 1%-D R406 2 0.1uF 0402 25V/6k-C C282

Rest=1.24k, Tp=92degree



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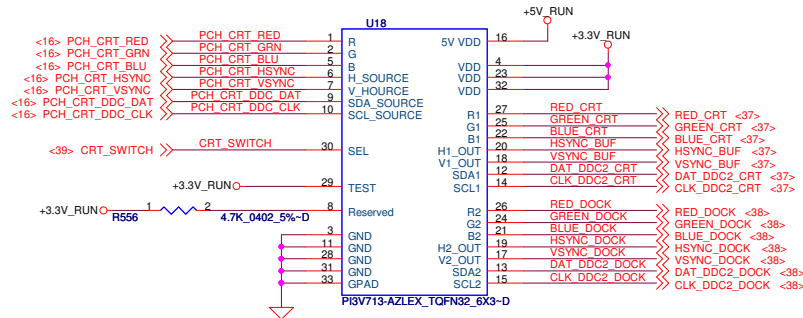
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Title			FAN & Thermal Sensor		
Size	Document Number	Rev			
	LA-7901P	1.0			
Date:	Friday, March 02, 2012	Sheet	22	of	61

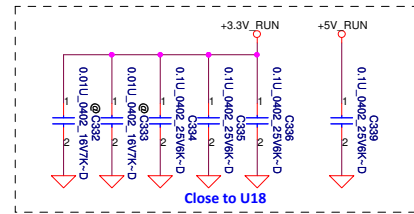
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SW for MB/DOCK



change TI(SA00004RS0L) as main source from Pericom

SEL1/SEL2	Chanel	Source
0	A=B1	MB
1	A=B2	APR/SPR



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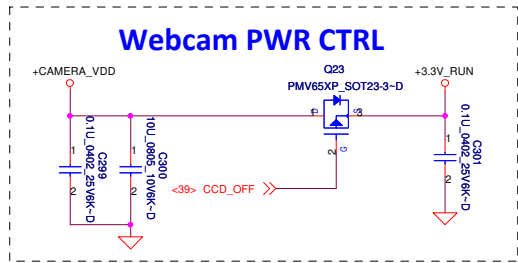
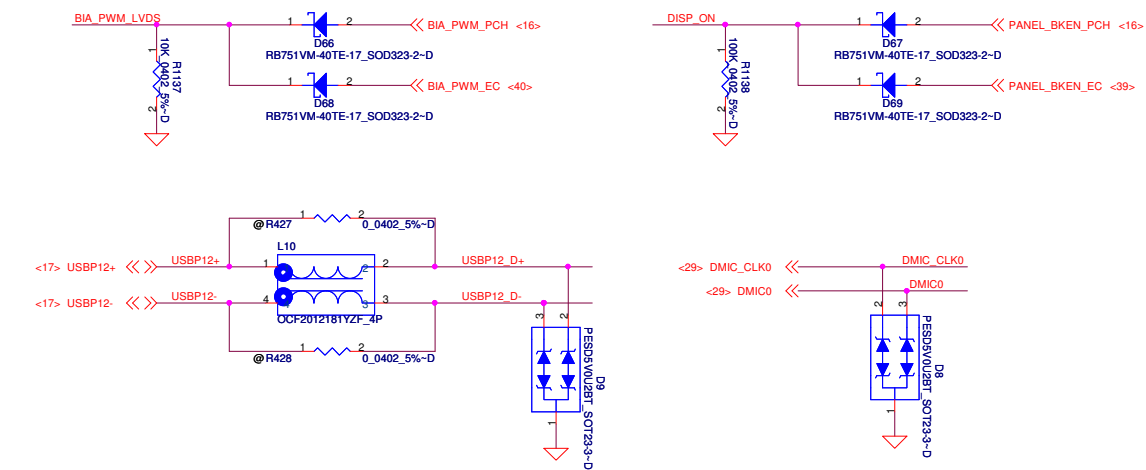
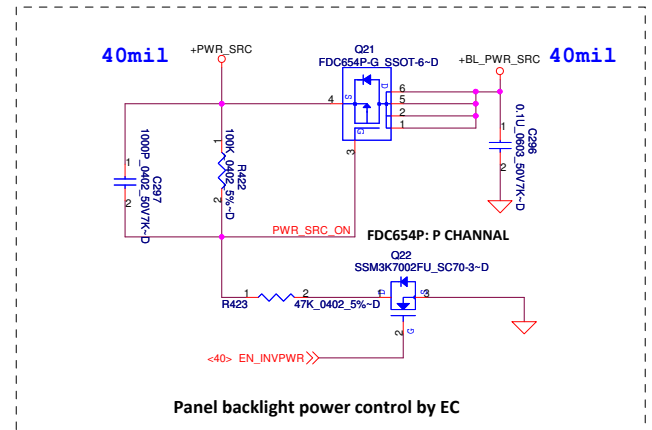
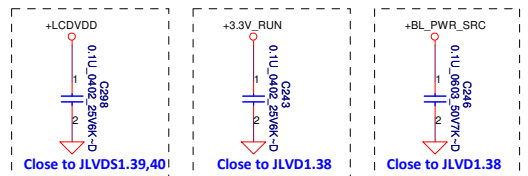
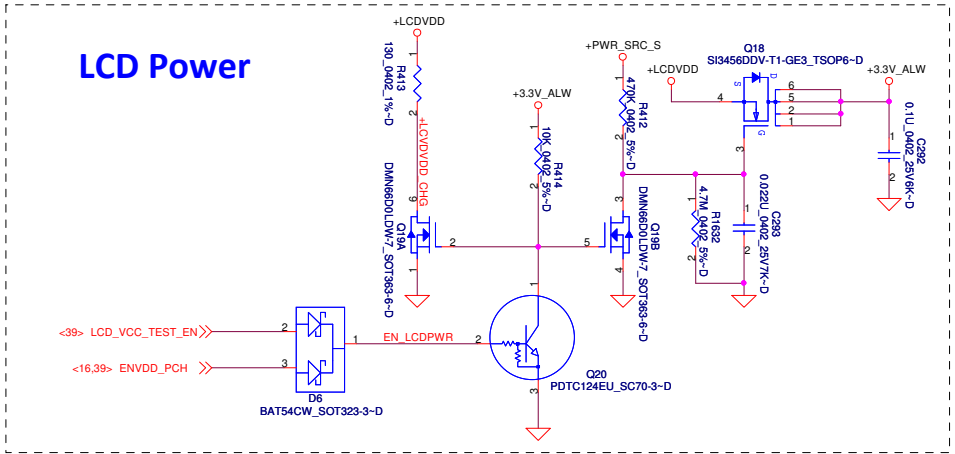
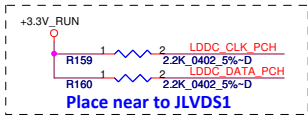
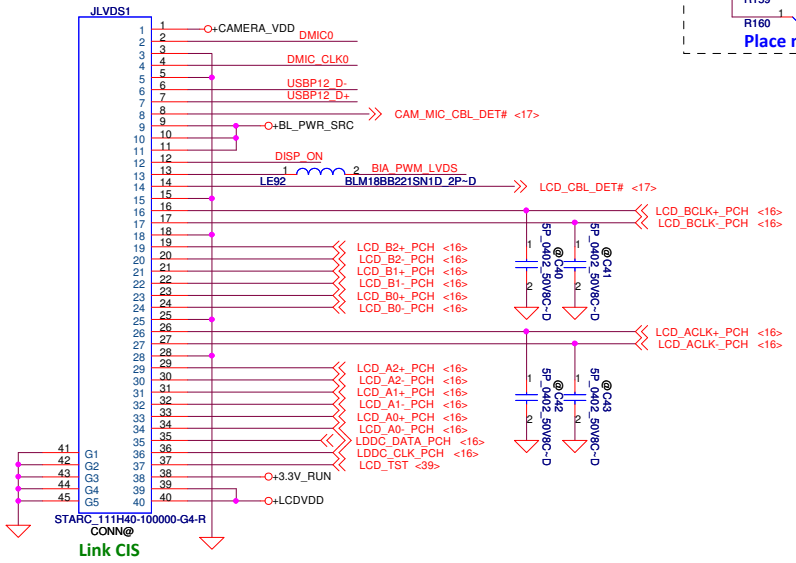
CRT/Video switch

LA-7901P

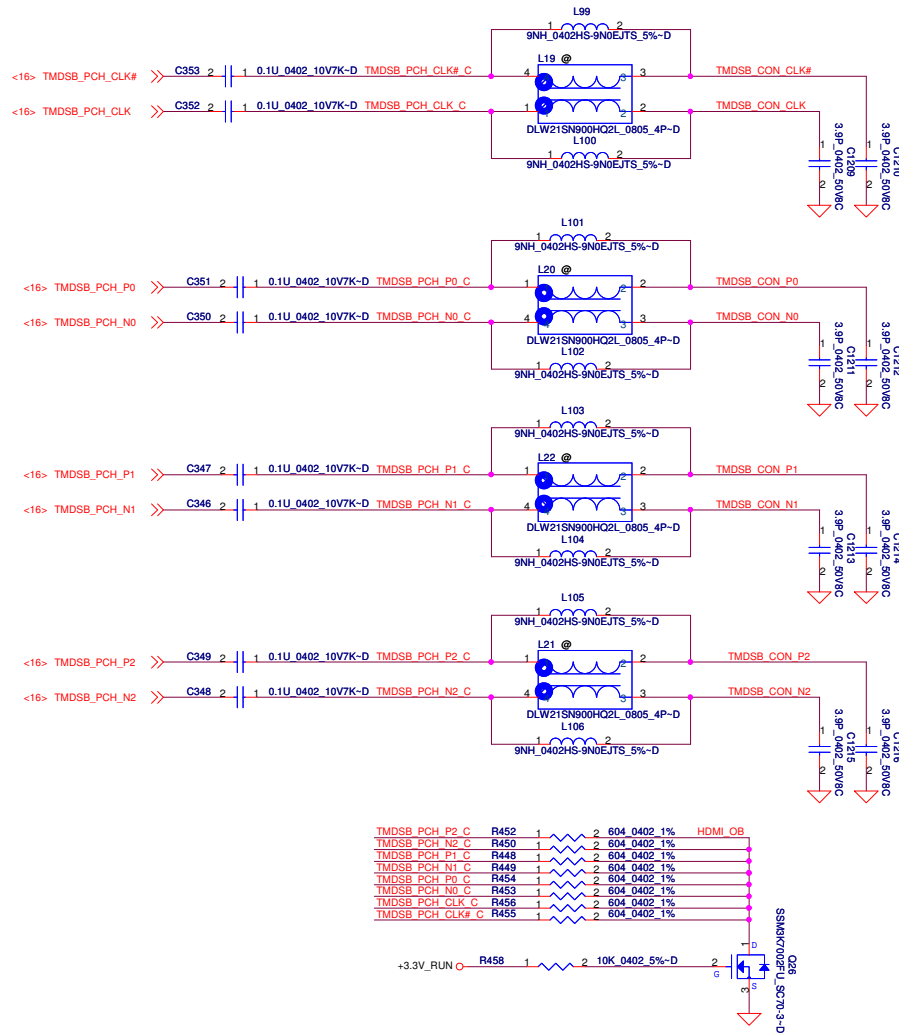
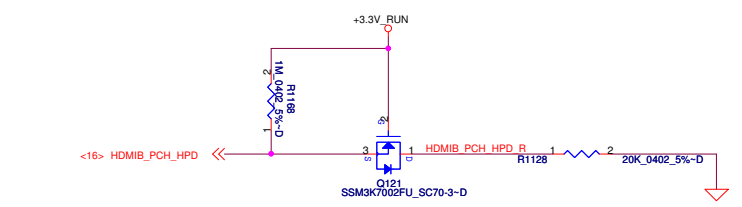
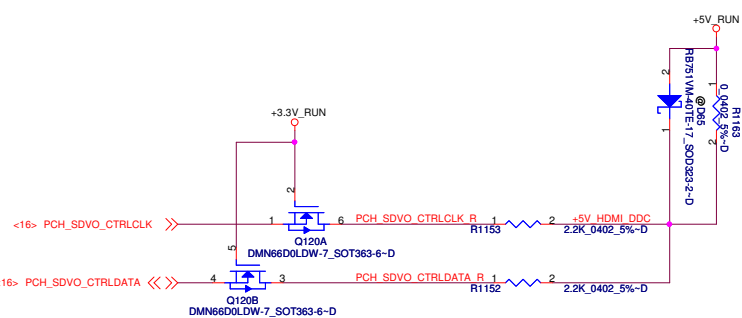
Date: Friday, March 02, 2012 Sheet 23 of 61

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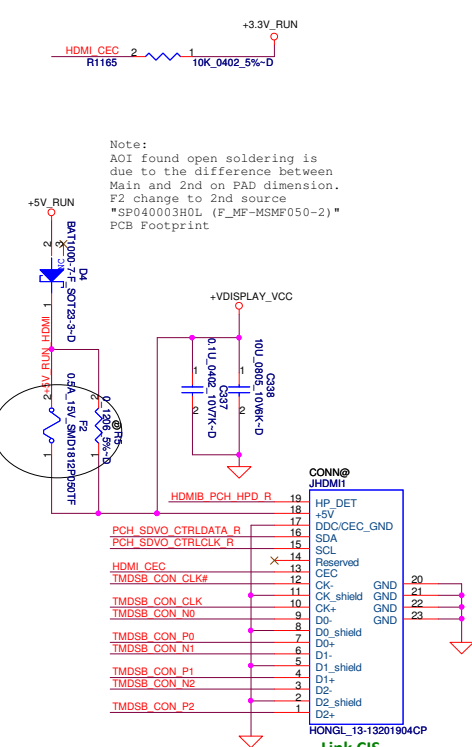




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Signal	Part	Value	Footprint	Notes
TMSB_PCH_P2_C	R452	1 604 0402 1%	HDMI_OB	
TMSB_PCH_N2_C	R450	1 604 0402 1%	HDMI_OB	
TMSB_PCH_P1_C	R448	1 604 0402 1%	HDMI_OB	
TMSB_PCH_N1_C	R449	1 604 0402 1%	HDMI_OB	
TMSB_PCH_P0_C	R454	1 604 0402 1%	HDMI_OB	
TMSB_PCH_N0_C	R453	1 604 0402 1%	HDMI_OB	
TMSB_PCH_CLK#_C	R456	1 604 0402 1%	HDMI_OB	
TMSB_PCH_CLK#_C	R455	1 604 0402 1%	HDMI_OB	



Note:
AOI found open soldering is due to the difference between Main and 2nd on PAD dimension. F2 change to 2nd source "SP040003H0L (F_MF-MSMF050-2)" PCB Footprint

HDMI 46@	
Part Number	Description
R0000002HM	HDMI W/Logo:R0000002HM

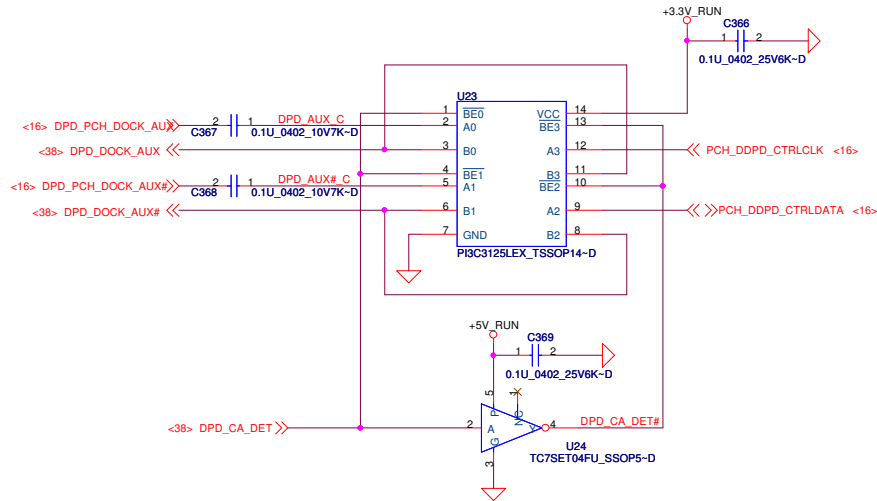
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HDMI port
LA-7901P

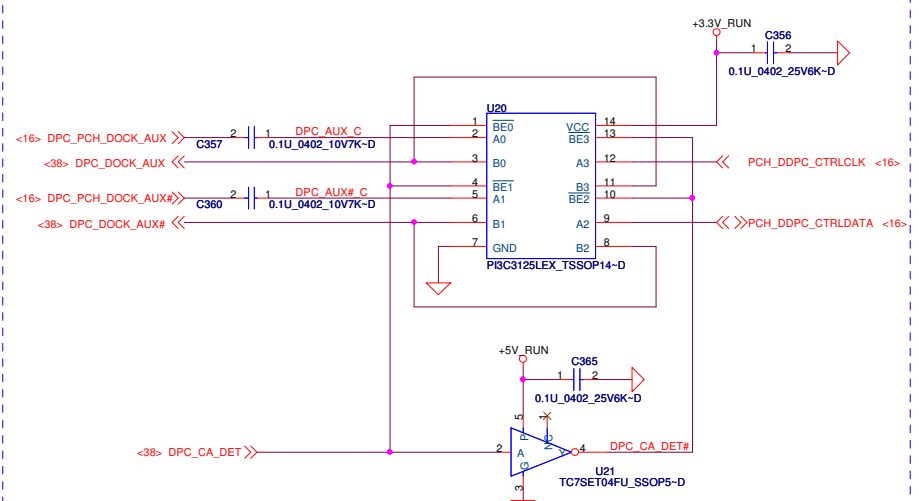
Title: _____
 Size: _____ Document Number: _____
 Date: Saturday, March 03, 2012 Sheet 25 of 81

AUX/DDC SW for DPD to E-DOCK

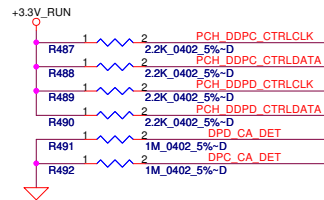


Note:When implement 2nd source, please check Vil and Vih spec is meet main source spec

AUX/DDC SW for DPC to E-DOCK



Note:When implement 2nd source, please check Vil and Vih spec is meet main source spec



Intel WW18 Strapping option

Intel WW18 Strapping option

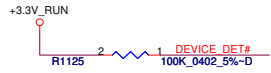
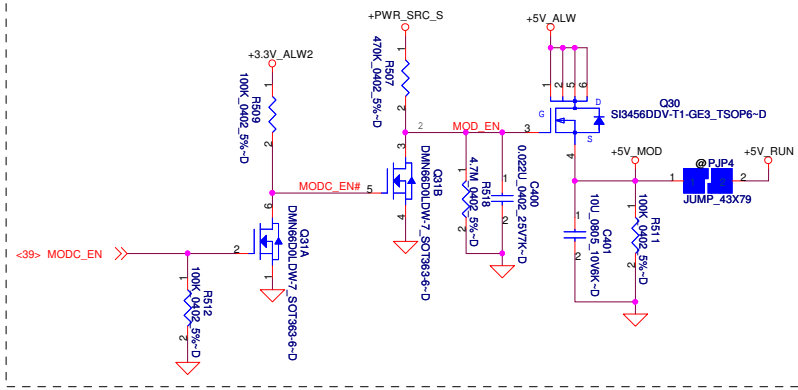
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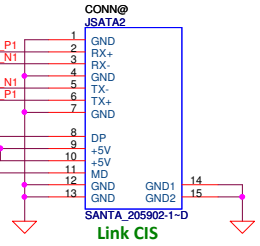
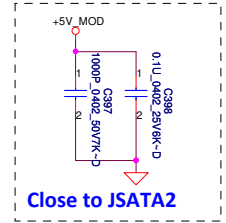
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Title DP SW		
Size	Document Number LA-7901P	Rev 1.0
Date: Friday, March 02, 2012	Sheet 26 of 61	

ODD power



- <14> SATA_ODD_PTX_DRX_P1_C << C407 2 1 0.01U 0402 16V7K-D SATA_ODD_PTX_DRX_P1
- <14> SATA_ODD_PTX_DRX_N1_C << C406 2 1 0.01U 0402 16V7K-D SATA_ODD_PTX_DRX_N1
- <14> SATA_ODD_PRX_DTX_N1_C << C405 2 1 0.01U 0402 16V7K-D SATA_ODD_PRX_DTX_N1
- <14> SATA_ODD_PRX_DTX_P1_C << C404 2 1 0.01U 0402 16V7K-D SATA_ODD_PRX_DTX_P1



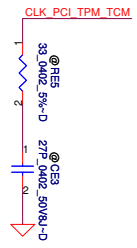
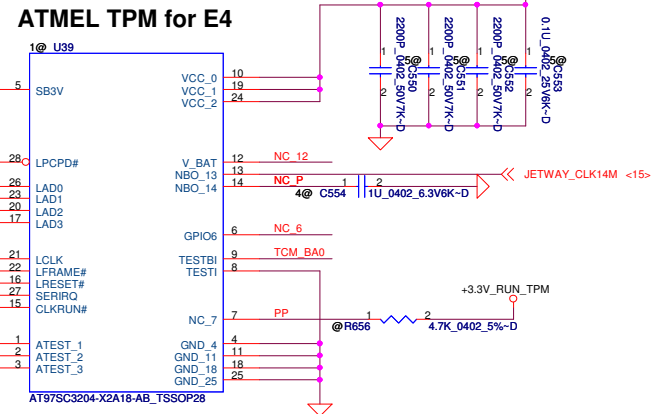
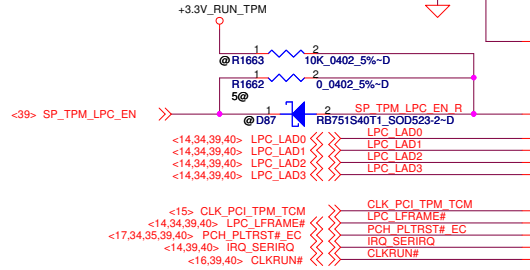
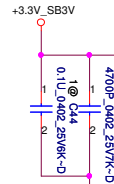
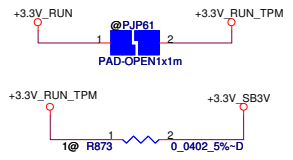
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Title			Rev		
ODD CONNECTOR			1.0		
Size	Document Number	Date		Sheet	of
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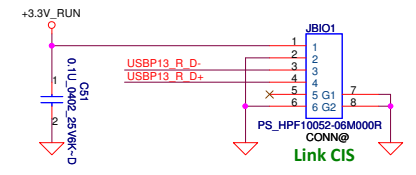
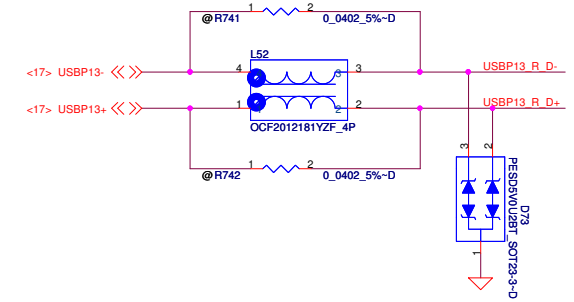
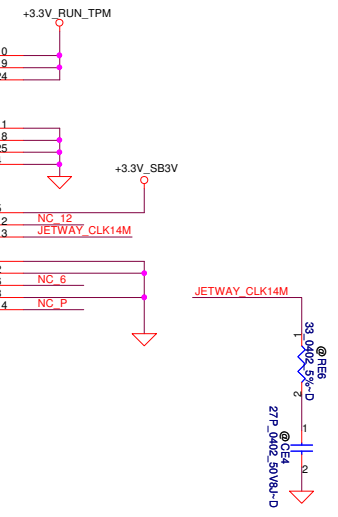
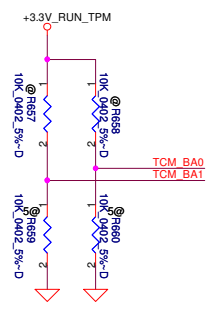
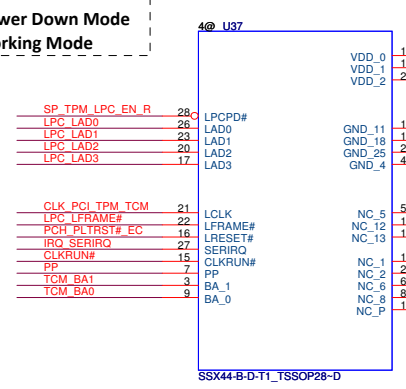
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Co-lay U37 and U39
LPC layout: Place TCM first and then end LPC with TPM.

China TCM: NationZ & Jetway co-lay

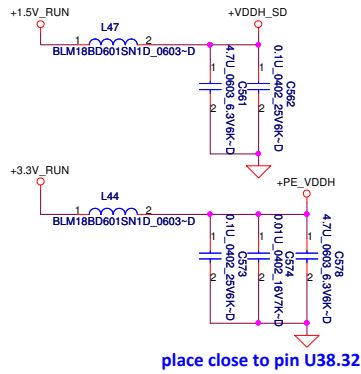
LOW: Power Down Mode
High: Working Mode



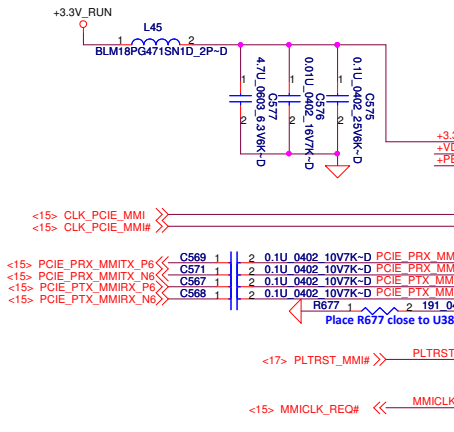
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TPM/TCM/BIO Conn			
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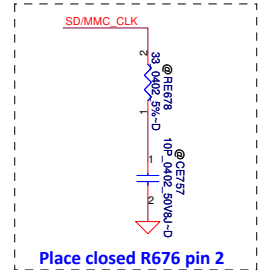
place close to pin U38.32



Place R677 close to U38

<17> PLTRST_MMI# <> PLTRST_MMI#

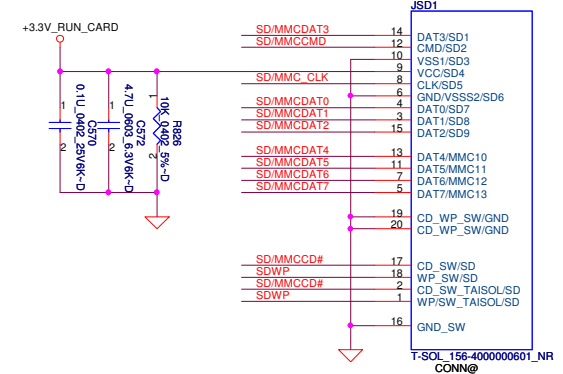
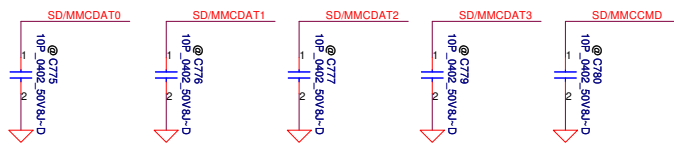
<15> MMICLK_REQ# <> MMICLK_REQ#



Place closed R676 pin 2

Note: The trace need to route as daisy-chain and the trace of SD signals need to route as short as possible

Vendor review in 6/22 and reserve for SD3.0 UHS-I 200MHz transfer



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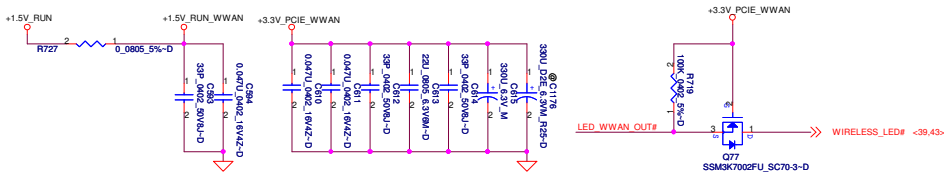
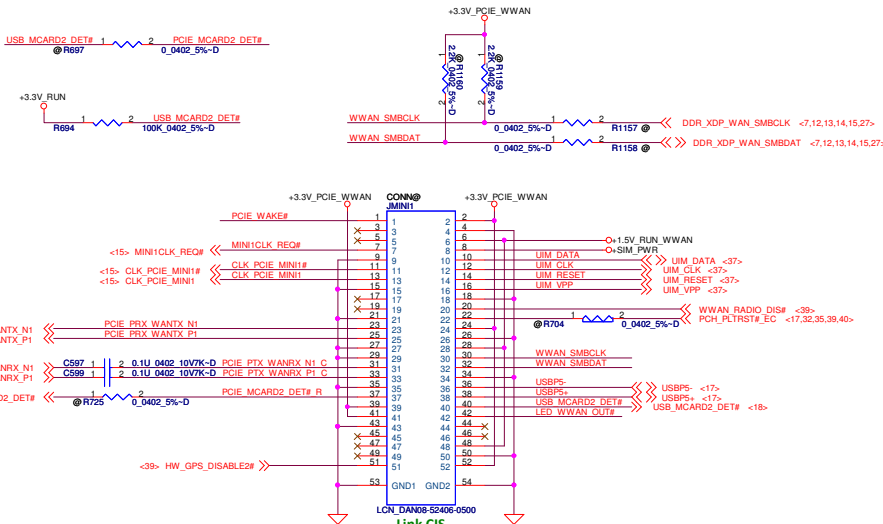
Card Reader OZ600FJ0

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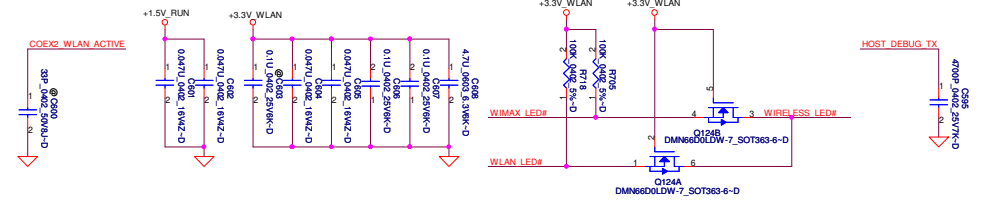
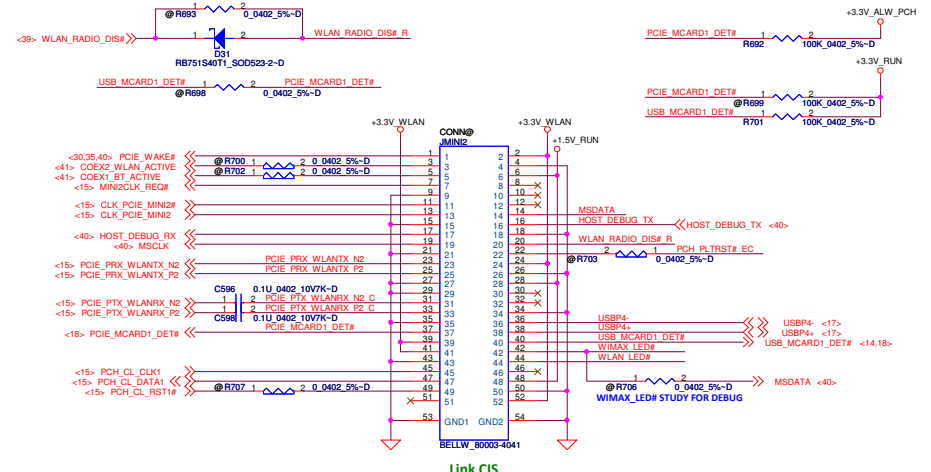
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Mini WWAN/GPS/LTE/UWB H=4

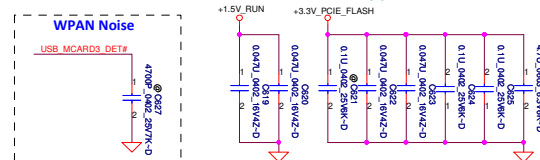
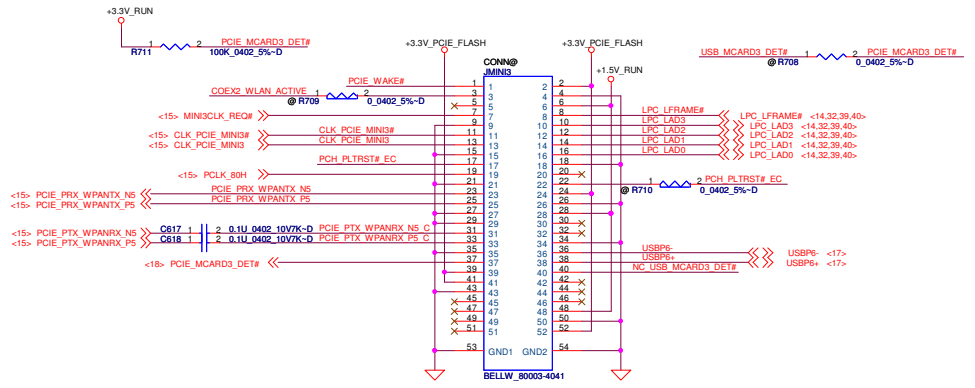


PWR Rail	Voltage Tolerance	Primary Power		Aux Power
		Peak	Normal	Normal
+3.3V	+9%	1000	750	
+3.3Vaux	+9%	330	250	250 (Wake enable) 5 (Not wake enable)
+1.5V	+5%	500	375	NA

Mini WLAN/WIMAX H=6.7

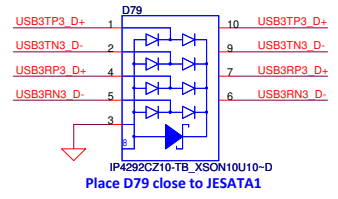
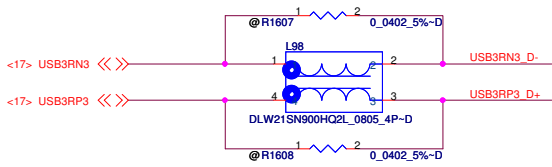
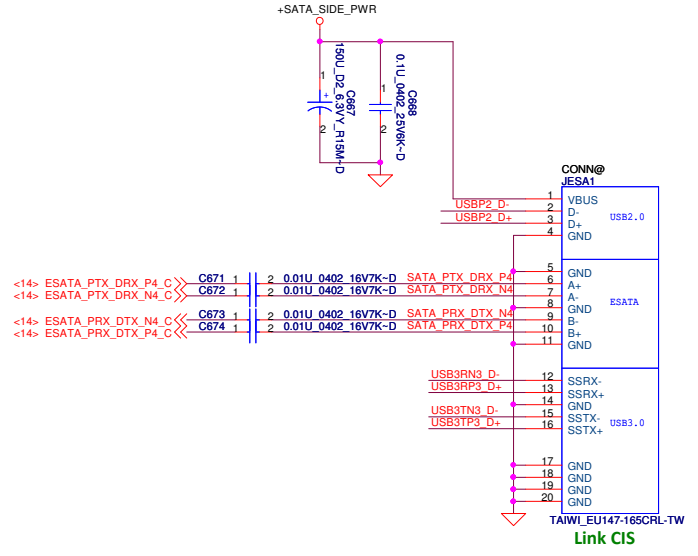
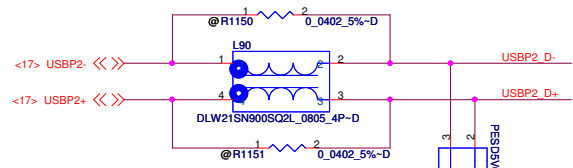
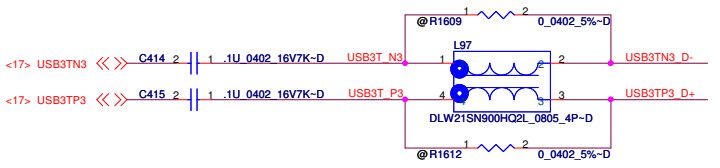
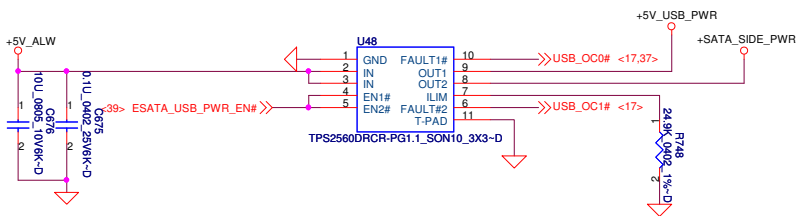
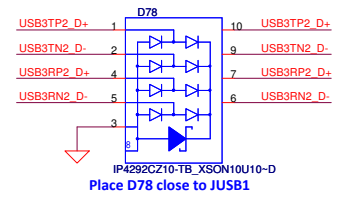
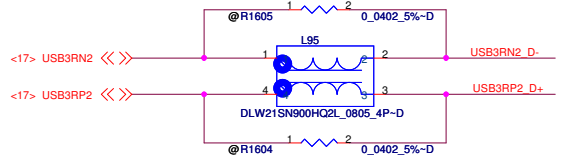
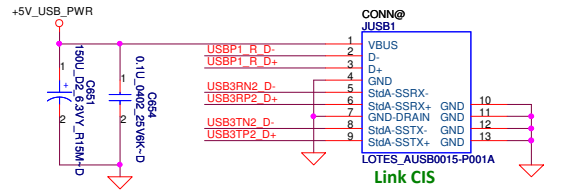
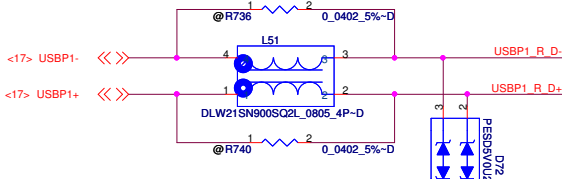
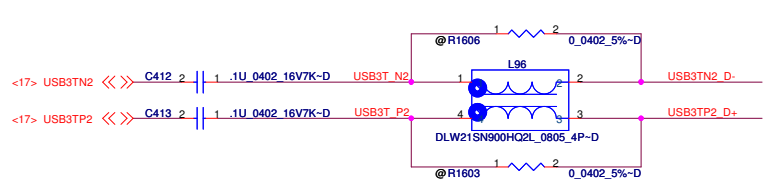


1/2 Minicard Pink Pather/60GHz Card H=6.7



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	Mini Card	
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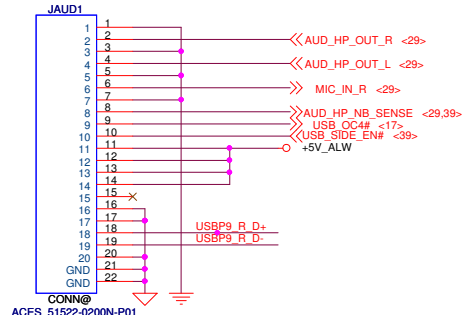
Compal Electronics, Inc.

USB2.0/3.0 ESATA

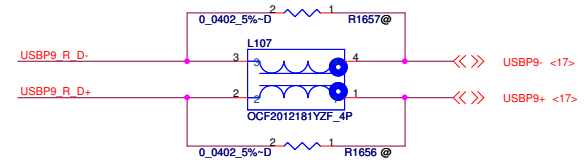
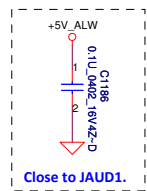
LA-7901P

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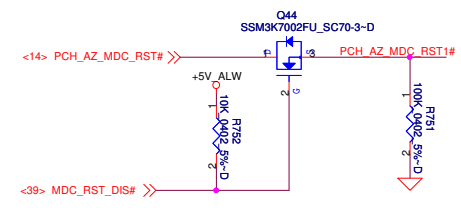
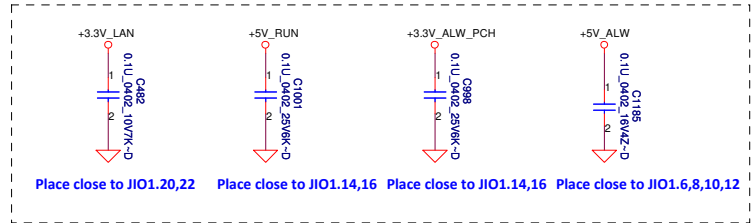
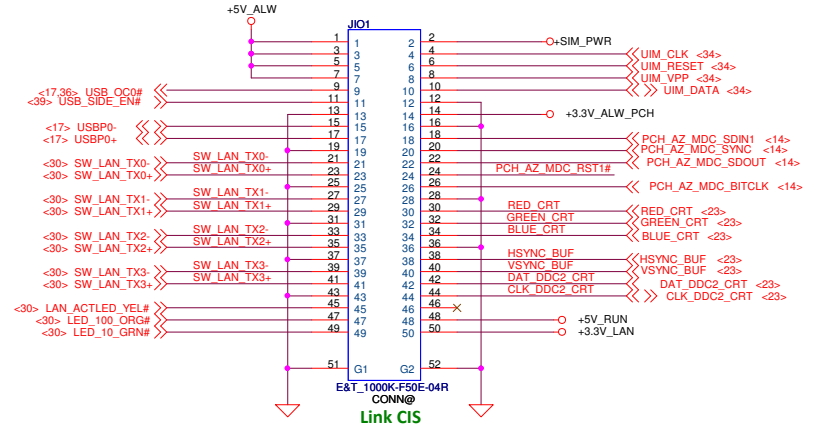
AUDIO BOARD



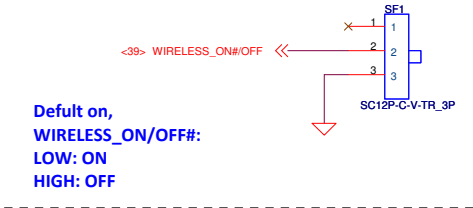
CONN@
ACES_51522-0200N-P01
Link CIS



IO BOARD



Sniffer Switch



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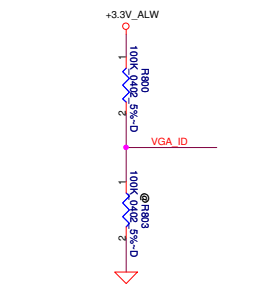
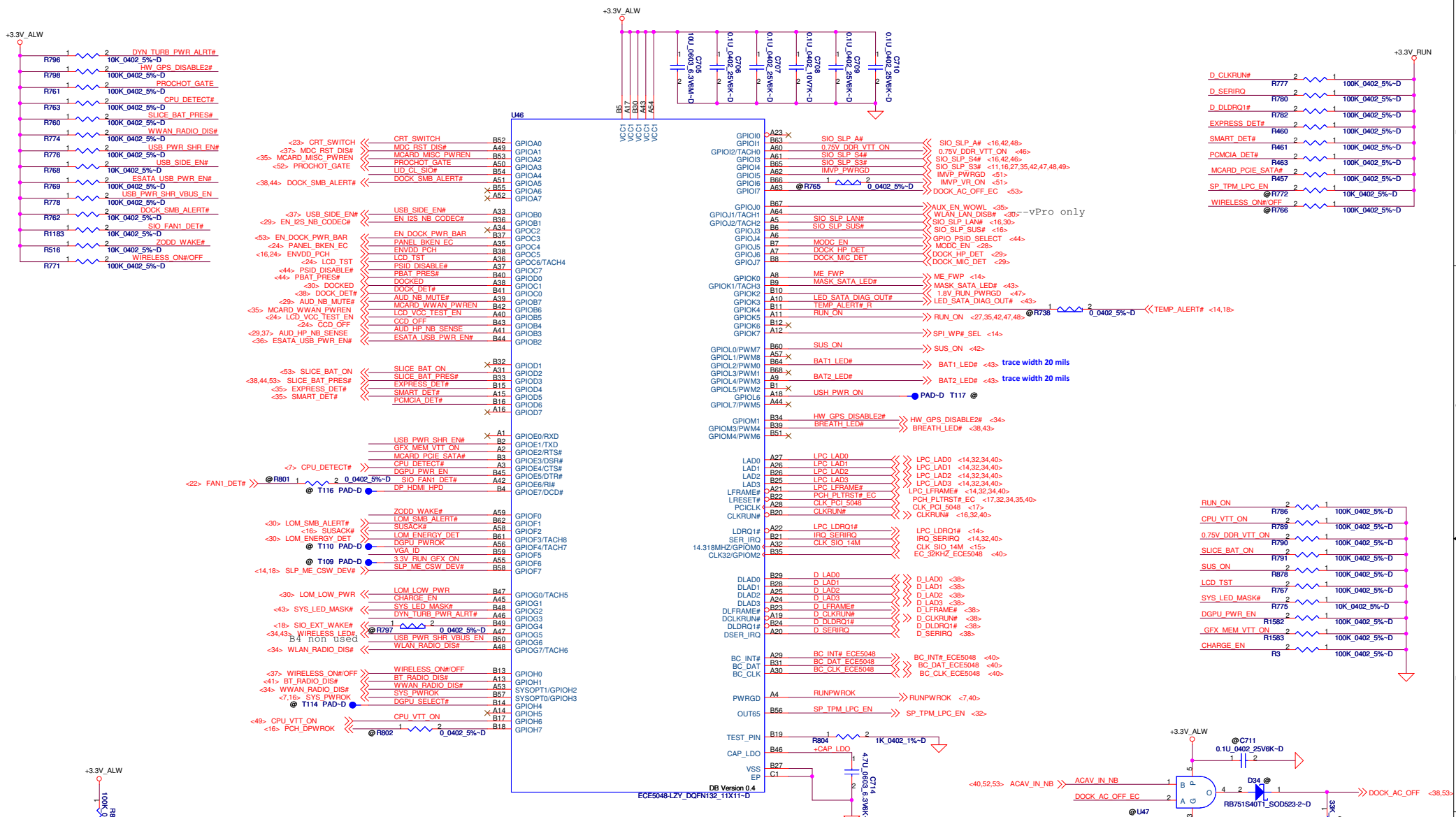
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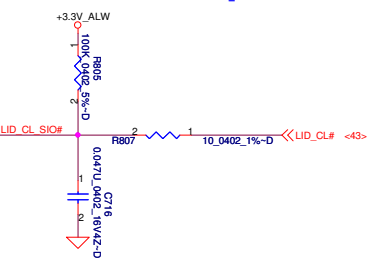
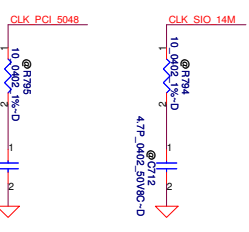
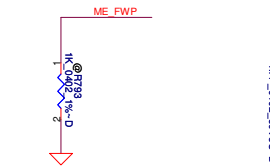
Title: IO/AUDIO/MEDIA/SNF

Size: Document Number: LA-7901P Rev: 1.0

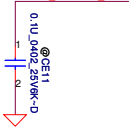
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ME_FWP PCH has internal 20K PD.
(suspend power rail)



Reserve for ESD in 6/22
Place closed U46



	VGA_ID0
Discrete	0
UMA	1

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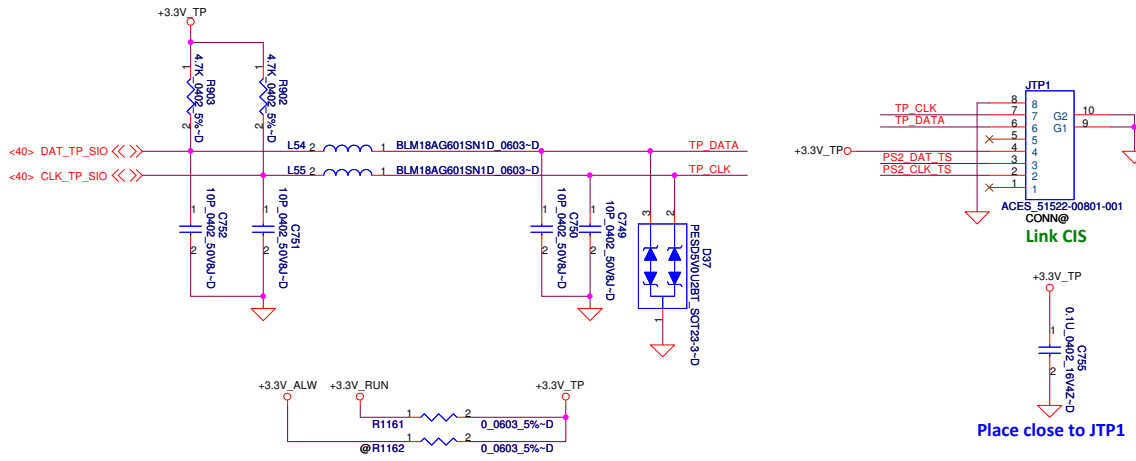


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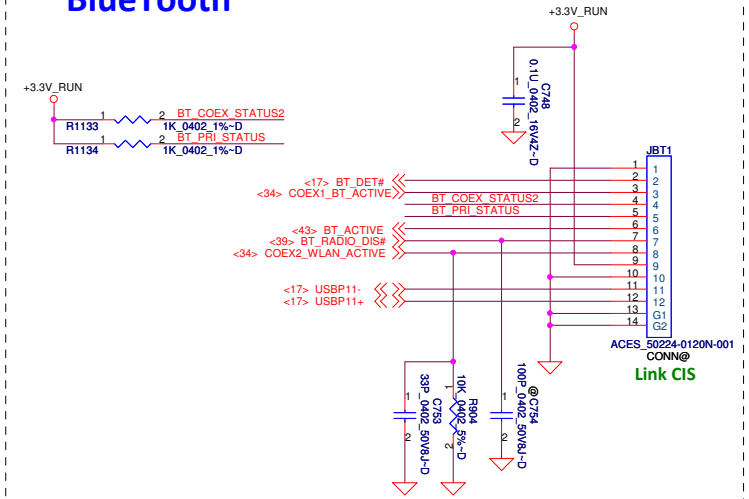
Compal Electronics, Inc.

Title		ECE5048	
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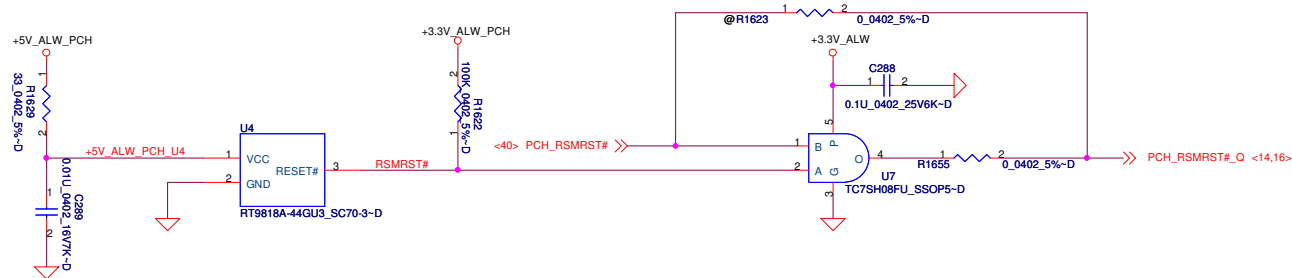
Touch Pad



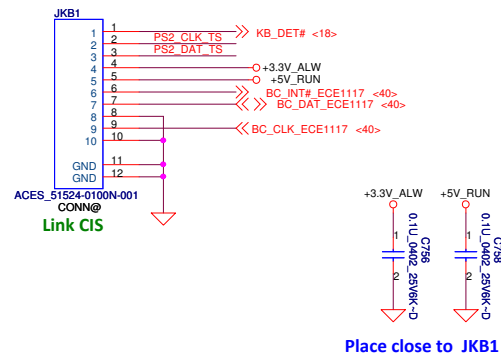
BlueTooth




RSMRST circuit



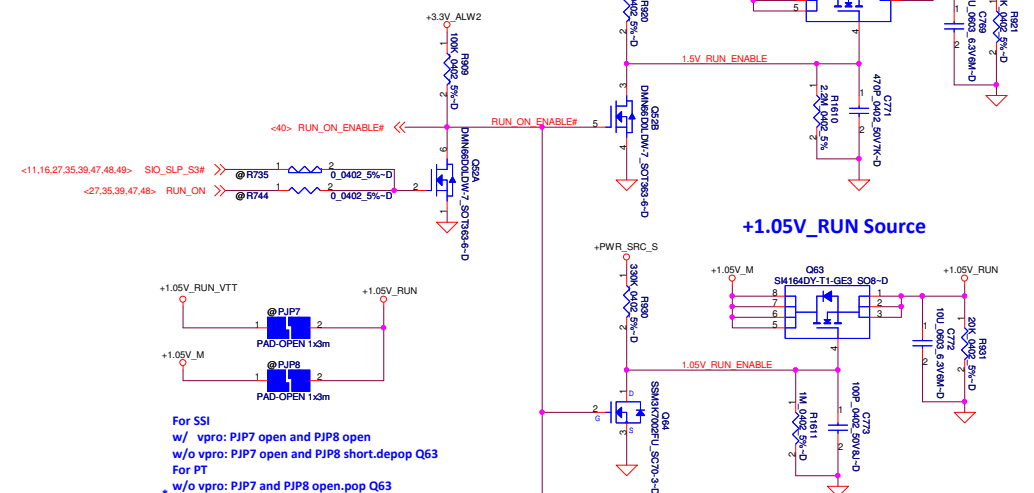
Keyboard

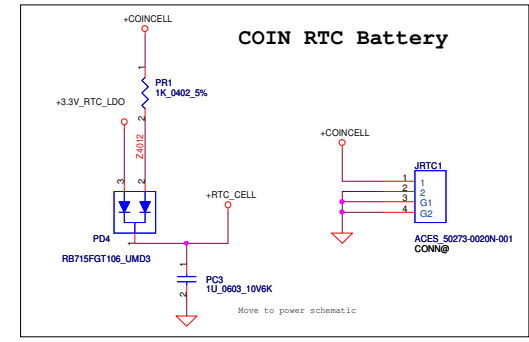
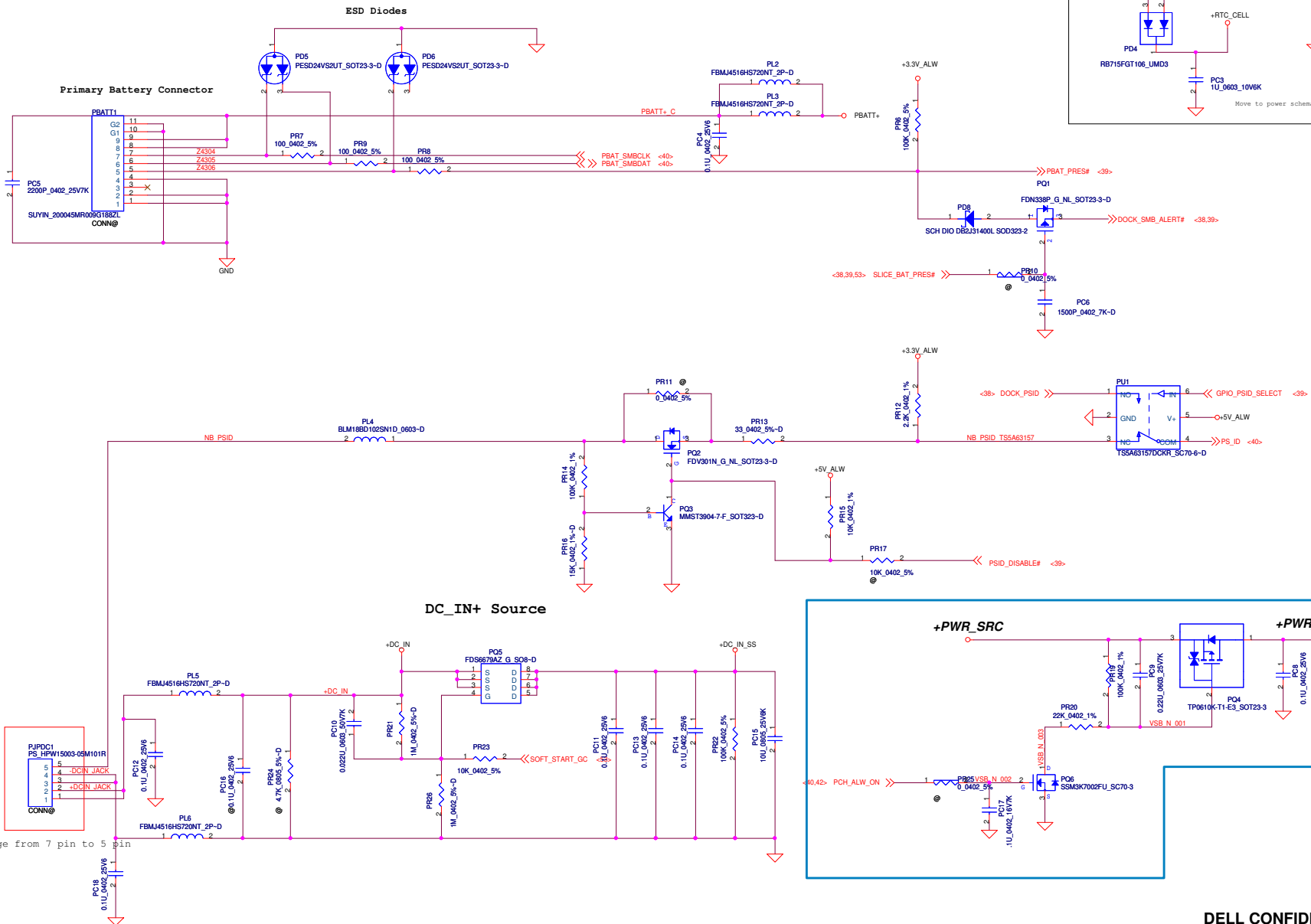


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		Int KB/TP/BT/RSMRST	
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DC/DC Interface



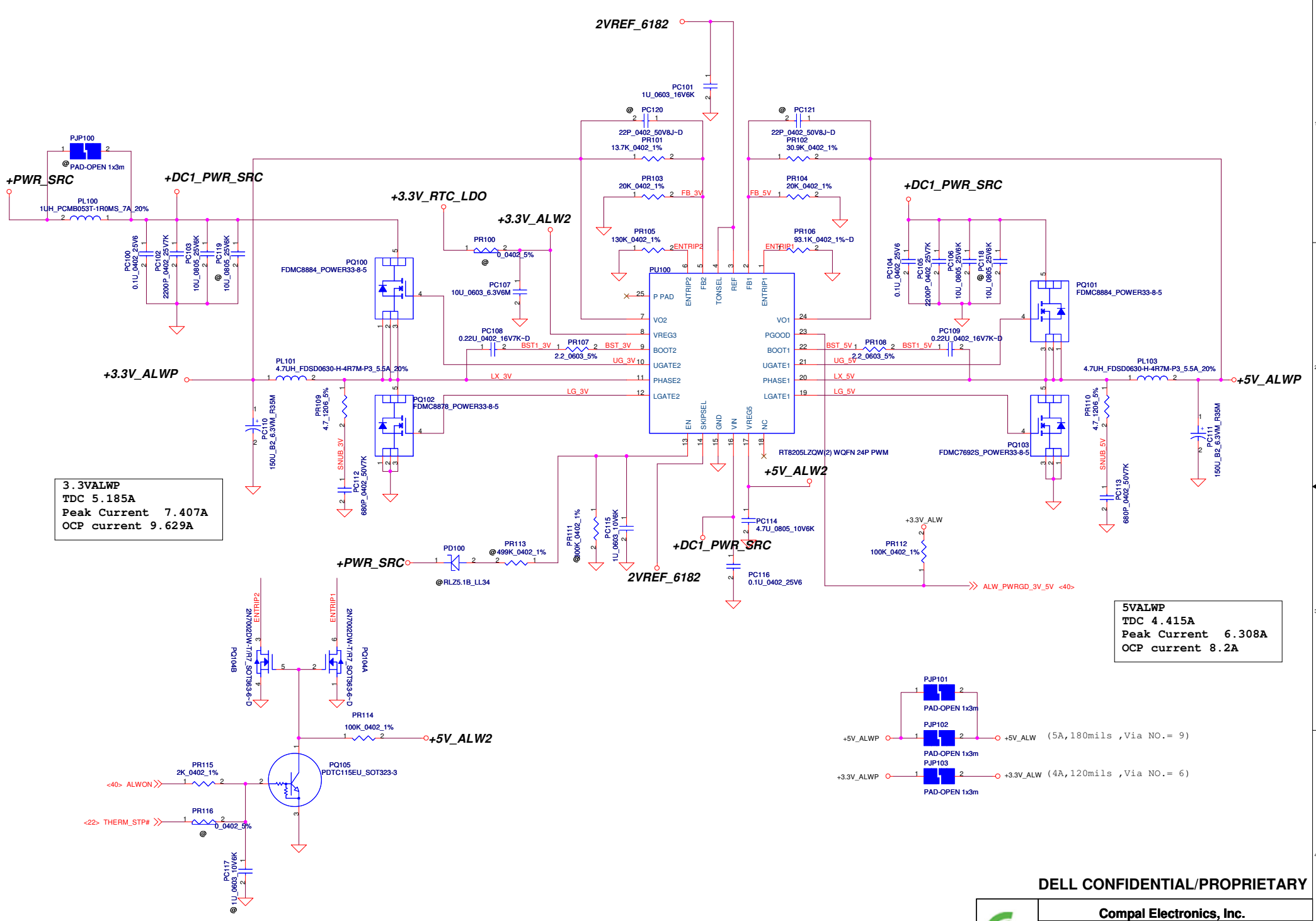


8/18 change from 7 pin to 5 pin

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3.3VALWP
 TDC 5.185A
 Peak Current 7.407A
 OCP current 9.629A

5VALWP
 TDC 4.415A
 Peak Current 6.308A
 OCP current 8.2A

+5V_ALWP (5A, 180mils, Via NO. = 9)
 +3.3V_ALWP (4A, 120mils, Via NO. = 6)

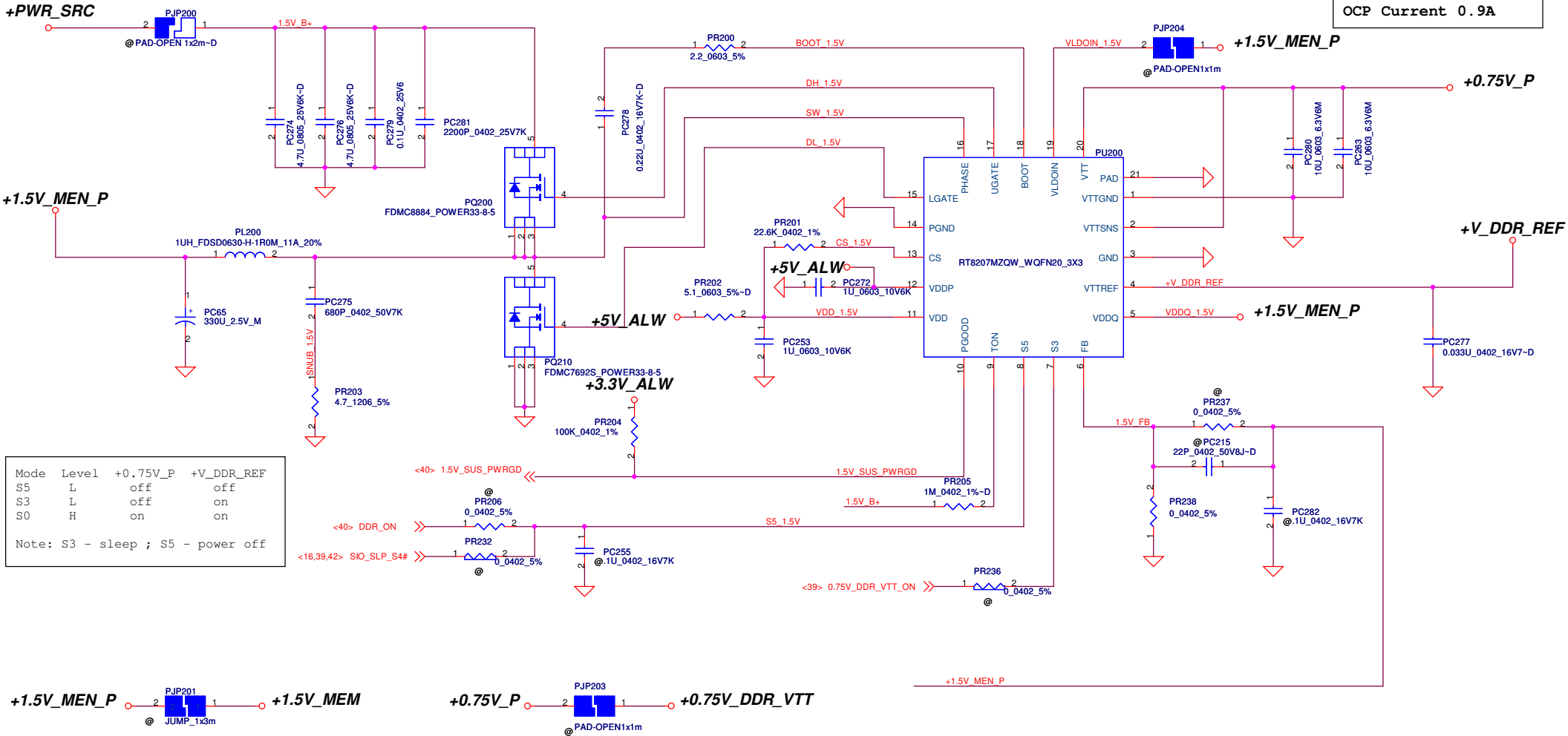
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+5V_ALW/3.3V_ALW		
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1.5Volt +/- 5%
 TDC 9.74A
 Peak Current 13.915A
 OCP current 16.698A

0.75Volt +/- 5%
 TDC 0.525A
 Peak Current 0.75A
 OCP Current 0.9A



Mode	Level	+0.75V_P	+V_DDR_REF
S5	L	off	off
S3	L	off	on
S0	H	on	on

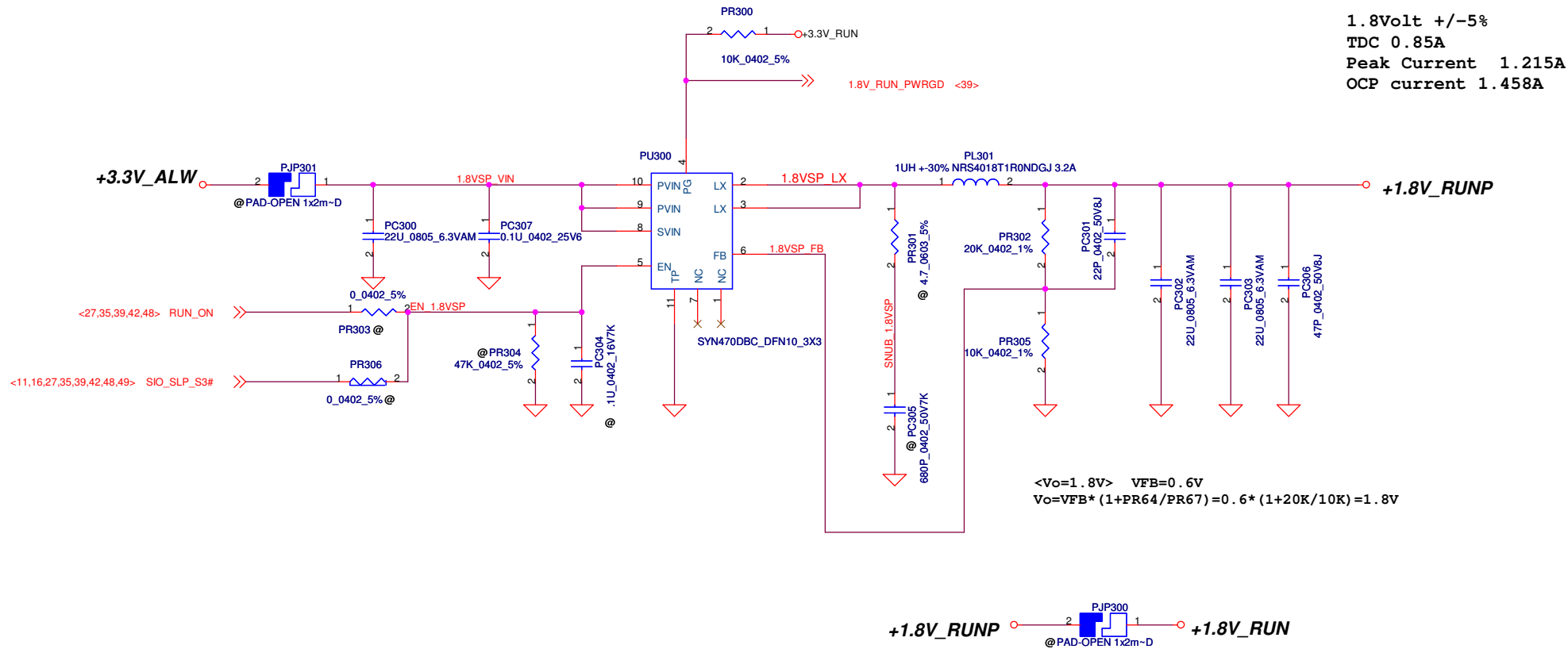
Note: S3 - sleep ; S5 - power off

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Title		+1.5V_MEN/+0.75V_DDR_VTT	
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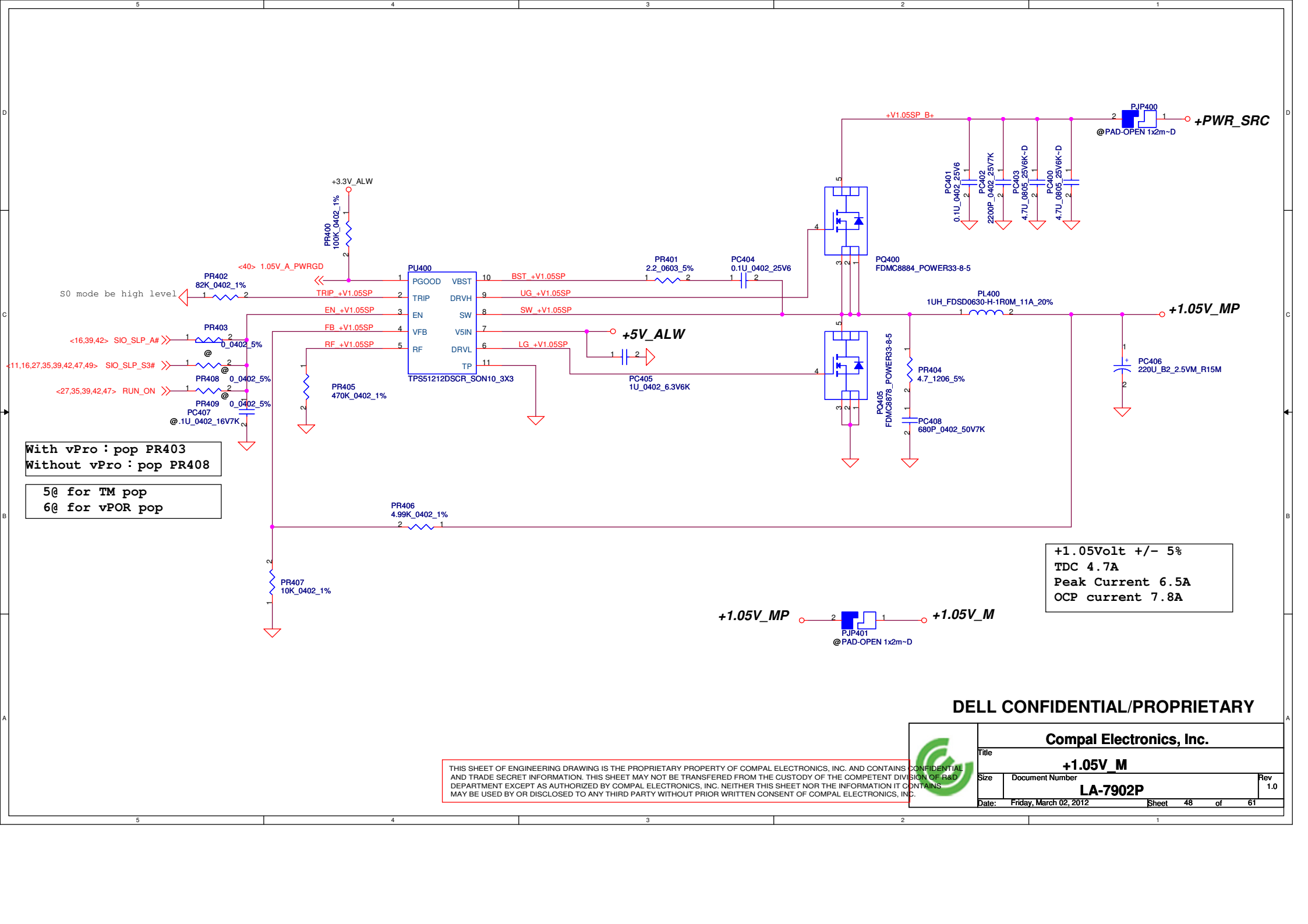
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Compal Electronics, Inc.		
+1.8V_RUN		
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With vPro : pop PR403
 Without vPro : pop PR408

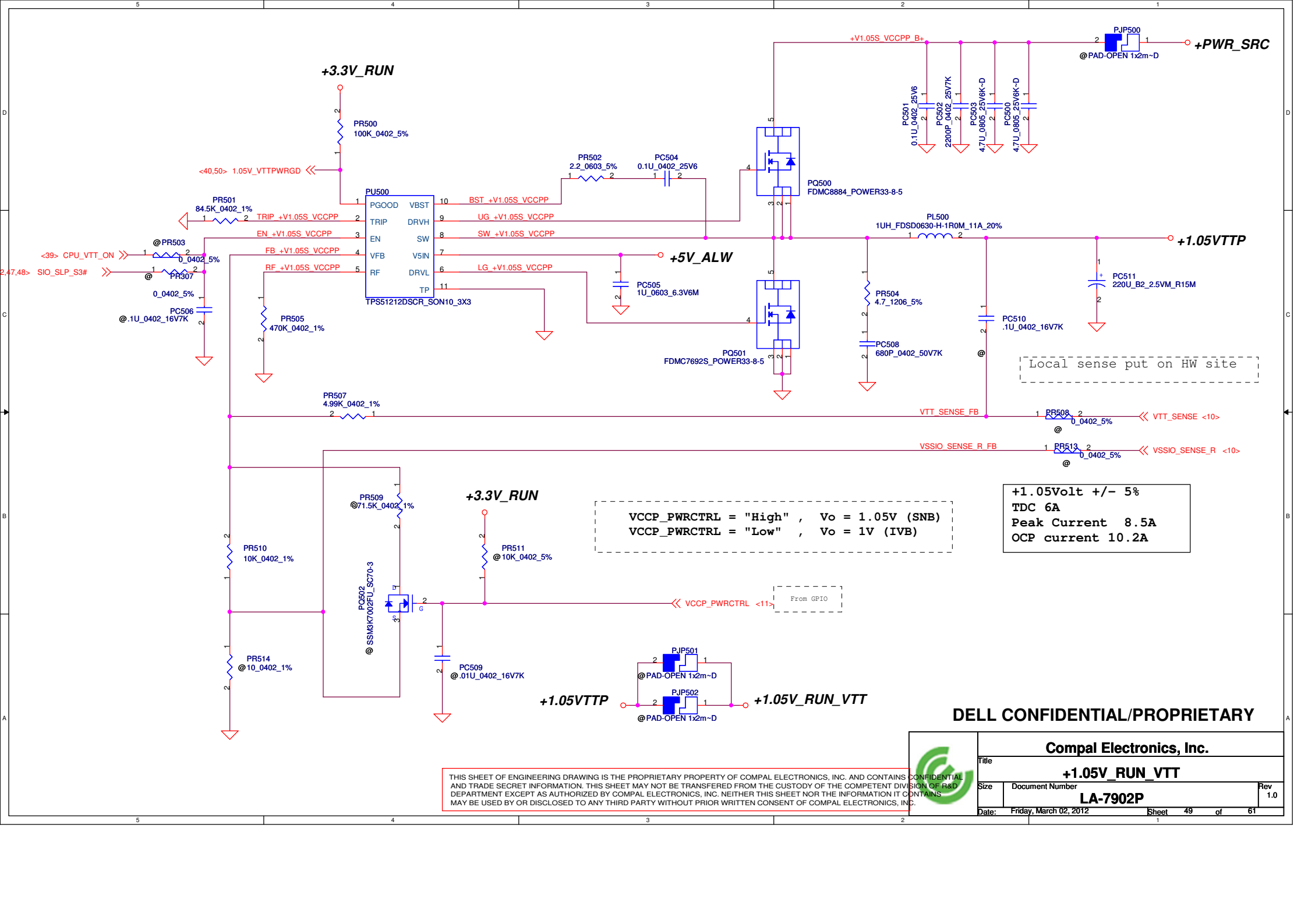
5@ for TM pop
 6@ for vPOR pop

+1.05Volt +/- 5%
TDC 4.7A
Peak Current 6.5A
OCP current 7.8A

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+1.05V_M			
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VCCP_PWRCtrl = "High" , Vo = 1.05V (SNB)
 VCCP_PWRCtrl = "Low" , Vo = 1V (IVB)

+1.05Volt +/- 5%
 TDC 6A
 Peak Current 8.5A
 OCP current 10.2A

Local sense put on HW site

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Title +1.05V_RUN_VTT		
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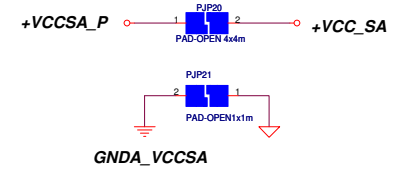
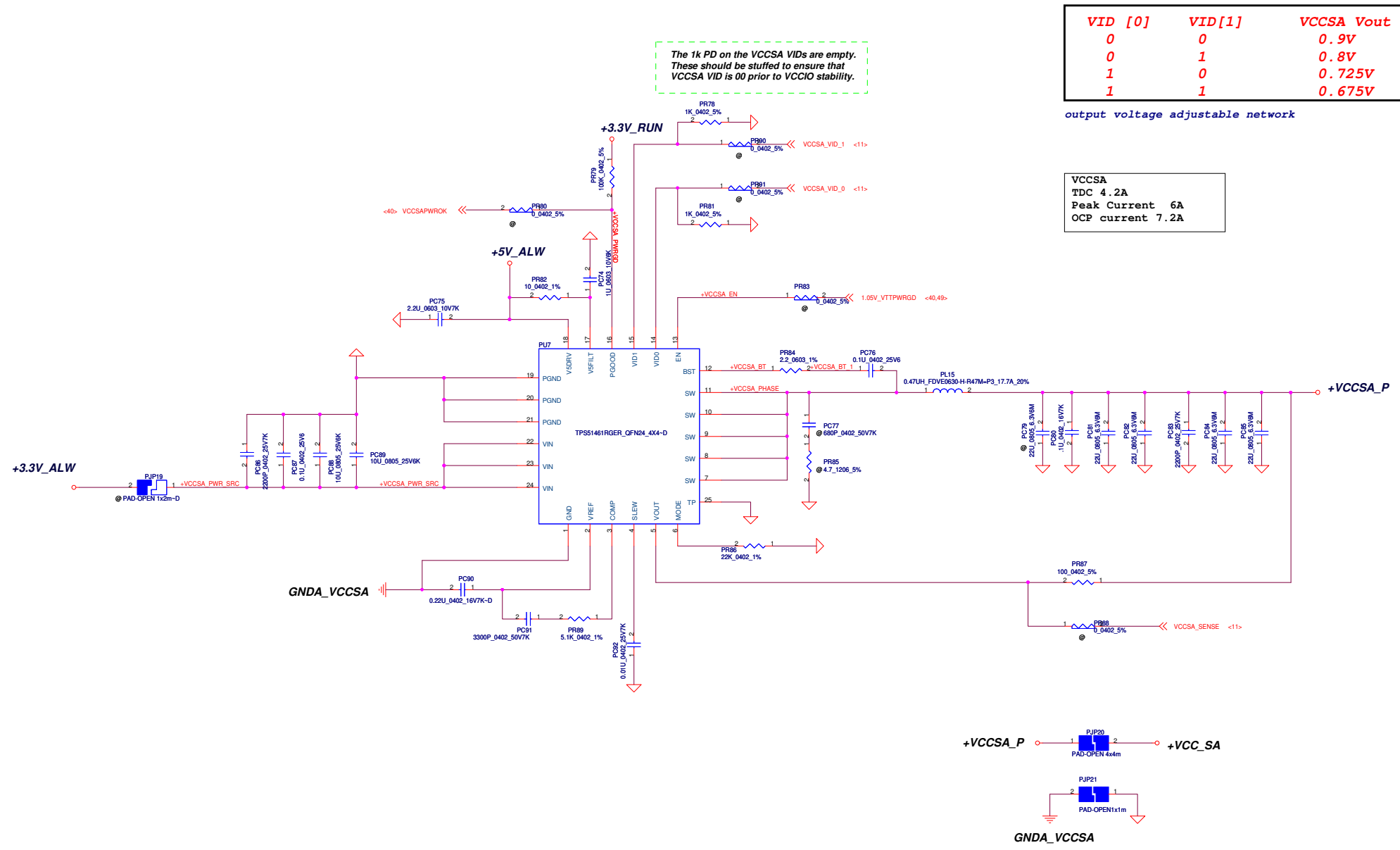


The 1k PD on the VCCSA VID's are empty.
These should be stuffed to ensure that
VCCSA VID is 00 prior to VCCIO stability.

VID [0]	VID[1]	VCCSA Vout
0	0	0.9V
0	1	0.8V
1	0	0.725V
1	1	0.675V

output voltage adjustable network

VCCSA
TDC 4.2A
Peak Current 6A
OCP current 7.2A



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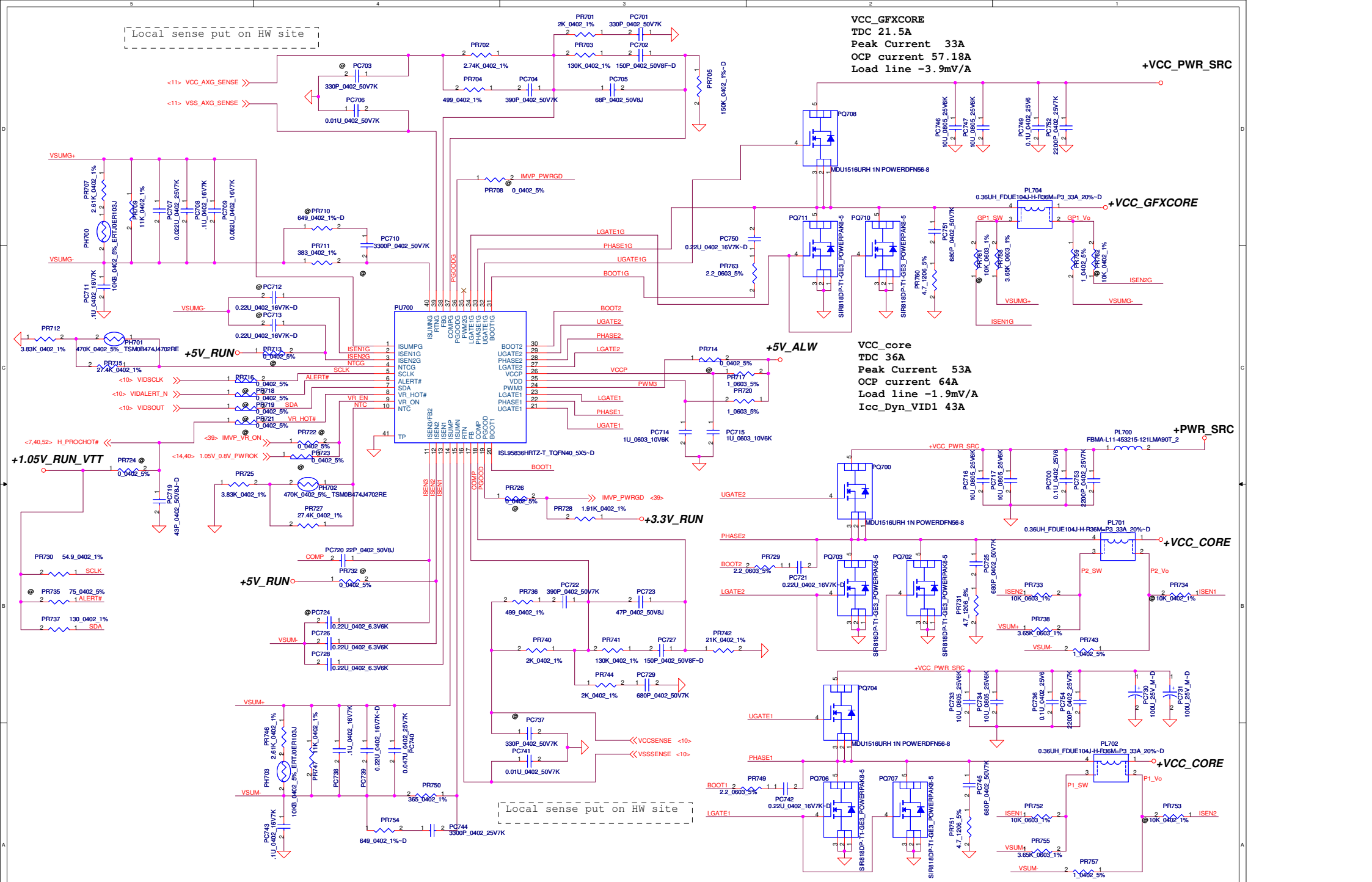
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+VCC SA

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VCC_GFXCORE
 TDC 21.5A
 Peak Current 33A
 OCP current 57.18A
 Load line -3.9mV/A

+VCC_PWR_SRC

VCC_core
 TDC 36A
 Peak Current 53A
 OCP current 64A
 Load line -1.9mV/A
 Icc_Dyn_VID1 43A

+VCC_GFXCORE

+PWR_SRC

+VCC_CORE

+VCC_CORE

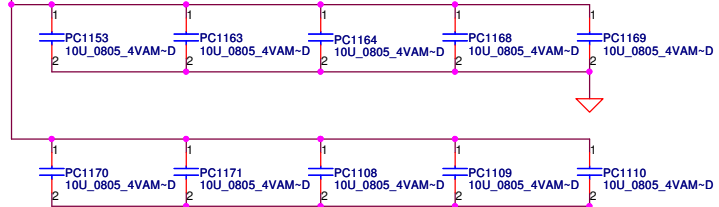
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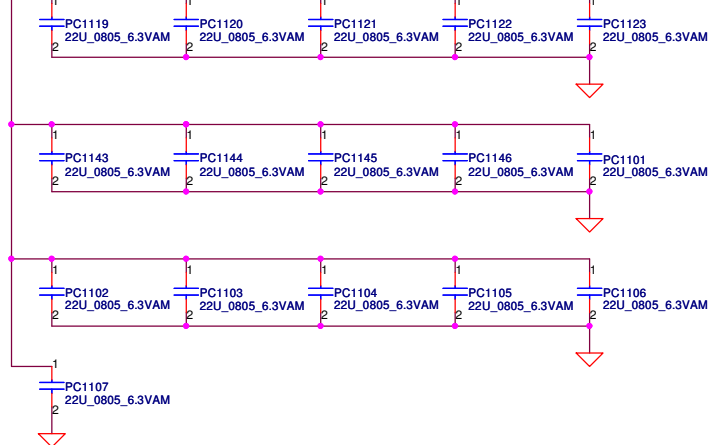


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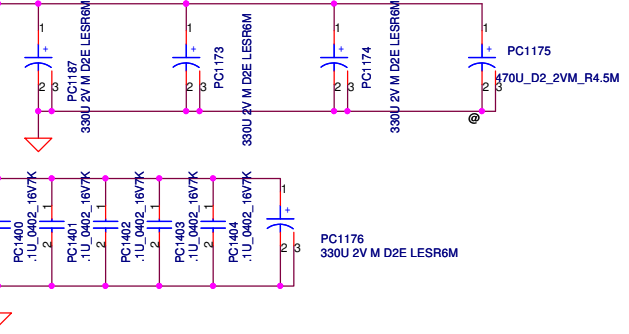
+VCC_CORE



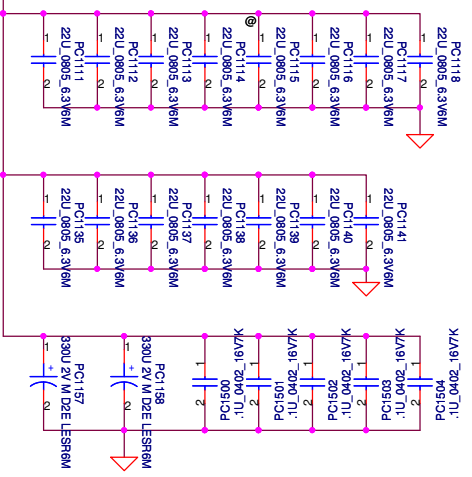
+VCC_CORE



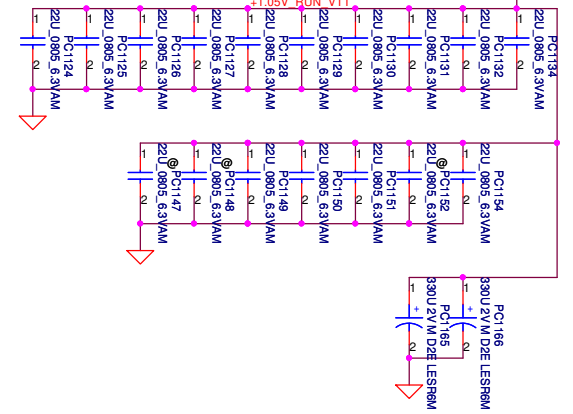
+VCC_CORE



+VCC_GFXCORE



+1.05V_RUN_VTT



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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	44	Power	8/18	Compal	ME design change.	PJPDC1 change from 7pin to 5pin	X01
2	45	Power	8/18	Compal	Main and 2nd IC common setting.	De-pop PD100,PR113,PR111	X01
3	45 46	Power	8/18	Compal	Prevent Jitter issue.	Add PC120,PC121,PC215 parallel with PR101,PR102,PR207	X01
4	51	Power	8/18	Compal	Prevent output voltage glitch when power up.	PU700 VCCP and VDD change form +5V_RUN to +5V_ALW	X01
5	53	Power	8/18	Dell	Change net name PBATT to SLICE_BAT_ON.	Change net name same as E4.	X01
6	50	Power	8/18	Compal	Reserve 0 ohm resistance for test.	Add PR90, PR91	X01
7	45 52	Power	8/30	Compal	For reduce EMI radiation.	Pop PL100, PL1300	X01
8	54	Power	8/30	Compal	Reserve cap for improve transient response.	Reserve PC1176	X01
9	53	Power	8/30	Compal	For reduce EMI radiation.	Add PC196, PC197, PC198, PC199, PC200	X01
10	54	Power	8/31	Compal	Change to green P/N.	Change PQ4, PC1153, PC1163, PC1164, PC1168, PC1169, PC1170, PC1171, PC1108, PC1109, PC1110, PC1187, PC1173, PC1174, PC1175, PC1157, PC1158, PC179, PQ1310, PQ1306 to green P/N	X01
11	48	Power	9/1	Dell	For support TL+TM	Change 6@ to pop for PC400~PC406, PC408, PL400, PQ400, PQ405, PR400~PR407, PU400. 5@ to @ for PR408.	X01
12	49	Power	9/1	Compal	For fix 1.05V_RUN_VTT on 1.05V	Depop PR509, PR511, PQ502. Change PR507 to 4.99k.	X01
13	51	Power	9/5	Compal	Follow EMI requirement.	Change PL700 to SM01000DJ00	X01
14	45 46	Power	9/6	Compal	Change to green P/N.	Change PC107, PC263, PC280, PC405, PC505 to HF P/N.	X01
15	52	Power	9/13	Compal	For reduce EMI radiation.	Pop PC1400~1404, PC1500~PC1504.	X01

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16	51	Power	9/14	Compal	Adjust CPU transient , loadline and OCP	Add PC740 to 0.1uF Change PR750 to 365 ohm Change PR741 to 130K ohm Change PC744 to 3300pF, PR754 to 649ohm.	X01
17	51	Power	9/14	Compal	Adjust AXG transient , loadline and OCP	Change PR703 to 130K ohm Change PC709 to 82nF Change PR702 to 2.74K ohm Change PR711 to 383 ohm	X01
18	52	Power	11/17	Compal	Shortage issue	Change PQ1303 from NTGD416 to AP2623	X02
19	52	Power	11/17	Compal	Need ESD protected	Change PQ1306, PQ1310 from SB57002040L to SB000009Q80	X02
20	53	Power	11/17	Compal	IC version upgrade	Change PU11 from CD3301 to CD3301A	X02
21	45	Power	11/17	Compal	Shortage issue	Change PC110, PC111 from SGA00004E00 to SGA00002N80	X02
22	45	Power	11/17	Compal	EMI request	Pop PC1138, PC1139, PC1149, PC1150	X02
23	44	Power	11/21	Compal	Erp lot6 tier2 Fail issue	PWR_SRC_S control signal change from +3.3V_ALW to PCH_ALW_ON	X02
24	44	Power	12/05	Compal	Prevent COS.	Change PD8 from SCS0340L01L to SCS00005C00 Change PD1301 from SCS00003M0L to SCS0000400L	X02
25	54	Power	12/13	Compal	Prevent COS.	Change PC1176, PC1174, PC1173, PC1187, PC1157, PC1158, PC1165, PC1166 to SGA00002U1L	X02
26	50	Power	12/13	Compal	Improve efficiency	change PR86 to 22K_0402_5%	X02
27	47	Power	12/16	Compal	Prevent COS.	Change PL301 from SH00000MN00 to SH00000MW00	X02

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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	11	HW	08/25/2011	COMPAL	INTEL review feedback	Add CC178,CC179,CC149,CC150	X01
2	14, 39	HW	08/25/2011	COMPAL	SMSC request to delete LPC_LDRQ0#	Leave LDRQ0# no connection on both of 5048 and PCH side Removed R743	X01
3	22	HW	08/25/2011	COMPAL	Removed reserve circuit for EMC4022	Removed R405,C280,R392,R394	X01
4	42	HW	08/25/2011	COMPAL	Load SW sources output rising time mismatch and COS. cost concern	Change back to E3 +3.3V/5V_RUN discrete solution Removed U78 and add Q55,Q61 circuit	X01
5	29	HW	08/25/2011	COMPAL	Codec is change to 92HD93	Pop R162~R166 and de-pop U73,R1540	X01
6	29	HW	08/25/2011	COMPAL	Reserve so lay with ALC290	Pop option for 92HD93/ALC290=>R1646/C1164; R1644/R1643; C965/R1642; Q107/R171 Reserve for ALC290 only: C1204, C1205, R1647, C1165, R1648 Reserve for 92HD93 only: R1645, C963 Add R174 depop and R175 pop	X01
7	20	HW	08/25/2011	COMPAL	Vgs less than cut-in voltage in battery mode	Add control circuit QH6,R279,CH107 for +5V_ALW_PCH	X01
8	27, 28	HW	08/25/2011	COMPAL	Vgs of 5V MOS maybe large than max rating	Add R517, R518	X01
9	11	HW	08/25/2011	COMPAL	Follow INTEL PDDG 0.8	De-pop RC140	X01
10	40	HW	08/25/2011	COMPAL	Change board ID to X01	Change R875 to 130Kohms	X01
11	34	HW	08/25/2011	COMPAL	PCH GPIO52 need 8.2~10K pull up +3.3VS	Change R695 from 100K to 10Kohms	X01
12	23	HW	08/25/2011	COMPAL	CRT SW 2nd source TI, TS3V713 pin29 is VDD	Connect U18 pin29 to +3.3V_RUN	X01
13	16	HW	08/25/2011	COMPAL	+1.05V_M turn off before APWROK de-assert	Add UH5,CH108 6@ circuit reserve for VPRO	X01
14	41	HW	08/25/2011	COMPAL	Reset IC threshold voltage issue	Change U4 to RT9801A (threshold adjustable) Add R1649~R1654;Reserve R1655 and pop R1623	X01
15	26	HW	08/25/2011	COMPAL	DPX_CA_DET voltage too low through dongle	Change U21 and U24 to SA000055G0L	X01
16	17, 18	HW	08/25/2011	COMPAL	Request from INTEL review feedback	Pop RH332 for PCH_GPIO3 and RH180 for GPIO27	X01
17	42	HW	08/25/2011	COMPAL	Material changed	Power team request Q59 change to SB00000L80L	X01
18	43	HW	08/25/2011	COMPAL	White light LED brightness is abnormal	Change R934, R938, R939, R949, R958, R957 and R959 to 1.2 Kohms	X01
19	40	HW	08/25/2011	COMPAL	Reserve C1208 for ESD backup plan	Reserve C1208 for ESD backup plan	X01
20	11	HW	08/25/2011	COMPAL	S3 can't resume issue	Control 1.5V_VDDQ by EC. Pop RC79 and de-pop RC82	X01
21	17	HW	08/25/2011	COMPAL	INTEL review feedback	Change RH331,RH272 to 10K ohm	X01

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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
22	34	HW	08/25/2011	COMPAL	WWAN card request	JMINI1 pin 1 connect to PCIE_WAKE#	X01
23	14	HW	08/25/2011	COMPAL	ROM size changed	Change U52 to 8M and R936,R895,R897,R900 to 6@	X01
24	11	HW	08/25/2011	COMPAL	Material package changed	Change CC161~CC166 from 0402 to 0603	X01
25	42	HW	08/25/2011	COMPAL	BOM changed	Change Q60 to 6@	X01
26	39	HW	08/25/2011	COMPAL	GPIO signal name changed same as E/P	Change PBATT_OFF to SLICE_BAT_ON	X01
27	34	HW	08/25/2011	COMPAL	Material package changed	Changed C615 to SF000002000	X01
28	30	HW	08/26/2011	COMPAL	LAN EA result	Changed RL23 to 1.2K Ohm.	X01
29	40	HW	08/26/2011	COMPAL	Backdrive issue	Depop R1169,R1197,R118 due to it has internal pull high.	X01
30	37	HW	08/29/2011	COMPAL	To avoid power short to GND	NC Pin 15 for JAUD1	X01
31	37	HW	08/30/2011	COMPAL	Follow connector list	Swap JAUD1 pin.	X01
32	12	HW	08/30/2011	COMPAL	Change part to HF part	Change QD1, QD2 part number to SB501380050 (for HF)	X01
33	15	HW	09/01/2011	COMPAL	For clock EA	Change RH311 and RH314 to 10 ohm	X01
34	43	HW	09/01/2011	COMPAL	ME drawing update	Add H19	X01
35	14, 16 19, 22 30, 40 42	HW	09/01/2011	COMPAL	BOM option change for TL	Change U53,R936,R895,R897,R900,RH350,UH5,CH108,RH116 RH202,R385,R426,R402,Q63,R931,Q58,Q60 R916,RL46,R871 to pop Change RH359,RH321,RH119,RH204,R430,R386,R408 ,R206,RL47,R877,to depop	X01
36	25	HW	09/02/2011	COMPAL	Due to EMI HDMI test Fail, add EMI solution	Change resistor to Inductor Change R451, R459, R462, R466, R468, R469, R470, R471 to 9nH L99, L100, L101, L102, L103, L104, L105, L106. Add C1209, C1210, C1211, C1212, C1213, C1214, C1215 and C1216 between Inductor and HDMI connector	X01
37	37	HW	09/05/2011	COMPAL	ME connector list change	Change JAU1 to 50271-0020N-001	X01
38	37	HW	09/06/2011	COMPAL	EMI issue	Add L107 & R1656,R1657	X01
39	15, 30	HW	09/06/2011	COMPAL	Follow LL to reserve SM bus for BRCM LAN	Add QH8,RL50,RL51	X01
40	36	HW	09/08/2011	COMPAL	Follow Intel design guide	Change C412~C415 to 0.1uF for USB3.0 signal	X01
41	7	HW	09/08/2011	COMPAL	Follow ESD recommand.	Reserve CC1141~CC144 for ESD	X01
42	14, 15, 40	HW	09/08/2011	COMPAL	Crystal EA result	Change CH2,CH3 to 18pF Change C741,C743 to 39pF Change CH18,CH19 to 10pF Change CL5,CL6=33pF,RL22=510 ohm	X01

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43	29	HW	09/13/2011	COMPAL	Follow IDT recomment	Swap R169~R172,C973~C976 connection	X01
44	42	HW	09/13/2011	COMPAL	Change Q55,Q61 part for open soldering issue.	Change Q55,Q61 from DMN3030LSS-13 to AO4478L	X01
45	40	HW	10/13/2011	COMPAL	Change board ID to X02	Change R875 to 62Kohm	X02
46	42	HW	10/13/2011	COMPAL	Rated Vgs of Q61 is 25V	De-pop R1627	X02
47	39	HW	10/13/2011	COMPAL	SMSC change 5048 pin A23 to GPIOIO	Re-link ECE 5048 symbol	X02
48	40	HW	10/13/2011	COMPAL	SMSC review feedback	Reserve R1658 and R1659 100Kohms to GND for I2S disabled	X02
49	41	HW	10/13/2011	COMPAL	Change reset IC to RT9818A-44GU3	Update U4 symbol and add R1629 for backup of inrush prevention. Change RSMRST# pull up with 100Koms. Pop R1655 and de-pop R1623.Delete R1649~R1654	X02
50	39	HW	10/13/2011	COMPAL	When suspend/resume cycles, wireless SW GPIO IRQs keeps giving	Change pull up rail to +3.3V_ALW for WIRELESS_ON#/OFF	X02
51	29	HW	10/13/2011	COMPAL	15" UMA speaker no sound issue	Pop snubber on speaker trace with C: 2200pF and R: 3.3ohms. Change bead rated current from 200mA to 2A.	X02
52	29	HW	10/13/2011	COMPAL	EMI request	Pop C981,C982,C983,C985,C986,C987	X02
53	27	HW	10/13/2011	COMPAL	Depop HDD control power circuit for cost down.	Depop R1624,Q28,R500,R499,R617,C393	X02
54	30	HW	10/18/2011	COMPAL	Crystal EA result	Change YL1 to 3G025000FA1H, CL5,CL6 to 12pF.RL22 to 200 ohm.	X02
55	All	HW	10/18/2011	COMPAL	For cost saving.	Change 0 ohm to R-short.	X02
56	42	HW	10/26/2011	COMPAL	1V leakage on 3.3V_RUN during system boot	Pop Q69 and R929	X02
57	42	HW	10/26/2011	COMPAL	Inrush current with Smart Card detect fail issue	change C763 and C766 to 2200p	X02
58	43	HW	10/27/2011	COMPAL	LED Conn PIN definition change	JLED1 PIN define change	X02
59	37	HW	10/27/2011	COMPAL	Remove 2pin connector for Audio performance	Remove JAG1 2 pin connector.	X02
60	42	HW	11/01/2011	COMPAL	Change MOSFET to without Schottky Diode for +1.5V_RUN leakage issue	change QC3, Q59 as AO4304L from AO4728L	X02
61	14	HW	11/15/2011	COMPAL	RTC issue	change CH2, CH3 to 15pF	X02
62	14	HW	11/15/2011	COMPAL	S5 power consumption over spec.	depop RH288	X02

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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
63	34	HW	11/29/2011	COMPAL	S3 had leakage in +3/5V_RUN	De-pop R725, remove R695 and add +3.3V_RUN pull high at PCH side(RH361)	X02
64	32	HW	11/29/2011	COMPAL	TPM is changed to AT97SC3204-X2A18-AB	U39(TPM) is changed to SA00004WQ10 (AT97SC3204-X2A18-AB) for WIN8 support	X02
65	32	HW	11/29/2011	COMPAL	+3.3V_RUN Giltch when AC plugin	Add D87, R1662 and R1663 (pull high to +3.3V_RUN_TPM) for HW solution backup	X02
66	14~21	HW	11/29/2011	COMPAL	Change PCH to C1 version	Change UH4 to SA00005AG1L(HM77 for non vpro)	X02
67		HW	11/29/2011	COMPAL	Change RC value at Gate of MOS Load SW to modify power rail soft start timing	RC72 from 100K to 330K; RC143 form 330K to 1M; CC136 form 0.1u to 0.022u R412 from 100K to 470K; R1632 form 1M to 4.7M; C293 form 0.1u to 0.022u R507 from 100K to 470K; R517 form 1M to 4.7M; C400 form 0.1u to 0.022u R722 from 100K to 470K; R1625 form 1M to 4.7M; C644 form 4700p to 220p R729 from 100K to 470K; R1628 form 1M to 4.7M; C650 form 4700p to 220p R917 from 100K to 470K; R1617 form 1M to 4.7M; C770 form 4700p to 220p R920 from 100K to 470K; R1610 form 470K to 2.2M; C771 form 4700p to 470p R930 from 100K to 330K; R1611 form 470K to 1M; C773 form 2200p to 100p R906 from 100K to 470K; C763 form 2200p to 220p R912 from 100K to 470K; C766 form 470p to 220p	X02
68	36	HW	12/01/2011	COMPAL	Change P/N for HF	Change C412~C415 P/N to SE076104K8L	X02
69	35	HW	12/01/2011	COMPAL	Reserve 0.1uF CAP to GND for ESD request	reserve CE14, CE20, CE22, CC151, CC152, CC153 to GND	X02
70	19	HW	12/05/2011	COMPAL	Change LH1 from bead to Inductor for CRT	Change LH1 to 1uH Inductor(SHI00007W0L)	X02
71	17, 38	HW	12/07/2011	COMPAL	EMI solution for E-Docking USB port	Swap USB Port7 and Port8 and reserve a choke(L108) at E-Docking side: Port7 from NA to E-docking Port8 from E-Docking to NA	X02
72	24, 32, 37	HW	12/07/2011	COMPAL	Change USB9,12,13 CMC to 180ohm for EMI request	Change L10,L52,L107 to SM070002X00(OCF2012181YZF)	X02
73	37	HW	12/08/2011	COMPAL	Follow CONN List_1130A Change JAUD1 to ACES_51522-0200N-P01	Change JAUD1 to ACES_51522-0200N-P01	X02
74	22	HW	12/09/2011	COMPAL	Thermal requests to change OTP from 88 to 92	Change R406 from 953ohm to 1.24Kohm	X02
75	41	HW	12/09/2011	COMPAL	To prevent inrush current at reset IC input	Change R1629 from 0ohms to 33ohms resistor	X02
76	19	HW	12/09/2011	COMPAL	For CRT issue	Change CH36 from 10uF to 22uF	X02
77	25	HW	12/13/2011	COMPAL	Change HDMI R,C value for EMI request	Change R448,R449,R450,R452,R453,R454,R455,R456 from 680ohm to 604ohm; C1209~C1216 from 4.7pF to 3.9pF	X02
78	42	HW	12/15/2011	COMPAL	+3.3V_SUS sequence timing	R911 from 100K to 470K; R1618 from 1M to 4.7M; C767 from 4700p to 220p	X02
79	43	HW	12/15/2011	COMPAL	Change current limit resistors of LED	R934 from 1.2K to 820, R957 from 1.2K to 1K, R951 from 330 to 270, R949 from 1.2K to 910,	X02

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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
80	34	HW	01/04/2012	COMPAL	Change RC25 value for ESD	Change RC25 from 0ohm to 1kohm(ST MEMO)	A00
81	40	HW	01/17/2012	COMPAL	SMSC creates a new catalog part number and IC marking for the MEC5055	Change U51 P/N to SA00003TZ2L	A00
82	43	HW	02/20/2012	COMPAL	Change current limit resistors of LED	Change R938 to 1.1k ohm, R958 to 560 ohm, R953 to 130 ohm, R951 to 470 ohm, change R939, R959, R957, R934, R949 to 1.2k ohm	A00
83	38	HW	02/24/2012	COMPAL	Dalmore14 UMA hang on white screen issue when attached AC+media battery after hot dock.	Change R755 from 100k ohm to 10k ohm	A00
84	40	HW	02/24/2012	COMPAL	Change board ID to A00	Change R875 to 33K ohm	A00
85	33	HW	02/24/2012	COMPAL	Change SD CLK damping resistor for EMI request	Change R676 from 33 ohm to 10 ohm	A00
86	32	HW	02/24/2012	COMPAL	Change BOM option for TPM/TCM funtion	Change C550, C551, C552, C553, R659, R660, R1662, RH311 BOM option to 5@	A00
87	25	HW	03/03/2012	COMPAL	SMT request to change F2 footprint	For DFX concern of F2 2nd source, SP040003H0L, change F2 footprint to F_MF-MSMF050-2	A00
88	14~21, 30	HW	03/03/2012	COMPAL	Change PCH P/N for X-build	UH4 is changed to SA00005AG3L	A00
89	14	HW	03/03/2012	COMPAL	De-pop resistor on PCH JTAG for power saving	De-pop RH288, RH47, RH48 and RH49	A00

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